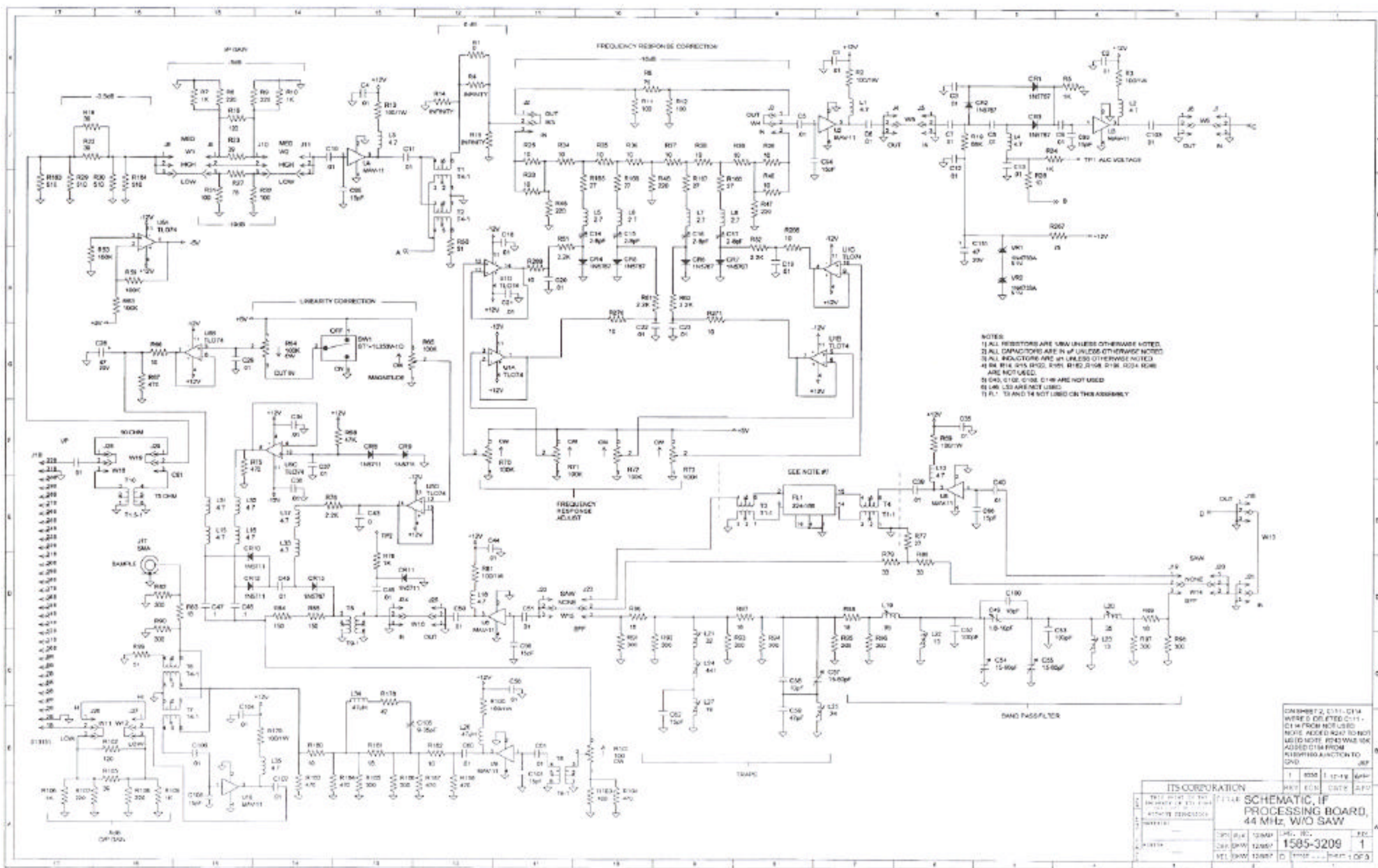
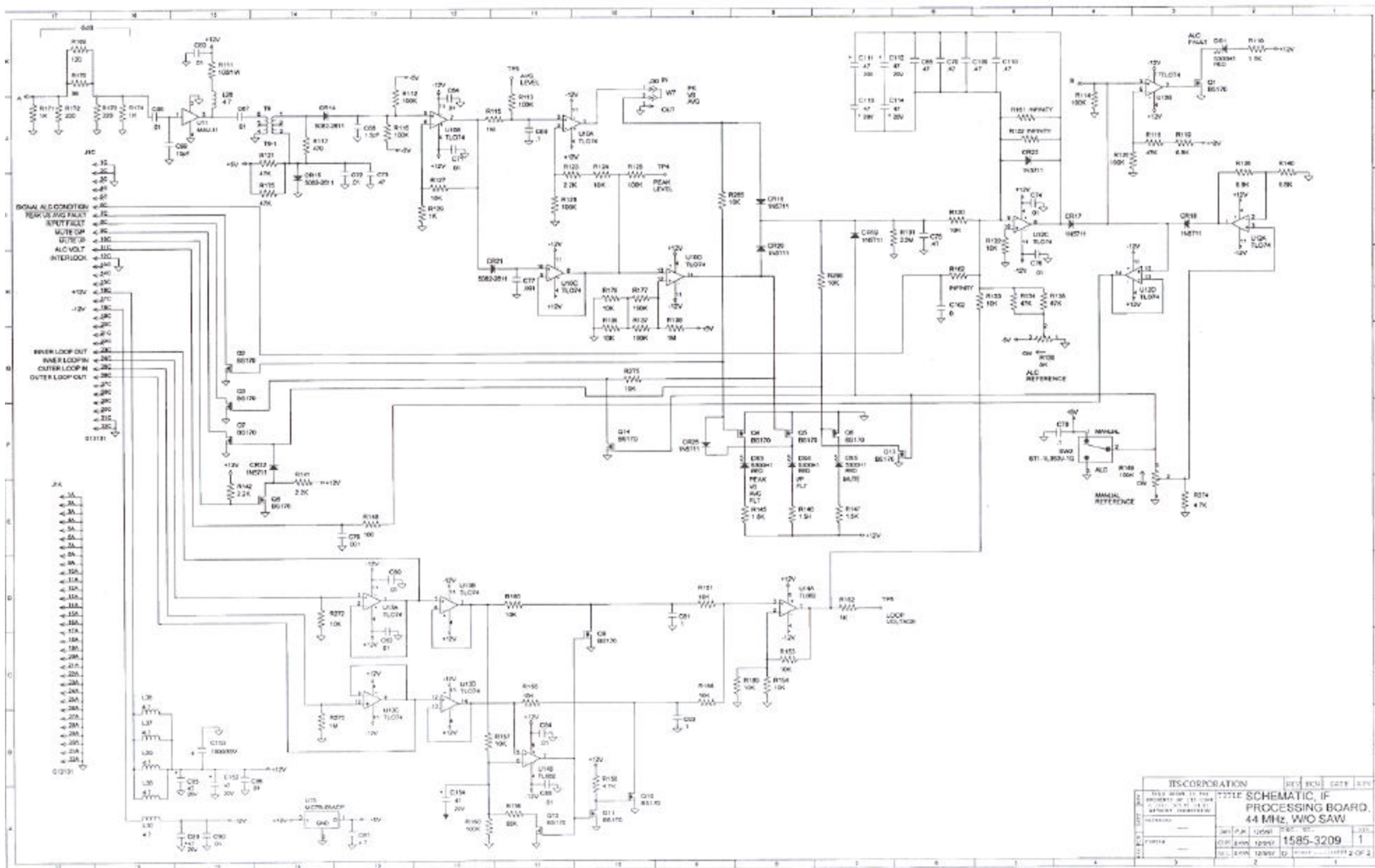
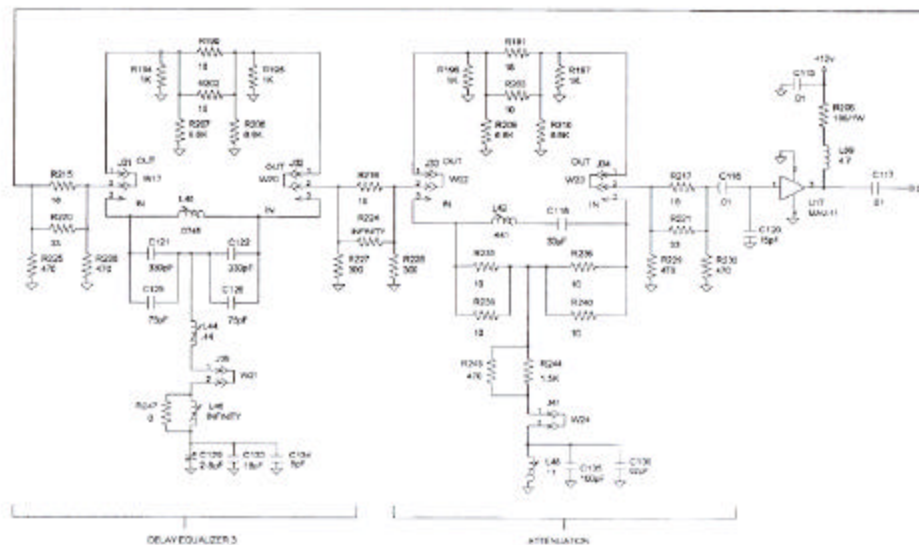
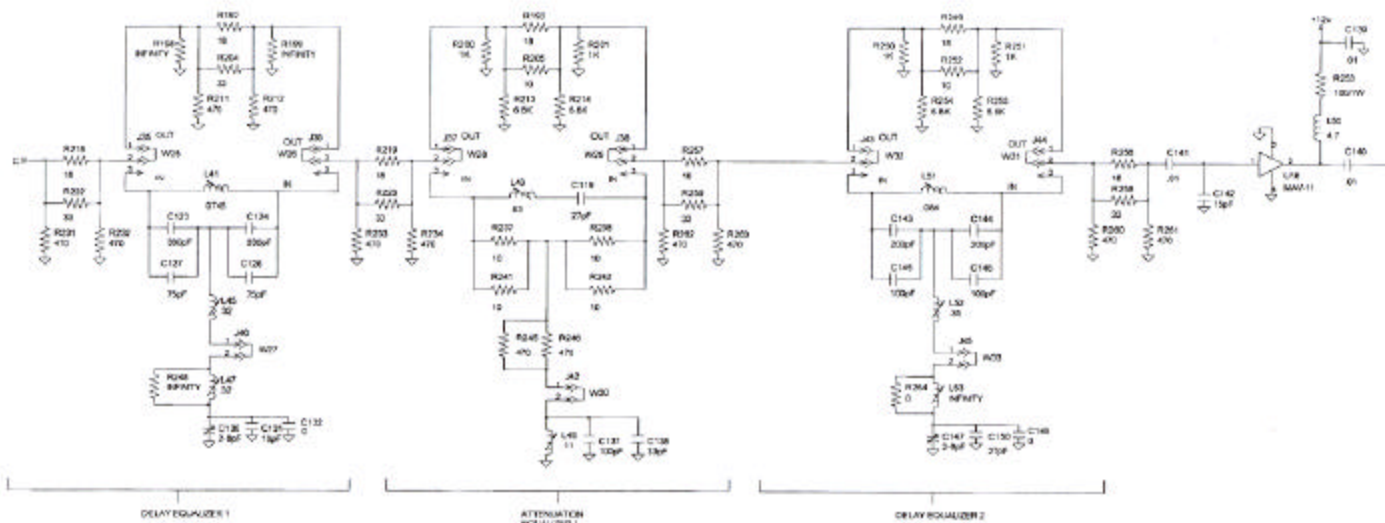
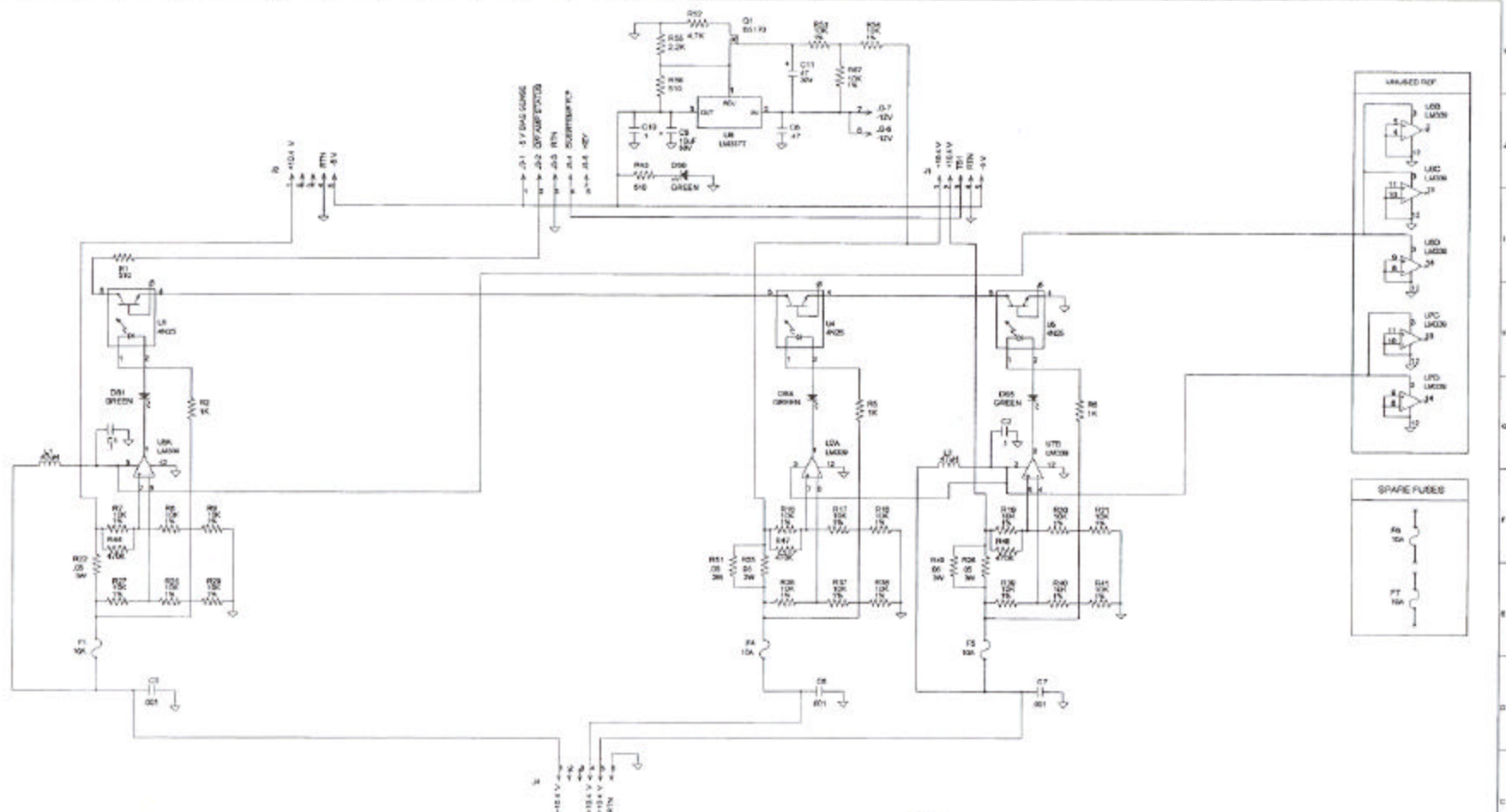






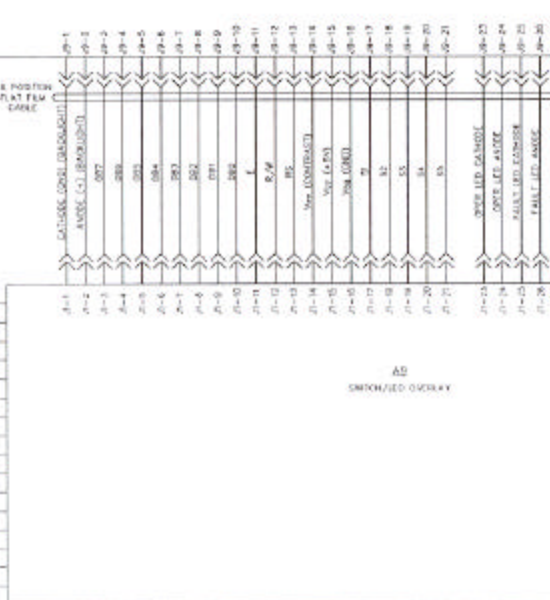
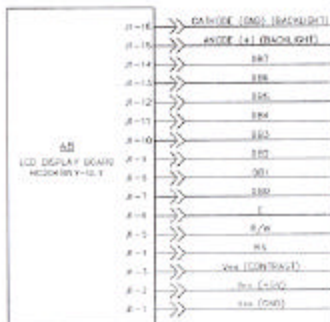


RSC CORPORATION				REV	DATE	APP
1	2	3	4	5	6	7
TITLE SCHEMATIC, IF PROCESSING BOARD, 44 MHz, W/O SAW				1585-3209	1	
DESIGNED BY	CHK'D BY	APP'D BY	DATE	1585-3209	1	
DRAWN BY	CHK'D BY	APP'D BY	DATE	1585-3209	1	



NOTES:
1: ALL RESISTORS ARE 1% TOLERANCE UNLESS OTHERWISE NOTED
2: ALL CAPACITORS ARE 5% TOLERANCE UNLESS OTHERWISE NOTED

ITS CORPORATION		REV	SCN	DATE	APV
TITLE		SCH. BIAS PROTECTION BOARD, 3 SECTION			
PROJECT		1585-3250			
REV		1	1	1	0
REV		2	1	1	0
REV		3	1	1	0
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REV		98	1	1	0
REV		99	1	1	0
REV		100	1	1	0



63 F PROCESSING MODELS 1585-1127

J-10	8. 8000
J-11	8. 00000
J-12	8. 00000
J-13	8. 00000
J-14	8. 00000
J-15	8. 00000
J-16	8. 00000
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J-97	8. 00000
J-98	8. 00000
J-99	8. 00000
J-100	8. 00000

SEE NOTE 2

63
1585-1127
1585-1127

1585-1127

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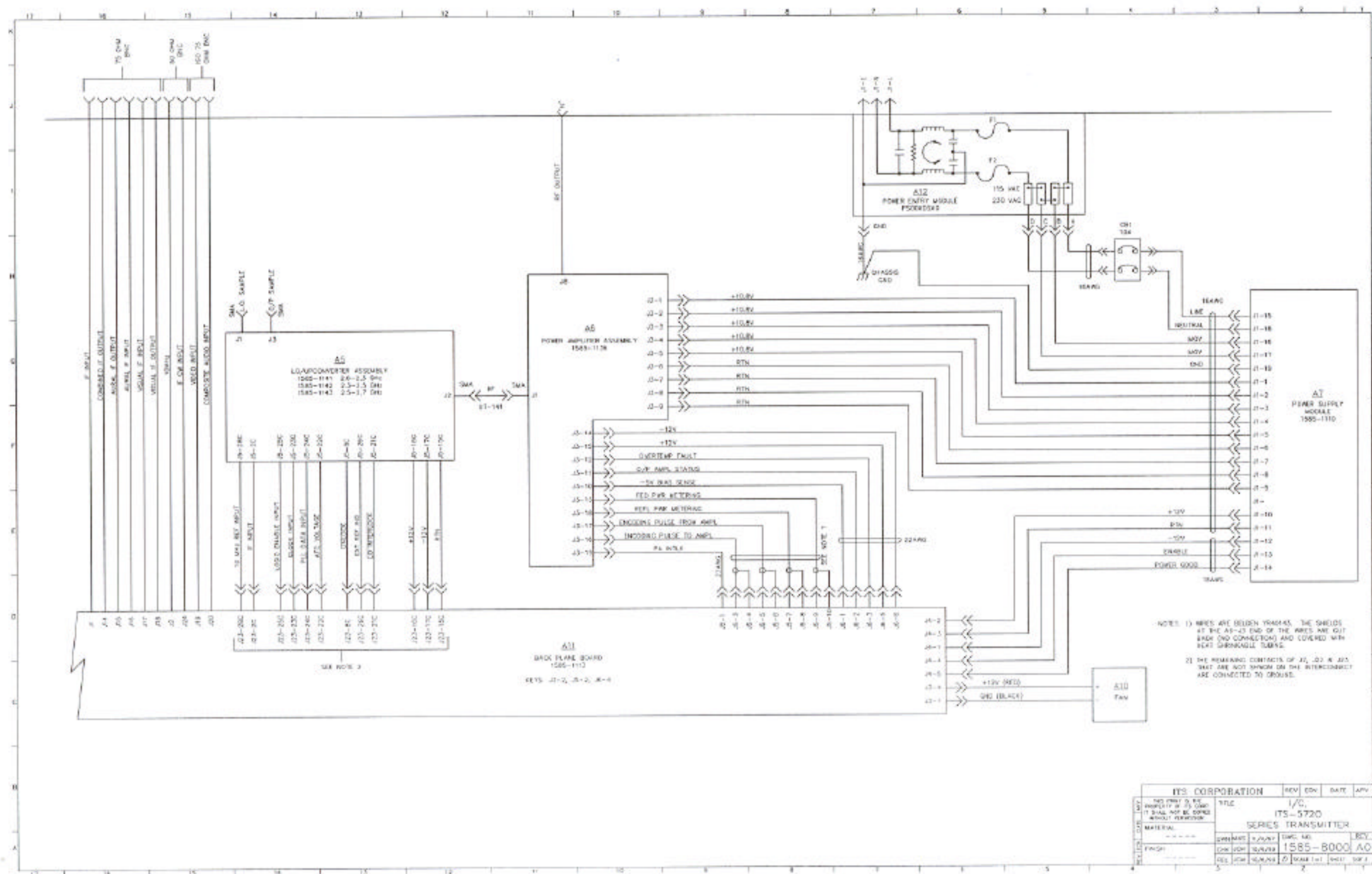
2007 AND 170

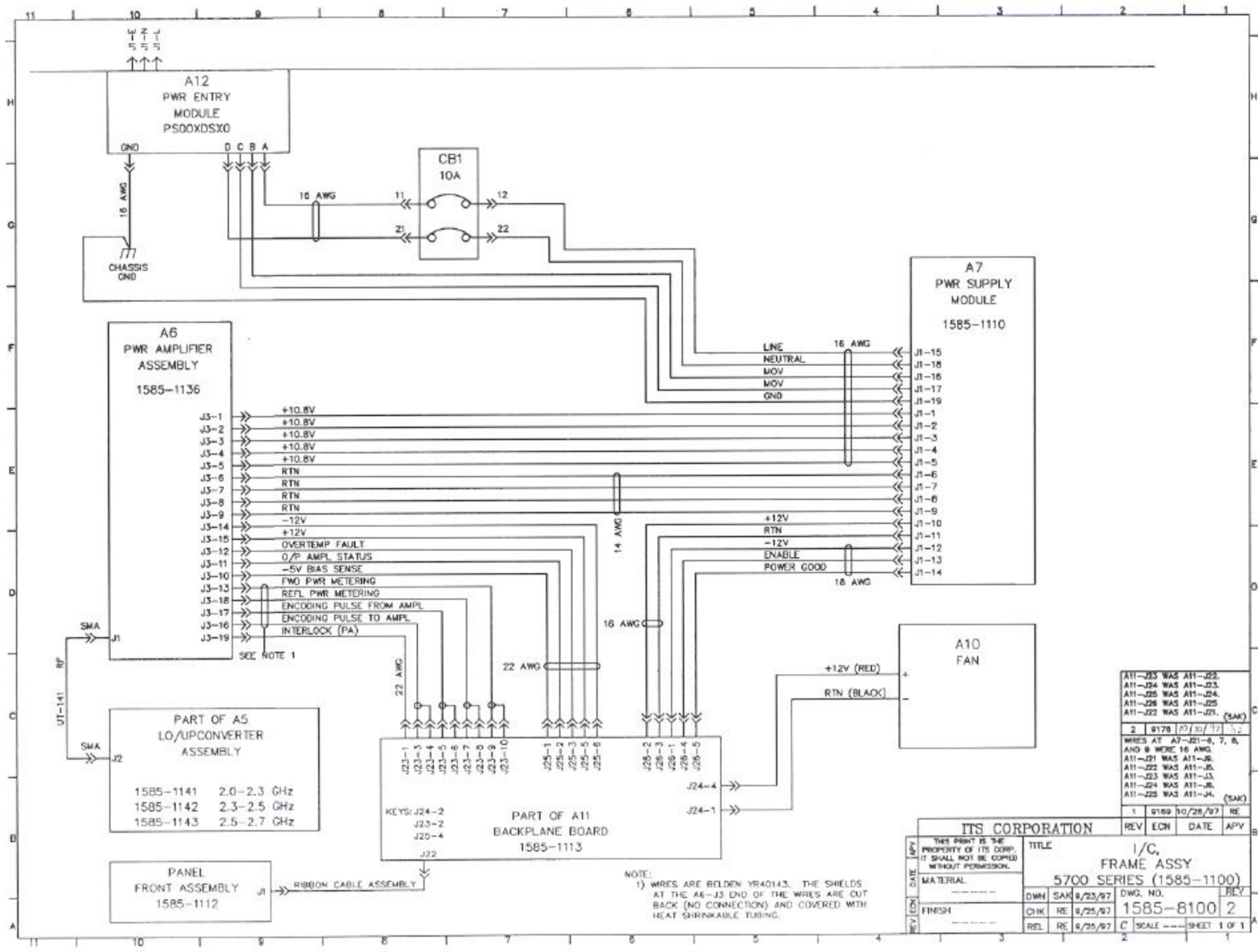
2007 AND 170

2007 AND 170

2007 AND 170

ITS CORPORATION			
REV	CON	DATE	APP
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ITS-5720			
SERIES TRANSMITTER			
1585-8000			
AD			



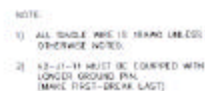
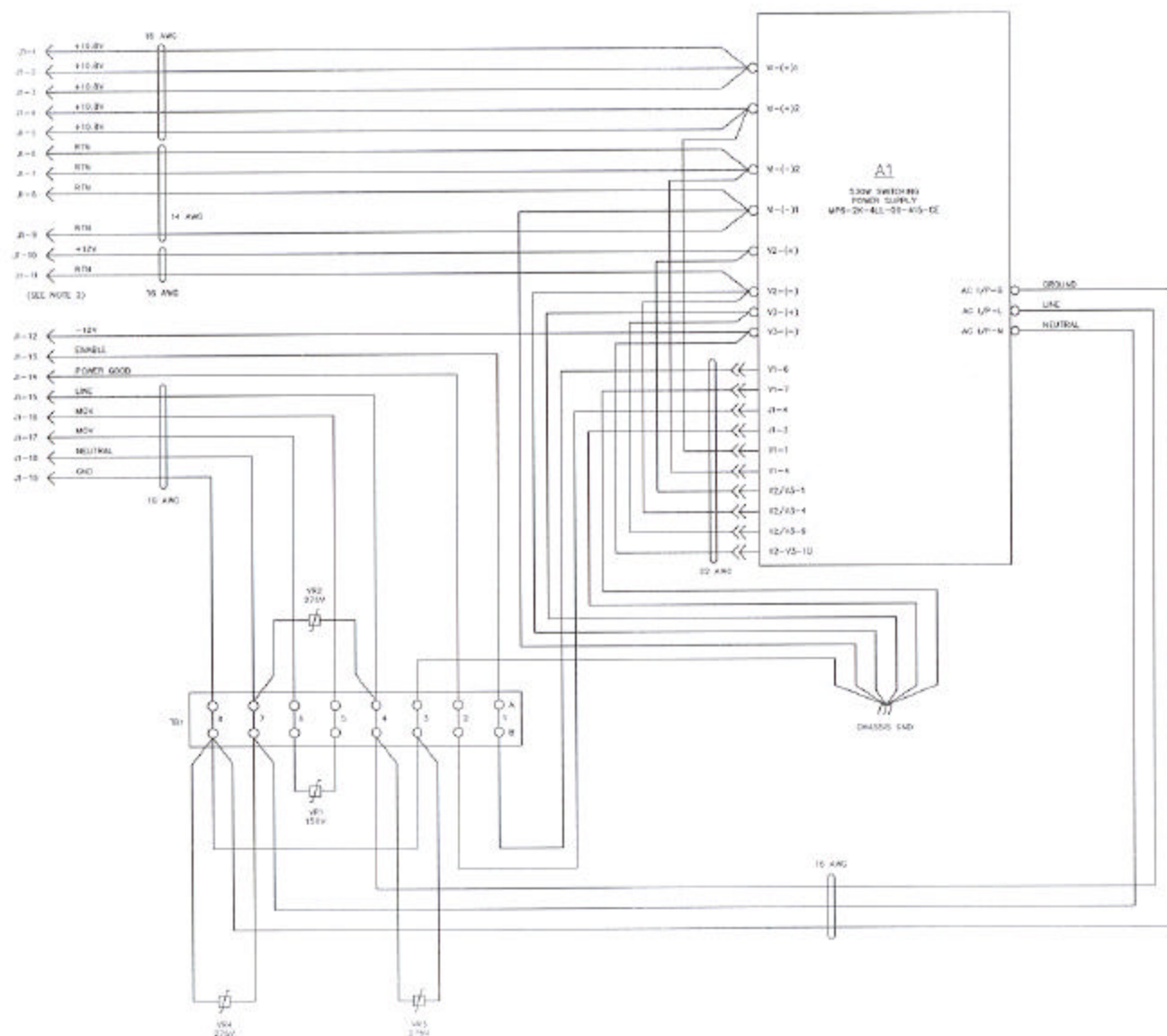


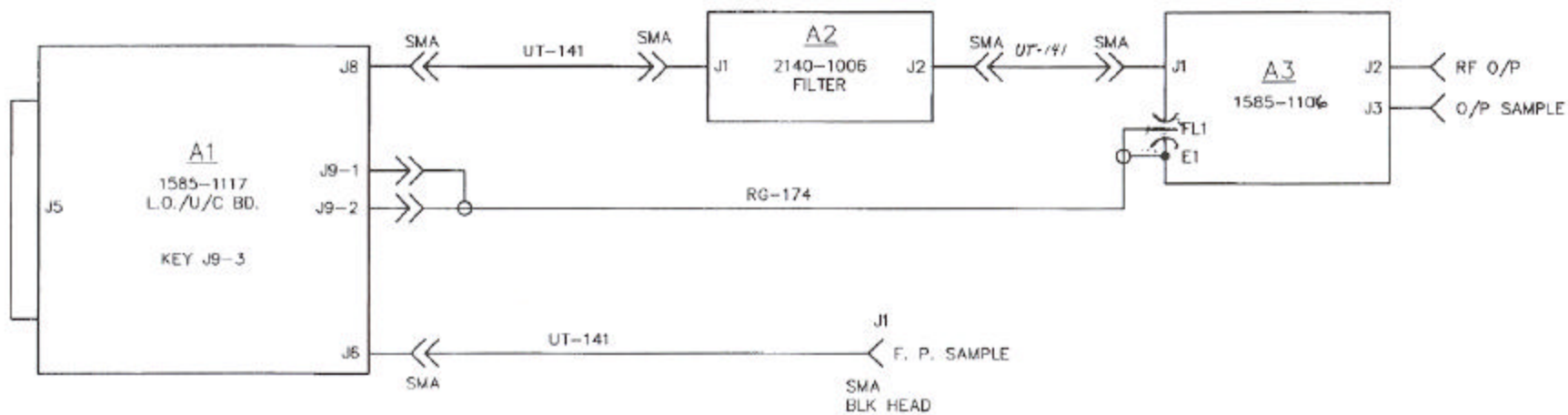
A11-J23 WAS A11-J22.
A11-J24 WAS A11-J23.
A11-J25 WAS A11-J24.
A11-J26 WAS A11-J25.
A11-J27 WAS A11-J26.
(SAC)

2 9178 10/28/97 10/28/97 10/28/97 10/28/97

WIRES AT A7-J21-6, 7, 8, AND 9 WERE 16 AWG.
A11-J21 WAS A11-J6.
A11-J22 WAS A11-J5.
A11-J23 WAS A11-J3.
A11-J24 WAS A11-J6.
A11-J25 WAS A11-J4.
(SAC)

ITS CORPORATION				TITLE			
REV	DATE	BY	CHK	REV	DATE	BY	CHK
1	9182	10/28/97	RE	1	9182	10/28/97	RE
MATERIAL				DWN SAK 9/23/97			
FINISH				REL RE 9/25/97			
SCALE				DWG. NO. 1585-8100			
SHEET 1 OF 1				2			

[illegible]



ITS CORPORATION							REV	ECN	DATE	APV
REV	ECN	DATE	APV	THIS PRINT IS THE PROPERTY OF ITS CORP. IT SHALL NOT BE COPIED WITHOUT PERMISSION.	TITLE					
					I/C					
					L.O./U/C ASSY					
					2.5 - 2.7 GHz					
REV	ECN	DATE	APV	MATERIAL						

				FINISH						

REV	ECN	DATE	APV	OWN	MAS	10/21/97	DWG. NO.		REV	
				CHK	gwb	11/20/97	1585-8143		0	
				REL	gwb	11/21/97	B	SCALE 1=1	SHEET 1 OF 1	