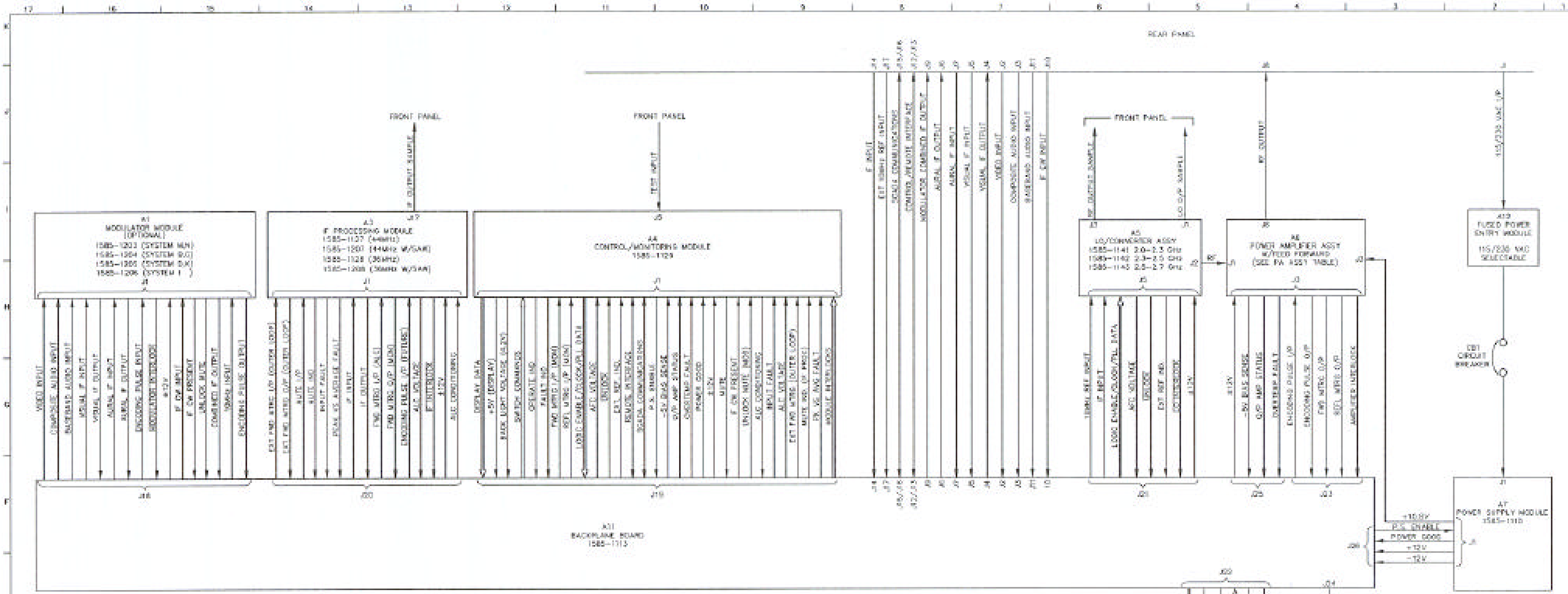
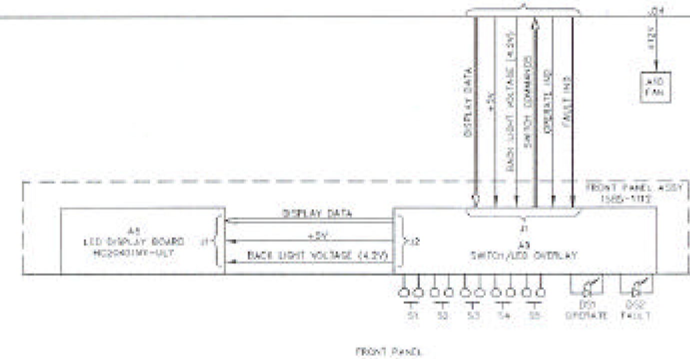




MODULE	SPECIFICATIONS
A1 MODULATOR	
A3 IF PROCESSING	IF INPUT: 36MHz, 44MHz (6MHz BW) INPUT LEVEL: -12 TO -2 dBm PEP (LOW GAIN) -17 TO -7 dBm PEP (MED GAIN) -32 TO -12dBm PEP (HI GAIN) INPUT RETURN LOSS: > 15dB IF OUTPUT LEVEL: -10dBm TO -1dBm PEP (LOW GAIN) -10dBm TO -6dBm PEP (HI GAIN) IF SAMPLE LEVEL: -16dB (LOW GAIN) -21dB (HI GAIN) POWER REQUIREMENTS: +12V @ 800mA -12V @ 100mA
A4 CONTROL/ MONITORING	POWER REQUIREMENTS: +12V @ 250mA -12V @ 50mA
A5 LO/UPCONVERTER	IF INPUT: 32-40 MHz OR 41-47 MHz INPUT RETURN LOSS: 18dB MIN @ 33-47 MHz INPUT LEVEL: -8dBm +1/-5dB DIGITAL AVERAGE POWER CONVERSION GAIN: 0 ±1.5dB RF OUTPUT: 2.0-2.3 GHz OR 2.3-2.5 GHz OR 2.5-2.7 GHz 10MHz INPUT LEVEL: 0 TO +6dBm LO OUTPUT LEVEL: 0 TO +6dBm LO TUNING STEP SIZE: 500kHz POWER REQUIREMENTS: +12V @ 400mA (750mA W/OPT INTERNAL 10MHz) -12V @ 50mA
A6 POWER AMPLIFIER	RF INPUT/OUTPUT: 2.0-2.3 GHz OR 2.3-2.5 GHz OR 2.5-2.7 GHz INPUT LEVEL: -10dBm ±2dB DIGITAL AVERAGE POWER INPUT RETURN LOSS: -10dB OUTPUT LEVEL: +34dBm (2.5W), +37dBm (5W), +40dBm (10W), +43dBm (15W) POWER REQUIREMENTS: +12V @ 0.2A -12V @ 5.5A
A7 POWER SUPPLY	AC INPUT LEVEL: 100-240VAC @ 10A 50/60/400Hz DC OUTPUT LEVEL: +10.8V @ 31A +12V @ 10A -12V @ 4A

PA ASSEMBLY TABLE

PART NUMBER	FREQ. (GHz)	DIGITAL PWR (WATTS)	ANALOG PWR (WATTS)
1585-1150	2.00-2.05	5	15
1585-1151	2.05-2.10		
1585-1152	2.10-2.15		
1585-1153	2.15-2.20		
1585-1154	2.20-2.25		
1585-1155	2.25-2.30		
1585-1156	2.30-2.35		
1585-1157	2.35-2.40		
1585-1158	2.40-2.45		
1585-1159	2.45-2.50		
1585-1160	2.50-2.55		
1585-1161	2.55-2.60		
1585-1162	2.60-2.65		
1585-1163	2.65-2.70		
1585-1164	2.00-2.05	10	30
1585-1165	2.05-2.10		
1585-1166	2.10-2.15		
1585-1167	2.15-2.20		
1585-1168	2.20-2.25		
1585-1169	2.25-2.30		
1585-1170	2.30-2.35		
1585-1171	2.35-2.40		
1585-1172	2.40-2.45		
1585-1173	2.45-2.50		
1585-1174	2.50-2.55		
1585-1175	2.55-2.60		
1585-1176	2.60-2.65		
1585-1177	2.65-2.70		
1585-1178	2.00-2.05	15	50
1585-1179	2.05-2.10		
1585-1180	2.10-2.15		
1585-1181	2.15-2.20		
1585-1182	2.20-2.25		
1585-1183	2.25-2.30		
1585-1184	2.30-2.35		
1585-1185	2.35-2.40		
1585-1186	2.40-2.45		
1585-1187	2.45-2.50		
1585-1188	2.50-2.55		
1585-1189	2.55-2.60		
1585-1190	2.60-2.65		
1585-1190	2.65-2.70		

REV	ECN	DATE	APV
P4	---	11/6/01	
P3	---	9/11/01	
P2	---	8/1/97	
P1	---	7/31/97	

ITS CORPORATION

MATERIAL: -----

FINISH: -----

TITLE: B/D, ITS-5700 SERIES TRANSMITTER

DWN: SH 7/8/97

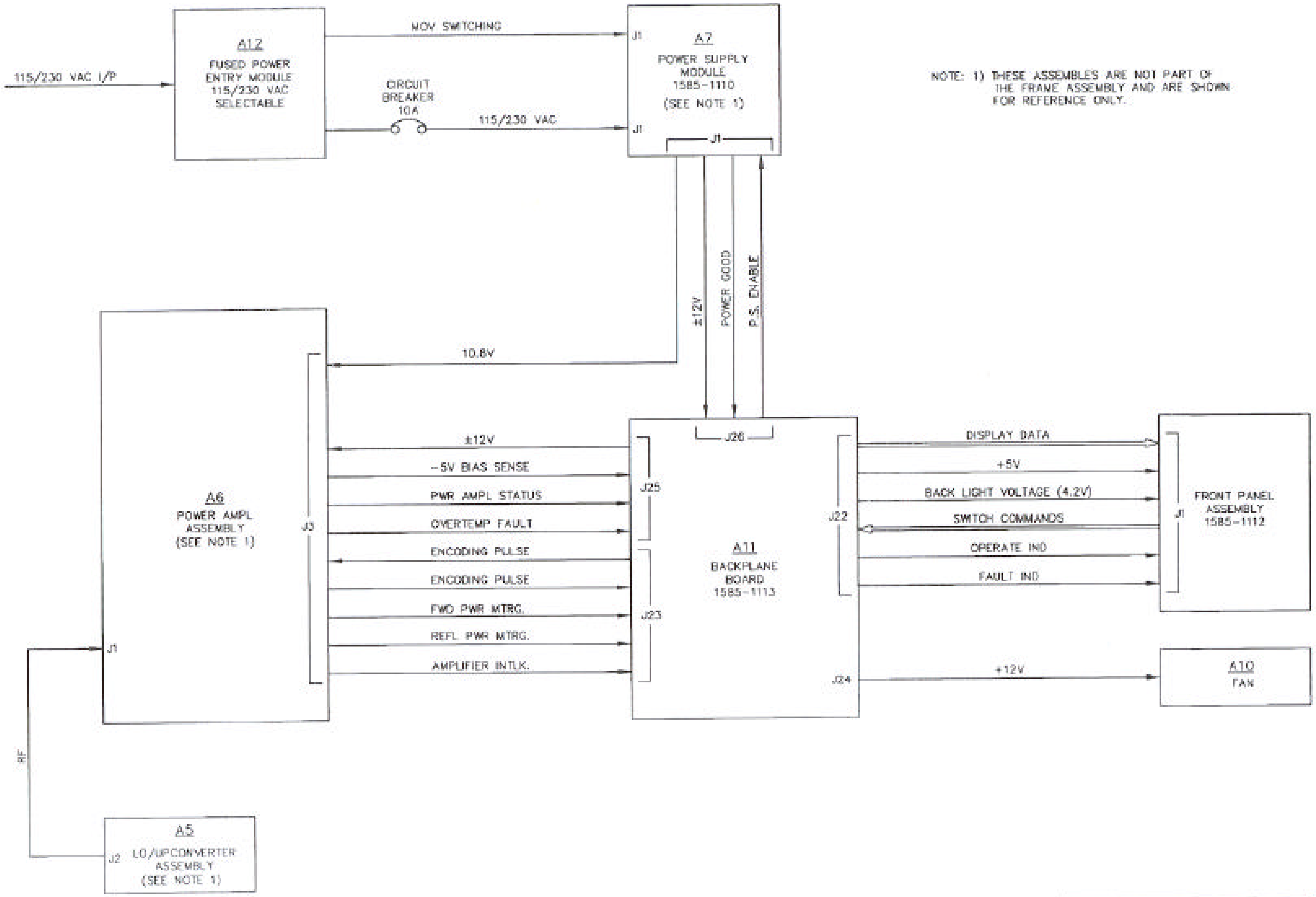
CHK:

AD:

DWG. NO: 1585-3000

REV: P4

D SCALE: ---- SHEET 1 OF 1



REV	ECN	DATE	APV	
1	9141	11/25/97	RE	ADDED NOTE 1 & REF TO NOTE 1 @ A5, A6, A7, @ A7; ADDED—A11—@ A12; ADDED ARROW TO I/P; RJB

ITS CORPORATION

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MATERIAL ------

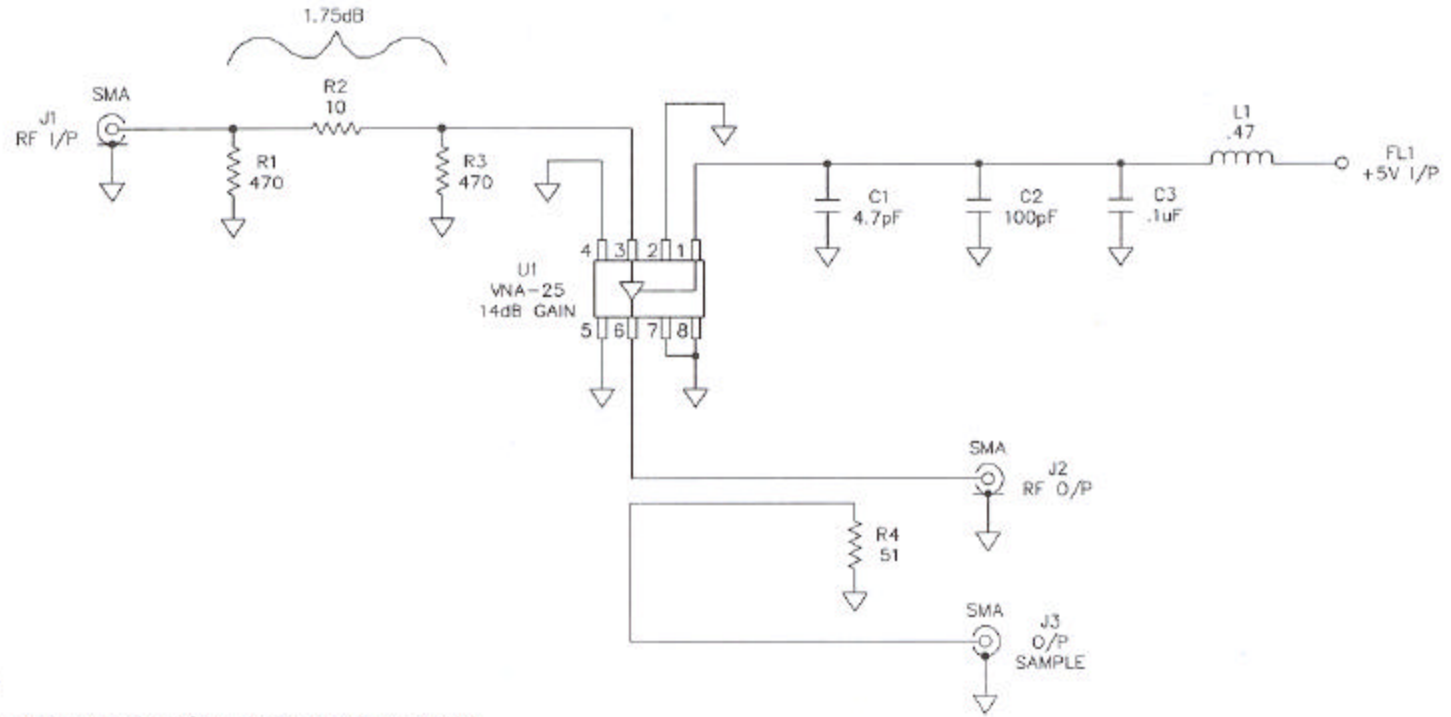
FINISH ------

TITLE: B/D FRAME ASSEMBLY, 5700 SERIES

DWN	MAS	10/29/97
CHK	RE	10/29/97
REL	RE	10/29/97

DWG. NO. 1585-3100 REV. 1

C SCALE 1=1 SHEET 1 OF 1



NOTE:

1) ALL RESISTORS ARE 1/8W UNLESS OTHERWISE NOTED.

2) ALL CAPACITORS ARE IN uF UNLESS OTHERWISE NOTED.

3) ALL INDUCTORS ARE IN uH UNLESS OTHERWISE NOTED.

R2 WAS 18 OHMS. R1 & R3 WAS 300 OHMS. (RFB)

REV	ECO	DATE	APV
1	9526	10/13/98	BKW

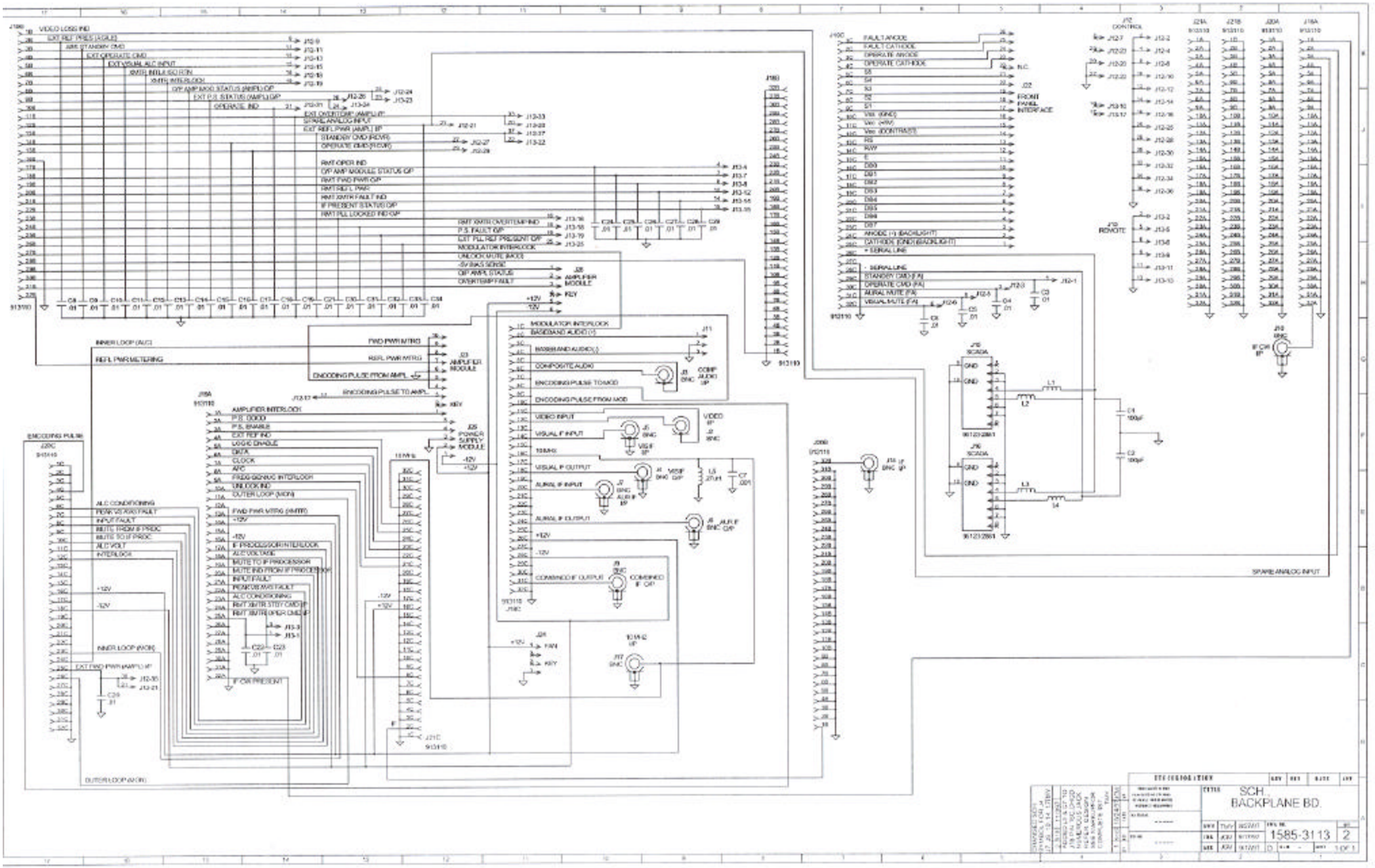
ADC Telecommunications

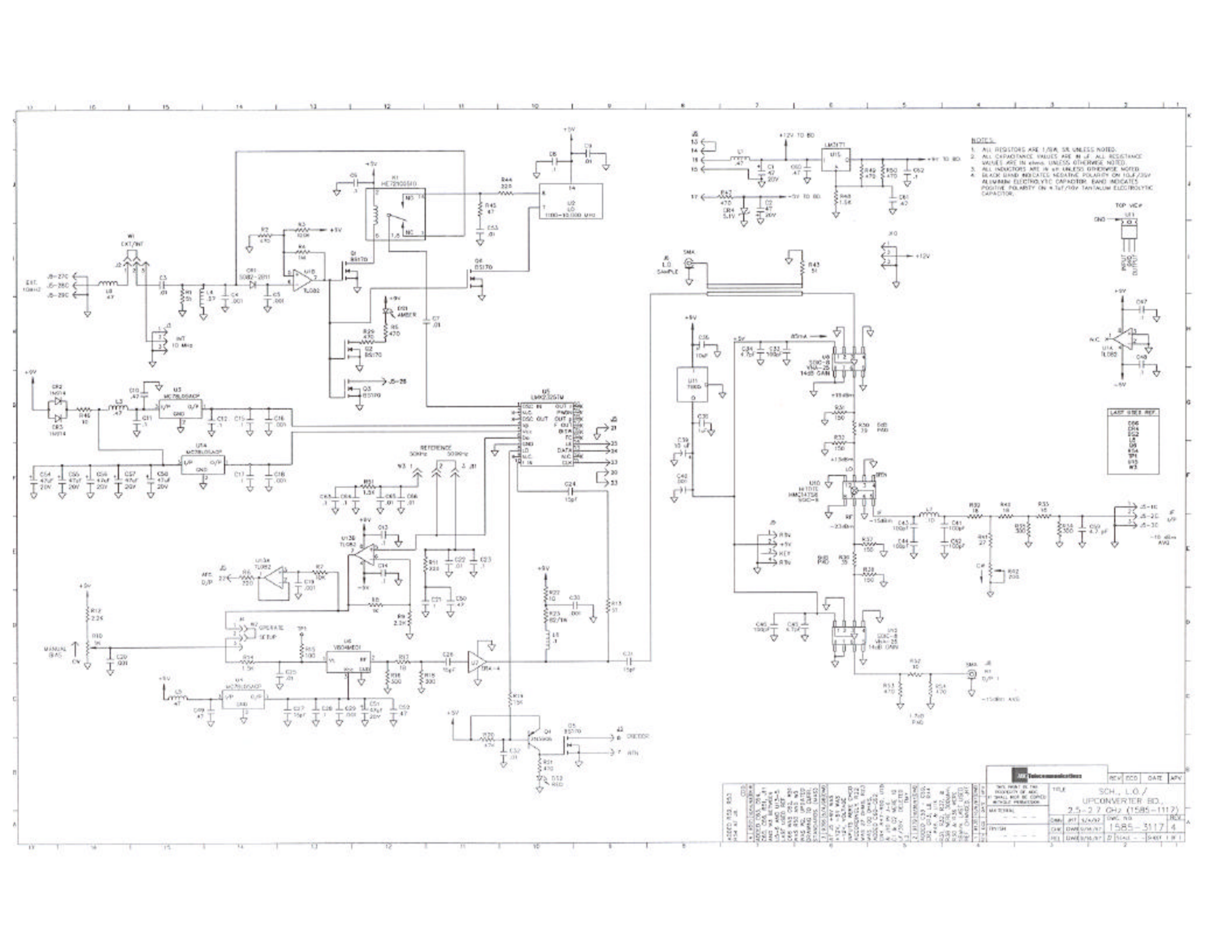
MATERIAL ---

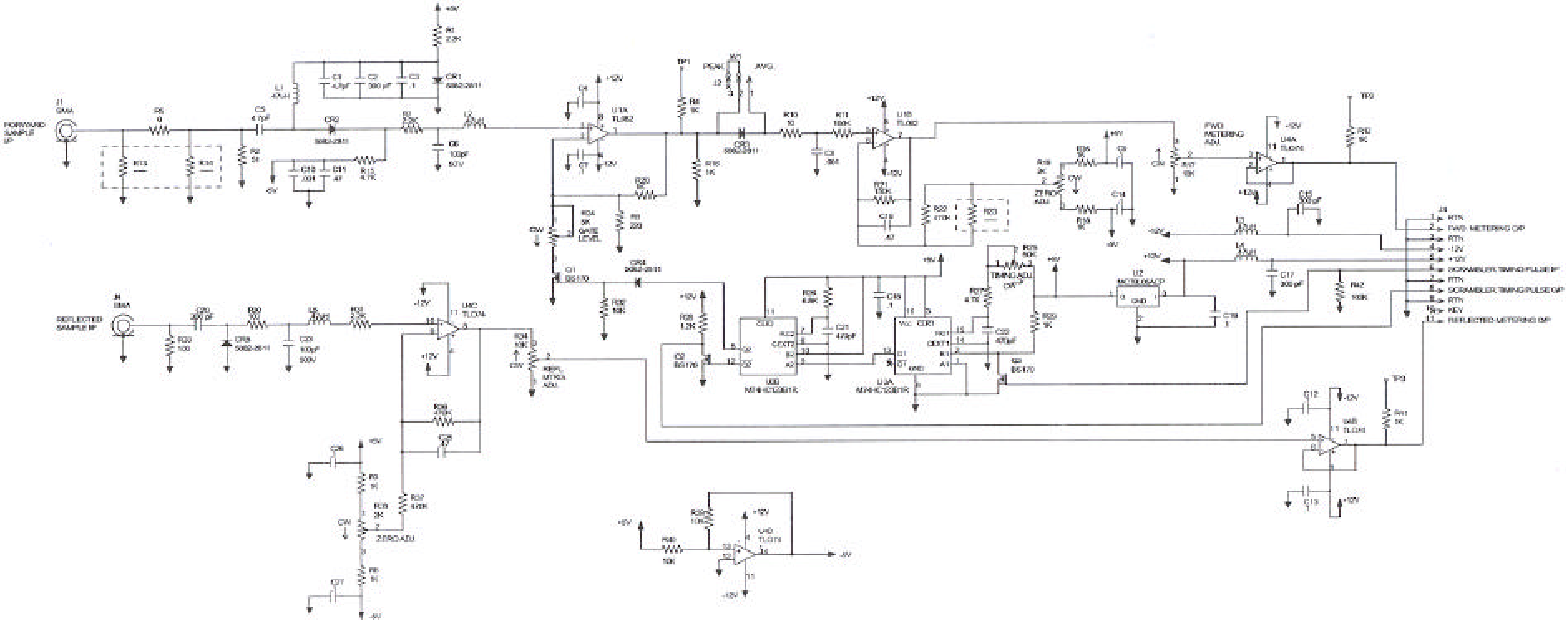
FINISH ---

TITLE: SCHEMATIC, SINGLE STAGE AMPLIFIER (1585-1101)

			DWG. NO.	REV
DWN	TMY	7/28/97	1585-3101	1
CHK	DWB	8/6/97		
REL	DWB	8/6/97	B SCALE 1=1	SHEET 1OF1


SCH.
BACKPLANE BD.
1585-3113
2
1 OF 1


NOTES:
1. ALL RESISTORS ARE 1/8W, 5% UNLESS NOTED.
2. ALL CAPACITANCE VALUES ARE IN uF. ALL RESISTANCE VALUES ARE IN ohms, UNLESS OTHERWISE NOTED.
3. ALL INDUCTORS ARE IN uH UNLESS OTHERWISE NOTED.
4. BLACK BAND INDICATES NEGATIVE POLARITY ON 10uF/35V ALUMINUM ELECTROLYTIC CAPACITOR. BAND INDICATES POSITIVE POLARITY ON 4.7uF/16V TANTALUM ELECTROLYTIC CAPACITOR.
LAST USED REF.
SCH., L.O./ UPCONVERTER BD., 2.5-2.7 GHz (1585-1117)
1585-3117 4



NOTES:

1.) ALL RESISTORS ARE 1/8W UNLESS OTHERWISE NOTED.
2.) ALL CAPACITORS ARE IN uF UNLESS OTHERWISE NOTED.
3.) ALL INDUCTORS ARE IN uH UNLESS OTHERWISE NOTED.

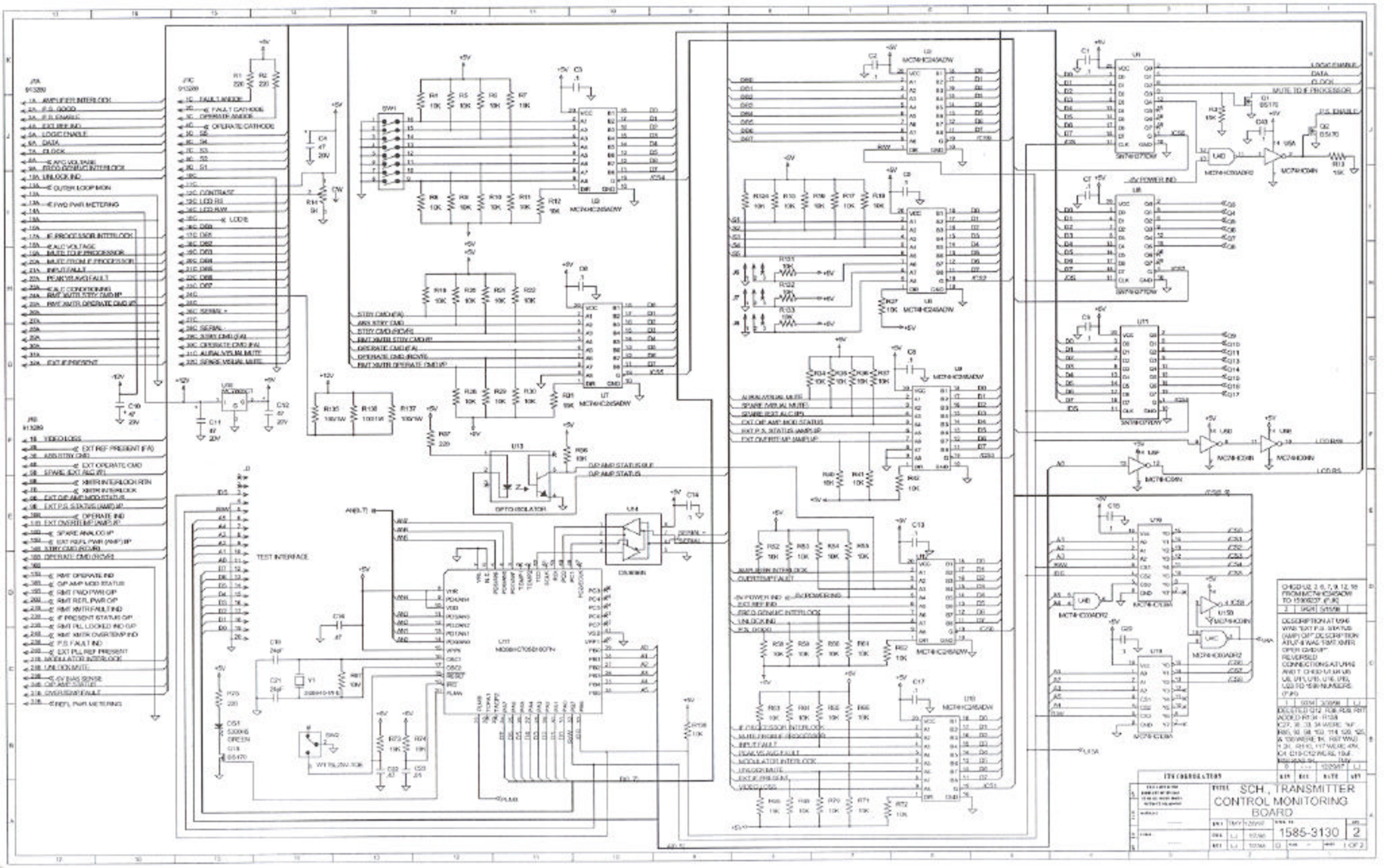
NOTE: R13, R14 AND R23 ARE NOT USED ON THIS SCHEMATIC.

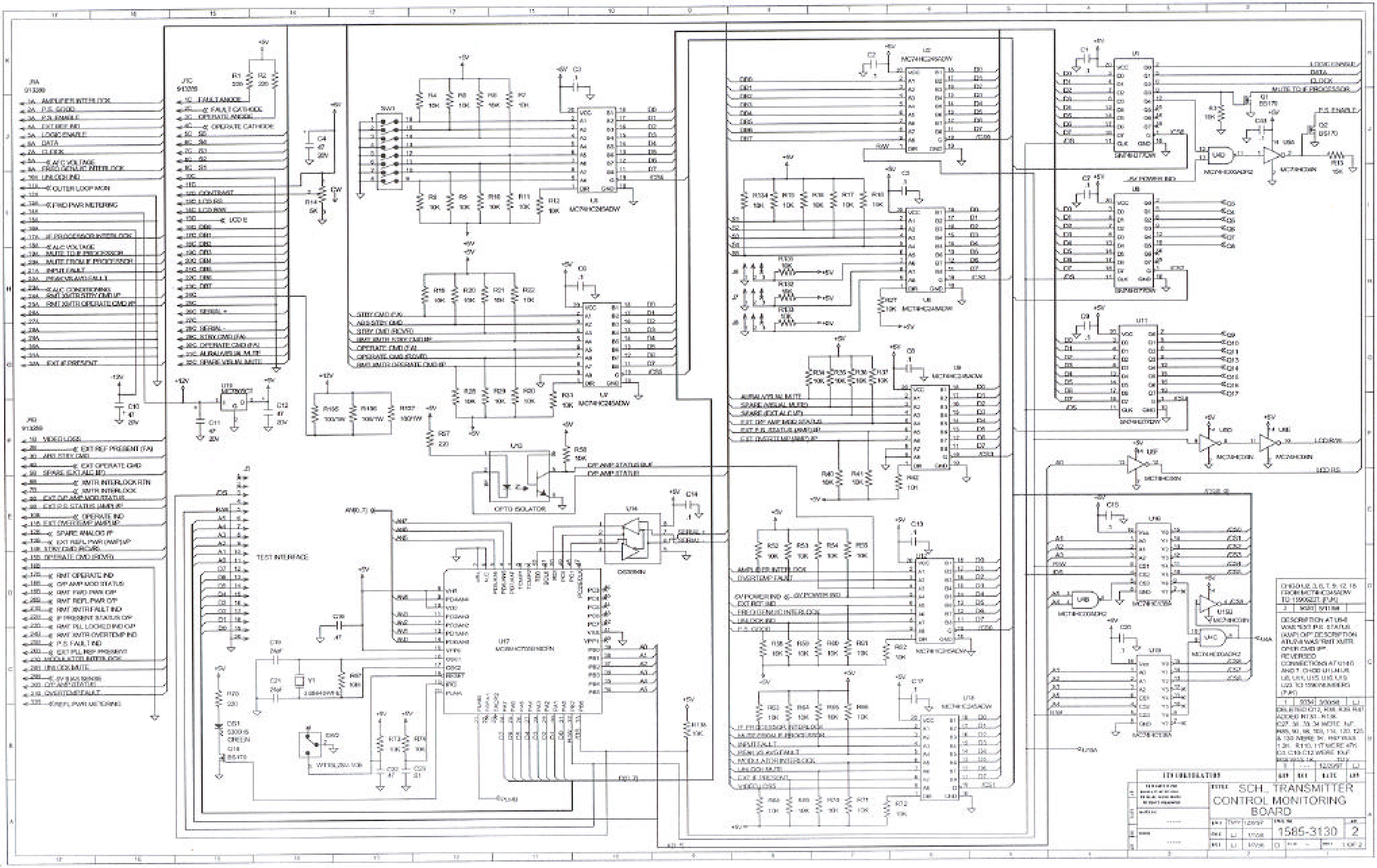
ADC Telecommunications

REV	ECO	DATE	APV

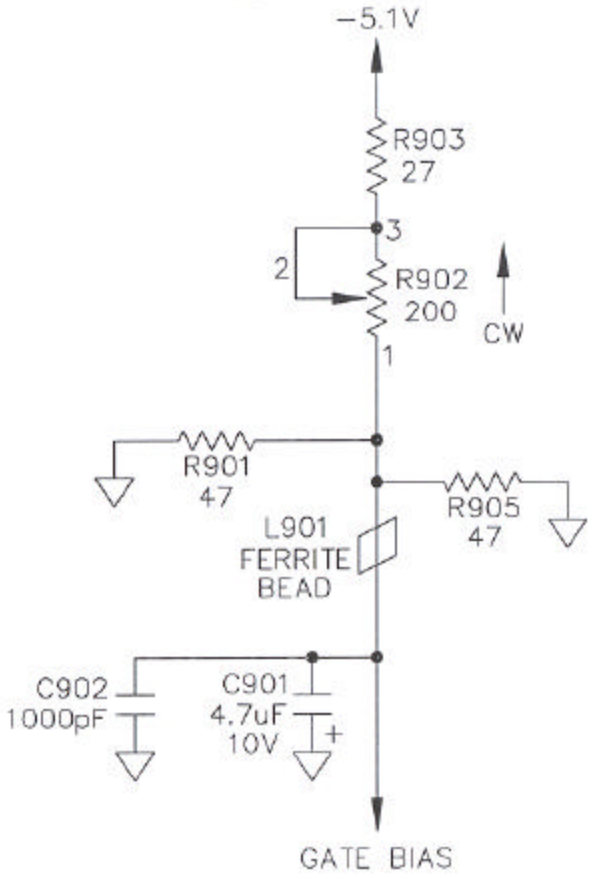
TITLE: SCH., DUAL POWER DETECTOR BD (1585-1125)

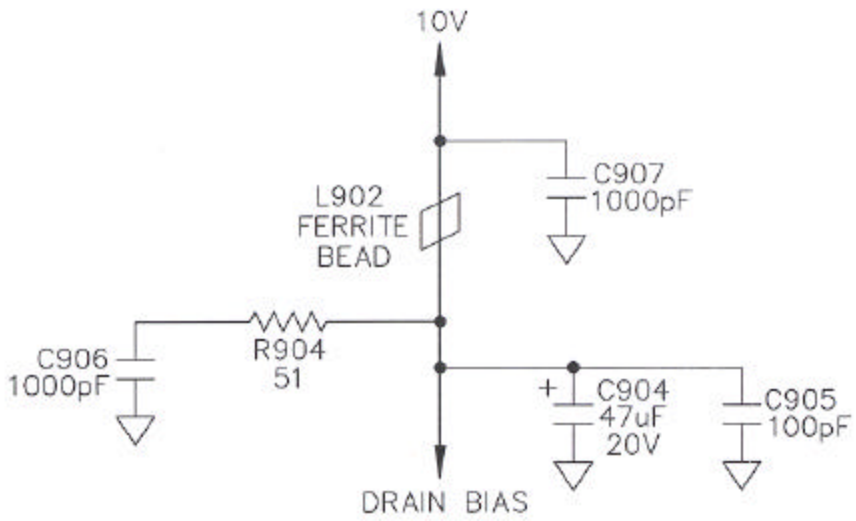
DWG NO: 1585-3125 | REV: 4 | SHEET 1 OF 1


SCH, TRANSMITTER CONTROL MONITORING BOARD
1585-3130
2
1 OF 2



SCH., TRANSMITTER CONTROL MONITORING BOARD
1585-3130
2
1 OF 2
TEST INTERFACE
OPTO ISOLATOR
MC74HC245ADW





NOTES:

1) ALL RESISTORS ARE 1/8W, 5% & IN VALUES OF OHMS UNLESS OTHERWISE NOTED.
2) IF 200 OHM POTS ARE NOT AVAILABLE, 100 OHM POTS MAY BE USED. HOWEVER, RESISTOR VALUES MAY HAVE TO CHANGE (SEE NOTE 2).
3) TO ACCOUNT FOR VARIATIONS IN THE FLL300 & FLL200, R903 MAY BE RAISED TO INCREASE DRAIN CURRENT (TYP 150 OHM) OR LOWERED TO DECREASE DRAIN CURRENT (TYP 27 OHM).

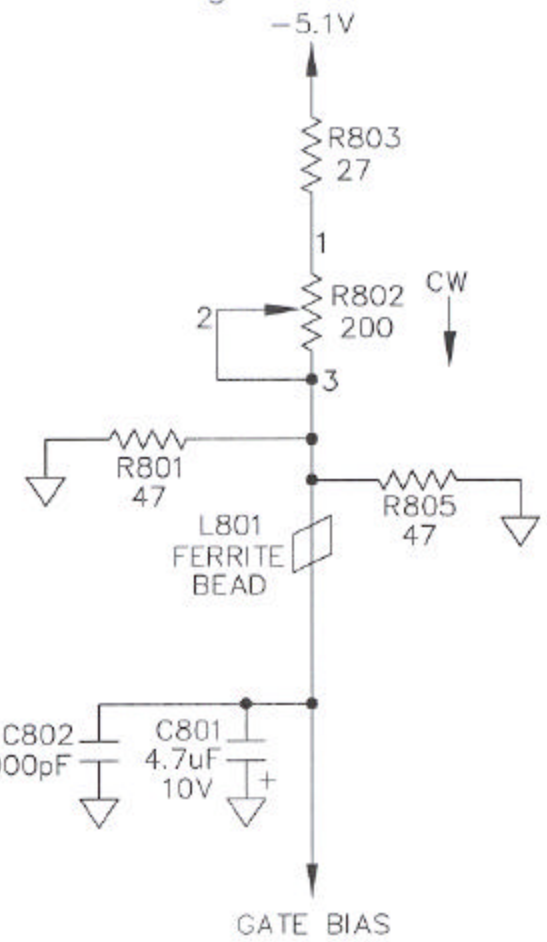
ITS CORPORATION	REV	ECN	DATE	APV

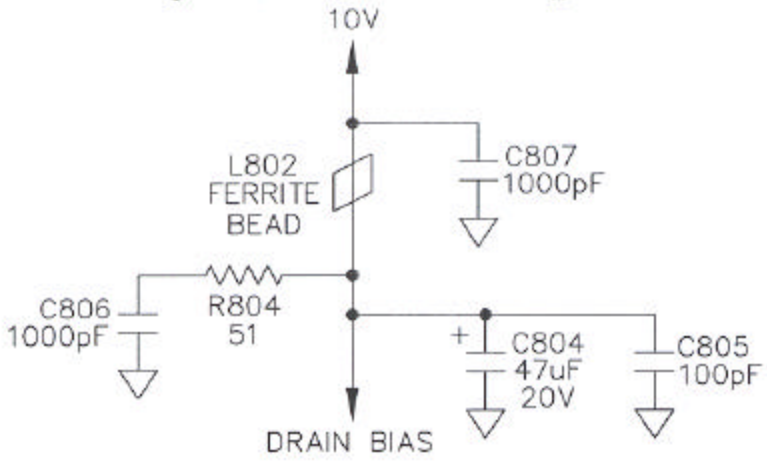
MATERIAL ———

FINISH ———

TITLE SCH, AMP. DAUGHTER BOARD, RIGHT SIDE (1585-1147)

			DWG. NO.	REV
DWN	JKF	10/1/97	1585-3147	0
CHK	DW	10/8/97		
REL	DW	10/8/97	A SCALE ---	SHEET 1 OF 1





NOTES:

1) ALL RESISTORS ARE 1/8W, 5% & IN VALUES OF OHMS UNLESS OTHERWISE NOTED.
2) IF 200 OHM POTS ARE NOT AVAILABLE, 100 OHM POTS MAY BE USED. HOWEVER, RESISTOR VALUES MAY HAVE TO CHANGE (SEE NOTE 2).
3) TO ACCOUNT FOR VARIATIONS IN THE FLL300 & FLL200, R803 MAY BE RAISED TO INCREASE DRAIN CURRENT (TYP 150 OHM) OR LOWERED TO DECREASE DRAIN CURRENT (TYP 27 OHM).

ITS CORPORATION	REV	ECN	DATE	APV

APV	THIS PRINT IS THE PROPERTY OF ITS CORP. IT SHALL NOT BE COPIED WITHOUT PERMISSION.	TITLE	SCH, AMP. DAUGHTER BOARD, LEFT SIDE (1585-1148)		
DATE	MATERIAL -----	DWN	JKF	10/1/97	DWG. NO. 1585-3148 REV 0
ECN	FINISH -----	CHK	DW	10/8/97	
REV		REL	DW	10/8/97	A SCALE--- SHEET 1 OF 1