

CIRCUIT DESCRIPTION

The CFS8DL5853AP is A 315 MHz version of the 345 MHz CFS8DL5853-3 glass-break detector. All designations and functions remain the same. When a glass-break event is detected by the microphone and U1 etc. a loop input on the MESSAGE ENCODER CHIP (U2 etc.) is activated this causes the OSCILLATOR ENABLE SWITCH (Q4 etc.) to turn on the OSCILLATOR (Q3 etc.) The OSCILLATOR (Q3 etc.'s) who's FDE is SAW Y2 whos frequency is 315 MHz. the RF is fed into the PA (Q7 etc.) which is MODULATED by (Q5 & Q6 etc.) The RF is tuned by L34 & C37, and the energy is coupled out to the ANTENNA (ANT. etc.) by C40, L5, ANT and C41. As always all message timing and supervision timing is controled by the MESSAGE ENCODER CHIP (U2 etc.) this ensures that all FCC & IC rules concerning timing are met.