

EXHIBIT 2

Circuit Description.

CFS8DL58450W

The complete PIR motion detector is constructed on a pair of PCBs interconnected via a header.

The class 2 permissive change consisted of removing the battery holder from the secondary PCB, and having the three 3V lithium batteries mounted in the plastic case power both PCB's.

The main PCB contains only the PIR processing circuitry, the secondary PCB includes all RF transmitter circuitry.

The transmitter is a SAW resonator Colpitts oscillator, Q2, Y1 etc. The transmitter is on-off keyed (AM) modulated by a control signal from the ASIC U1 which turns PA Q3 on or off via Q5/Q5, thus modulating the output signal. The RF output signal is connected to a PCB mounted antenna ANT1, tuned via C17/C18.

U1 is an ASIC which generates the alarm and identification messages and controls timing etc. The alarm input from the PIR board is via header TB1.