

5800PIR-OD CIRCUIT DESCRIPTION

The 5800PIR-OD consists of two (2) circuit boards; a PIR (Passive Infra Red) motion detector board, and the 5800PIR-OD transmitter board. The PIR motion detector board is a Non-RF sensor board. The 5800PIR-OD Transmitter Board functions as follows: an analog signal level coming from the PIR Board indicates a Motion / Tamper Event. The MESSAGE ENCODER CHIP (U1) performs the following functions: 1st it generates the transmitted message. 2^{ed} it performs all timing functions in accordance with FCC Part 15.231, and RSS210 Table 1; such as message repartition, adherence to the 5 second rule, and generation of the supervision messages; thus insuring compliance. Upon receipt of a Motion / Tamper Event U1 turns on the transmitter VCC via Q1 etc., Oscillator Q2 etc, who's FDE is SAW RESONATOR YL1 drives the PA Q3 etc., The PA is MODULATED with U1's alarm message by MODULATOR Q4 & Q5 etc., The PA output is applied to the ANTENNA via MATCHING NETWORK C2, C3, L4, L5, etc.