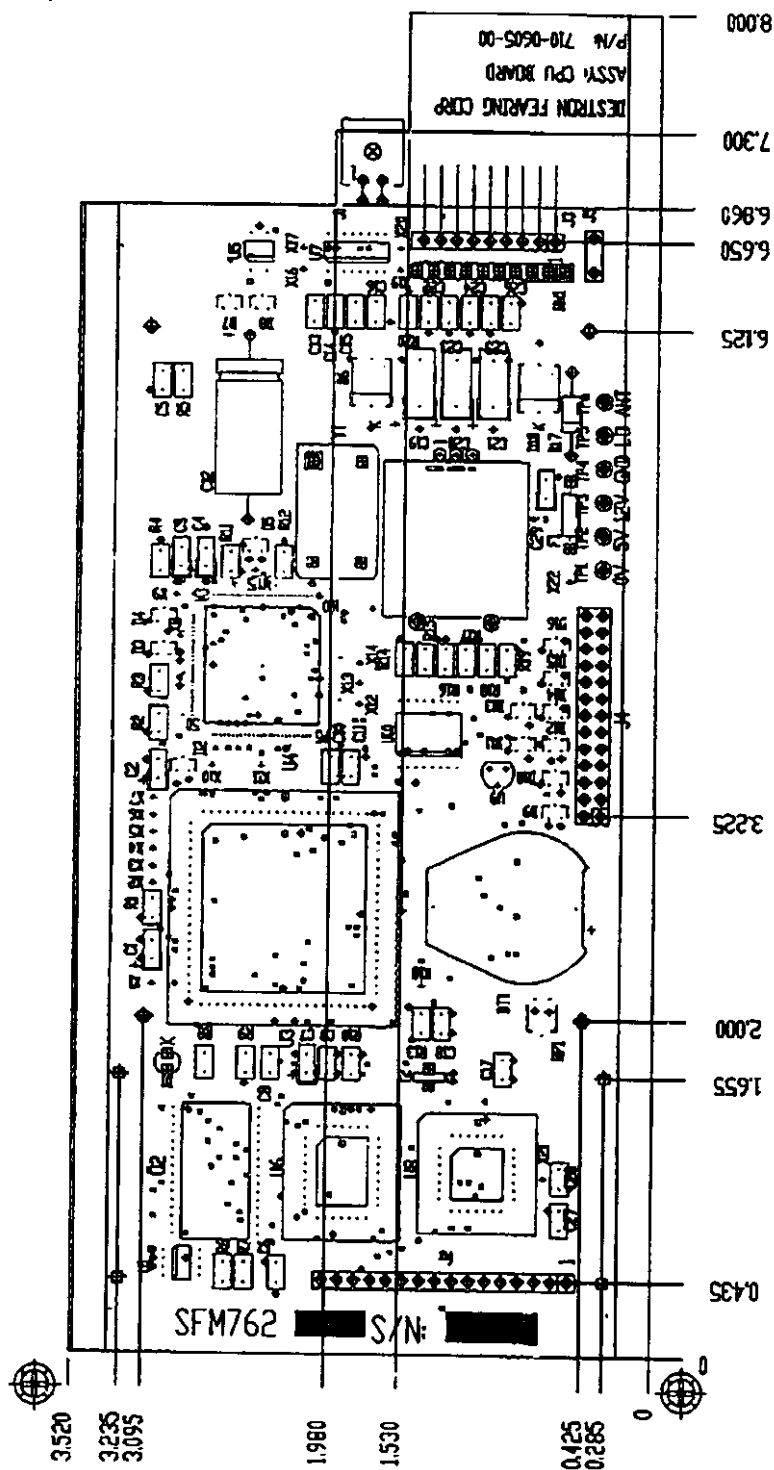


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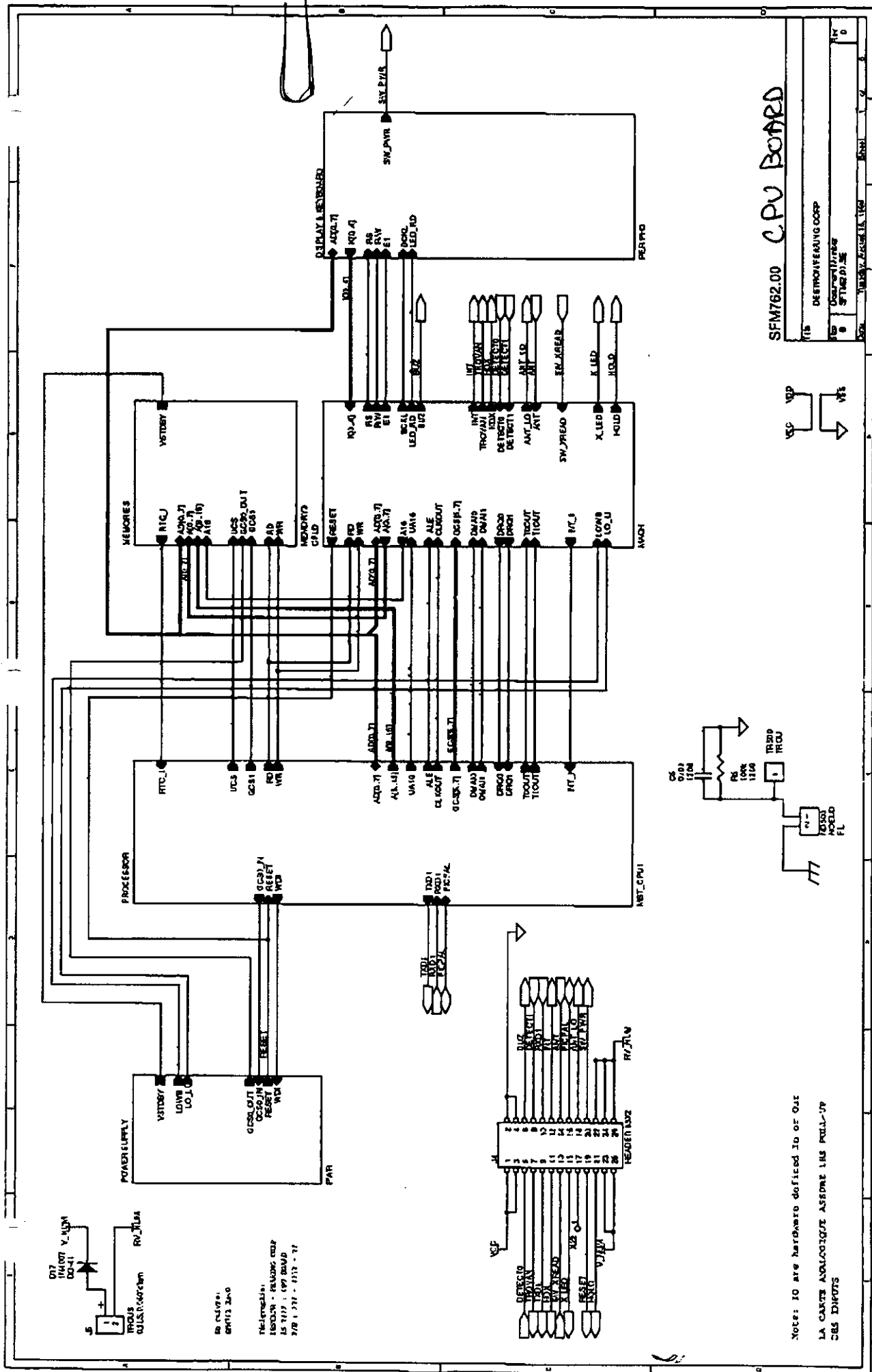


CPU BOARD

FINAL DIMENSIONS				HOLE SIZES
PLATE	UNPLATED	PLATED	THICK.	
1/8"	1/8"	1/8"	1/8"	1/8"
1/4"	1/4"	1/4"	1/4"	1/4"
3/8"	3/8"	3/8"	3/8"	3/8"
1/2"	1/2"	1/2"	1/2"	1/2"
5/8"	5/8"	5/8"	5/8"	5/8"
3/4"	3/4"	3/4"	3/4"	3/4"
7/8"	7/8"	7/8"	7/8"	7/8"
1"	1"	1"	1"	1"
1 1/8"	1 1/8"	1 1/8"	1 1/8"	1 1/8"
1 1/4"	1 1/4"	1 1/4"	1 1/4"	1 1/4"
1 1/2"	1 1/2"	1 1/2"	1 1/2"	1 1/2"
1 3/4"	1 3/4"	1 3/4"	1 3/4"	1 3/4"
2"	2"	2"	2"	2"
2 1/2"	2 1/2"	2 1/2"	2 1/2"	2 1/2"
3"	3"	3"	3"	3"
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4"	4"	4"	4"	4"
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5"	5"	5"	5"	5"
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7"	7"	7"	7"	7"
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LANDMATION SEQUENCE	1	2	3	4	OVERALL THICKNESS TO BE
(TOP)	SILK	REINFORCEMENT	AD PLANE	YOU PLANE	
(BUTT)				SOLDER	

VLSI		VLSI CIRCUITS INC. MONTREAL, Q.E., CANADA		FOR STR Solutions keeps reel BOUTHERVILLE, QC, CANADA	
TITLE:		9807070 RD		DATE	
REF:		1/1		JEP	
SCALE:		E.F.		JEP	
DRAWN#		SFH762		JEP	



SFM762.00 CPU BOARD

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SEAN CASEY
NEW PORTABLE

Note: IO are hardware defined ID of QIC
LA CASE AUDIOLOGIC ASSOCIATES INC. PULL-UP
CDS INPUTS

MASTER CPU BOARD

DESIGNED BY: J. L. BROWN

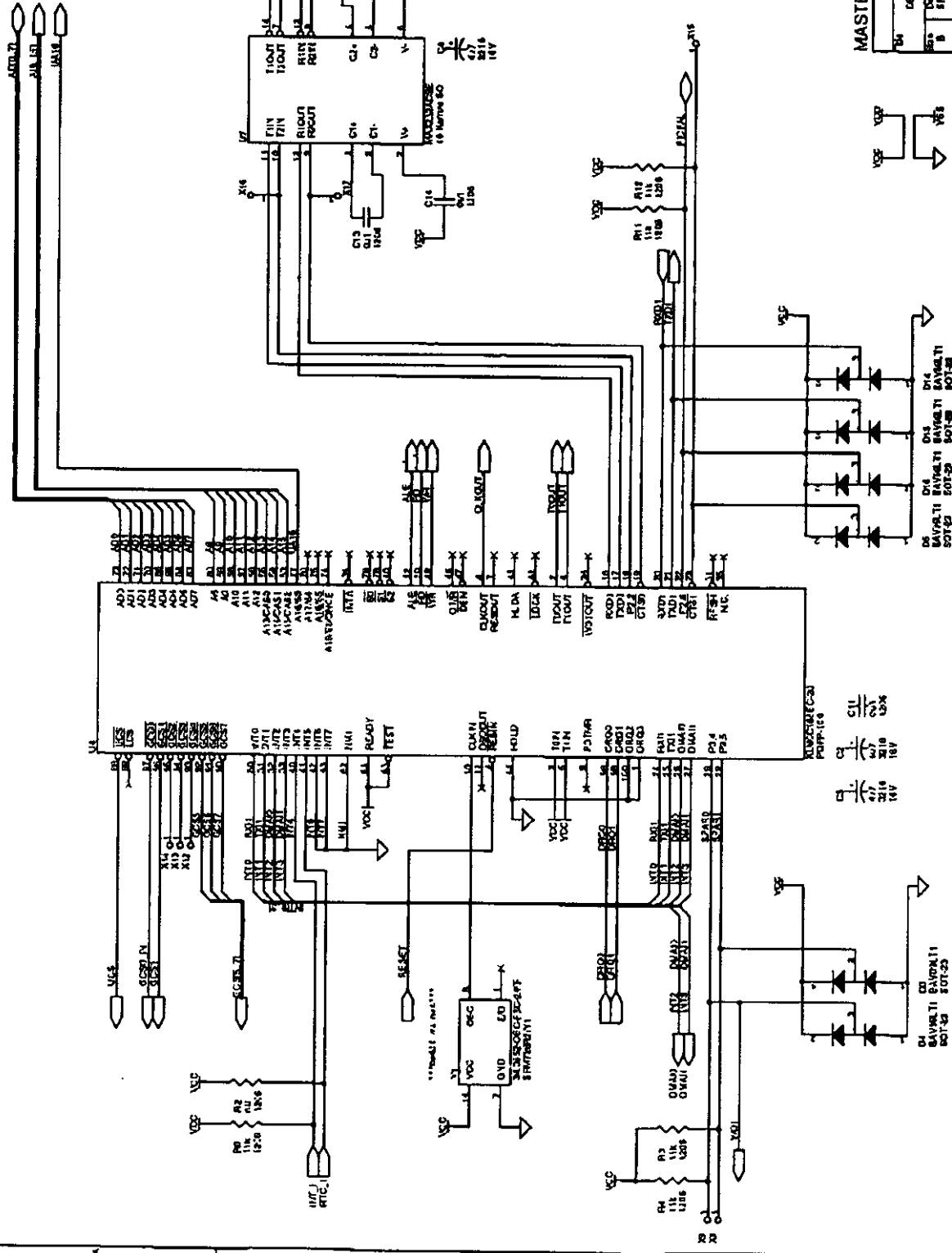
DATE: 10/15/77

REV: 1.0

10/15/77 J. L. BROWN

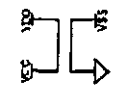
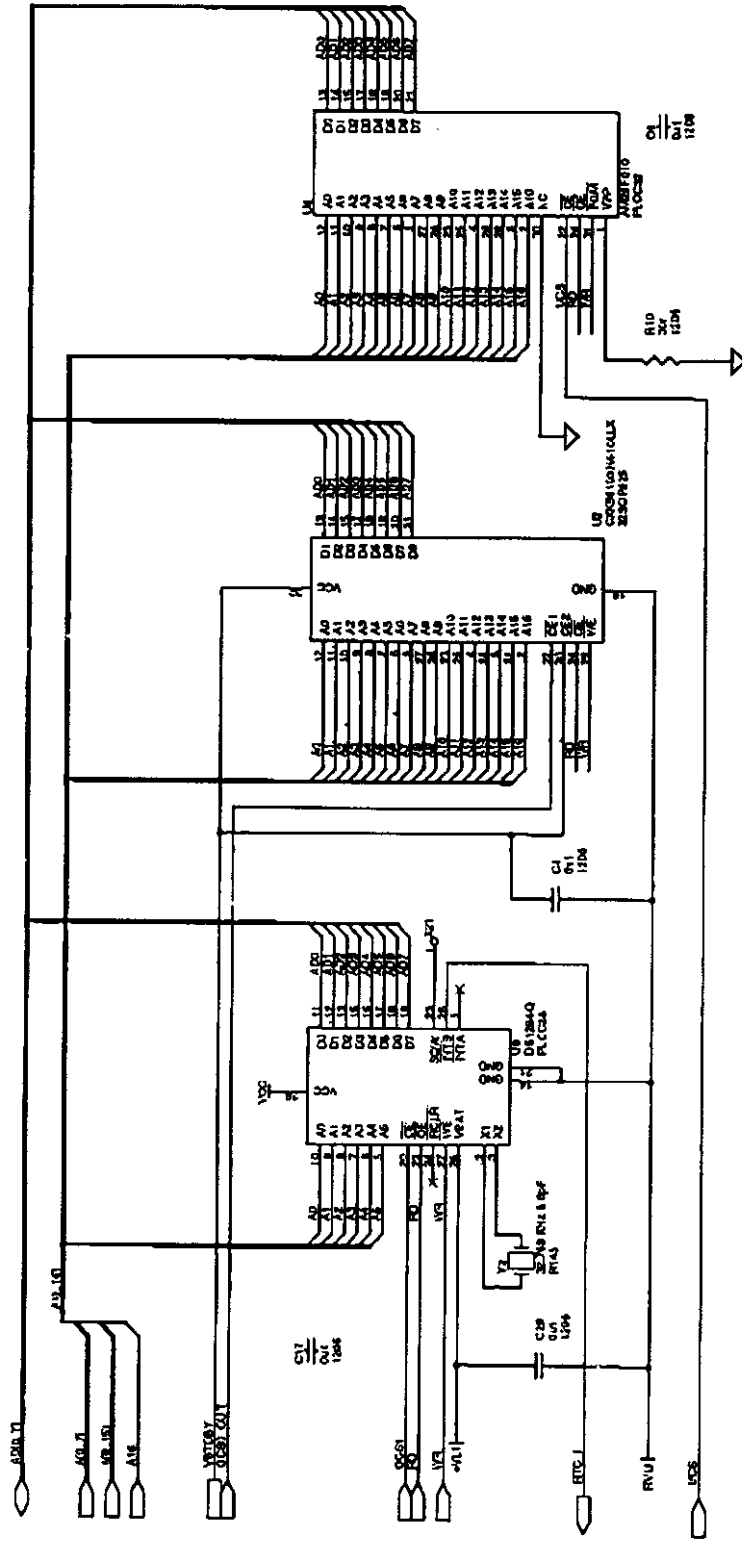
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 2. 10K
 3. 1K
 4. 100Ω
 5. 10Ω
 6. 1Ω
 7. 100M
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 10. 100K
 11. 10K
 12. 1K
 13. 100Ω
 14. 10Ω
 15. 1Ω

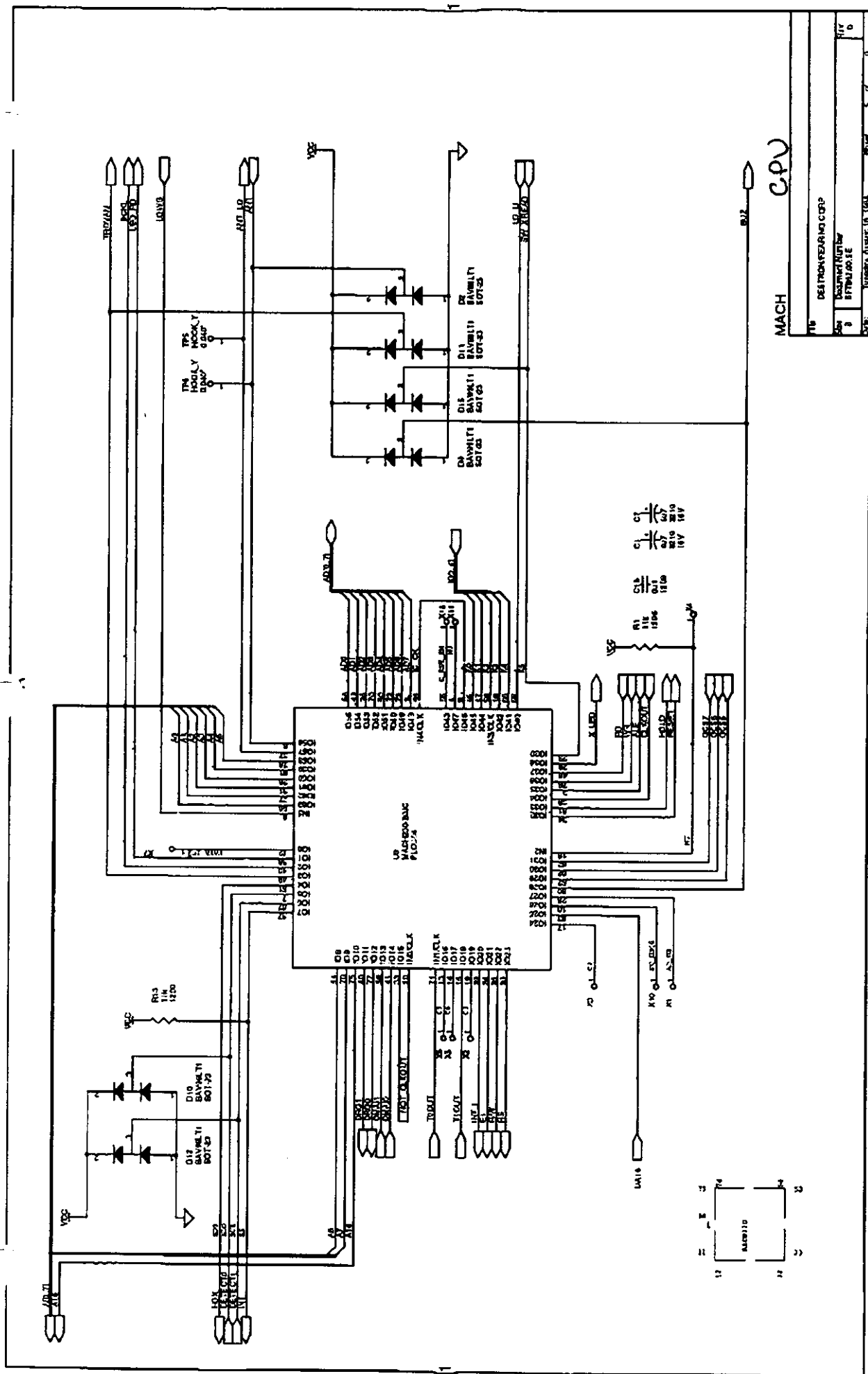
Notes:
 1. Time delay circuit per 10 via X
 2. Time delay circuit per 10 via X
 3. Time delay circuit per 10 via X



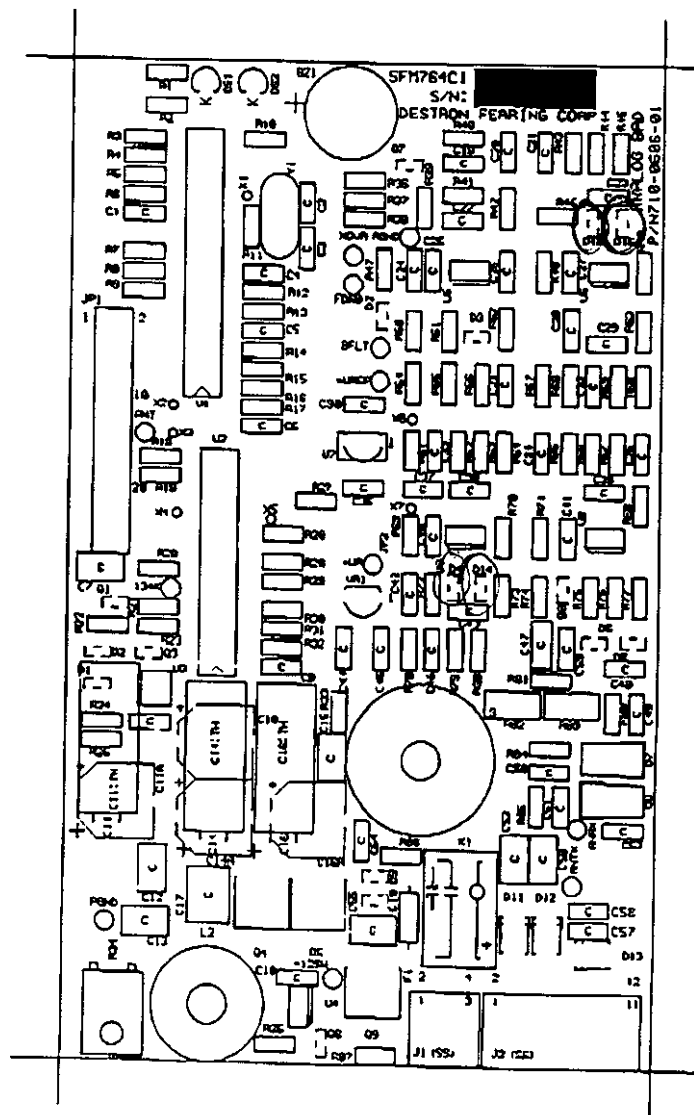
MEMORY & RTC & DAC CPU

11B	DESTRUCTION CODE
11B	DOCUMENT NUMBER
11B	SPRING DATE
11B	DATE





A



REVISIONS:

REV. 1 : LINES ON R40, R52: ADD D14, D15, D16; CORRECT D11, D12;
ADD FIDUCIALS; CHANGE CAPACITORS CASE SIZE.

DESTRON FEARING

DRAWING:
MSJ

CIRCUIT :

VERIFICATION:
PT

ARTWORK SILKSCREEN
FS2001F ANALOG BOARD

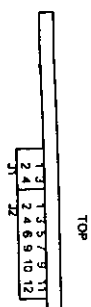
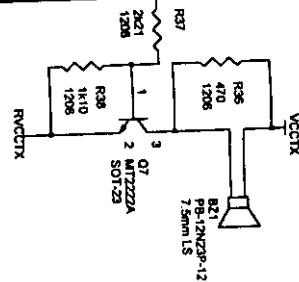
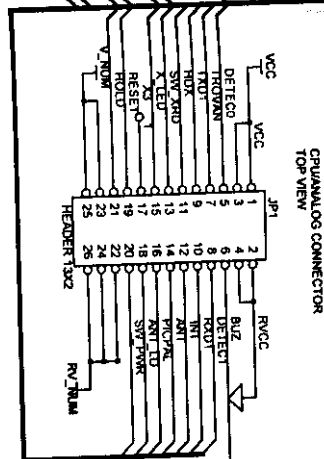
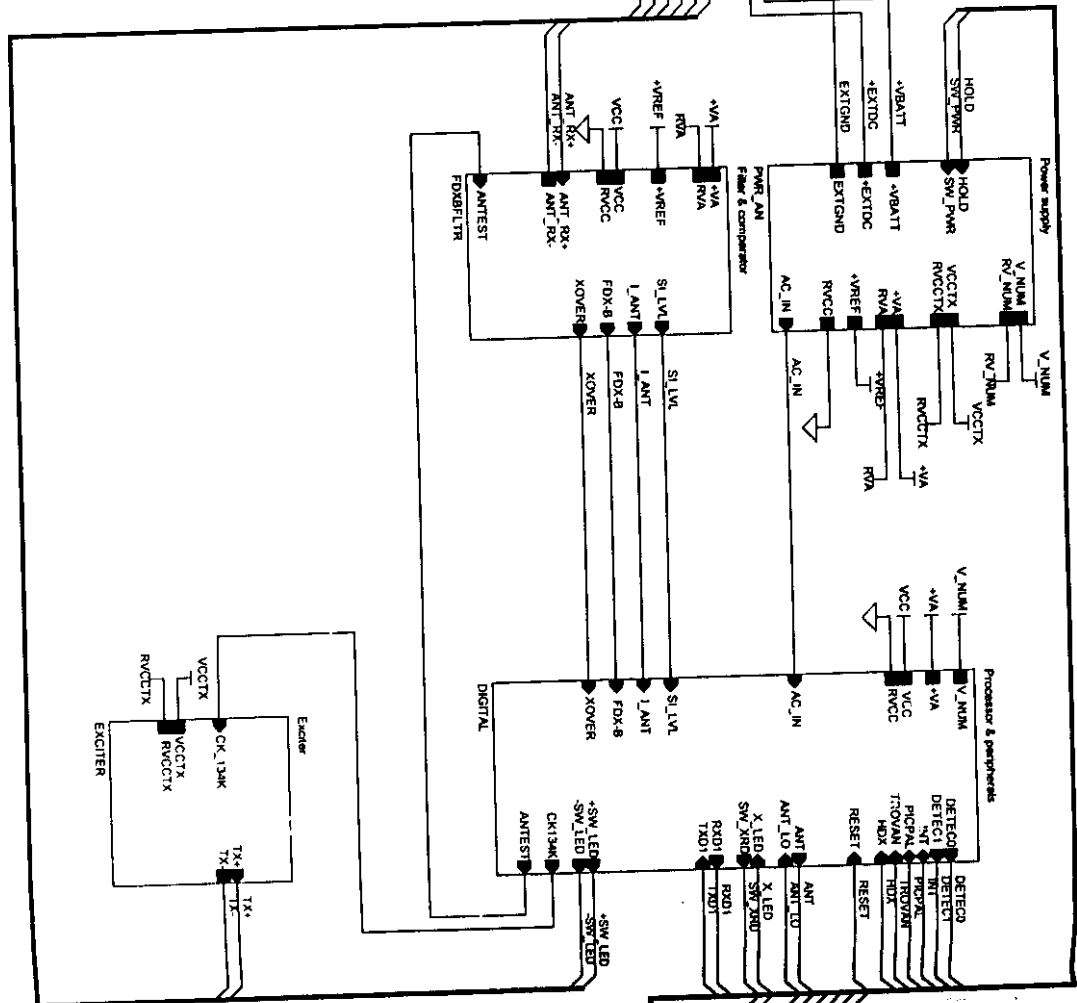
APPROVAL:

DESIGN :

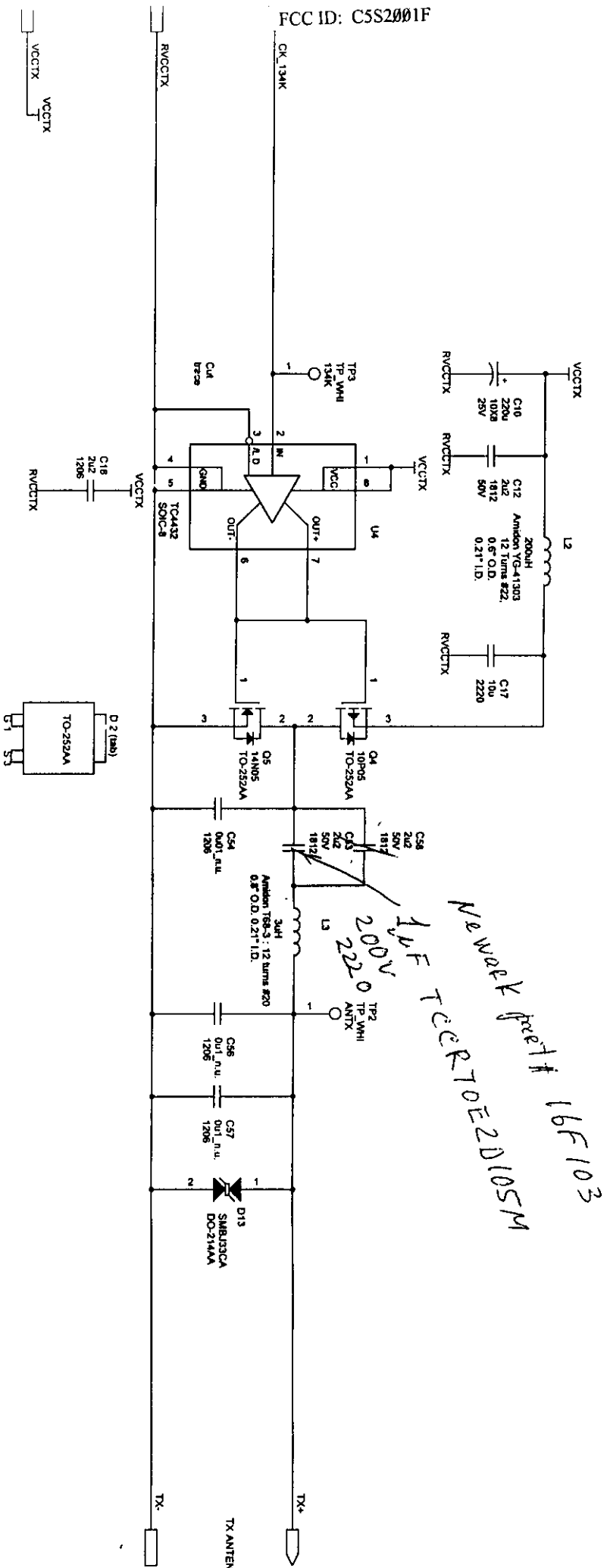
DATE :
98/10/26

STR, REAL TIME SOLUTIONS INC.

DRAWING NUMBER:
SFH764C1.1

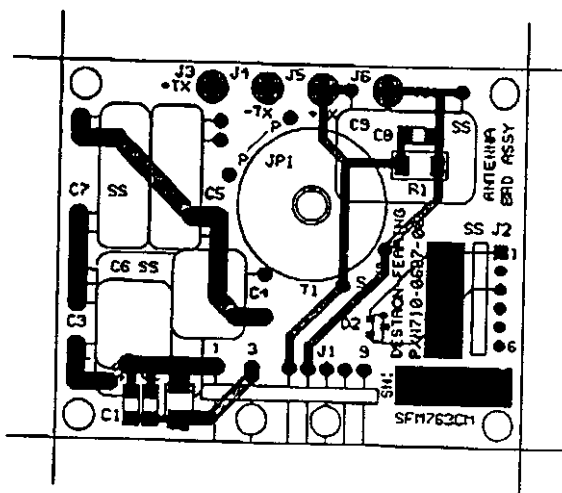


DESTRON-PEARING CORP			
116	DOCUMENT NUMBER	SFM764.01 ANALOG BOARD OVERVIEW	Rev 1
116	DATE	WORKING, OBSOLETE 26, 1998	5



DESTRON-PEARING CO	
EXCITER	
Size	Document Number
8	SFT962.01 SE
Date	Monday, October 26, 1998
	Sheet 3

FCC ID: C5S2001F



REVISIONS:

DESTRON-FEARING

DRAWING:
MSJ

CIRCUIT :

VERIFICATION;
PT

ARTWORK SOURCEBOOKSIDE
FS2001F ANTENNA BOARD

APPROVAL:

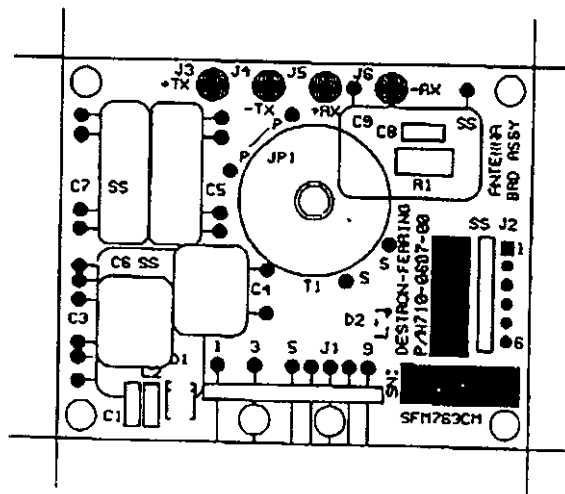
DESIGN:

DATE : 96/07/08

STR, SOLUTIONS TEMPS REEL

DRAWING NUMBER:
SFM763C1.0

FCC ID: C5S2001F



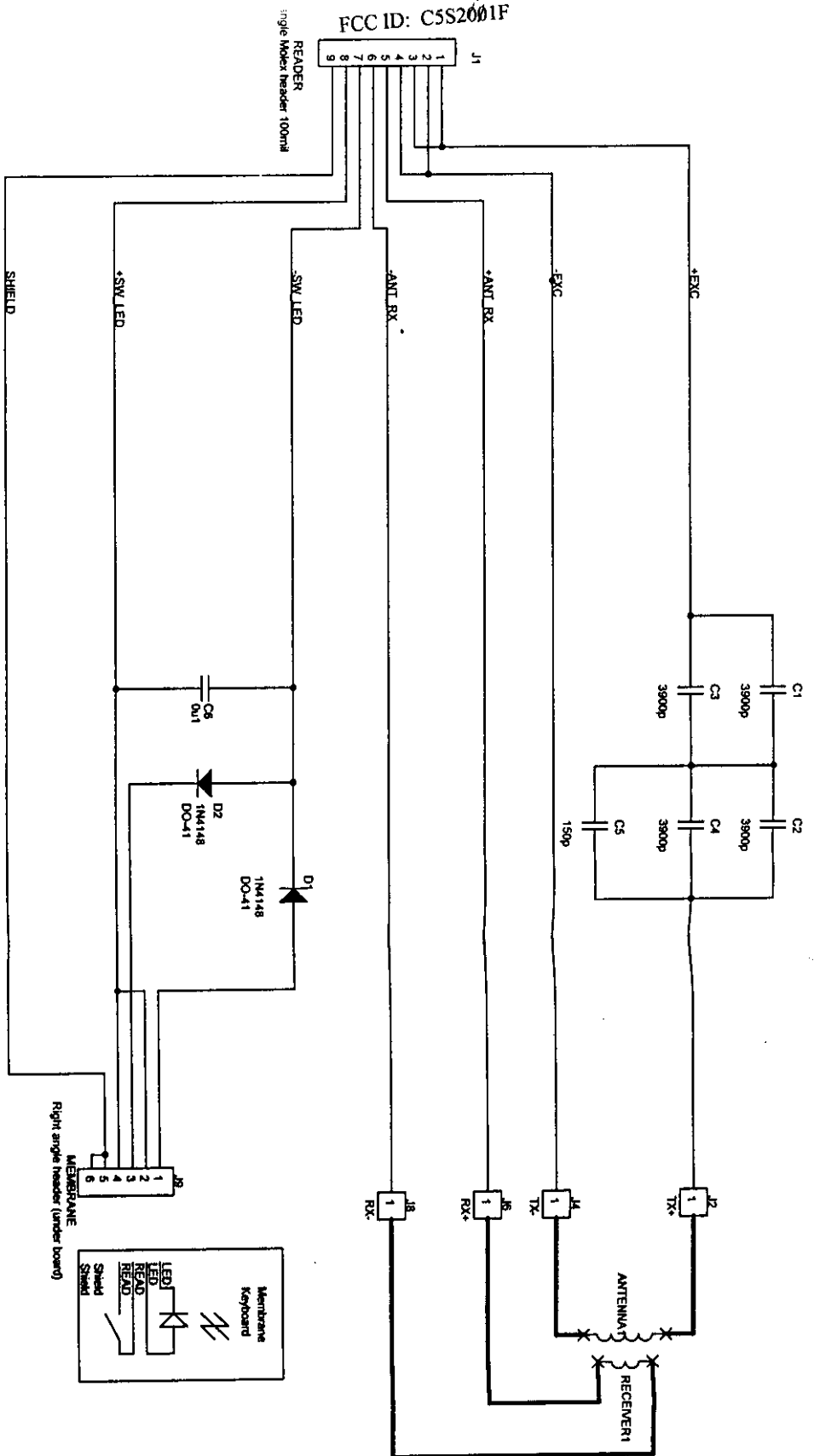
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