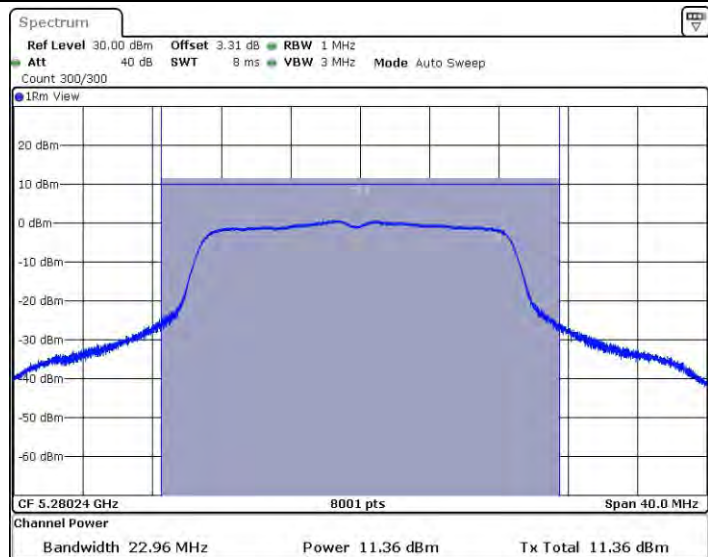
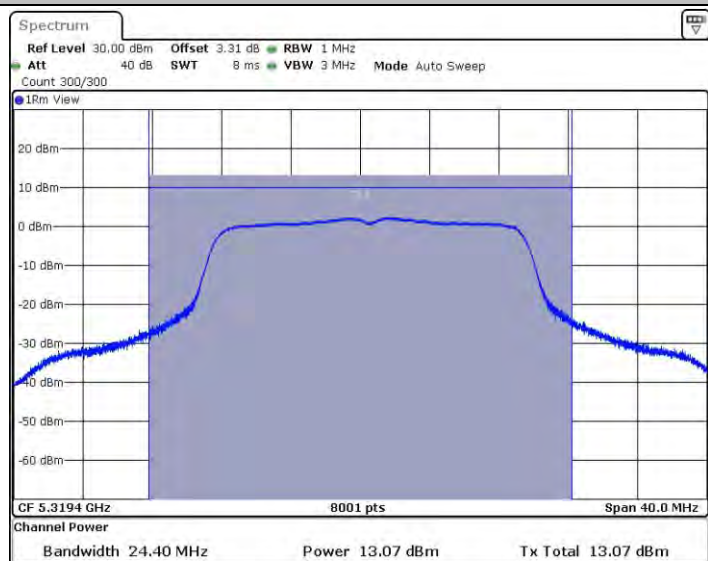
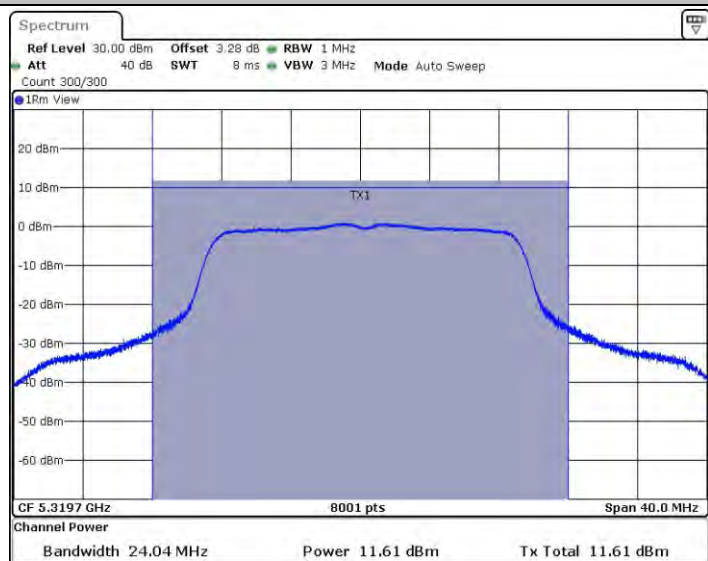




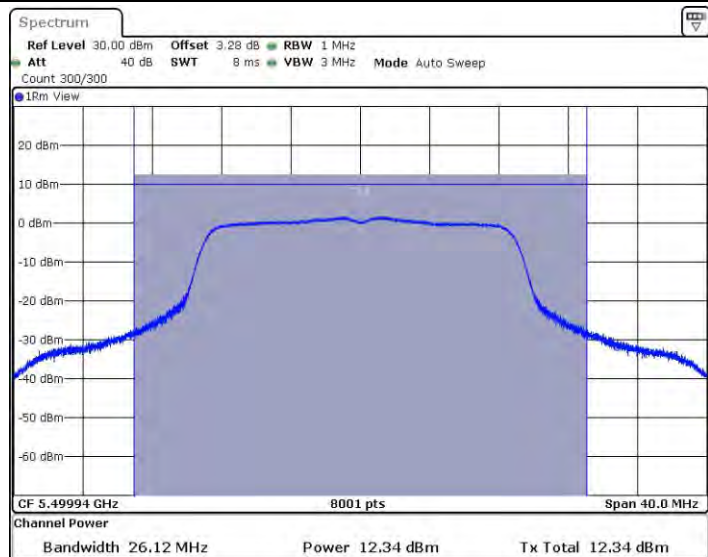
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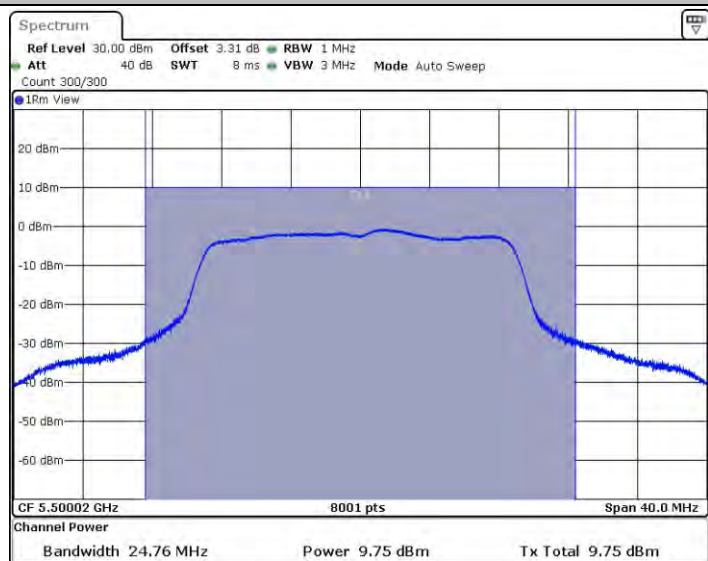
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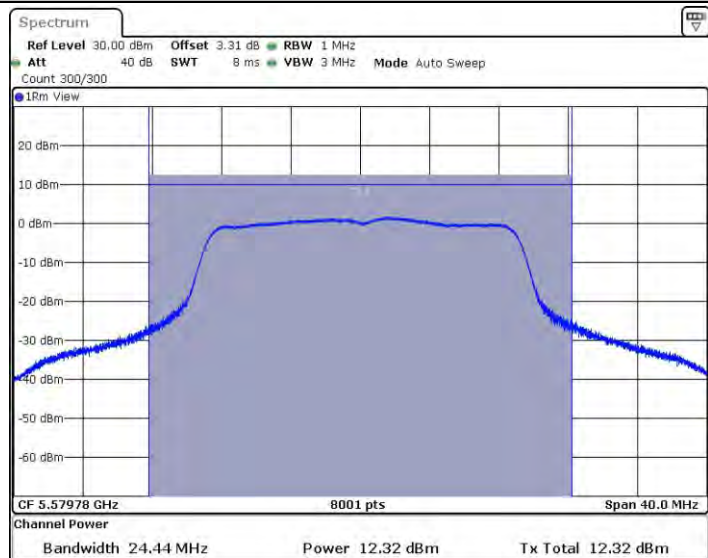
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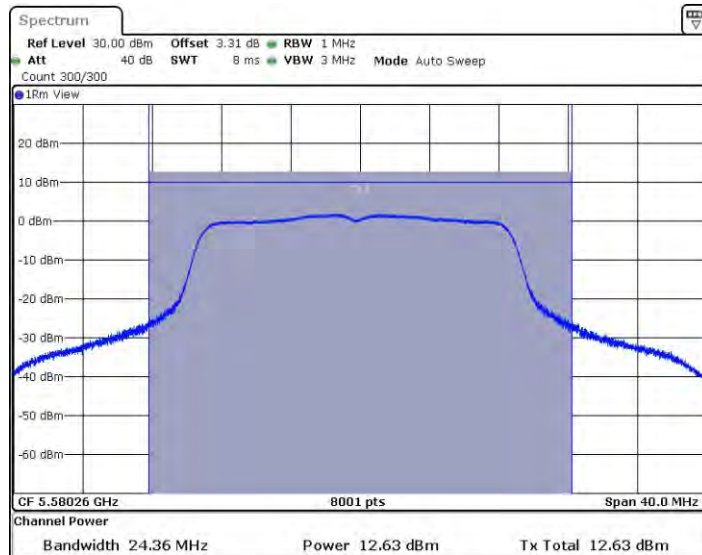
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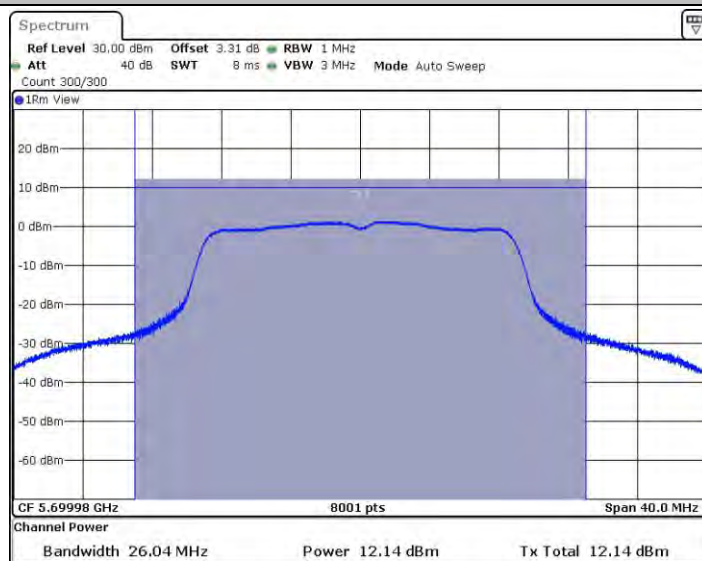
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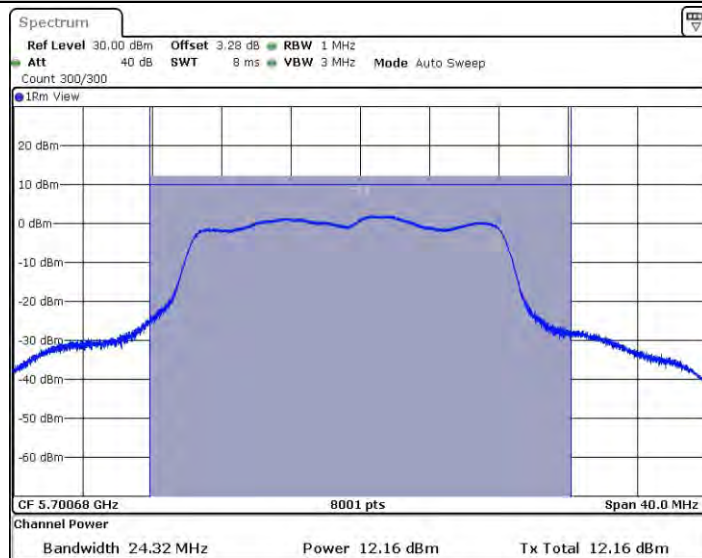
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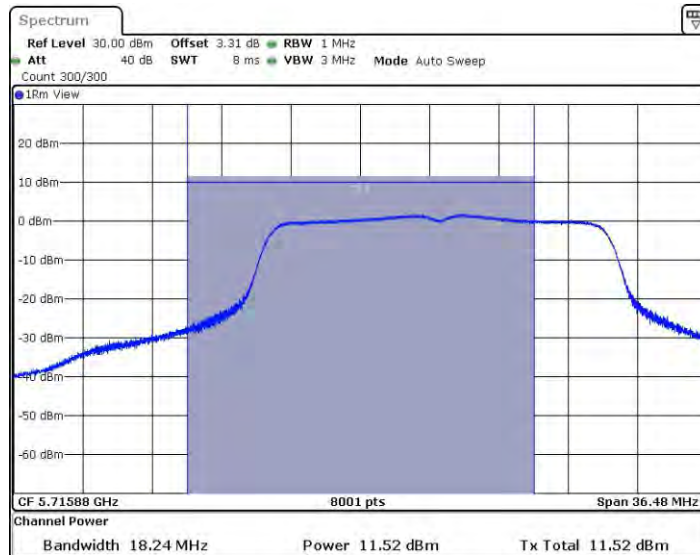
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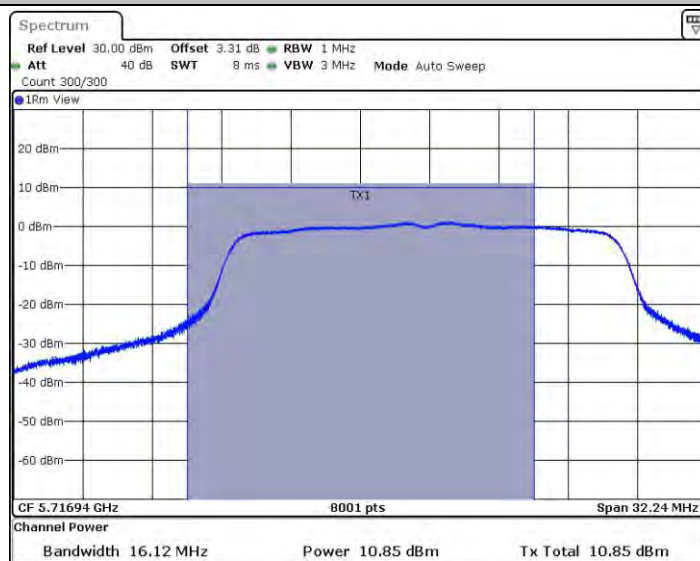
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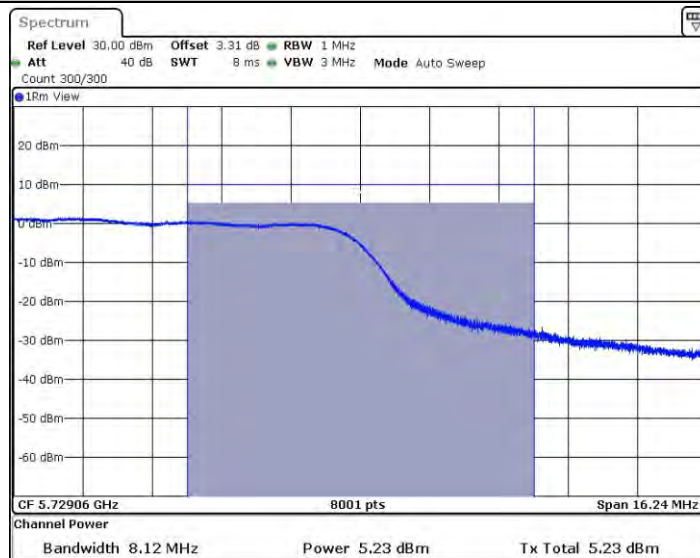
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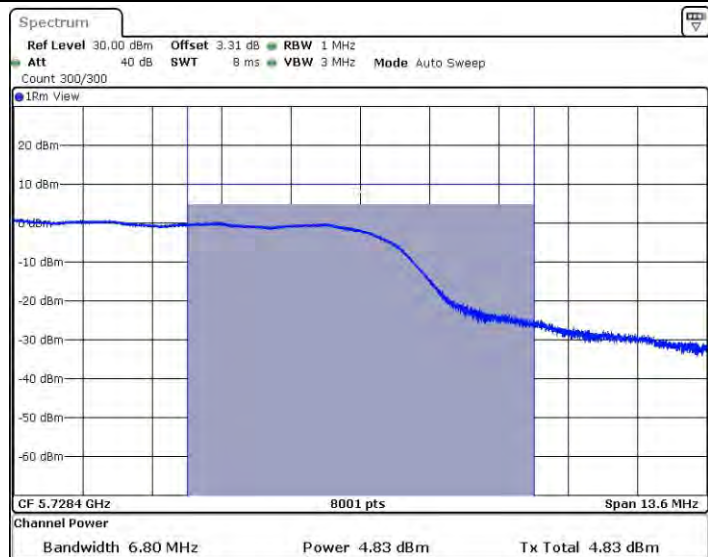
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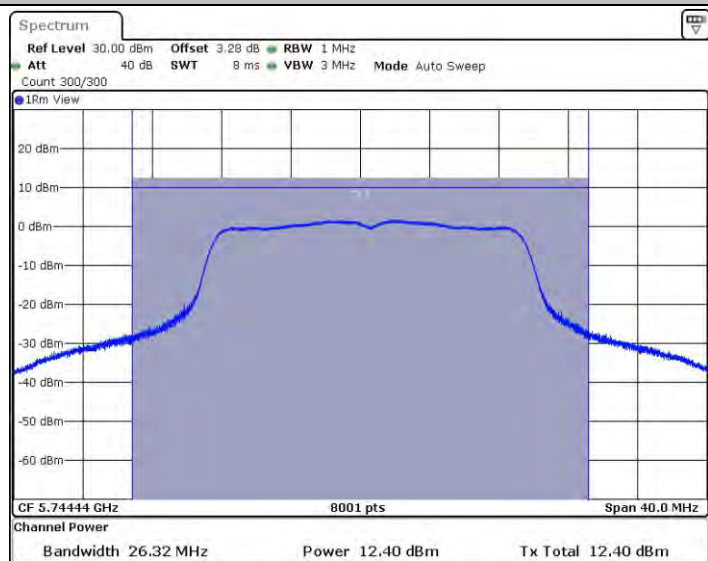
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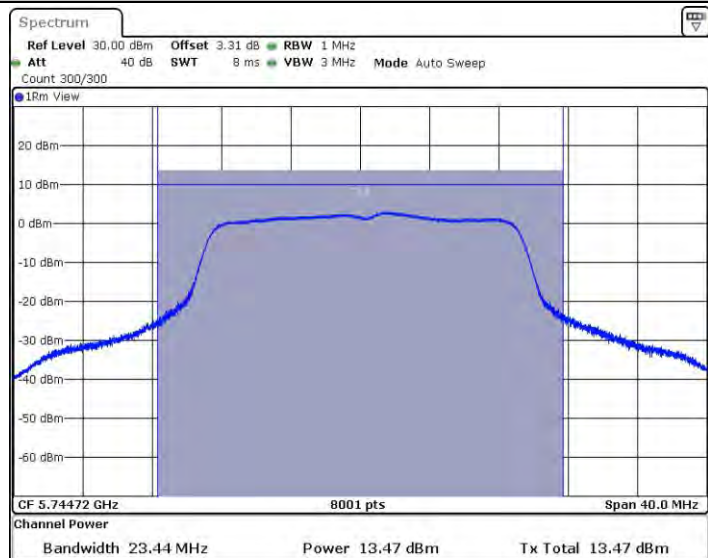
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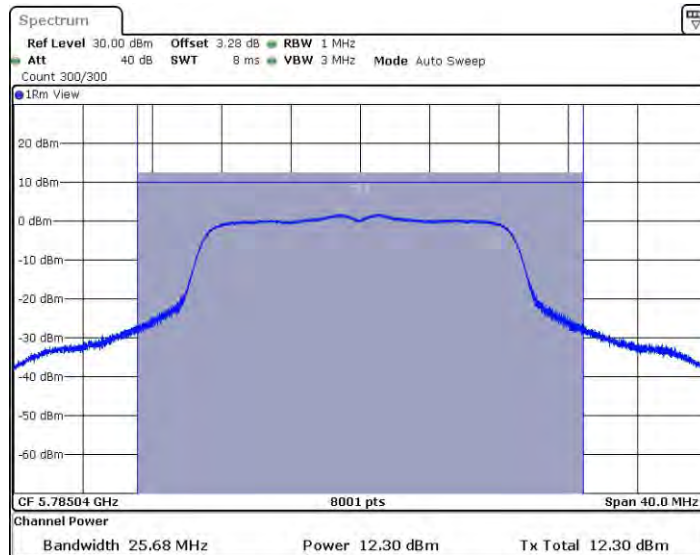
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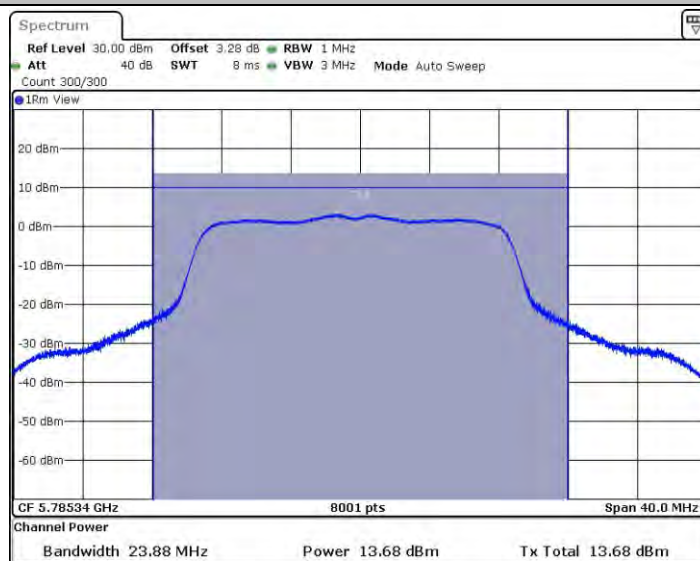
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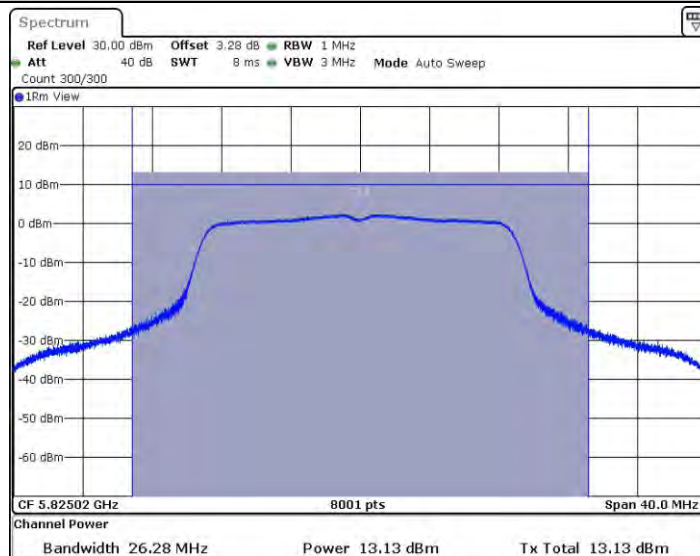
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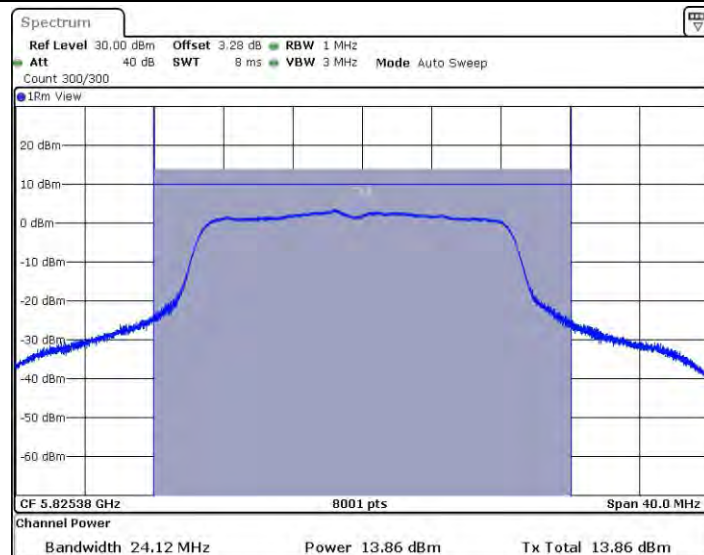
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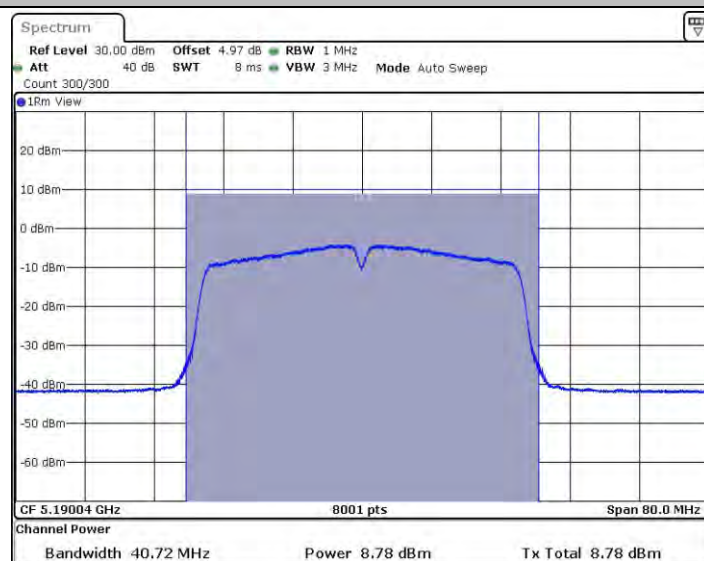
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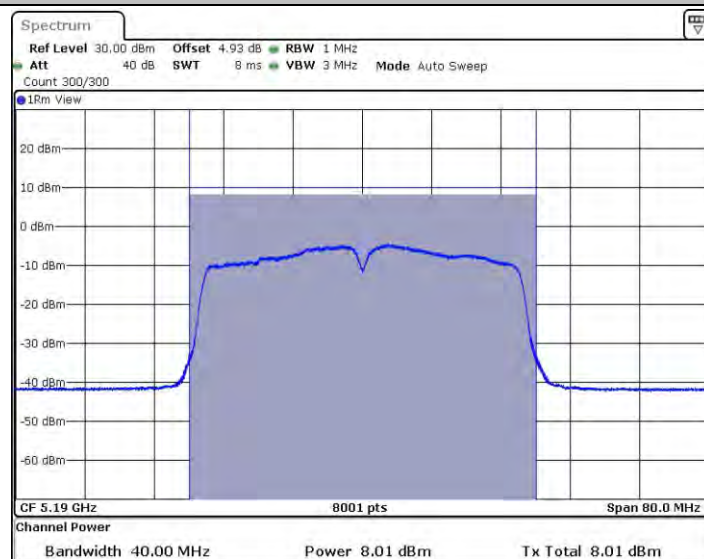
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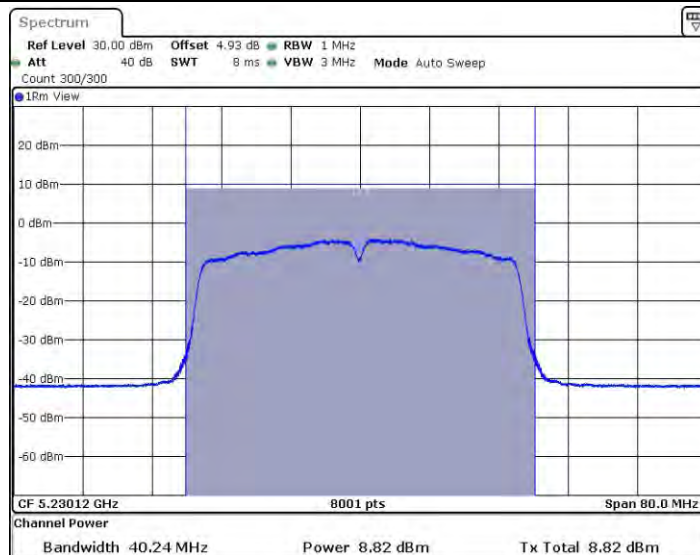
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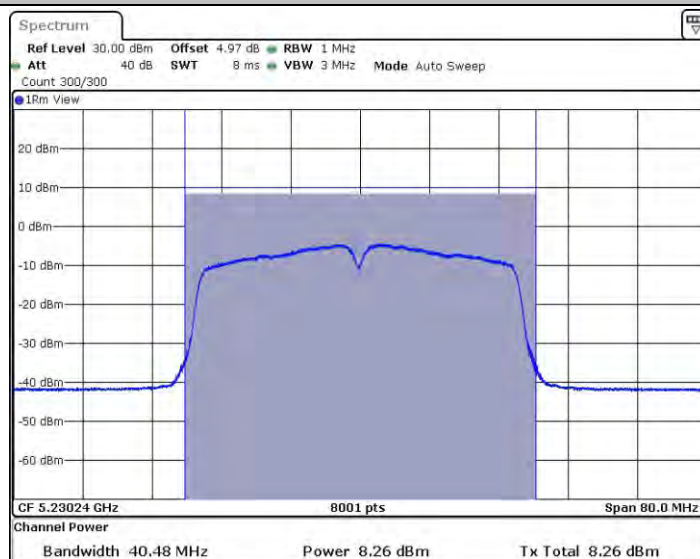
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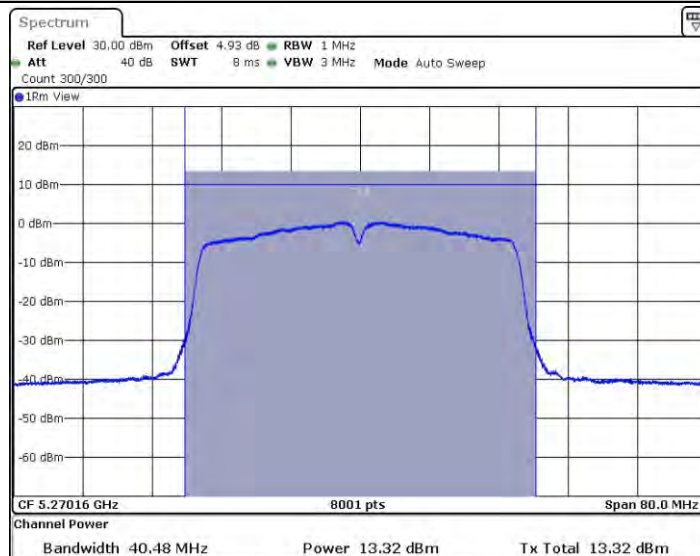
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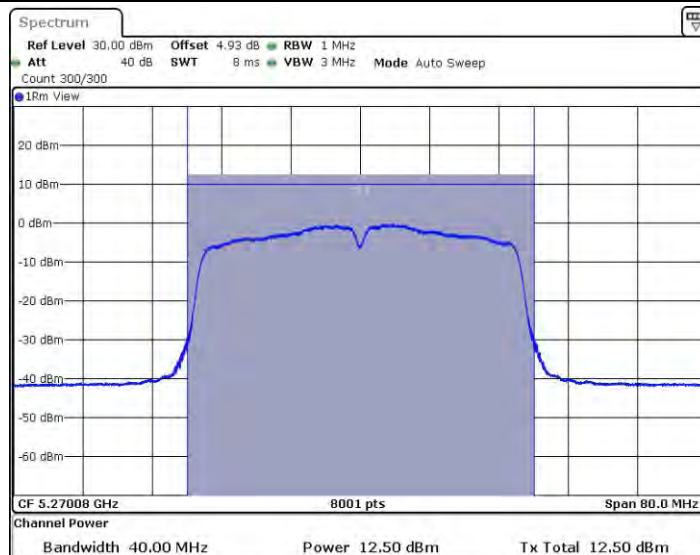
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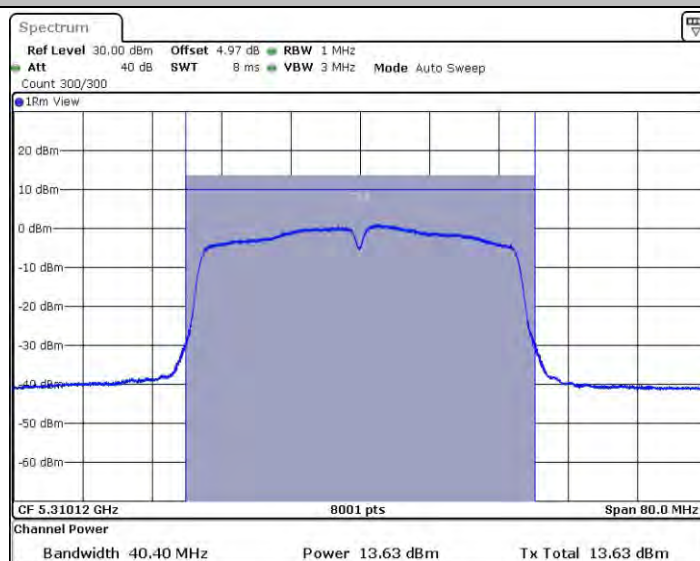
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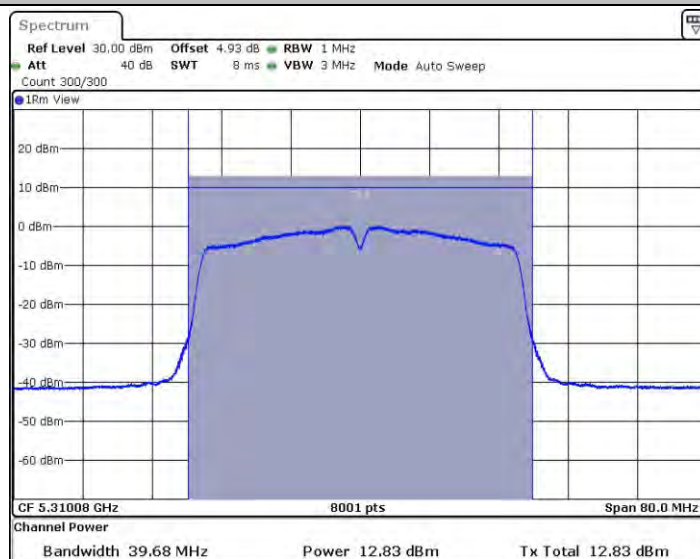
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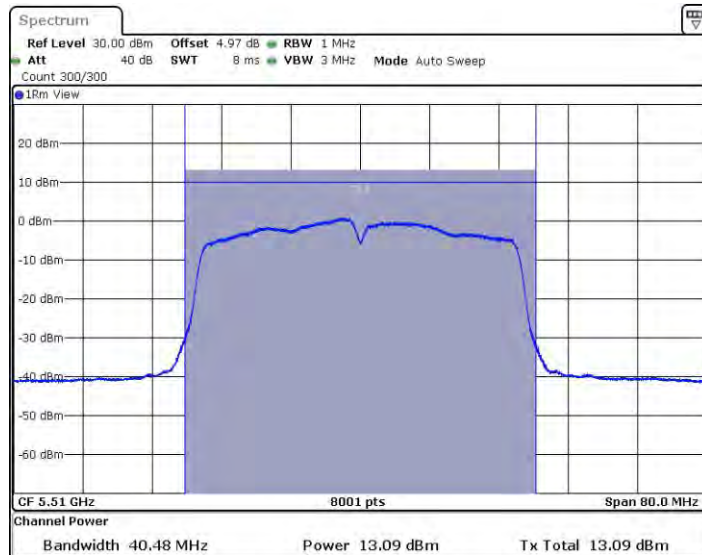
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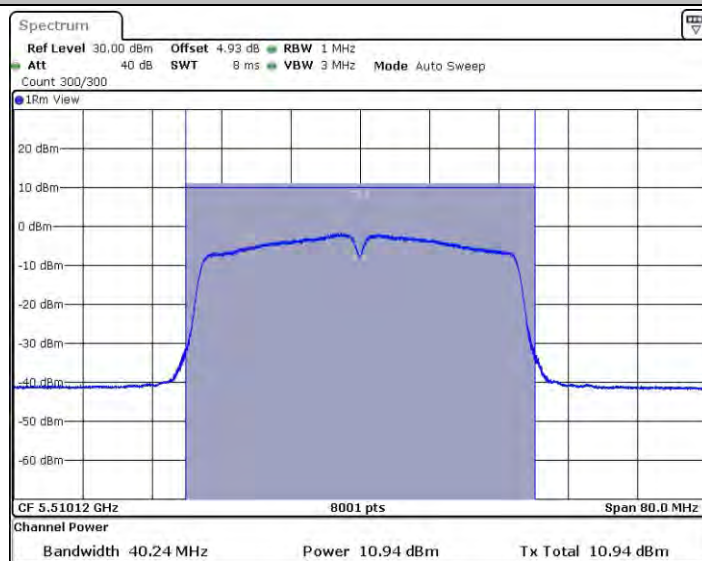
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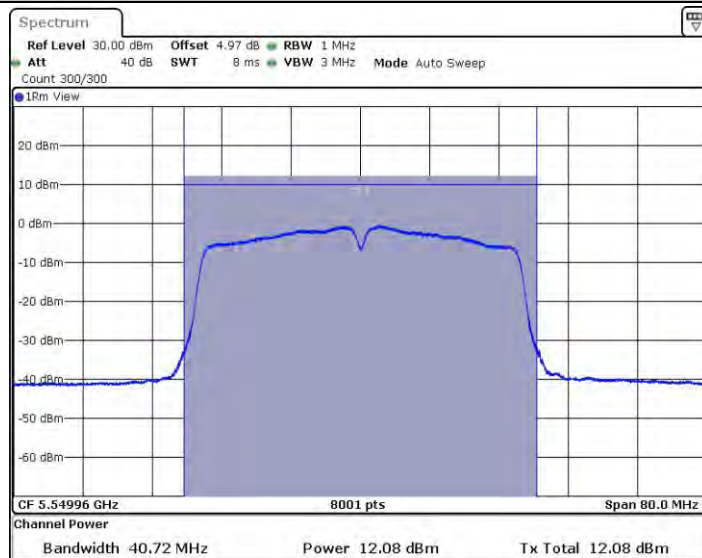
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11AC40SISO_Ant1_5510



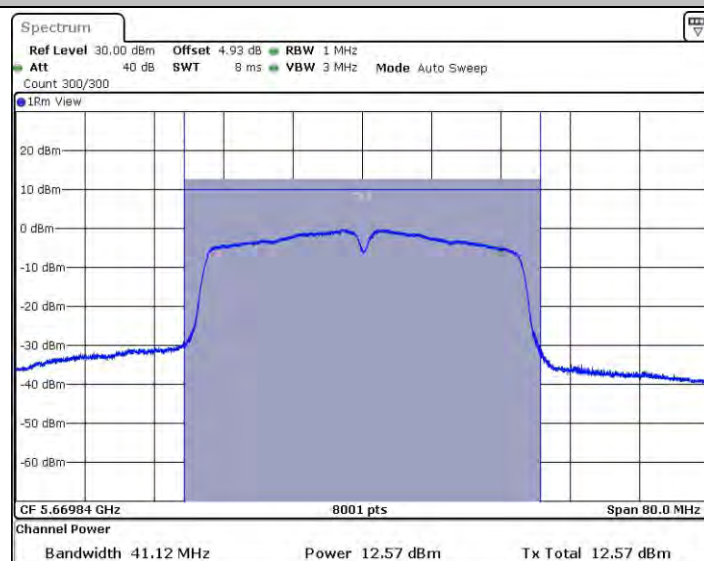
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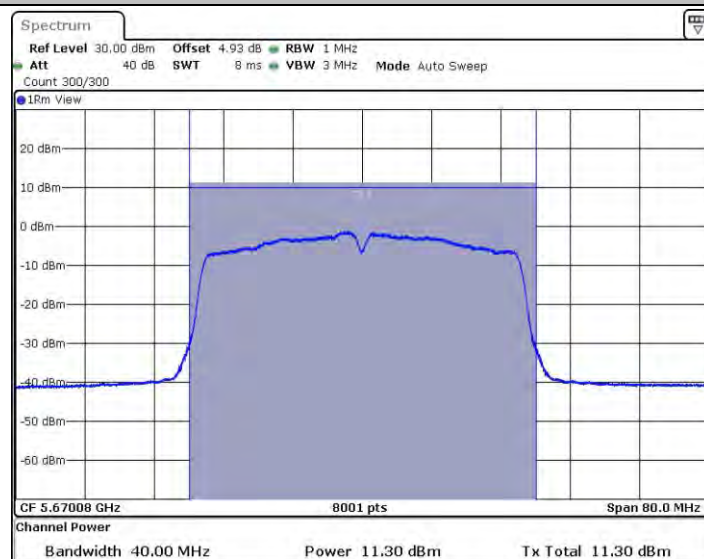
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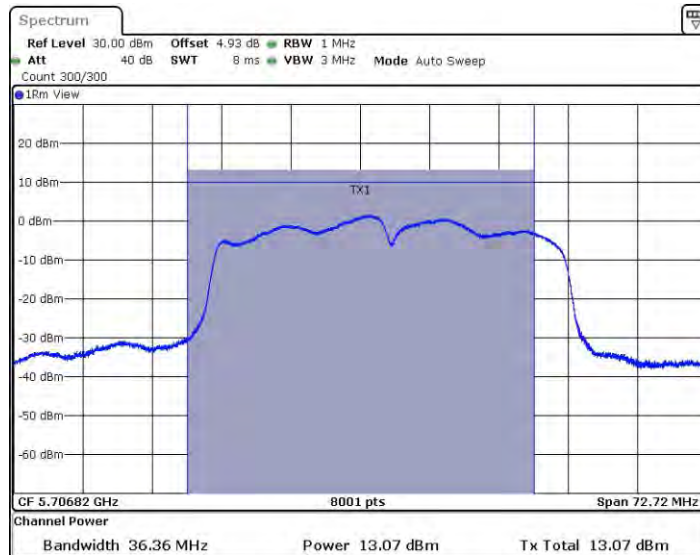
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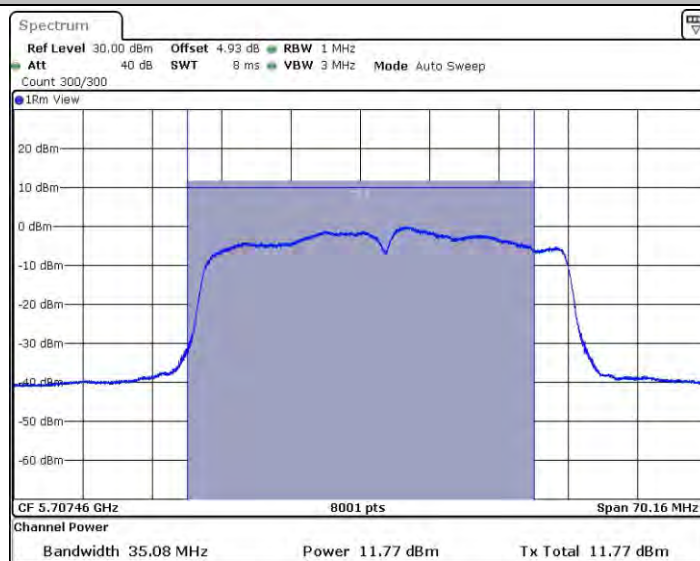
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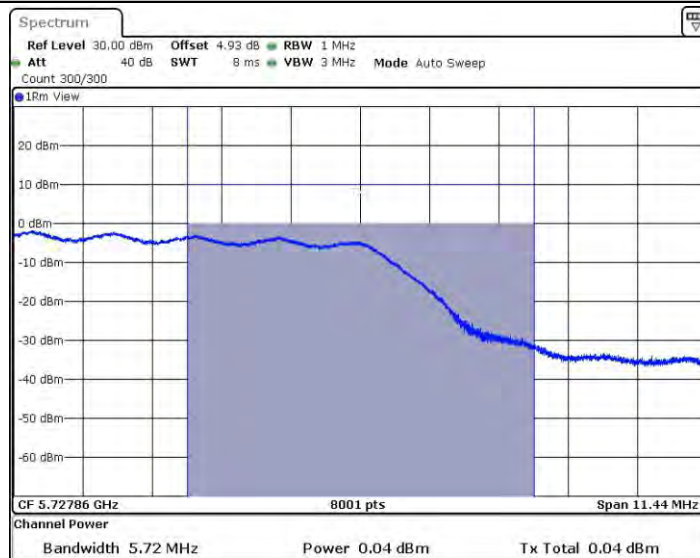
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11AC40SISO_Ant1_5710_UNII-2C



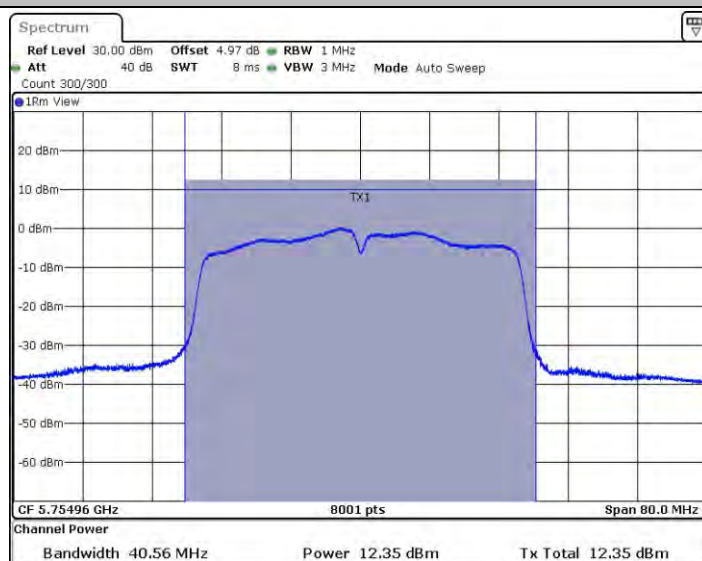
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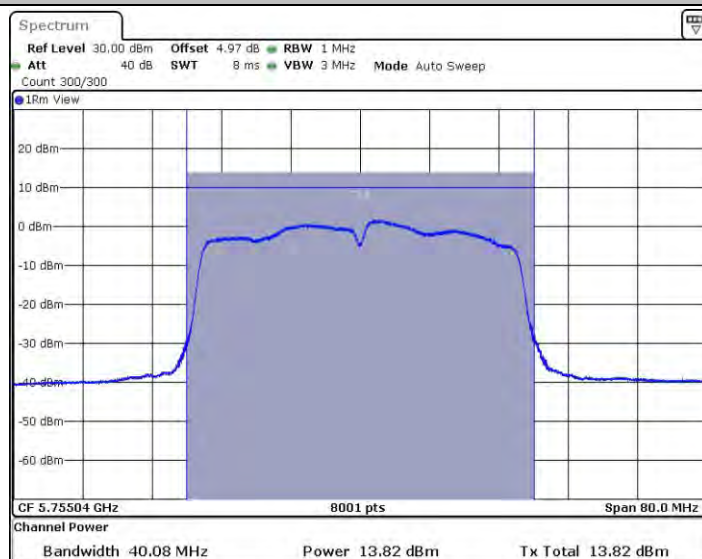
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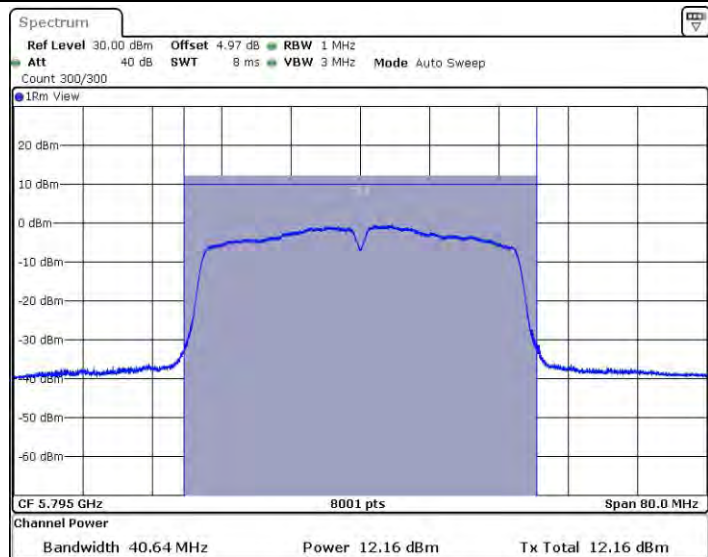
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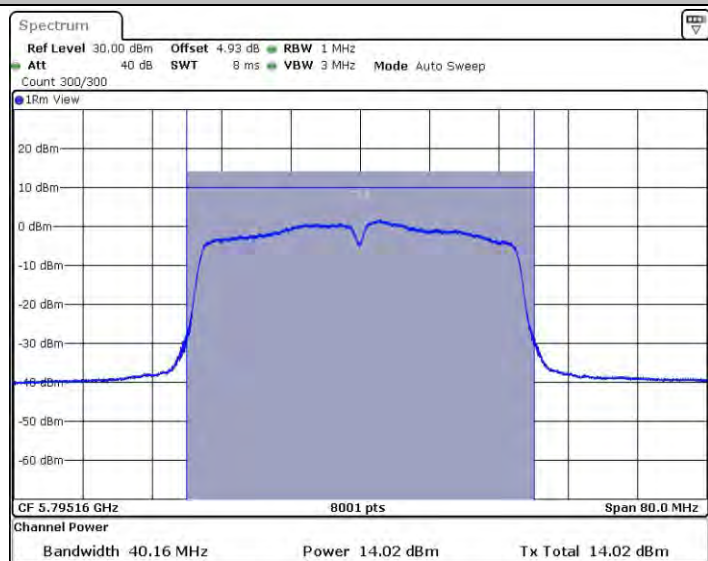
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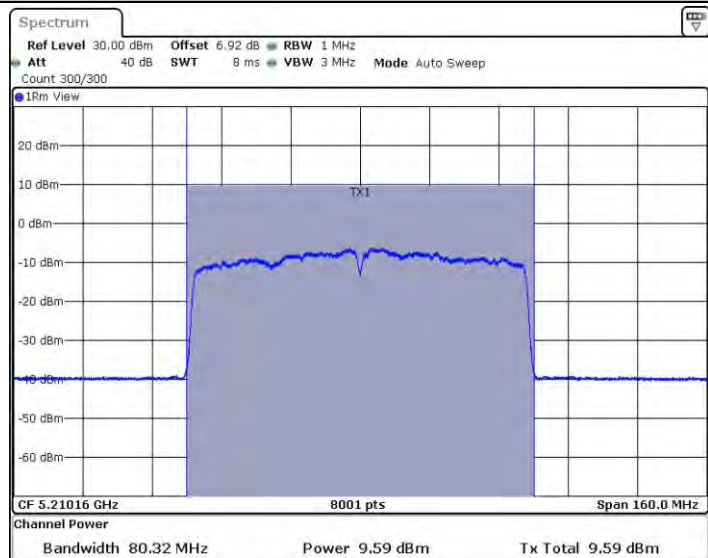
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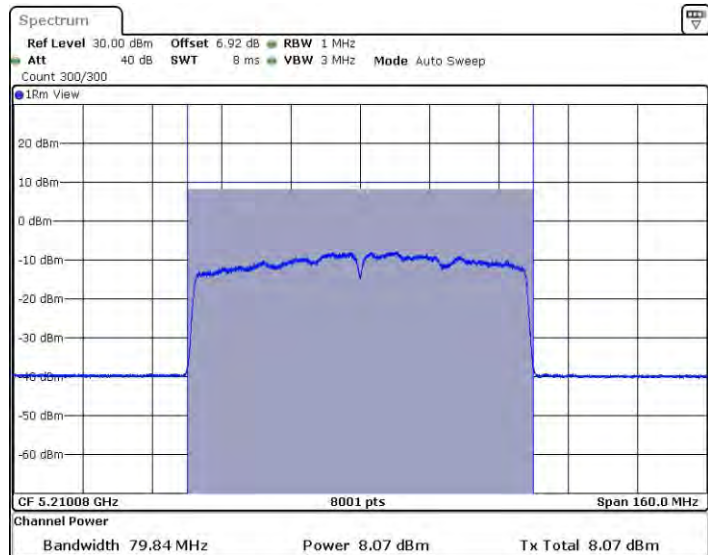
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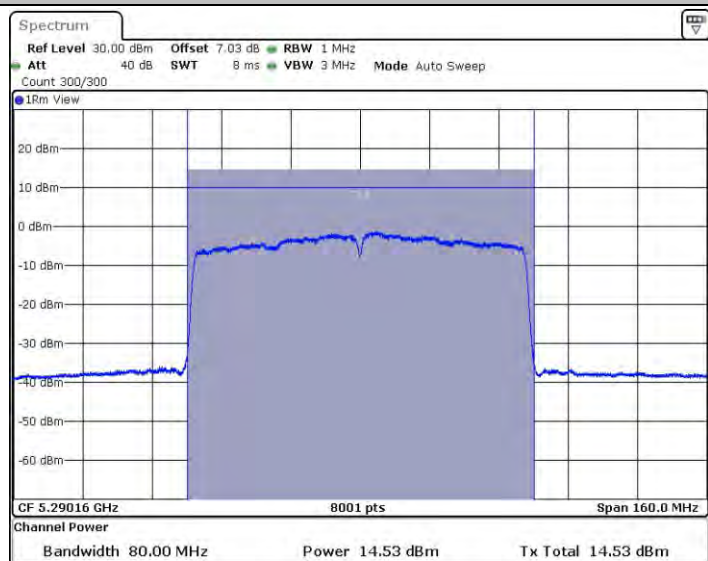
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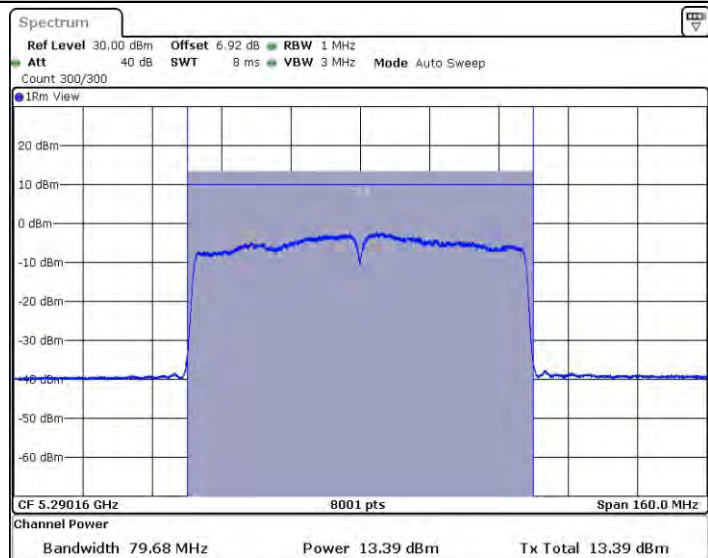
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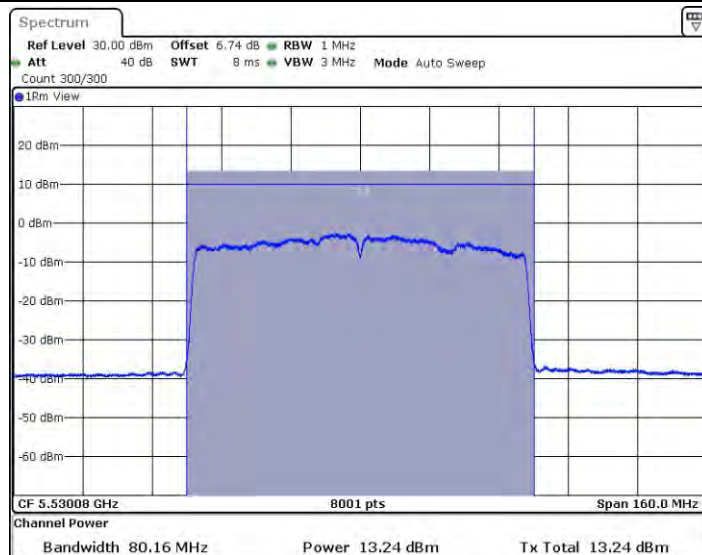
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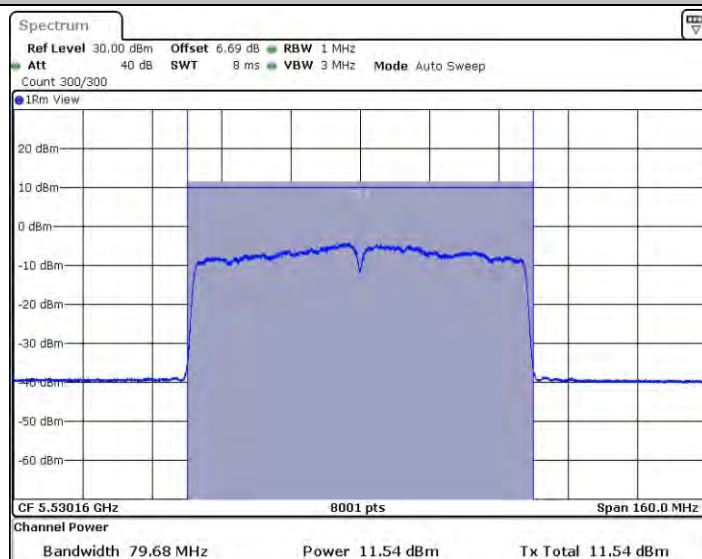
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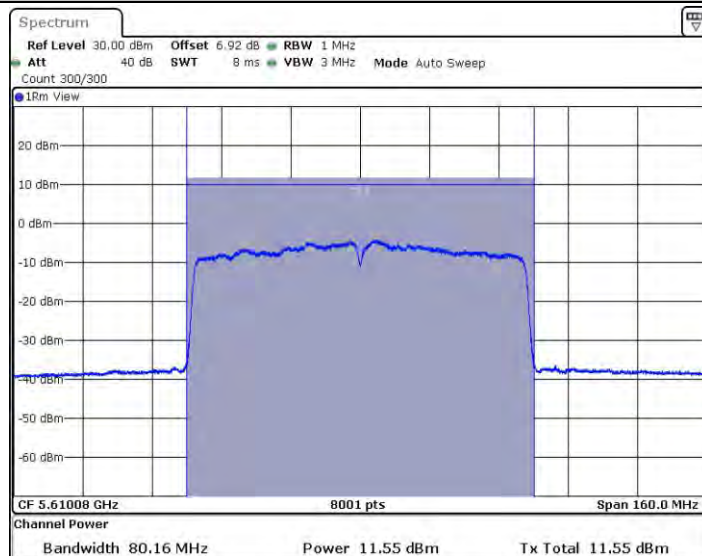
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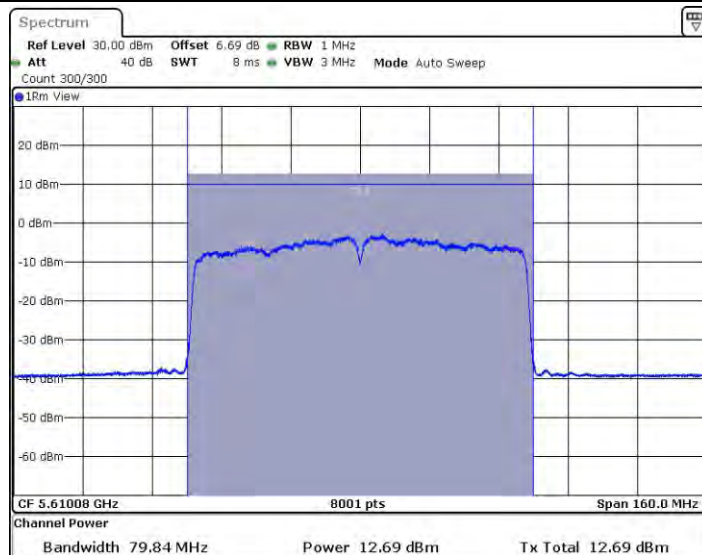
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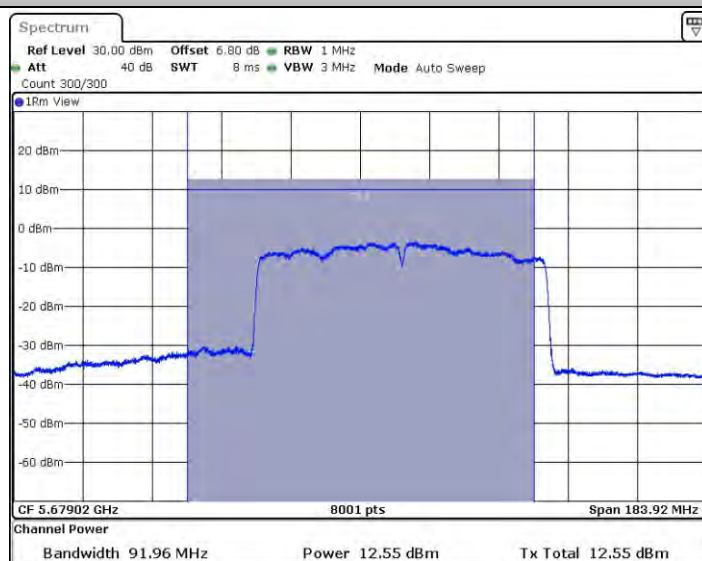
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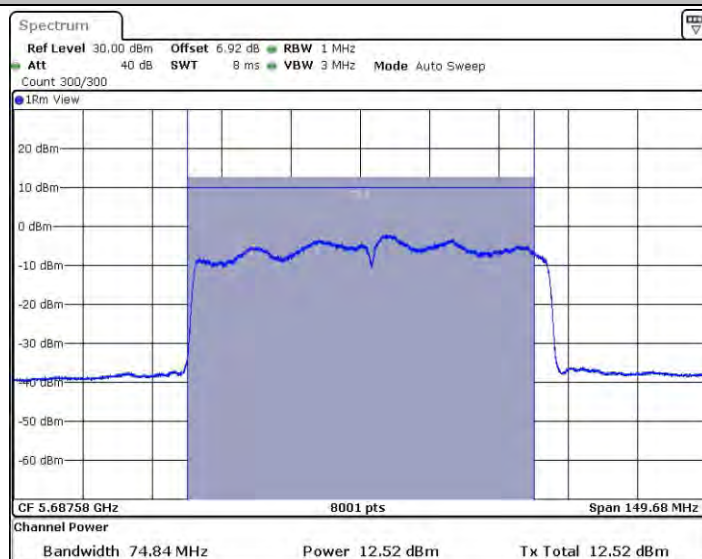
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11AC80SISO_Ant2_5610



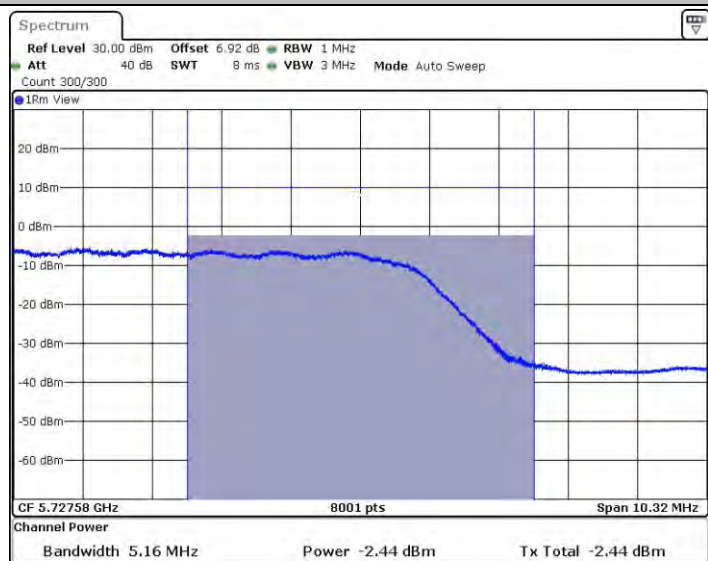
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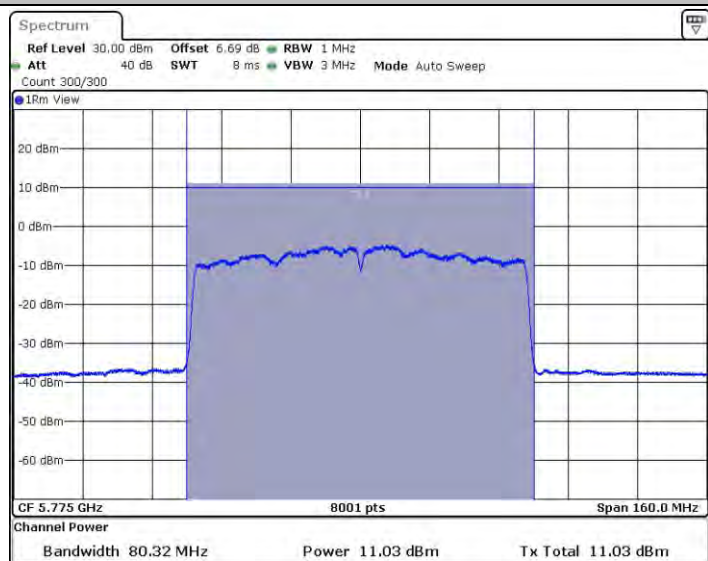
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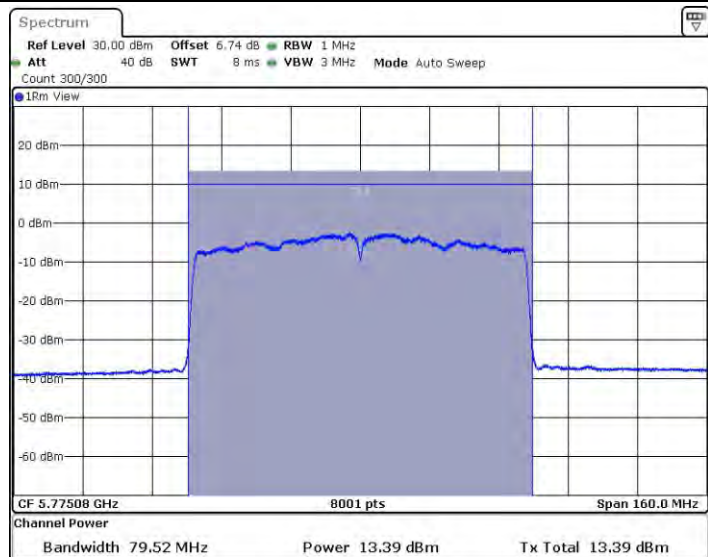
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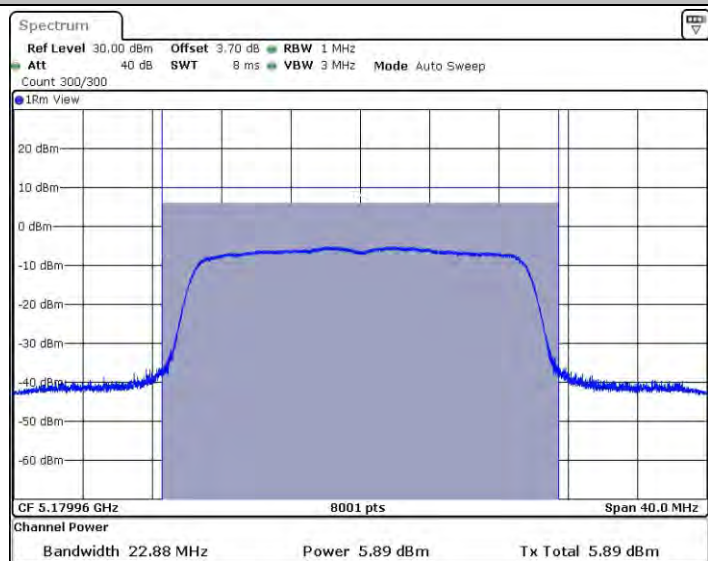
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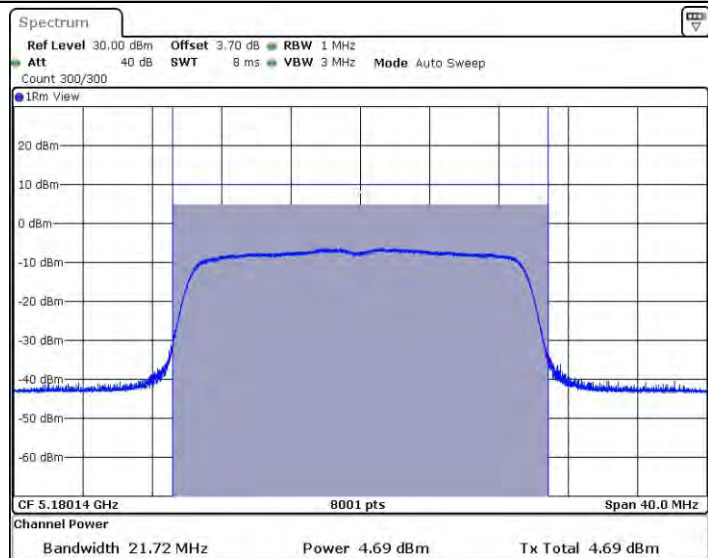
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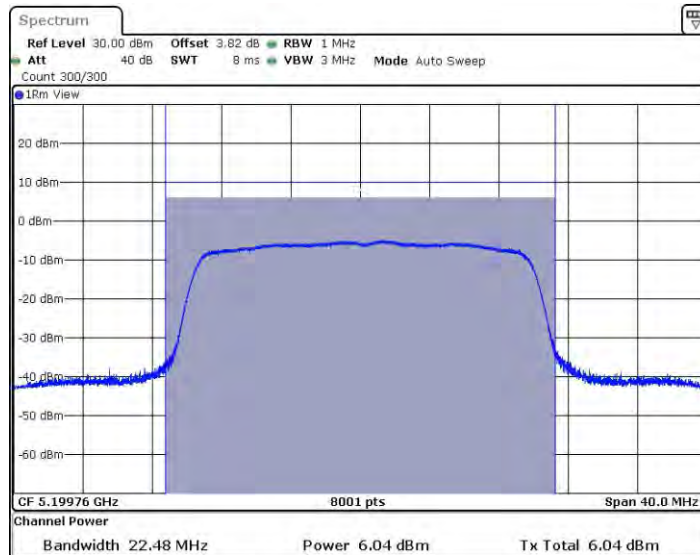
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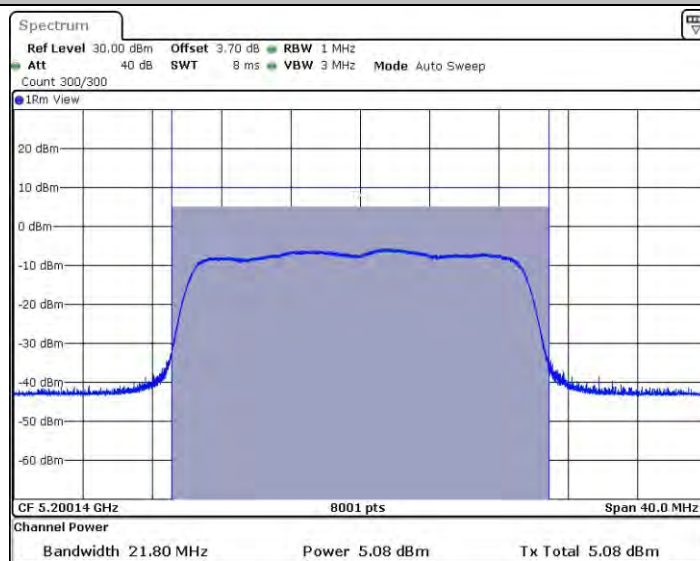
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11AX20SISO_Ant2_5180



11AX20SISO_Ant1_5200



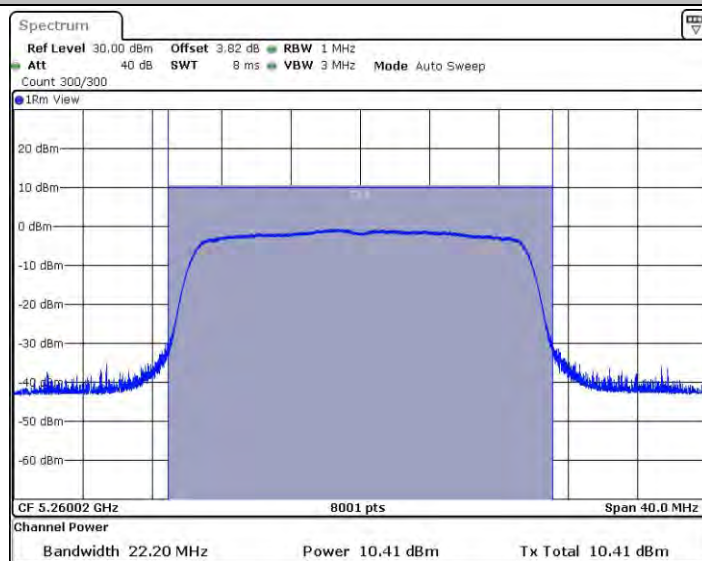
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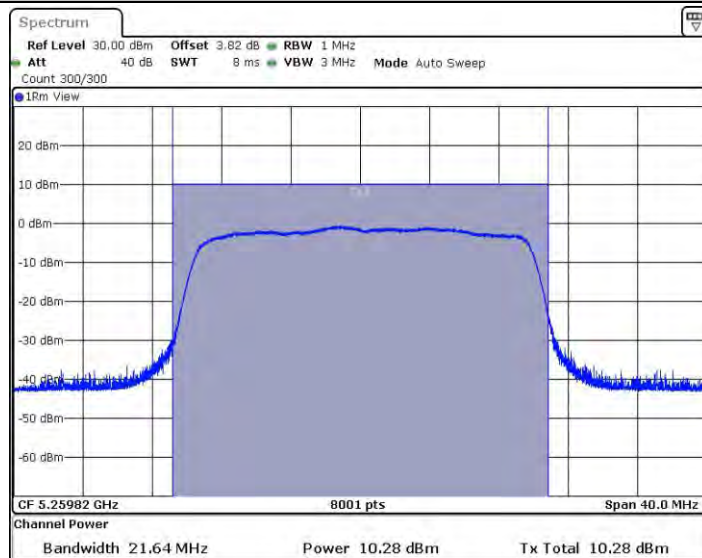
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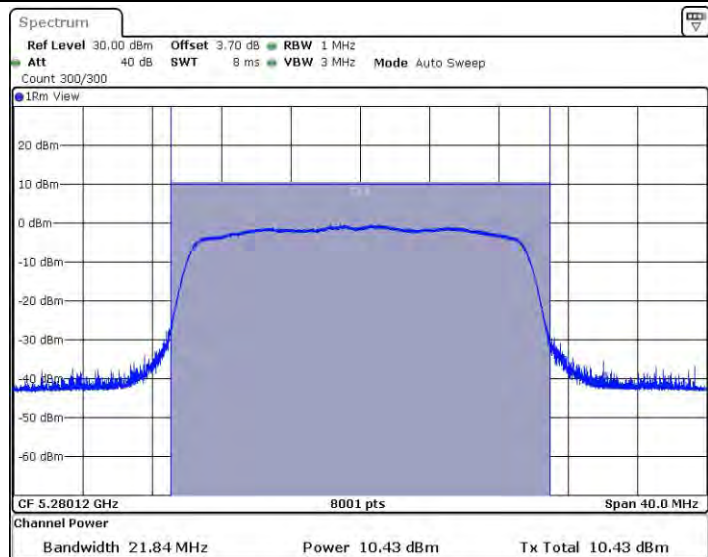
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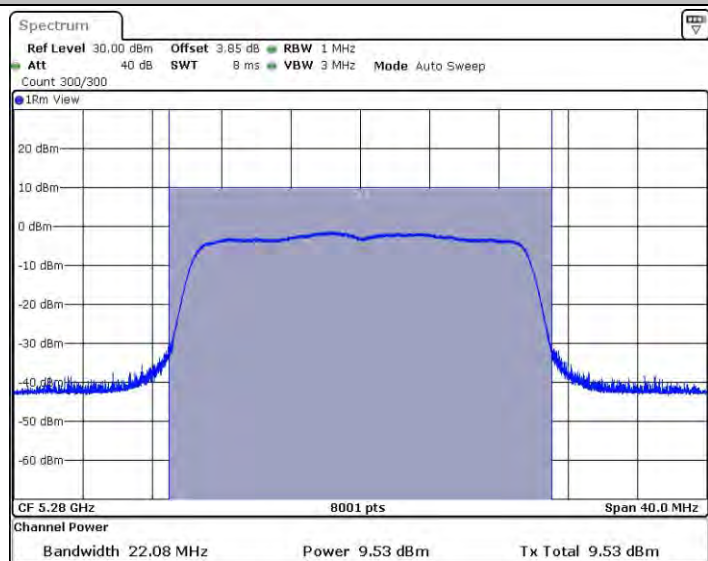
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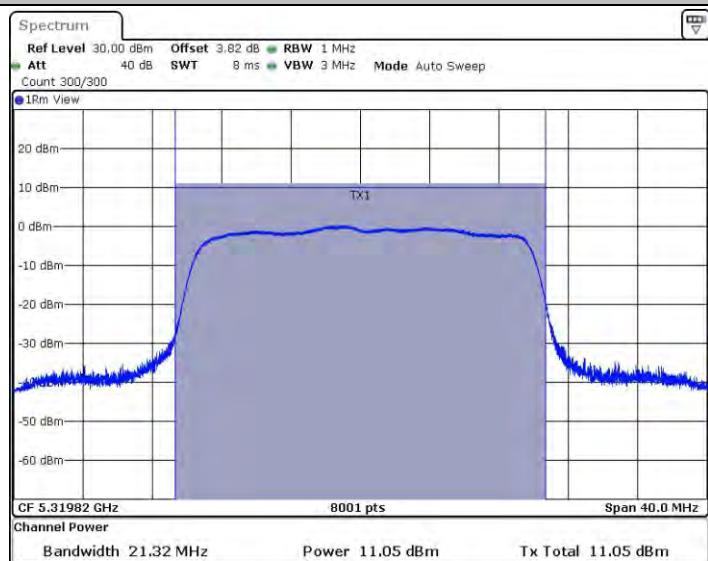
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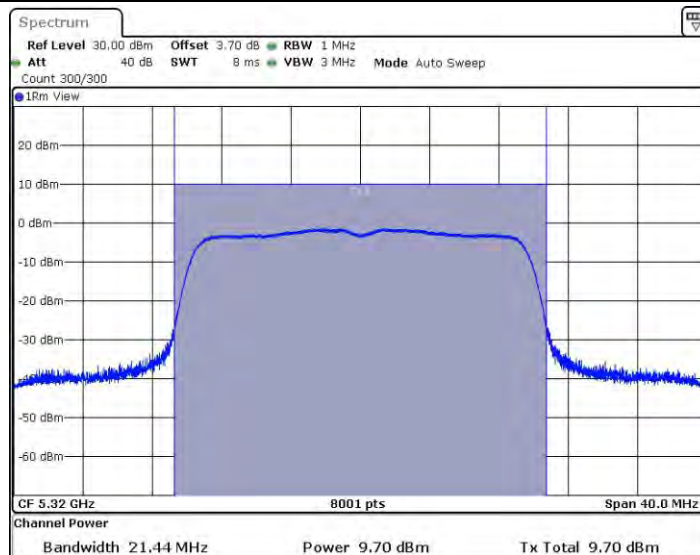
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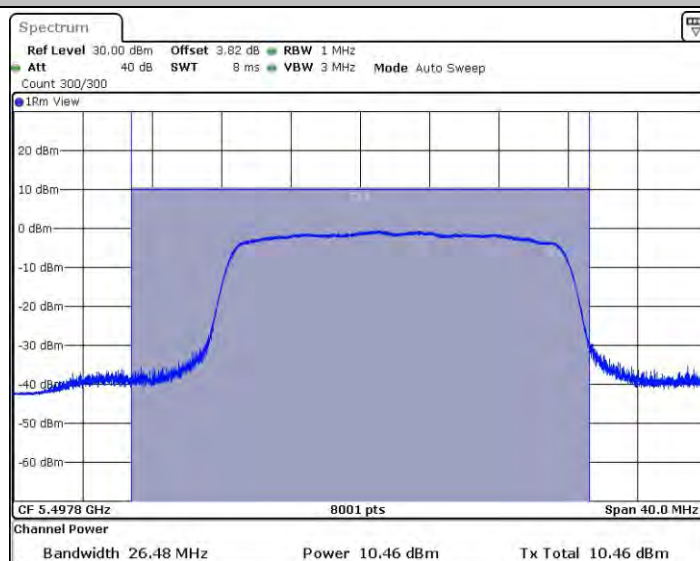
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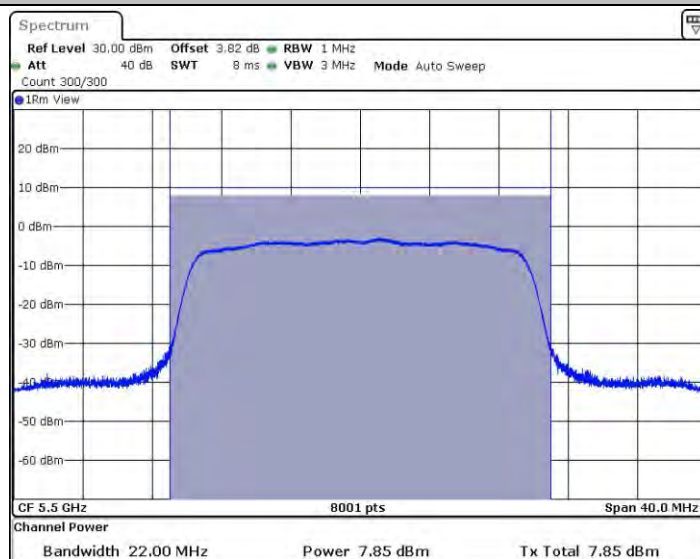
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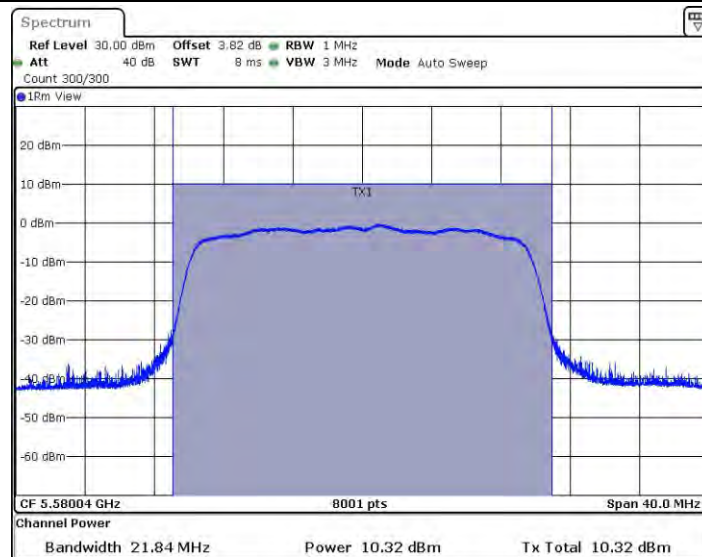
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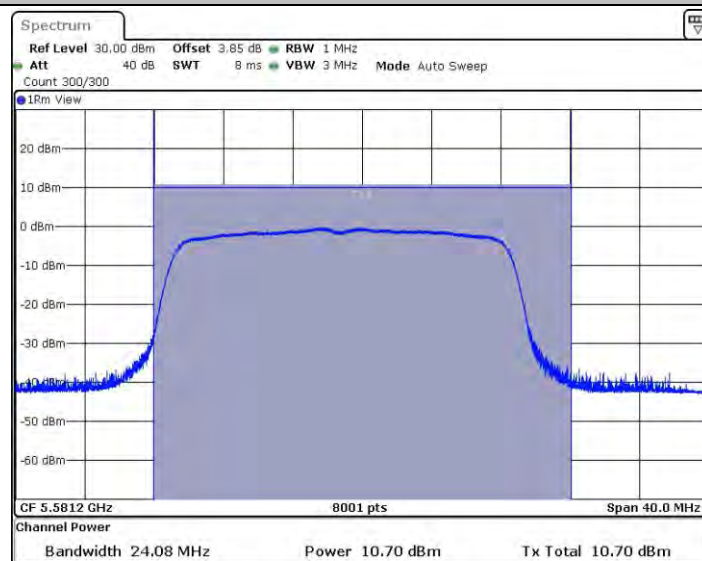
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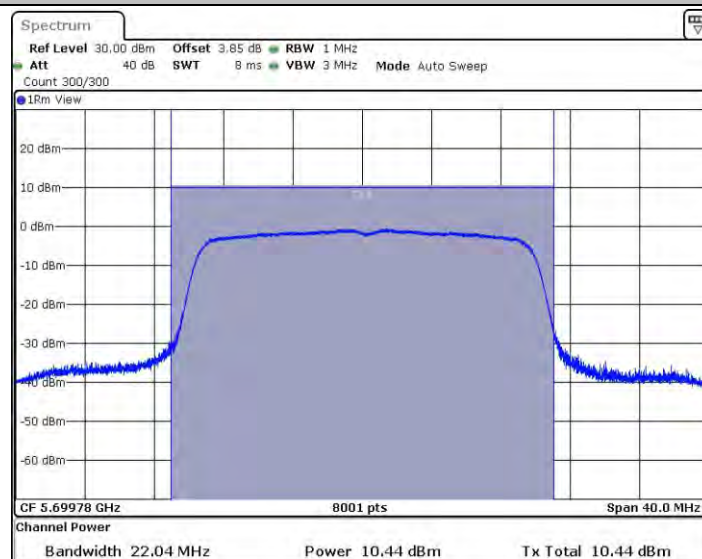
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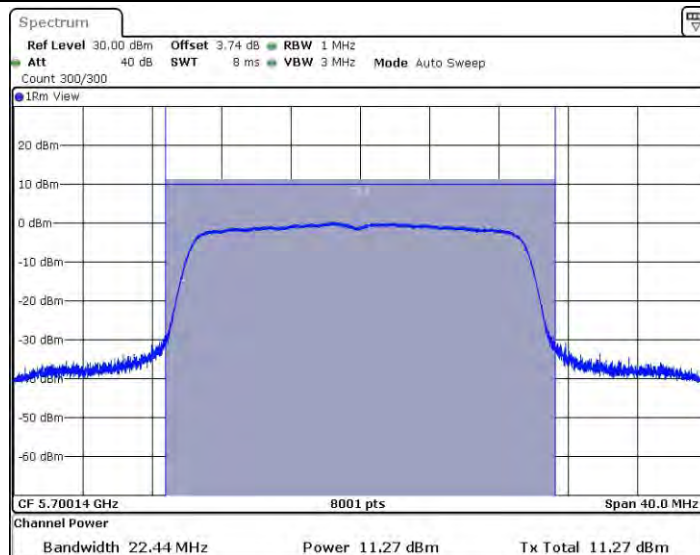
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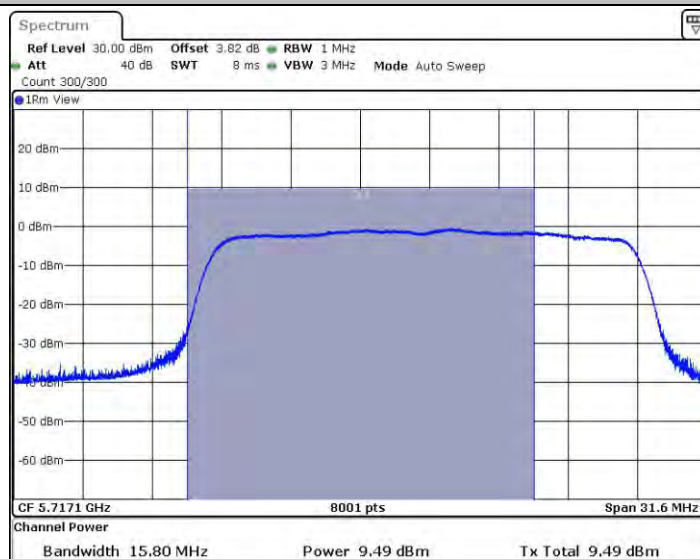
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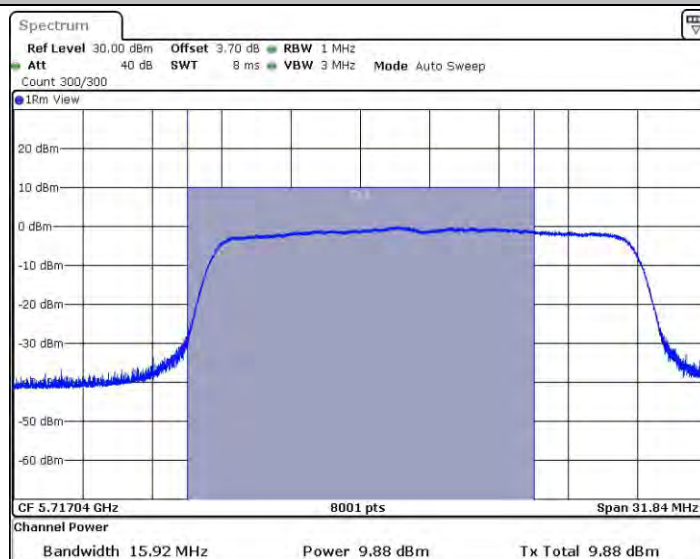
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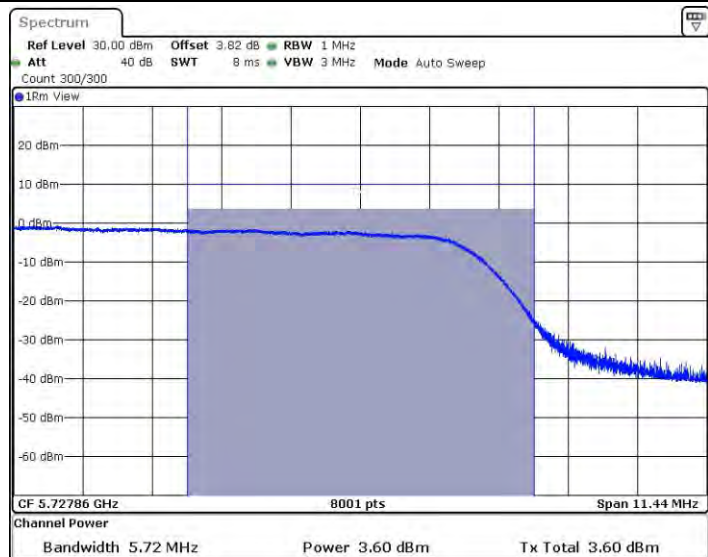
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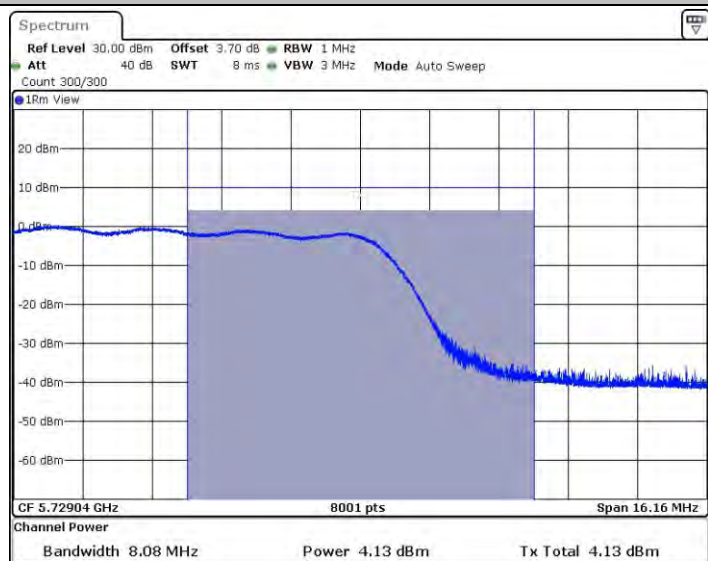
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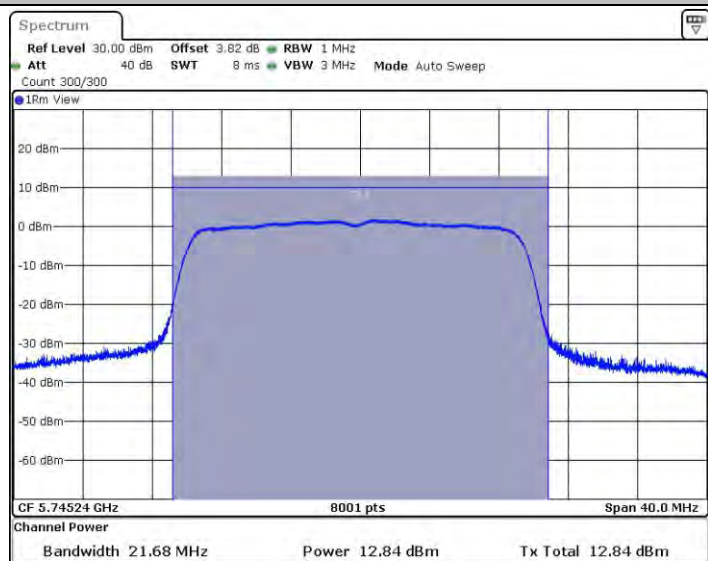
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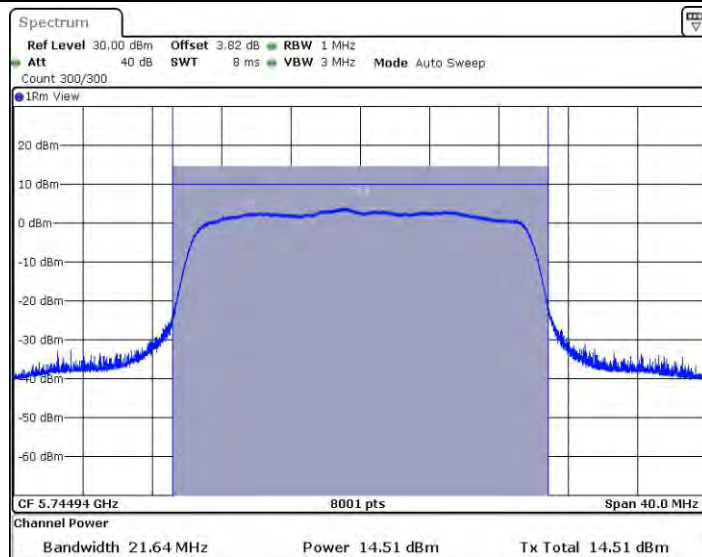
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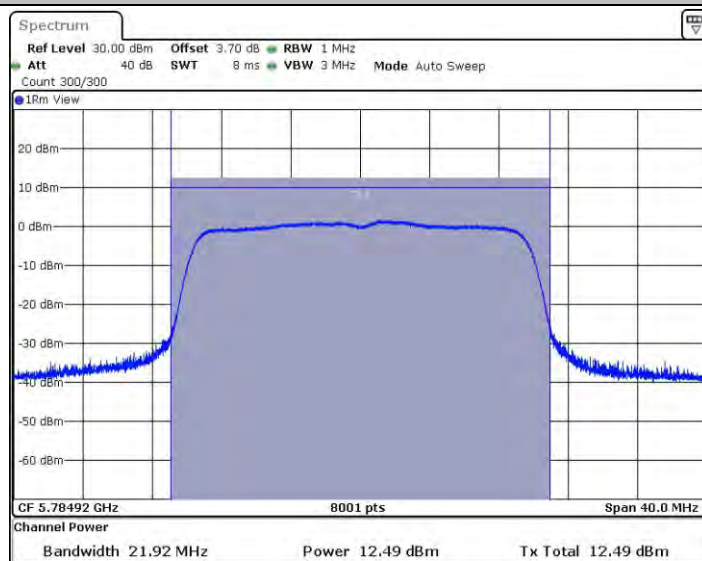
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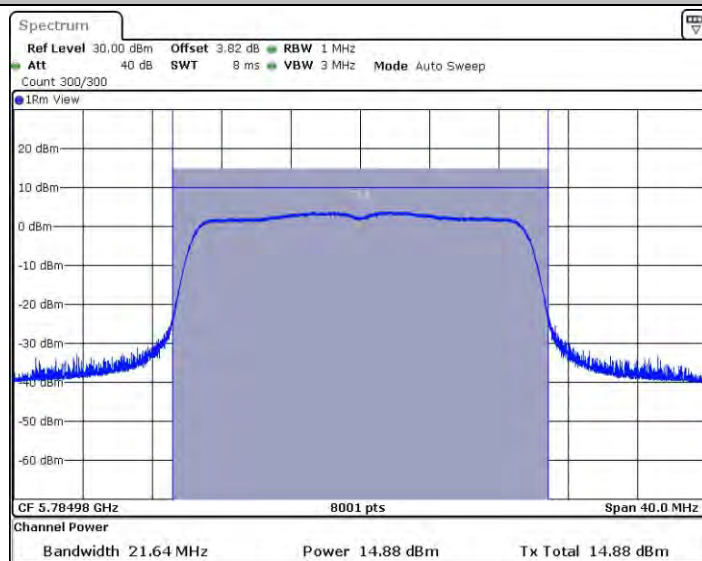
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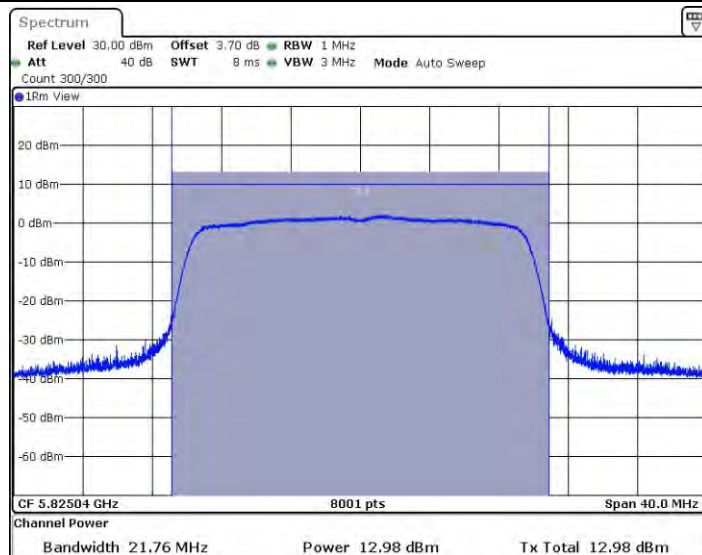
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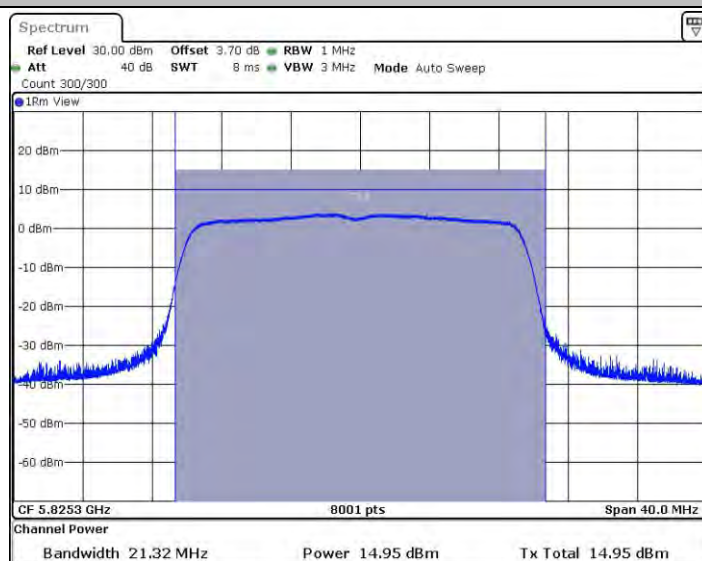
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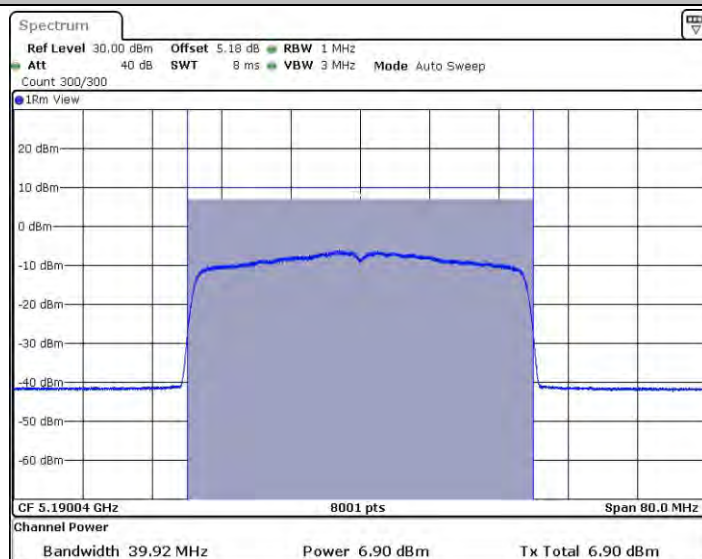
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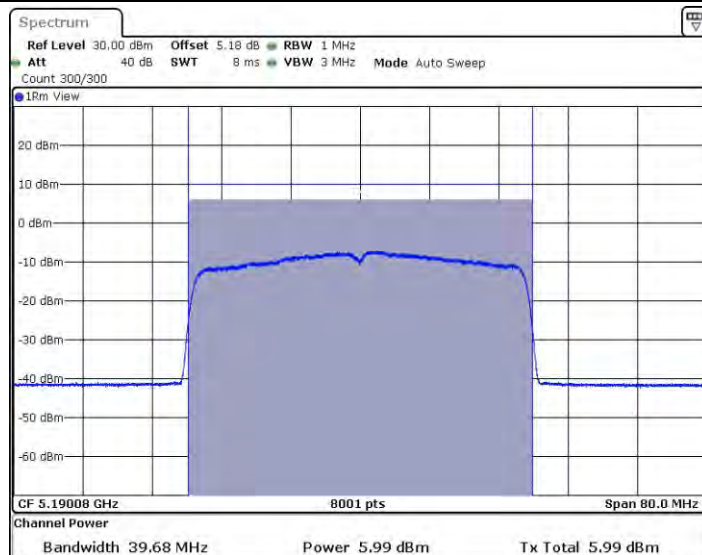
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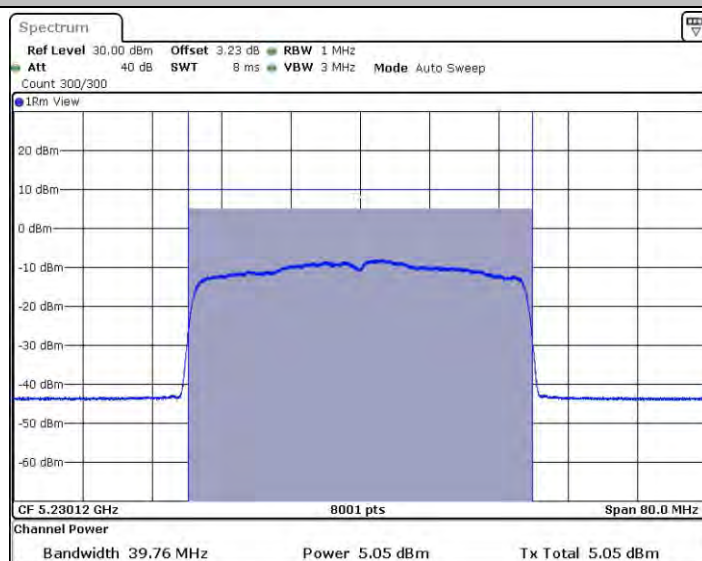
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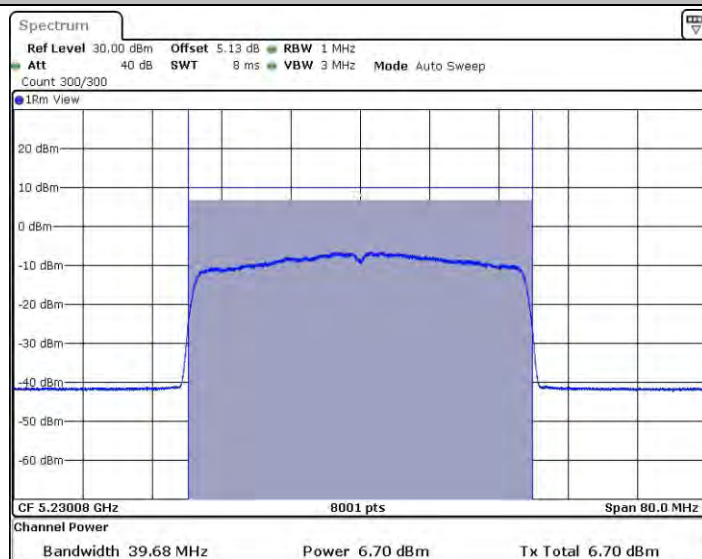
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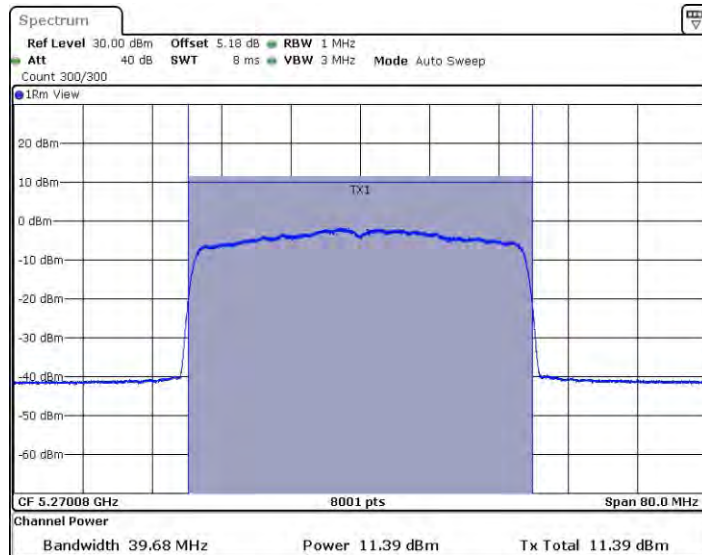
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11AX40SISO_Ant1_5230



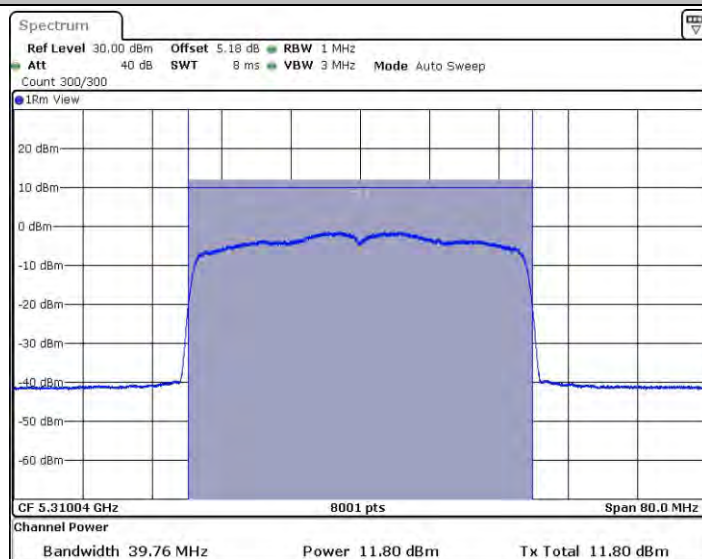
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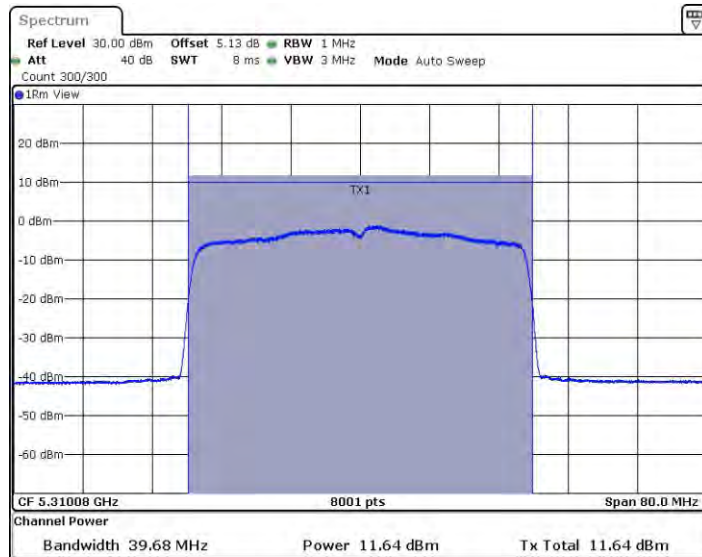
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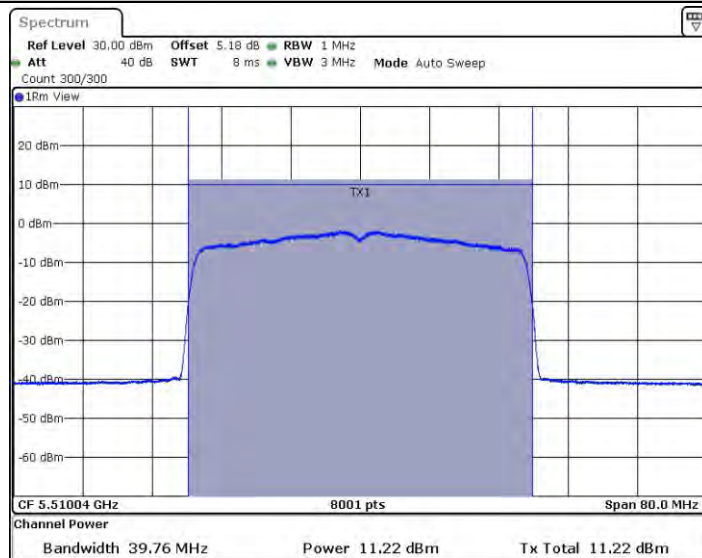
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11AX40SISO_Ant1_5310



11AX40SISO_Ant2_5310



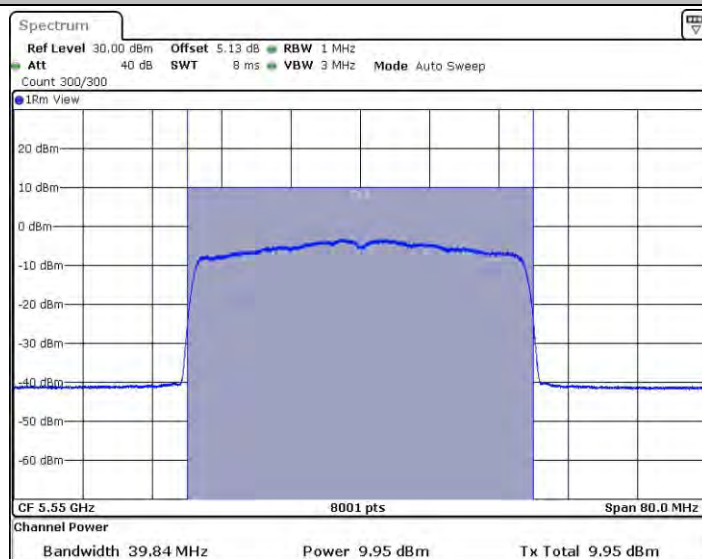
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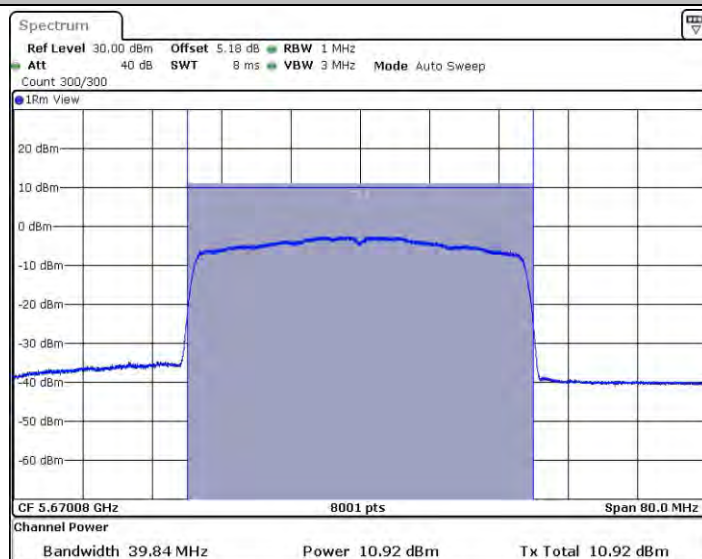
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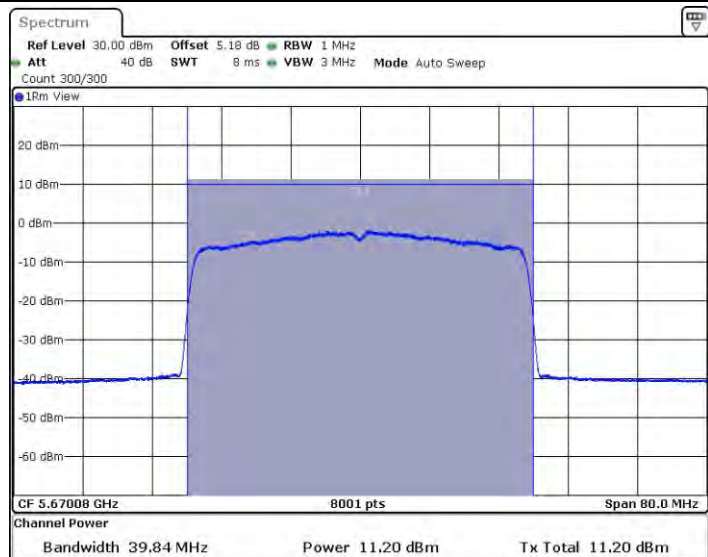
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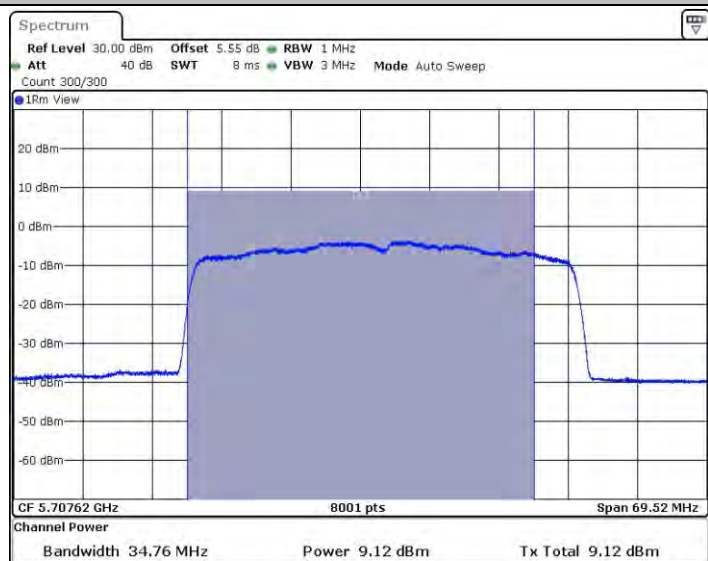
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11AX40SISO_Ant1_5670



11AX40SISO_Ant2_5670



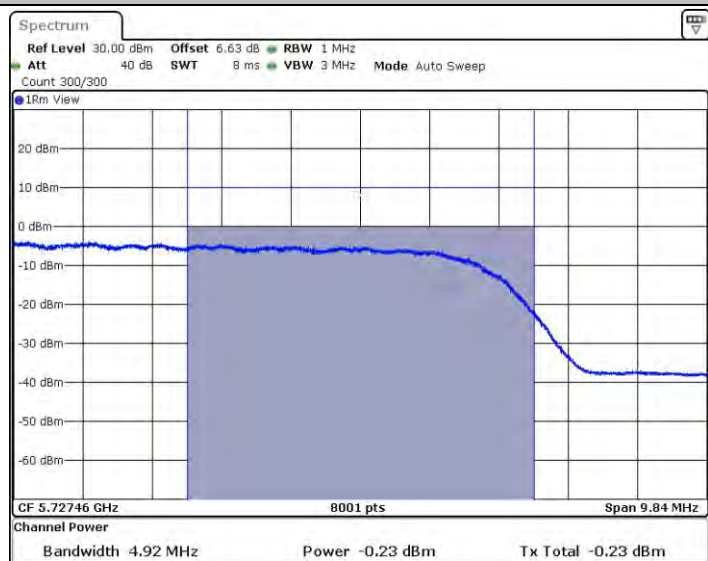
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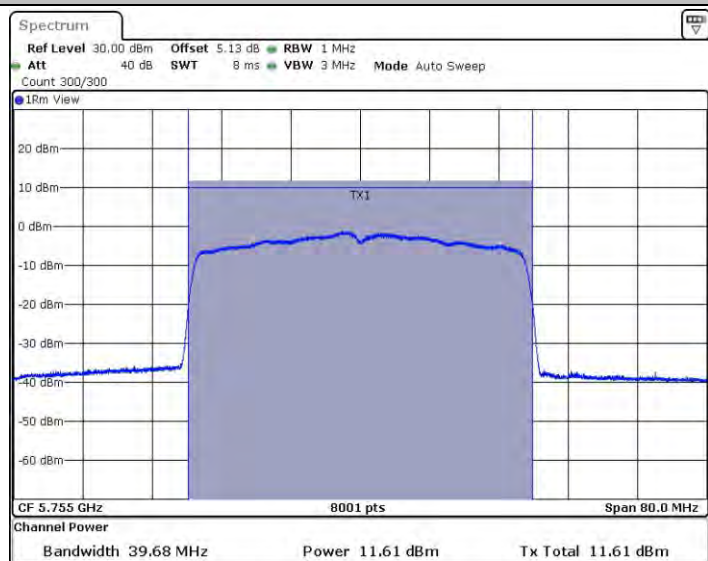
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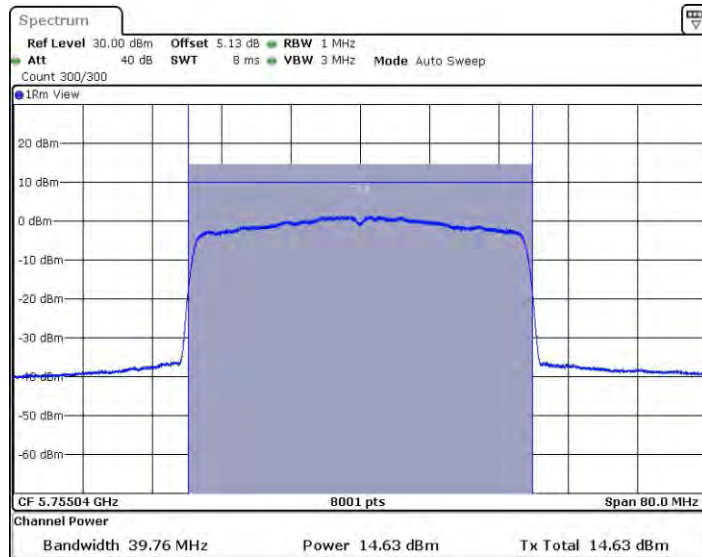
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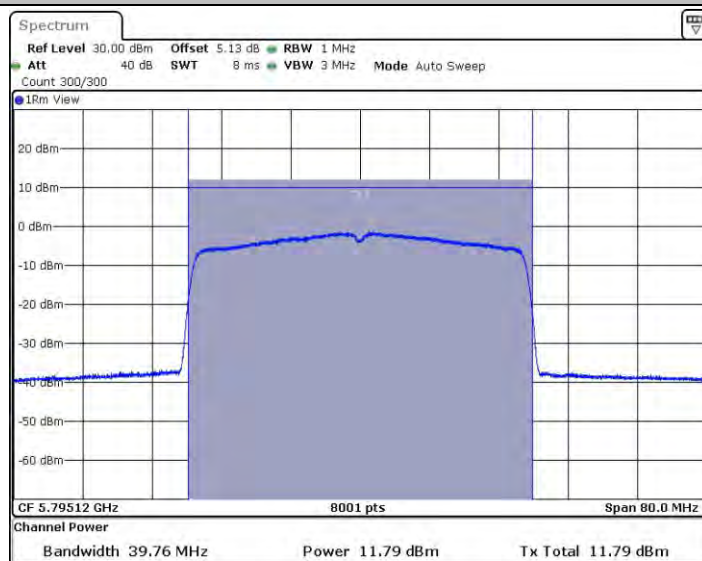
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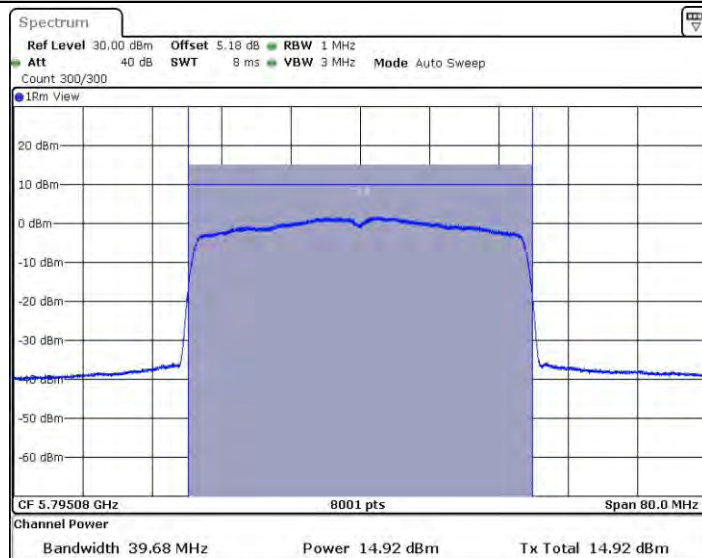
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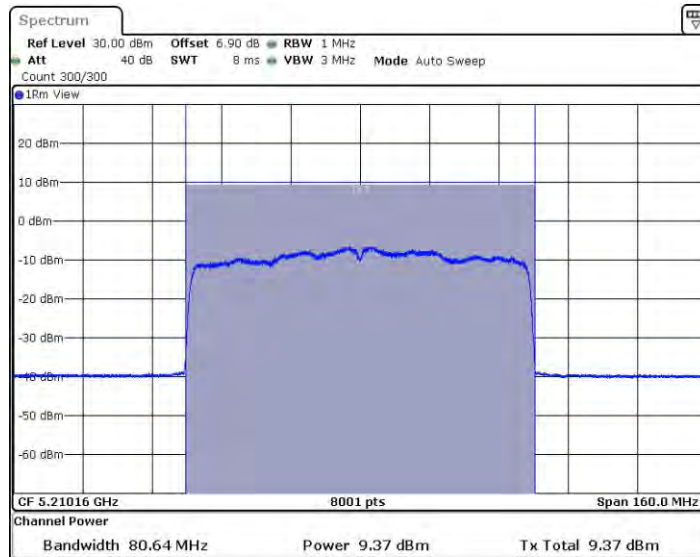
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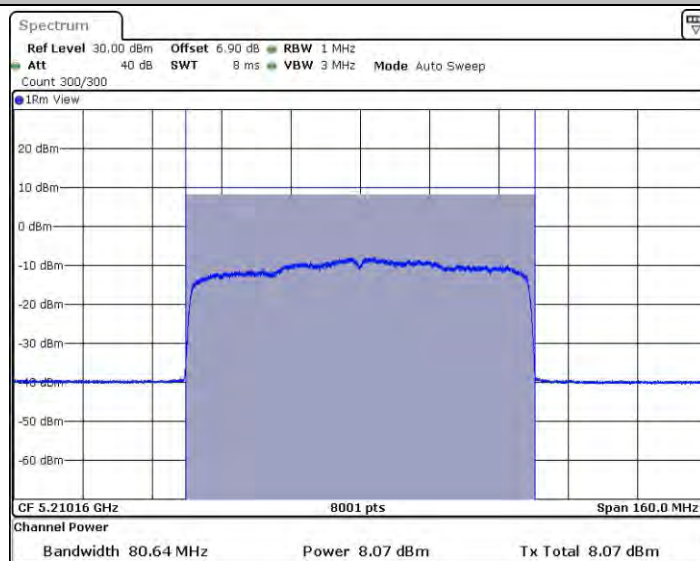
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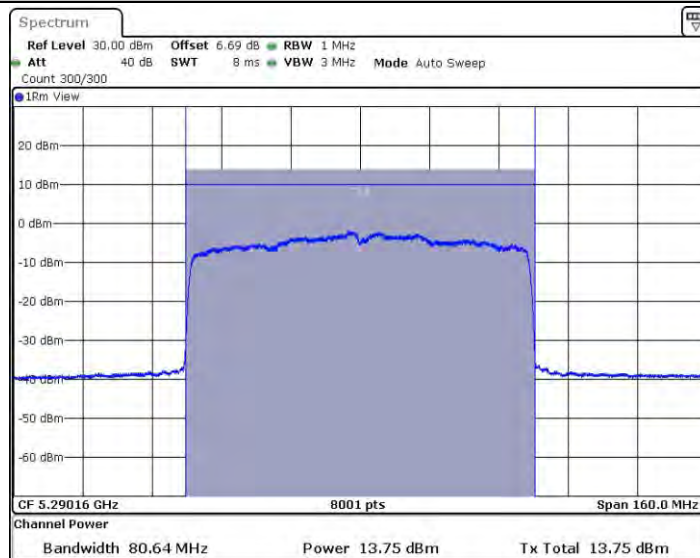
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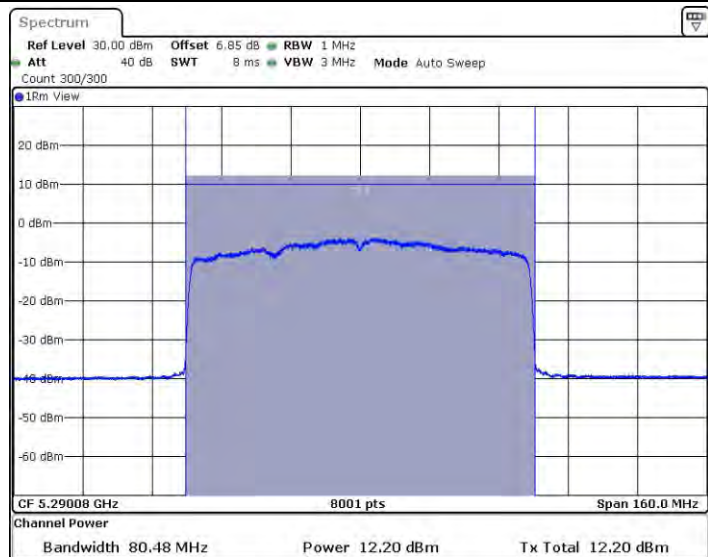
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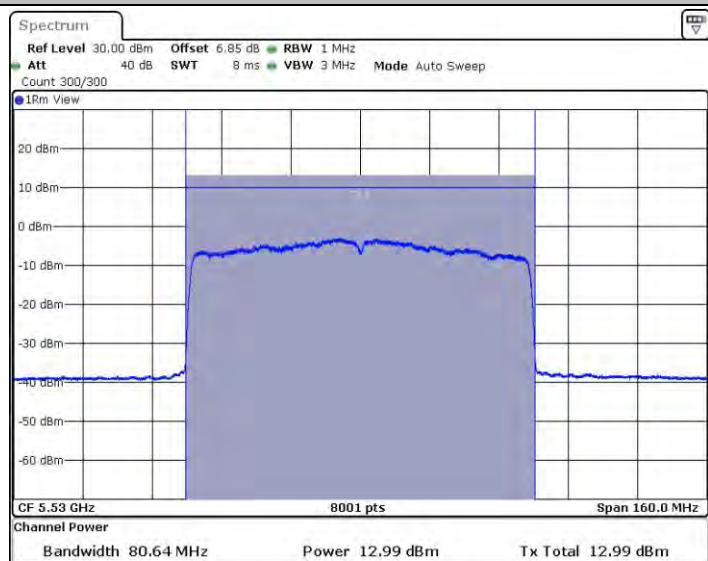
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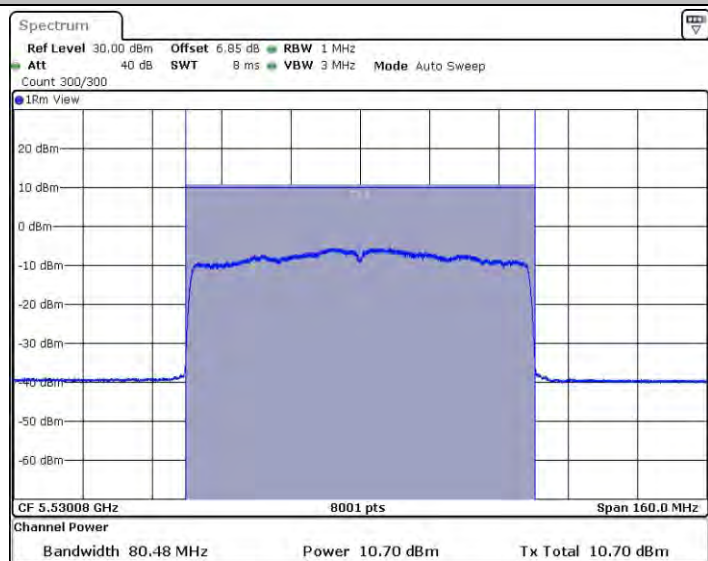
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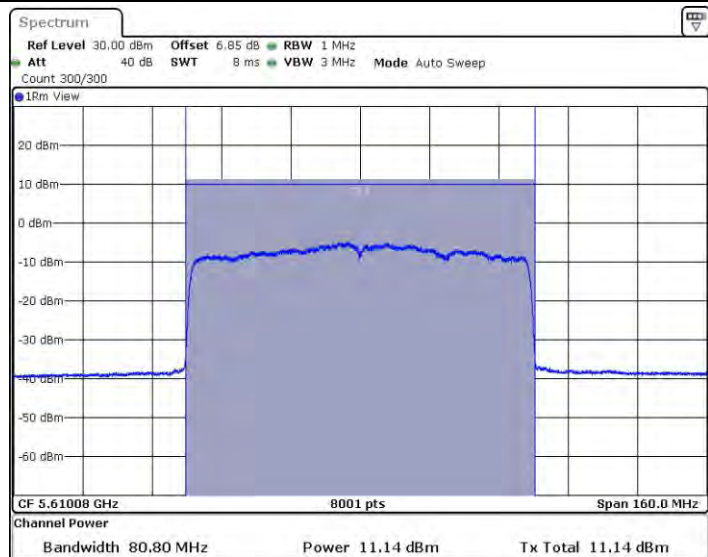
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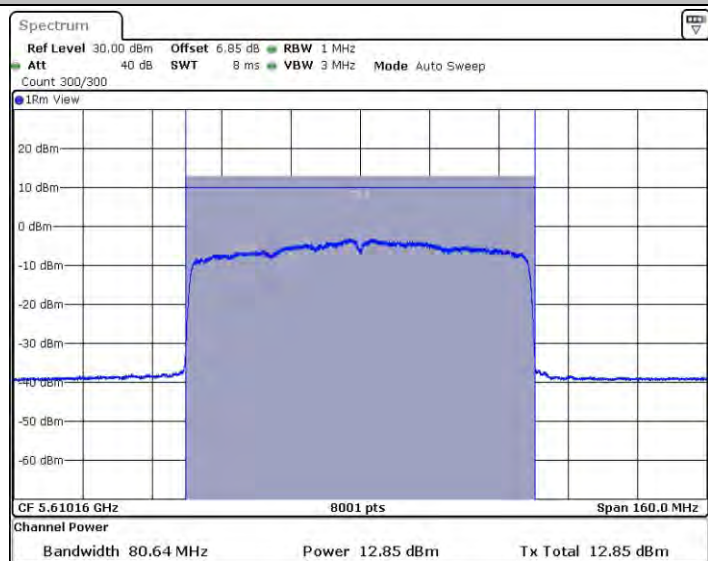
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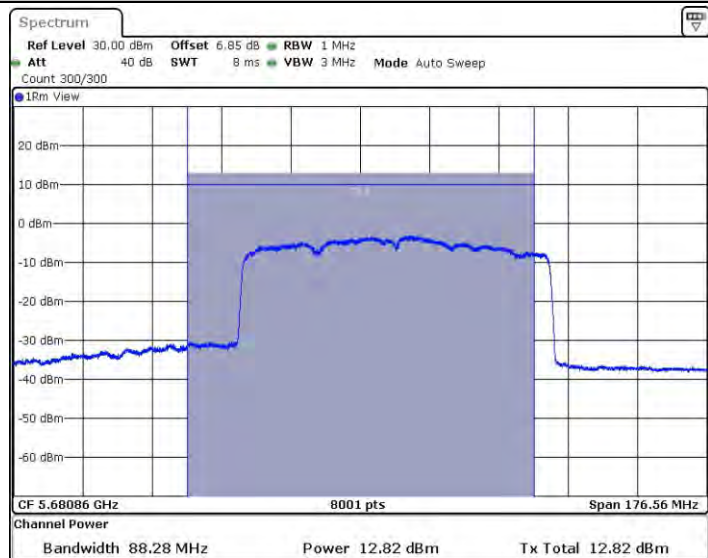
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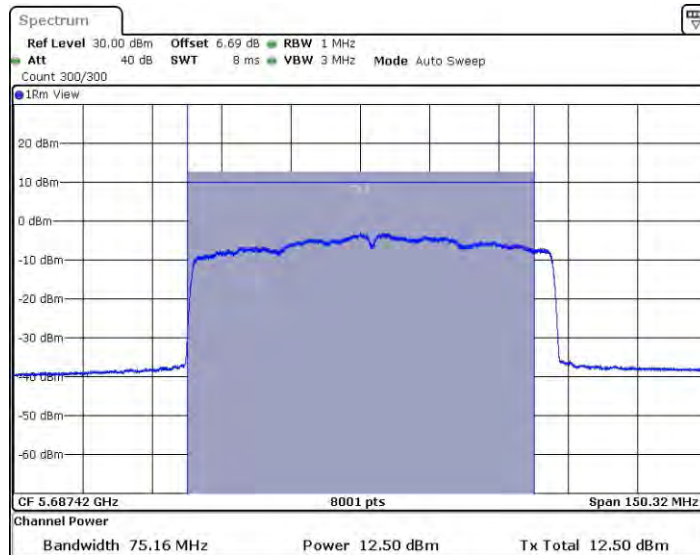
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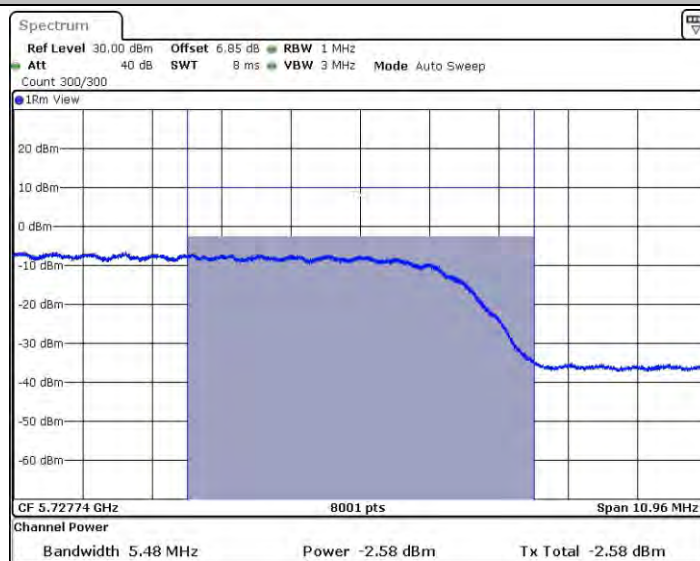
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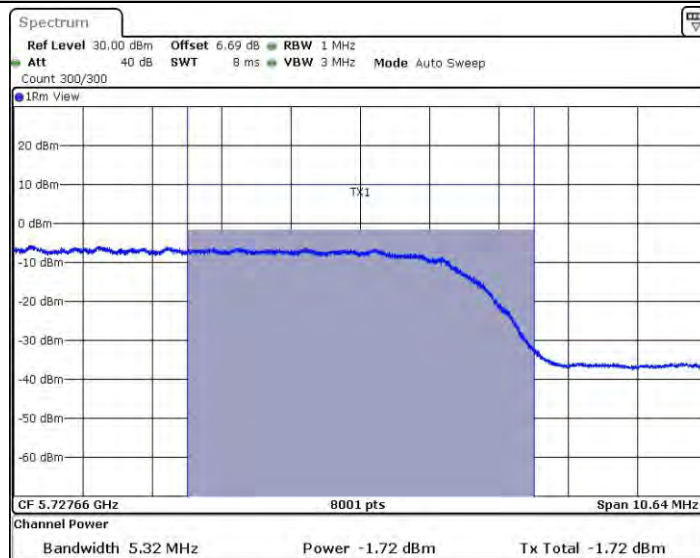
11AX80SISO_Ant1_5690_UNII-2C



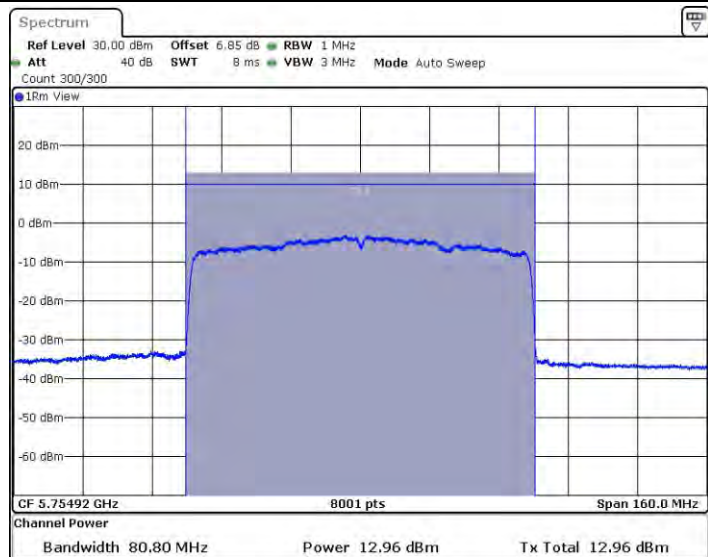
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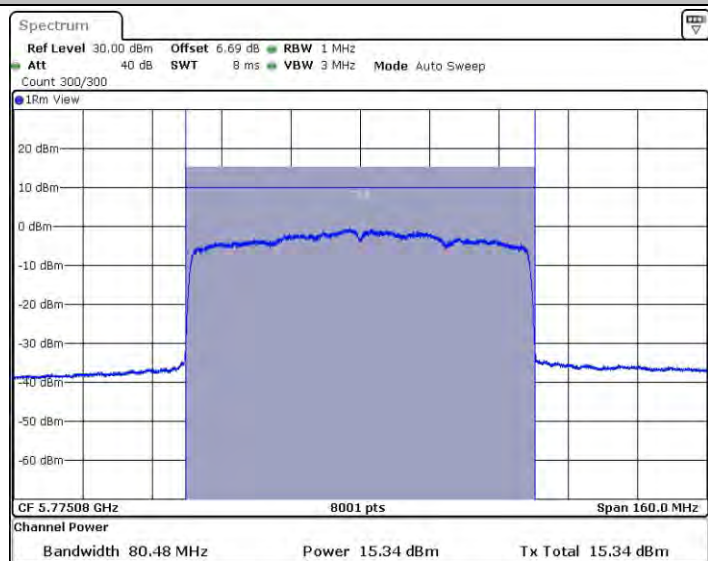
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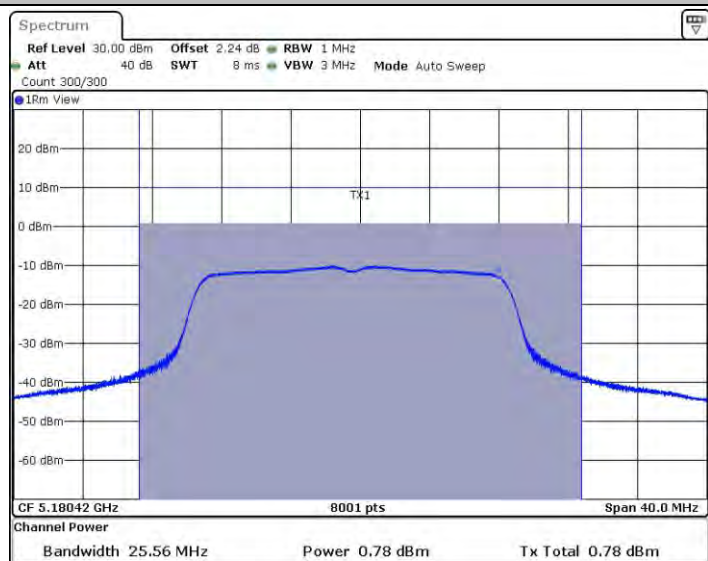
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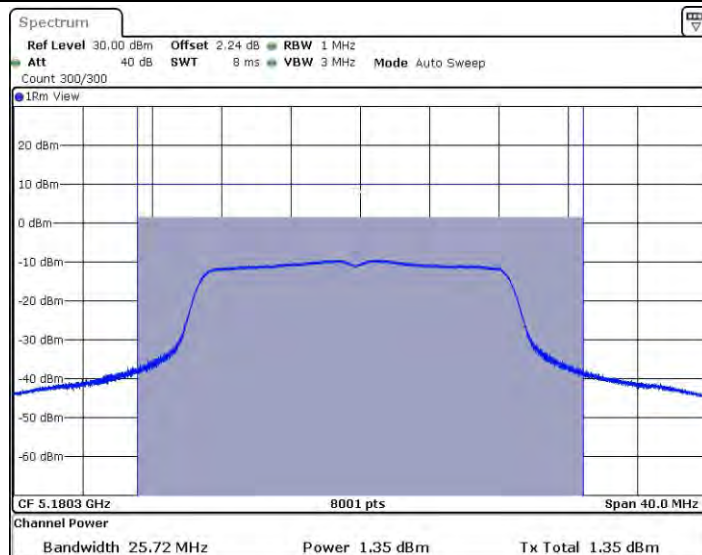
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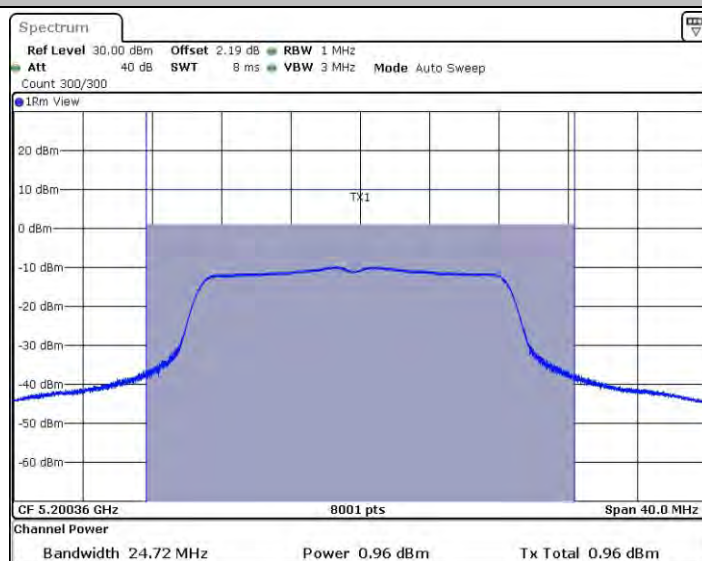
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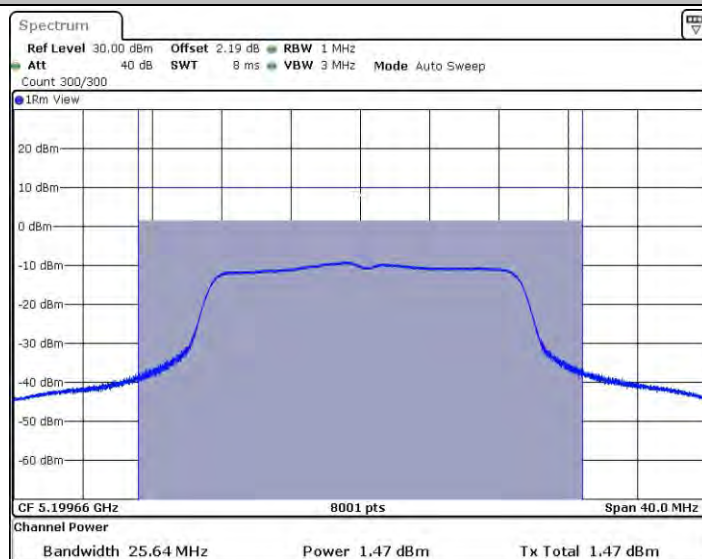
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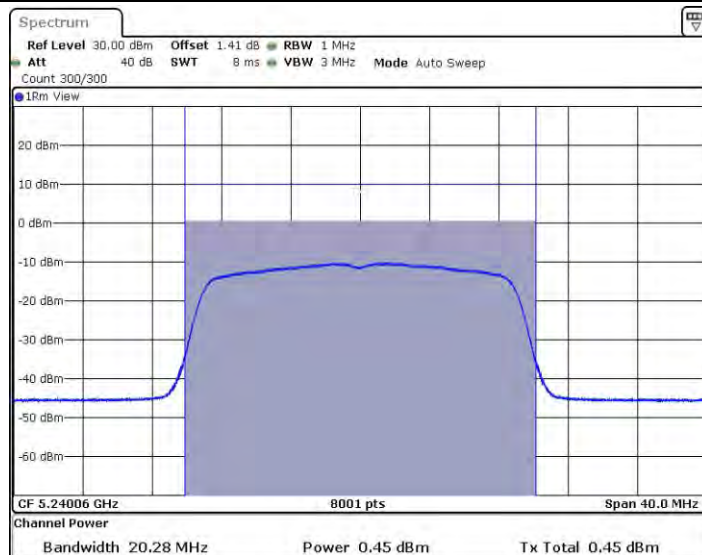
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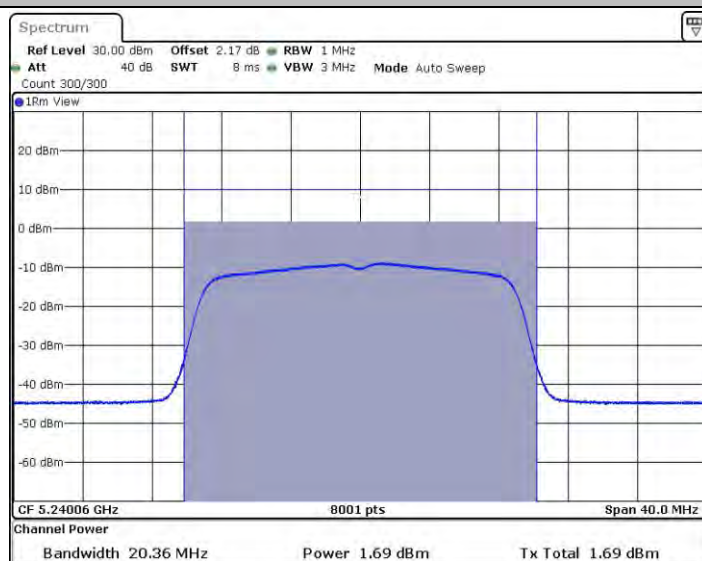
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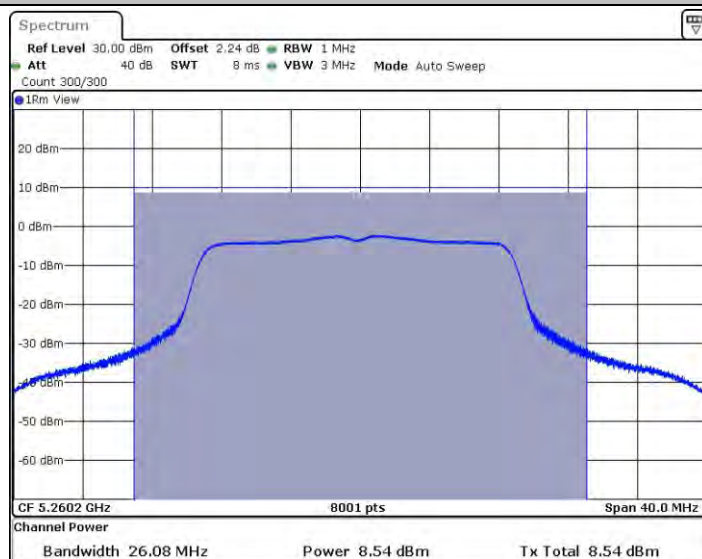
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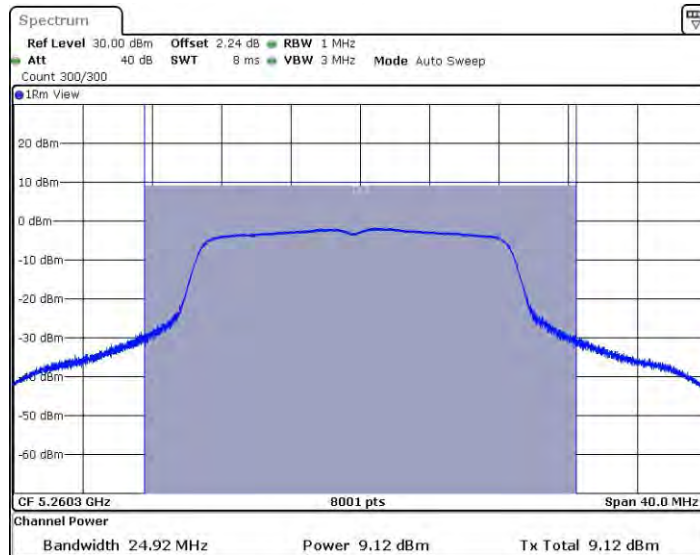
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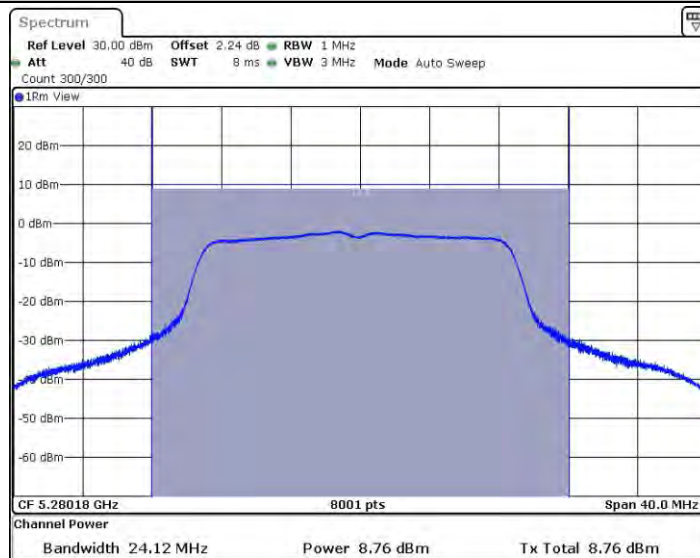
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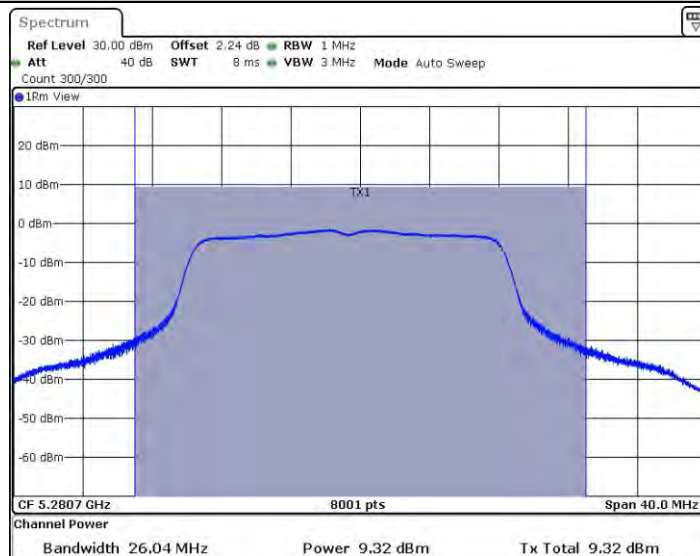
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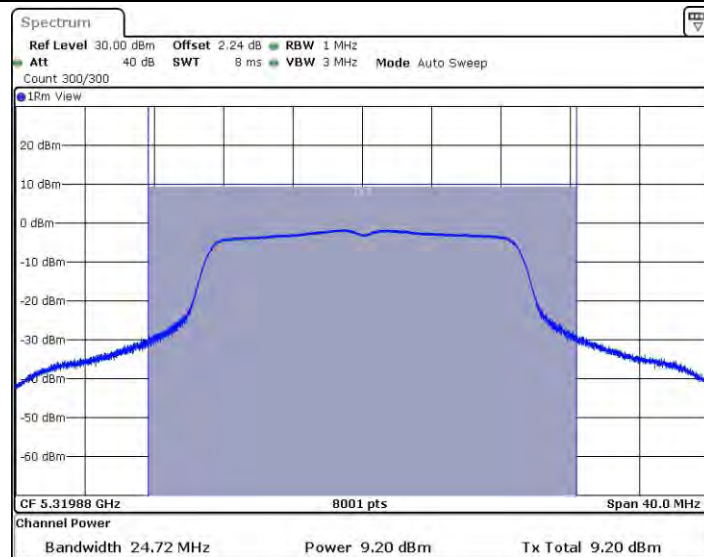
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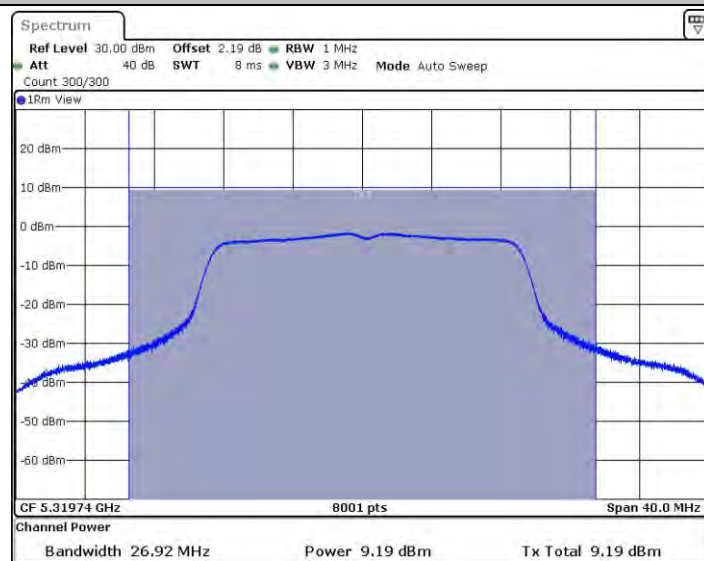
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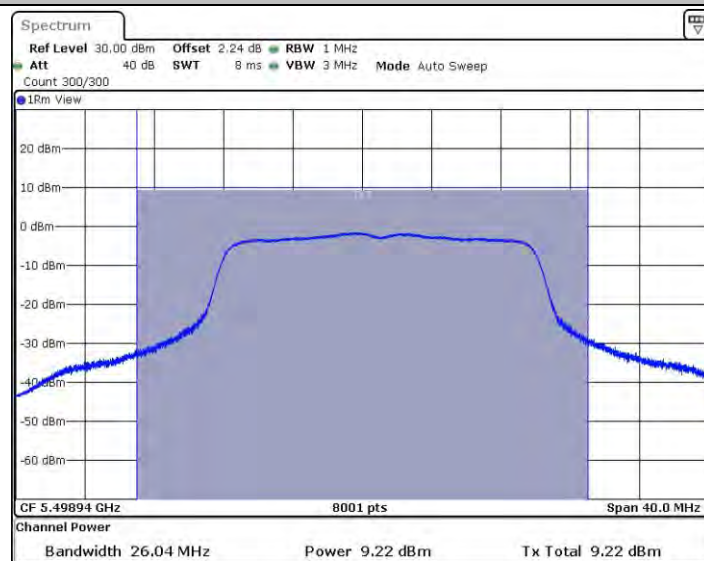
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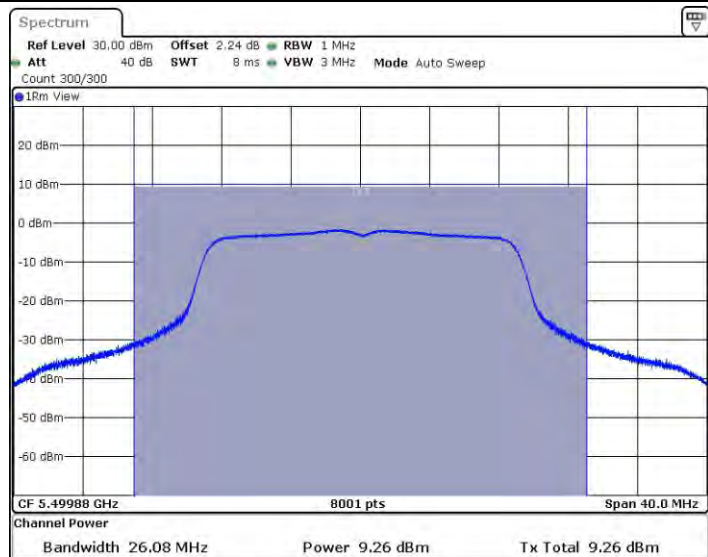
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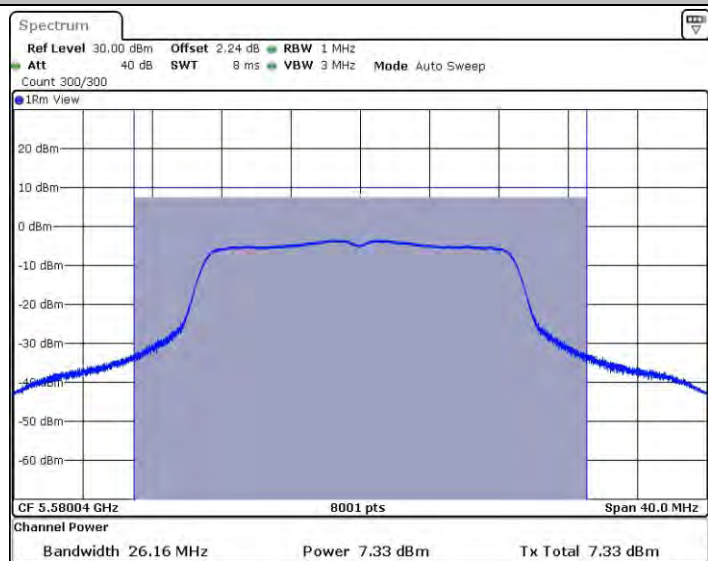
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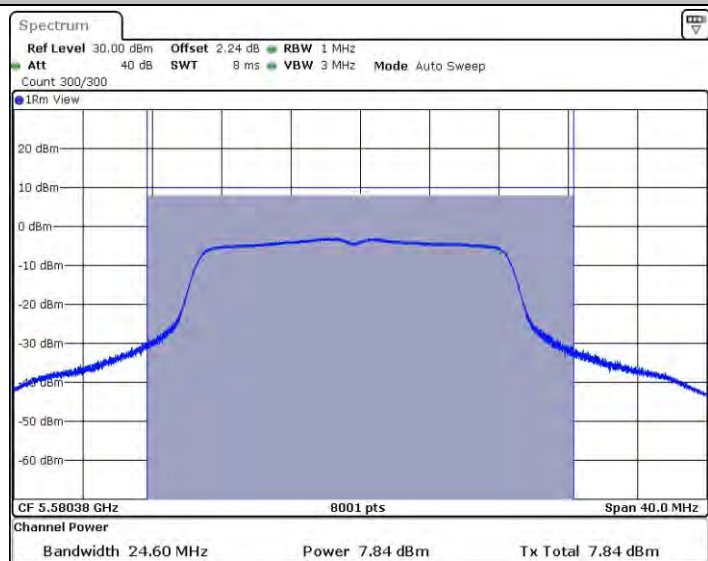
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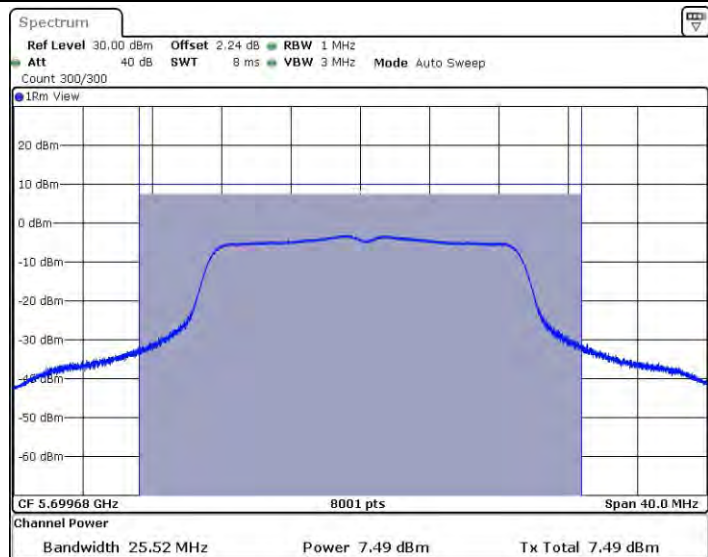
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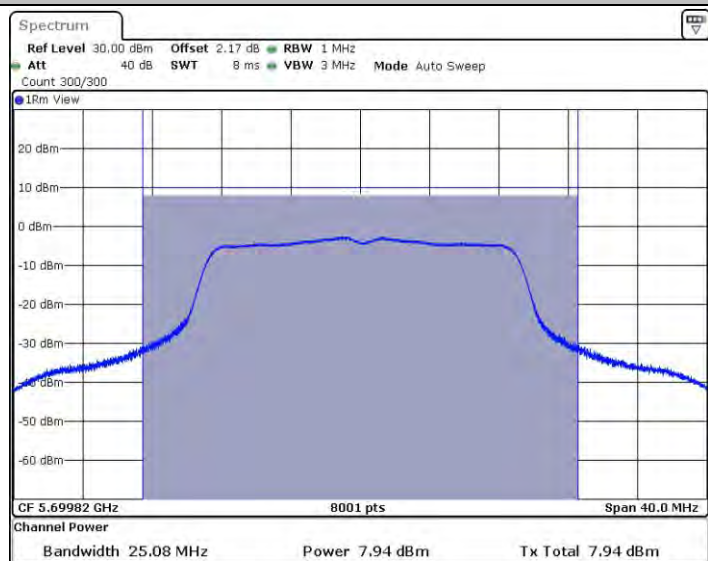
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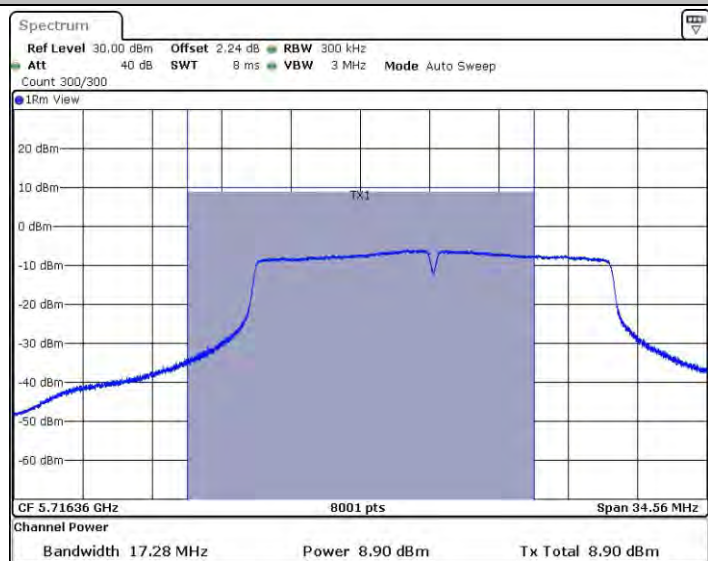
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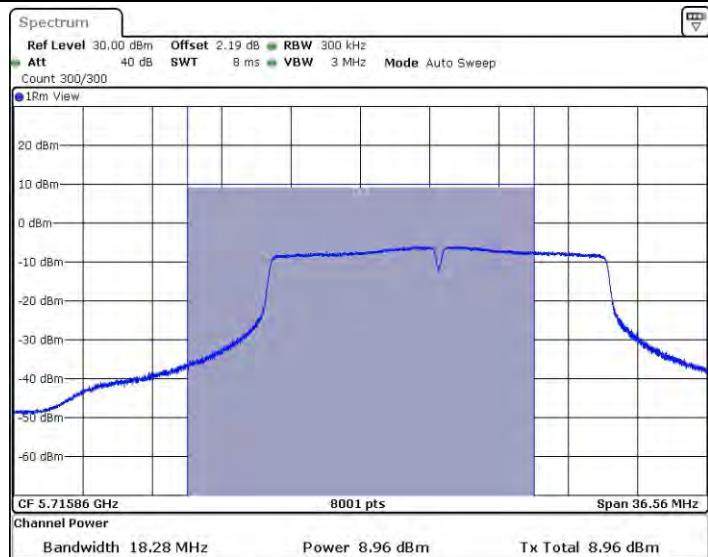
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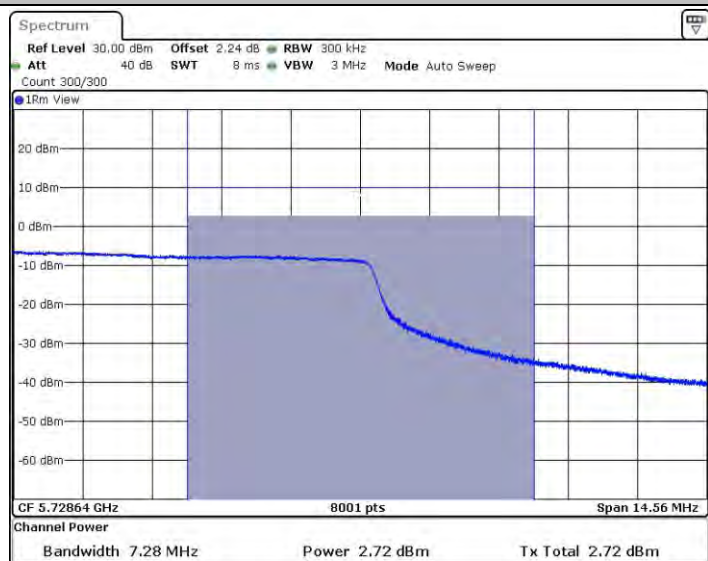
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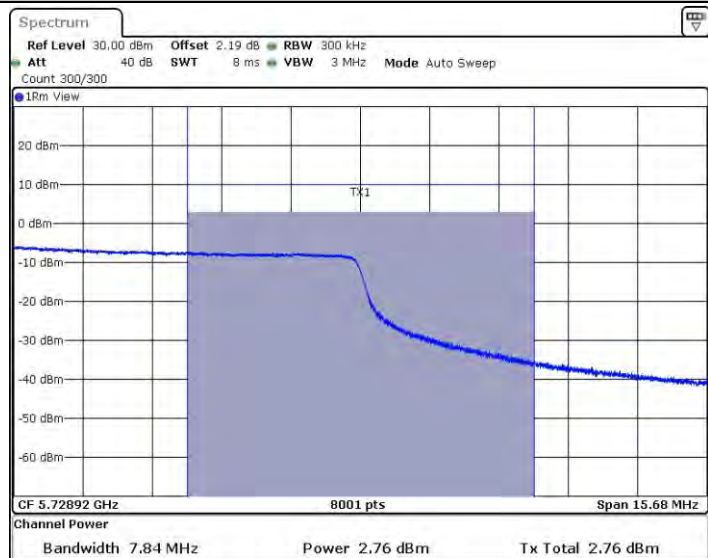
11N20MIMO_Ant1_5720_UNII-2C



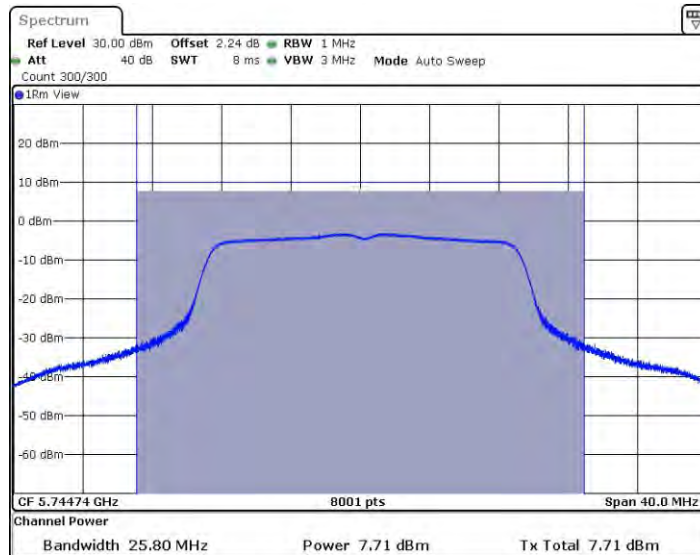
11N20MIMO_Ant2_5720_UNII-2C



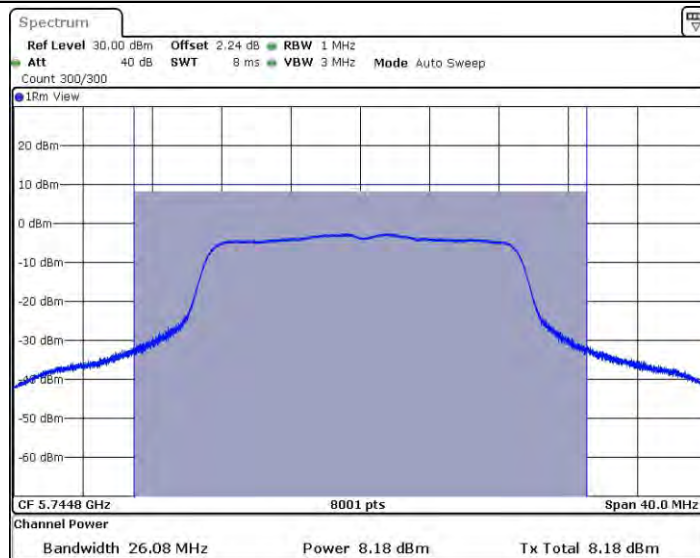
11N20MIMO_Ant1_5720_UNII-3



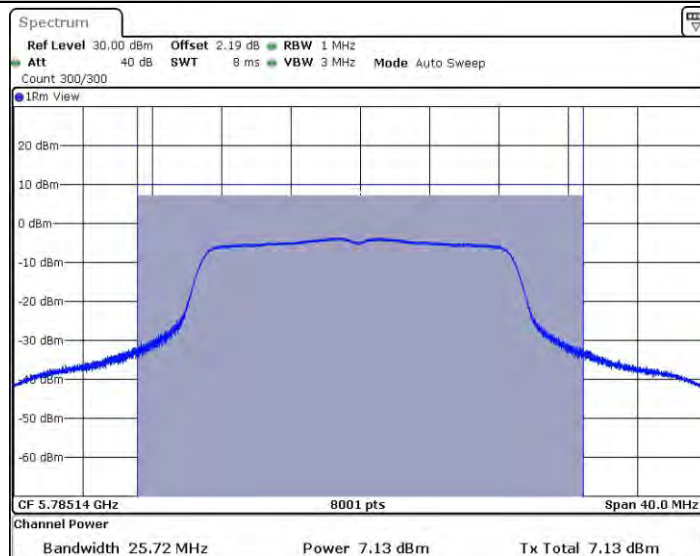
11N20MIMO_Ant2_5720_UNII-3



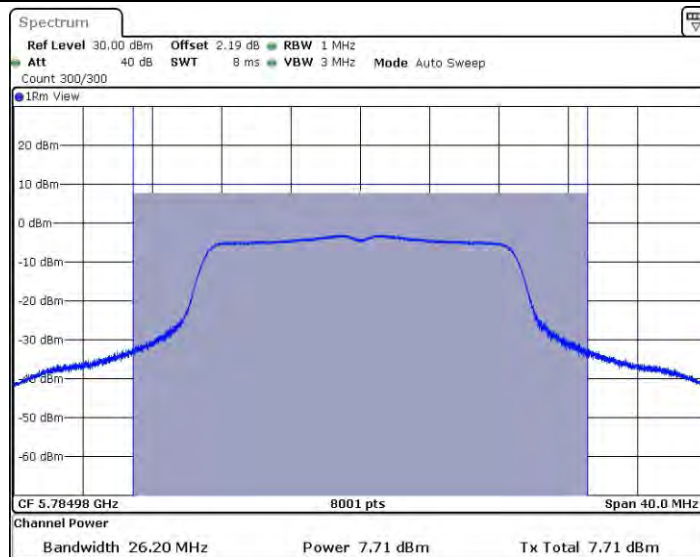
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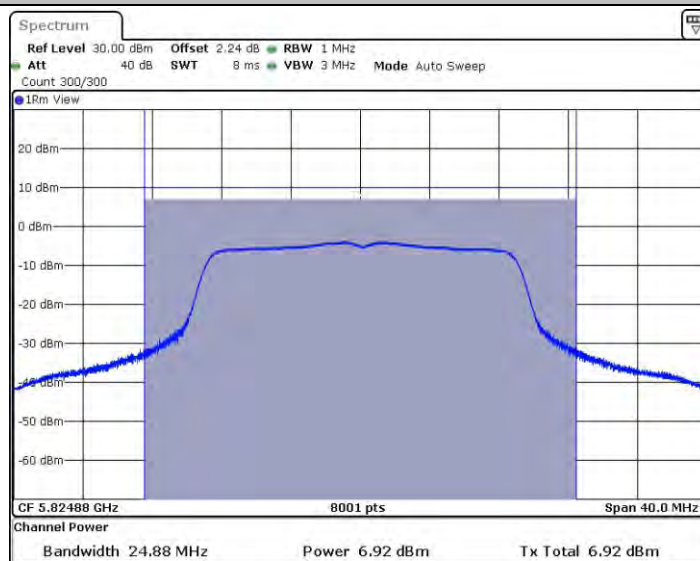
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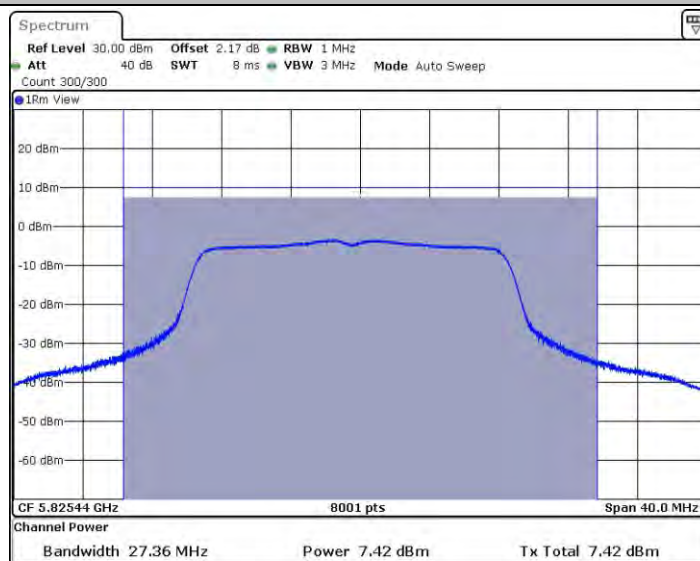
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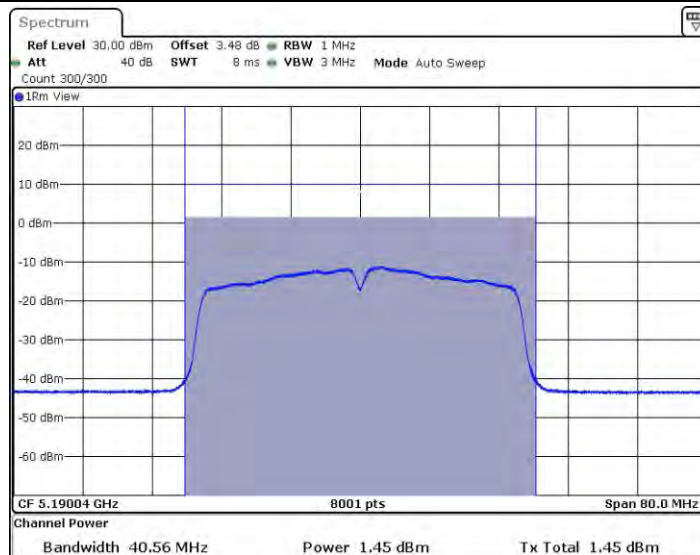
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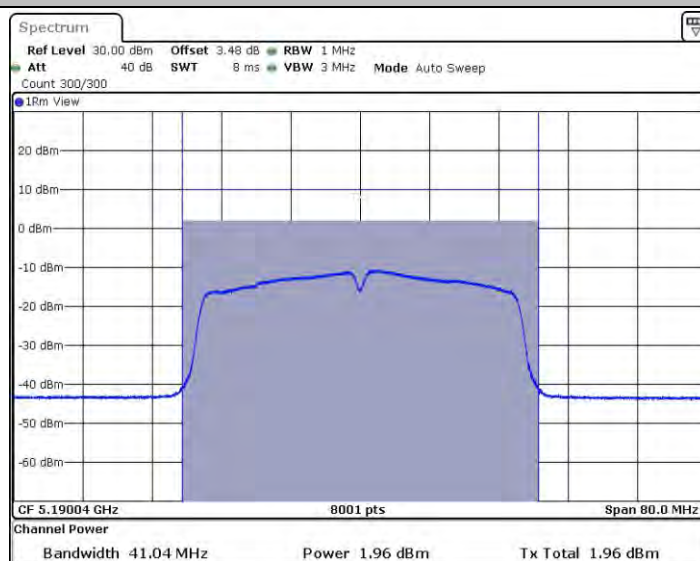
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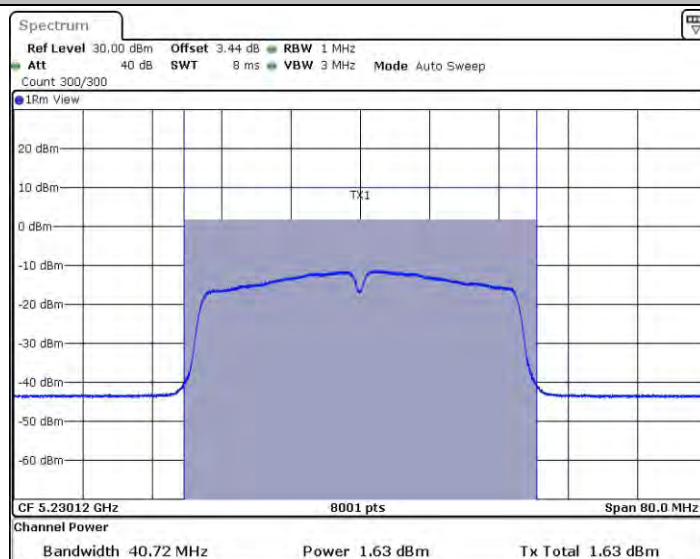
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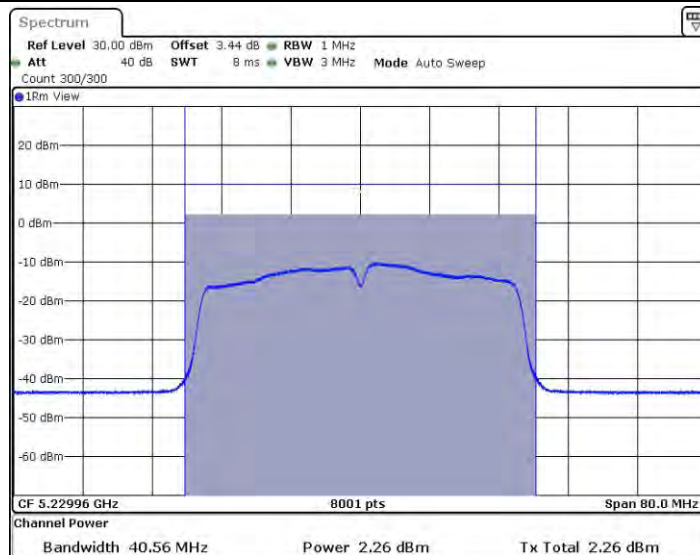
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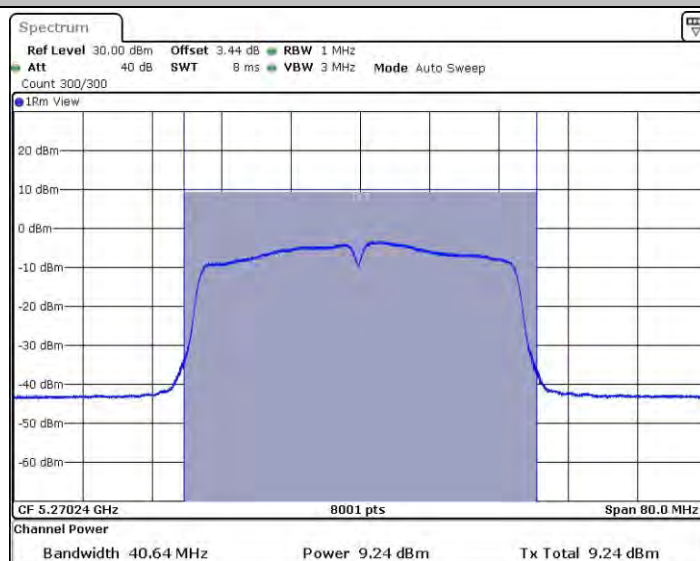
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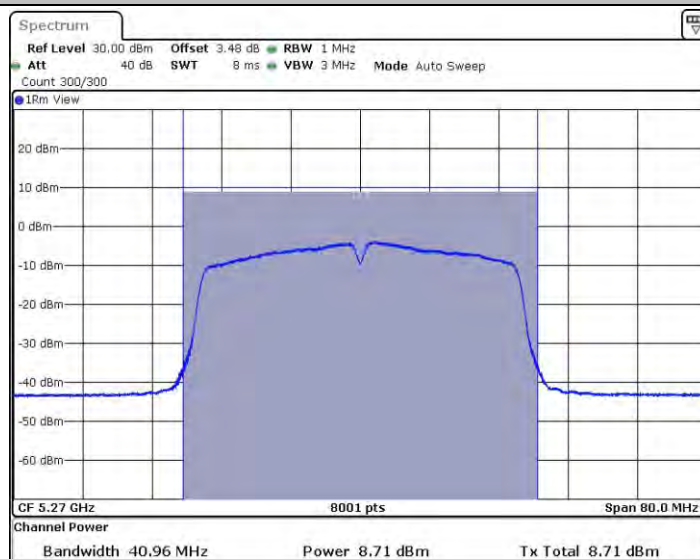
11N40MIMO_Ant1_5230



11N40MIMO_Ant2_5230



11N40MIMO_Ant1_5270



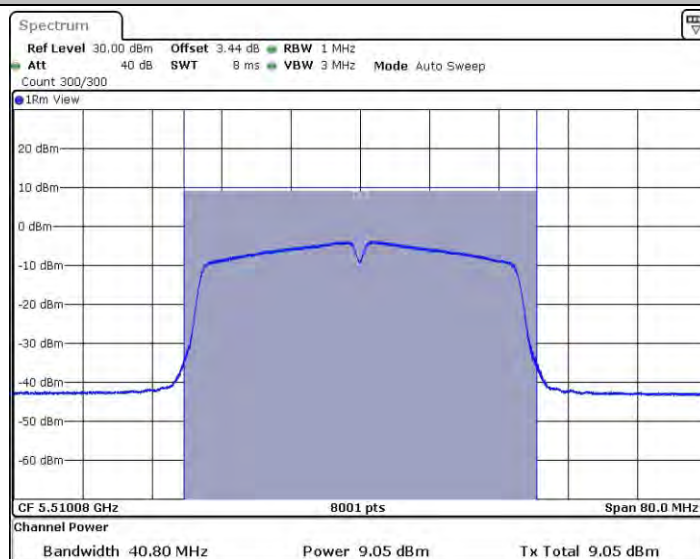
11N40MIMO_Ant2_5270



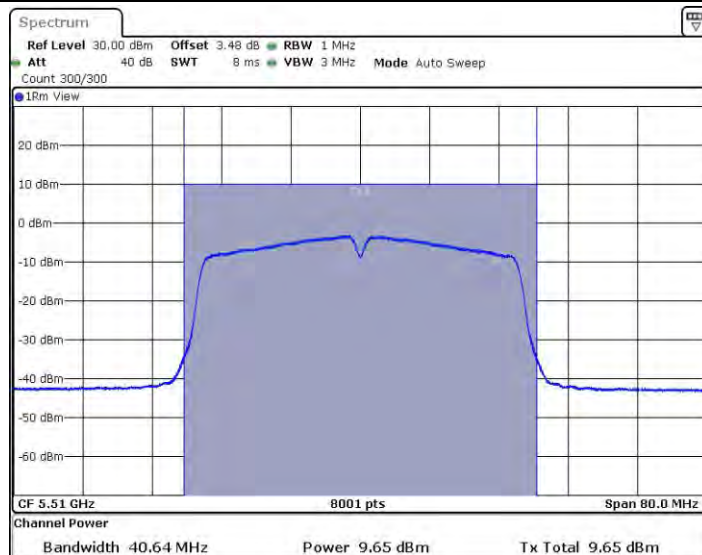
11N40MIMO_Ant1_5310



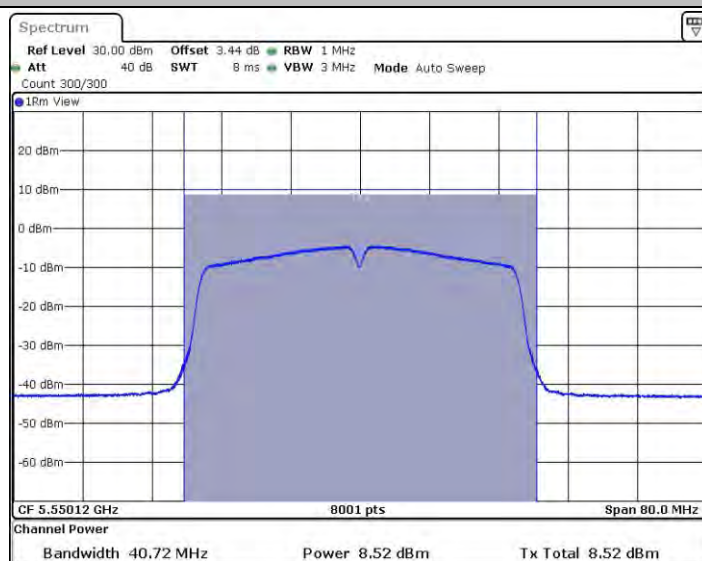
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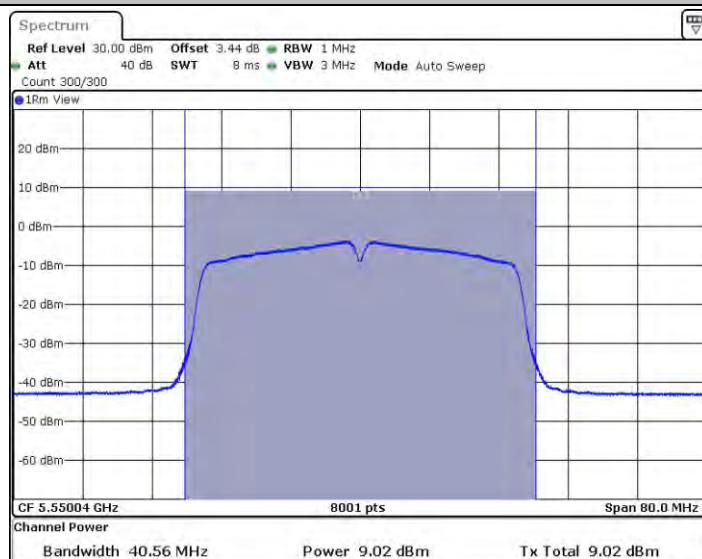
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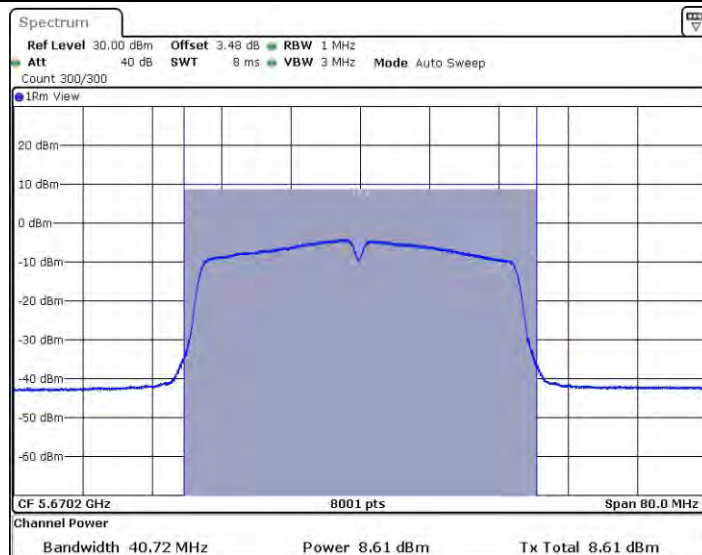
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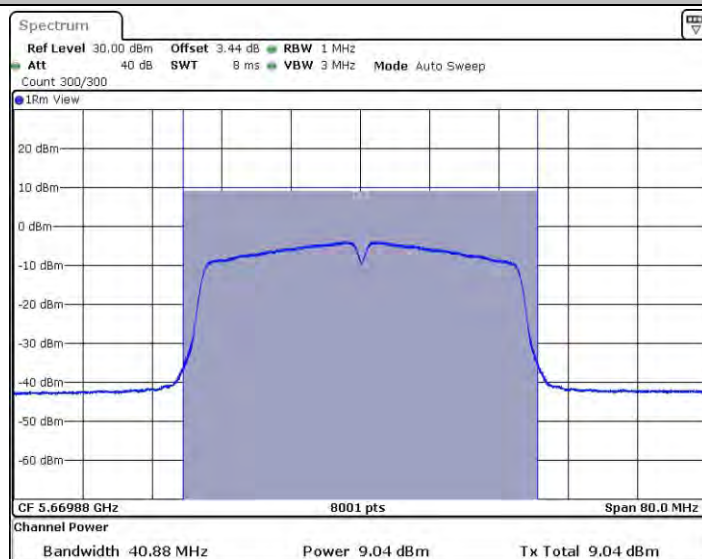
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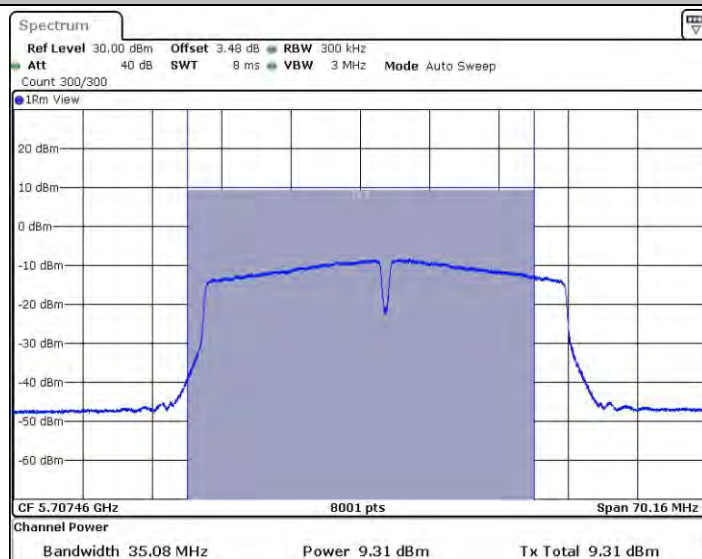
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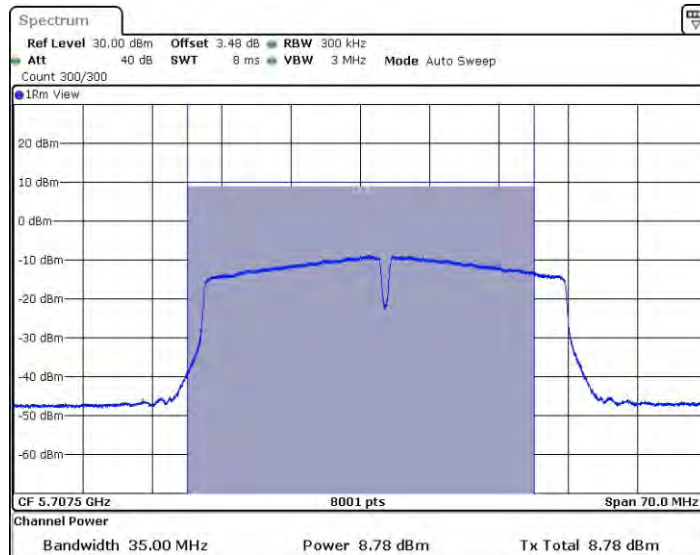
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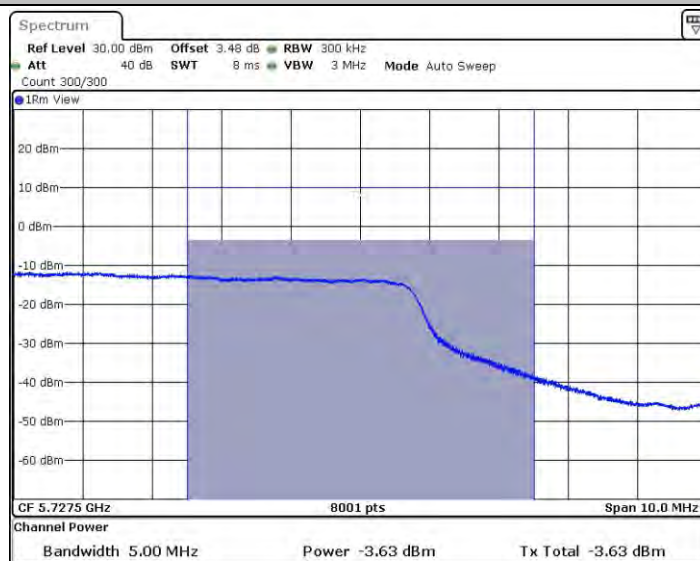
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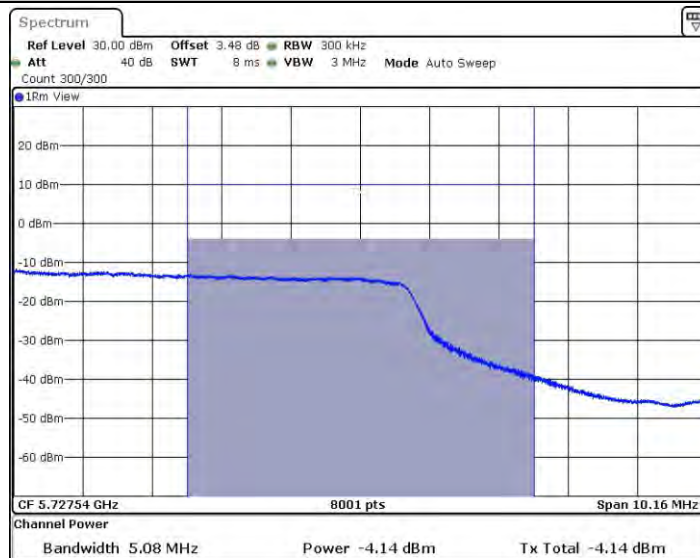
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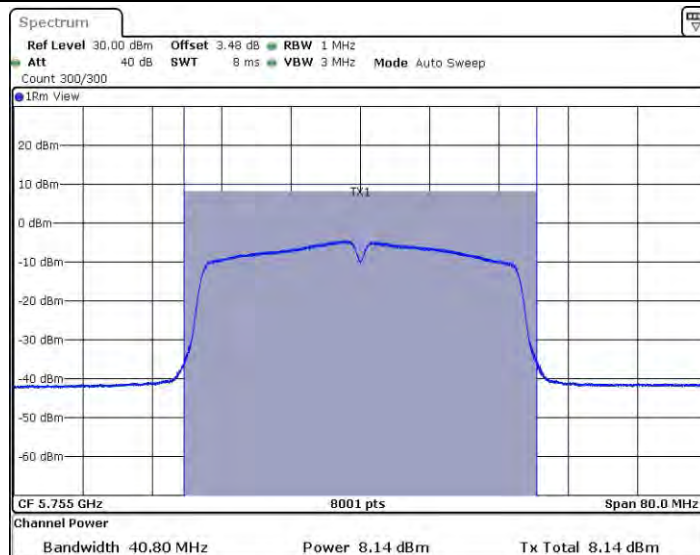
11N40MIMO_Ant2_5710_UNII-2C



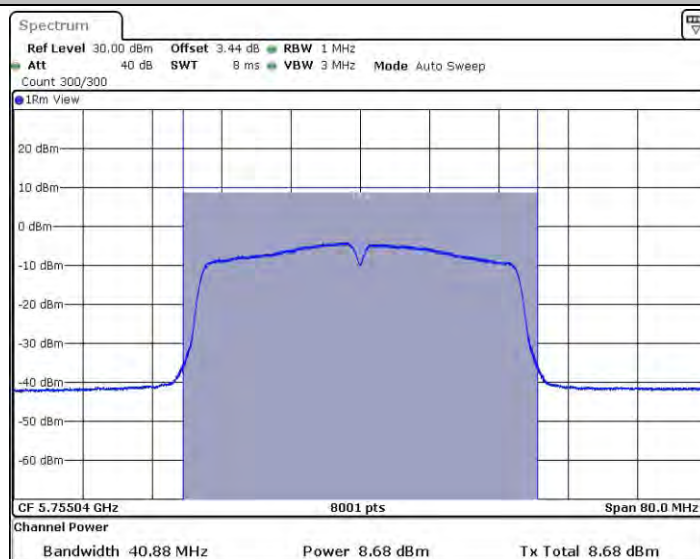
11N40MIMO_Ant1_5710_UNII-3



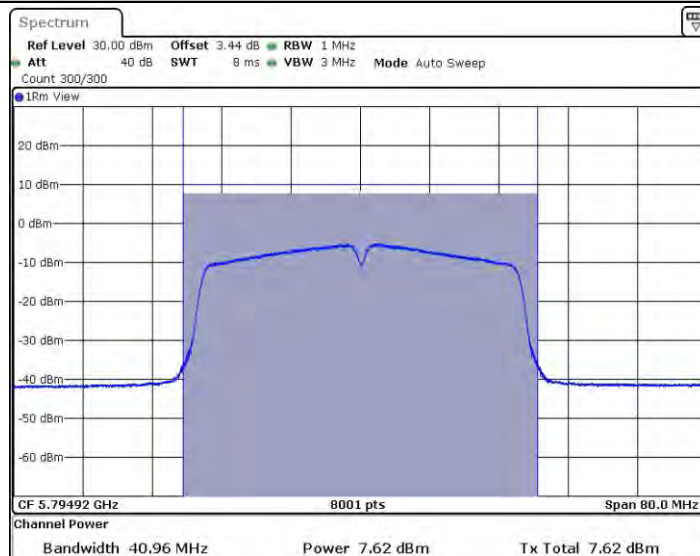
11N40MIMO_Ant2_5710_UNII-3



11N40MIMO_Ant1_5755



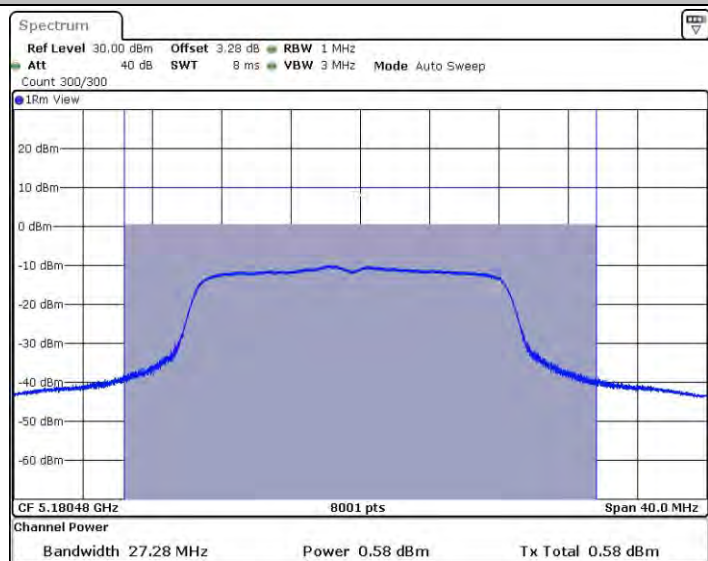
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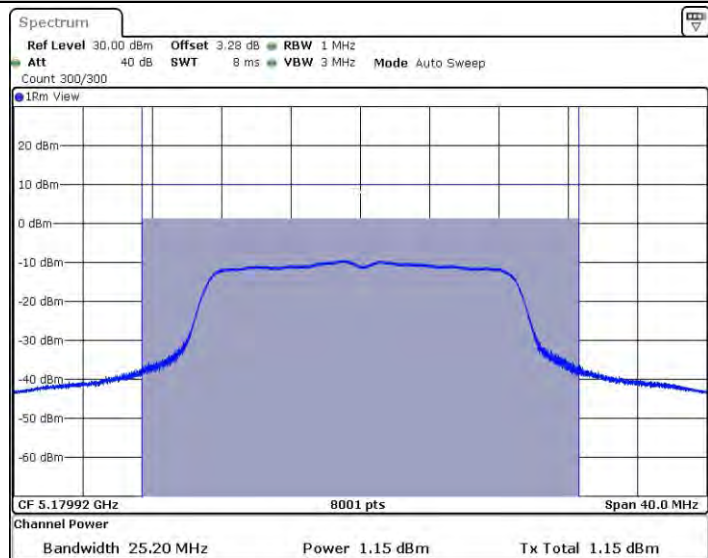
11N40MIMO_Ant1_5795



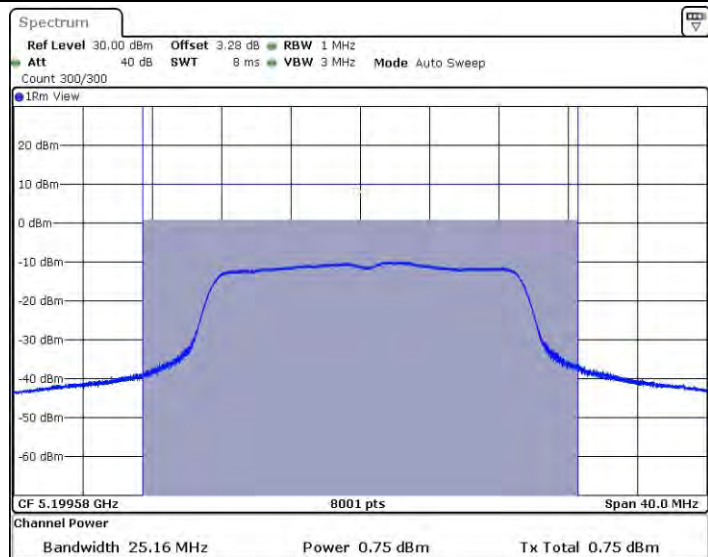
11N40MIMO_Ant2_5795



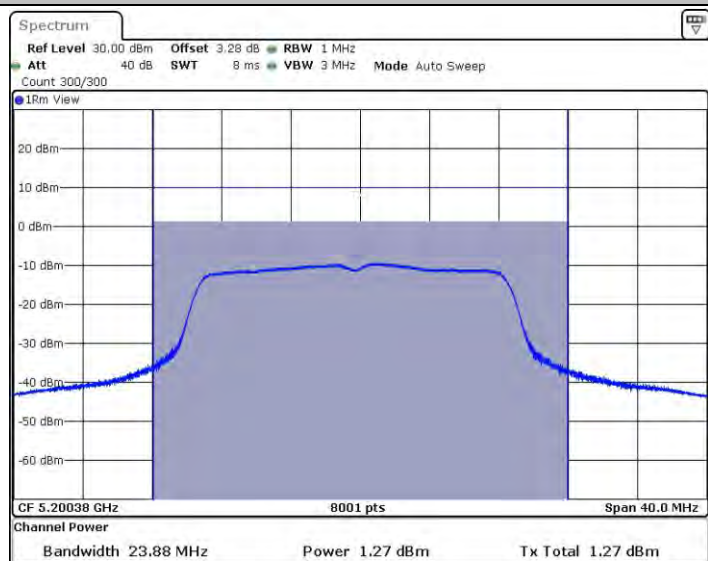
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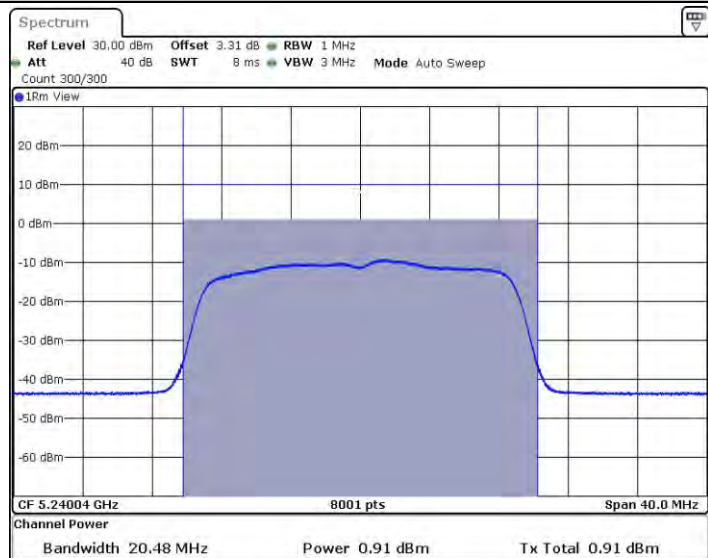
11AC20MIMO_Ant2_5180



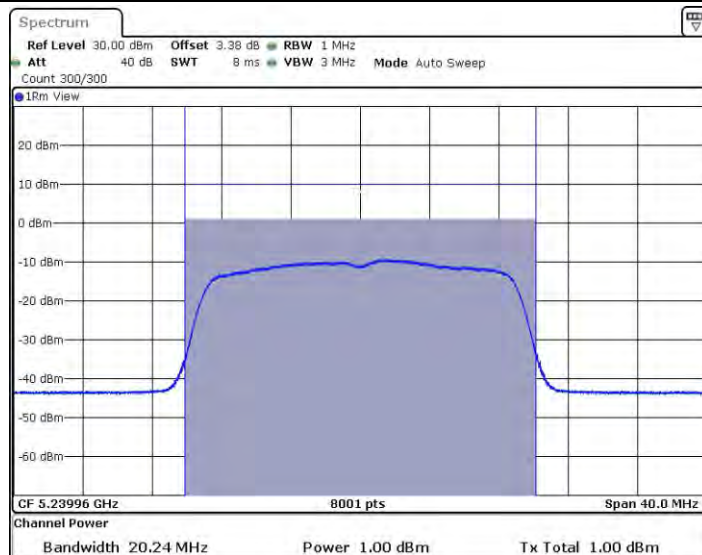
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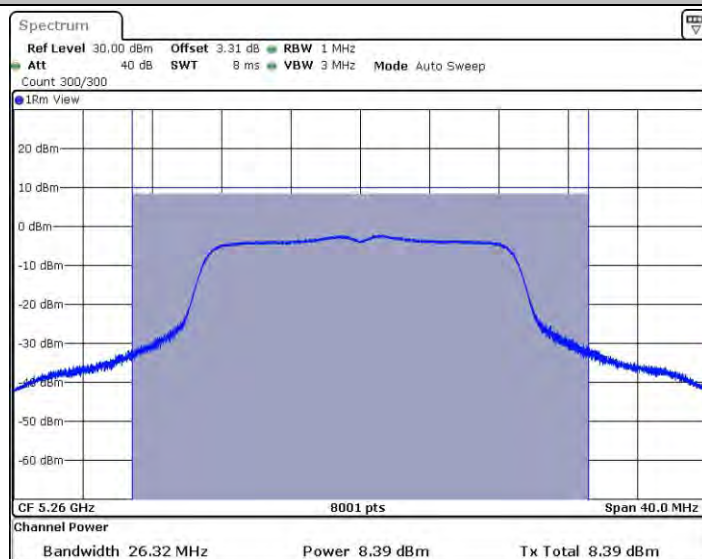
11AC20MIMO_Ant2_5200



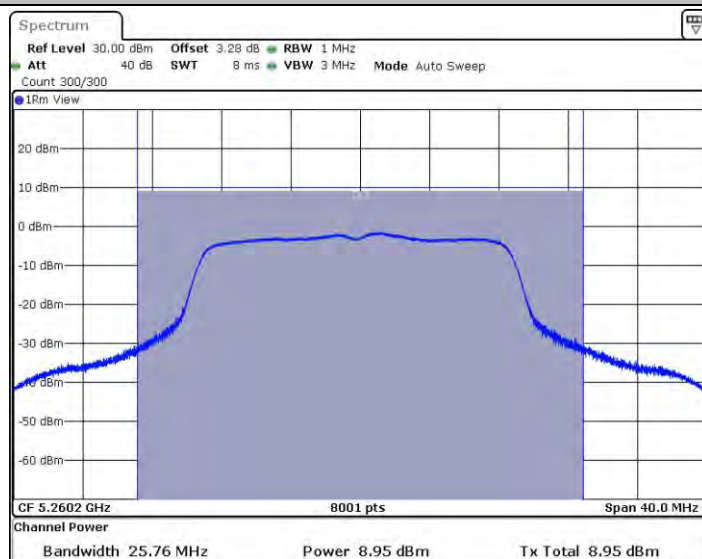
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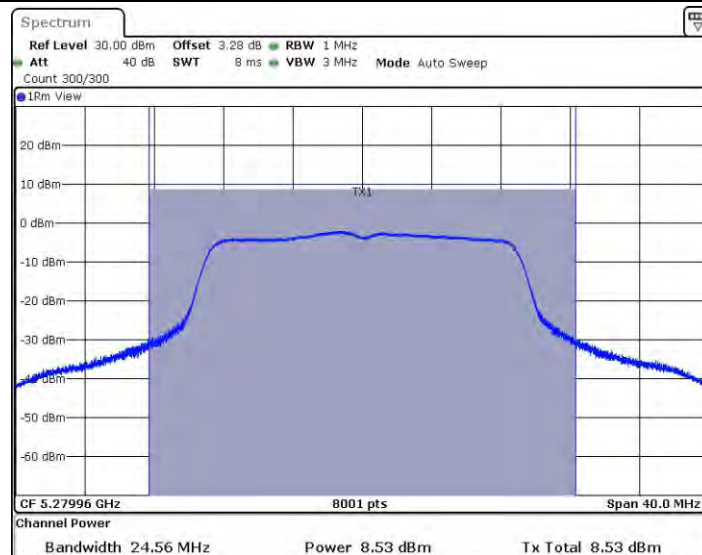
11AC20MIMO_Ant2_5240



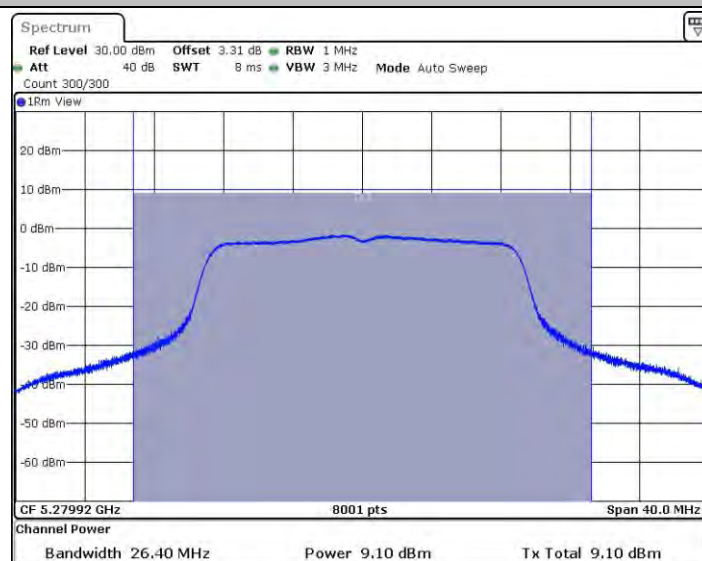
11AC20MIMO_Ant1_5260



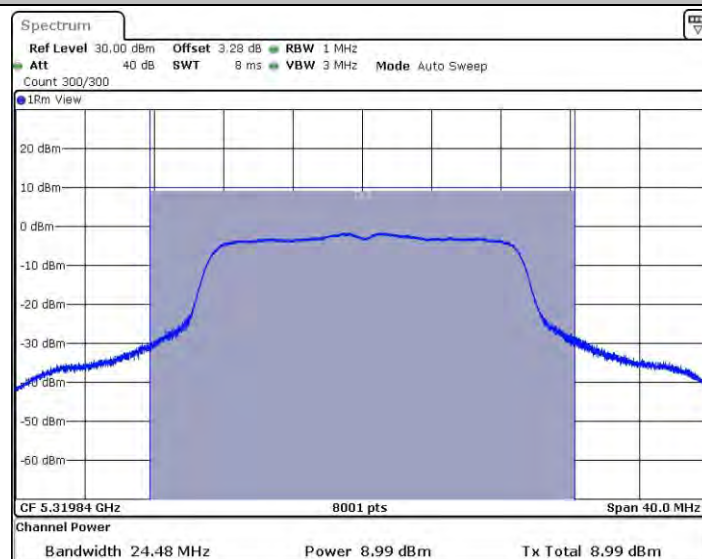
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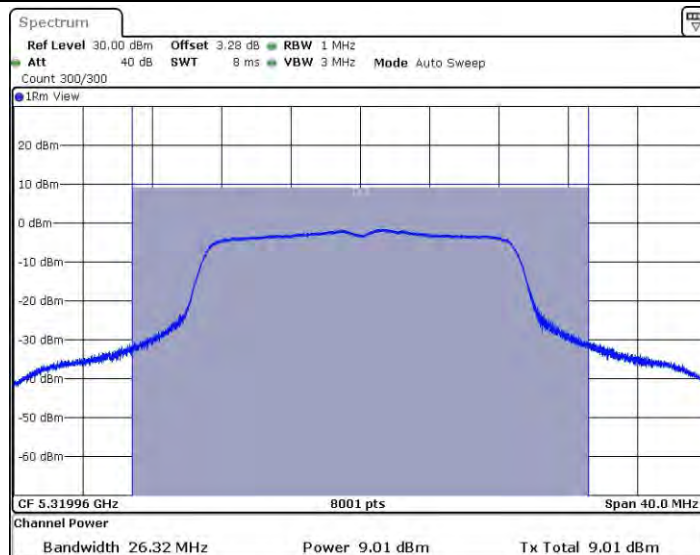
11AC20MIMO_Ant1_5280



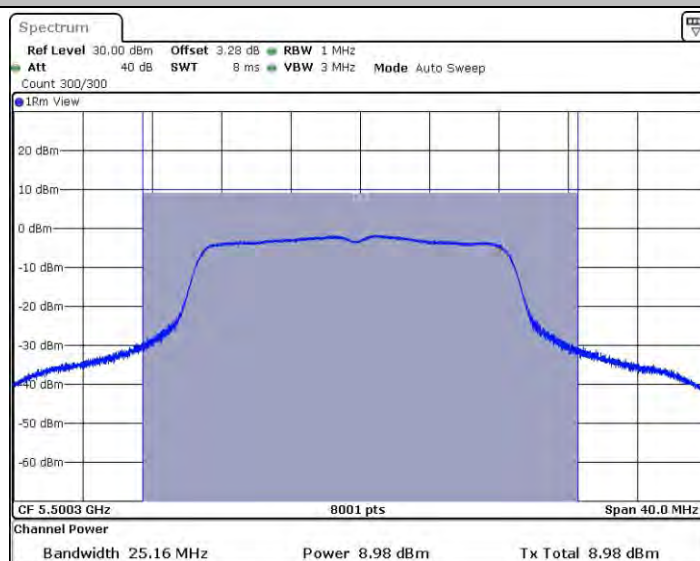
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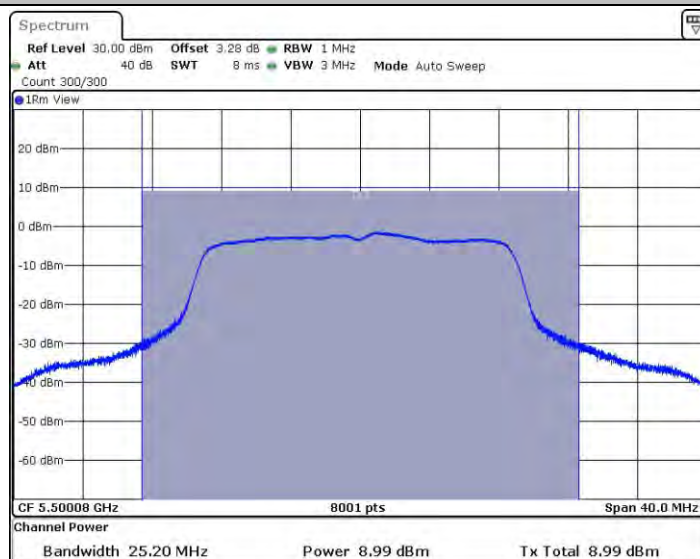
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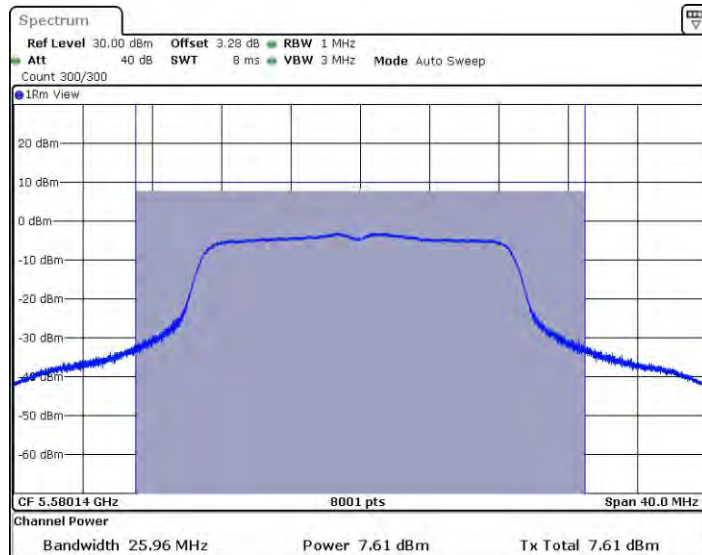
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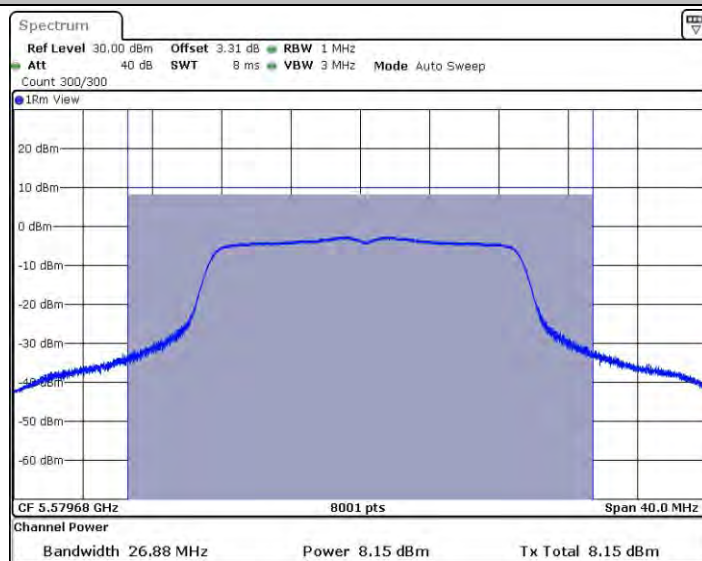
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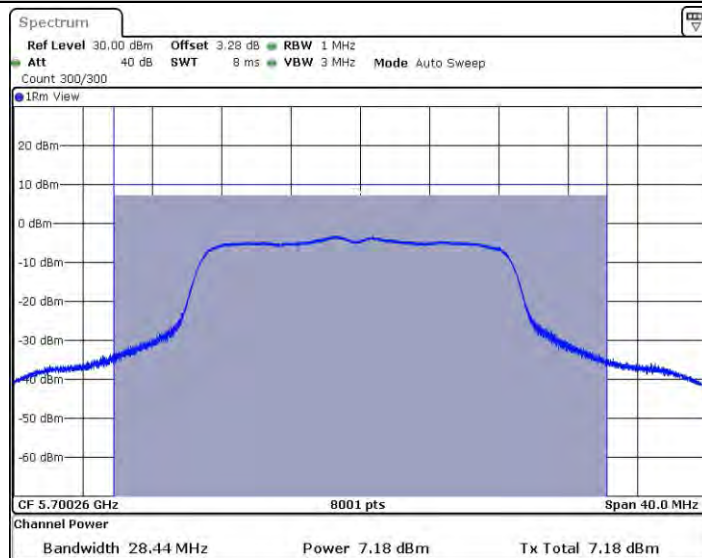
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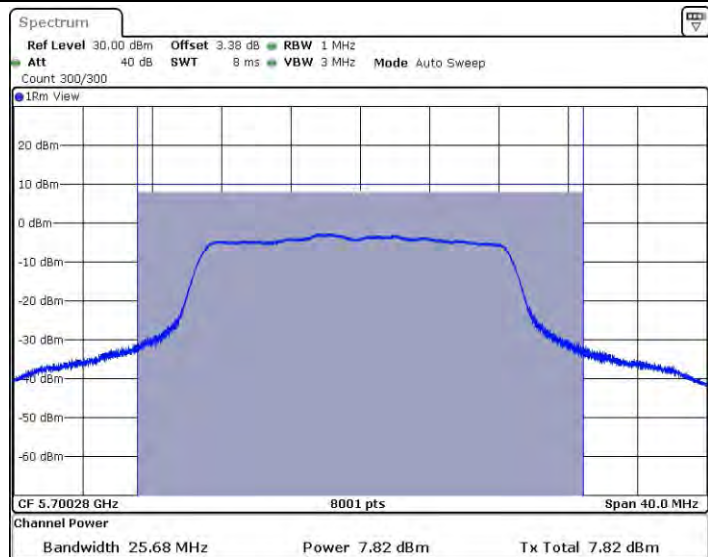
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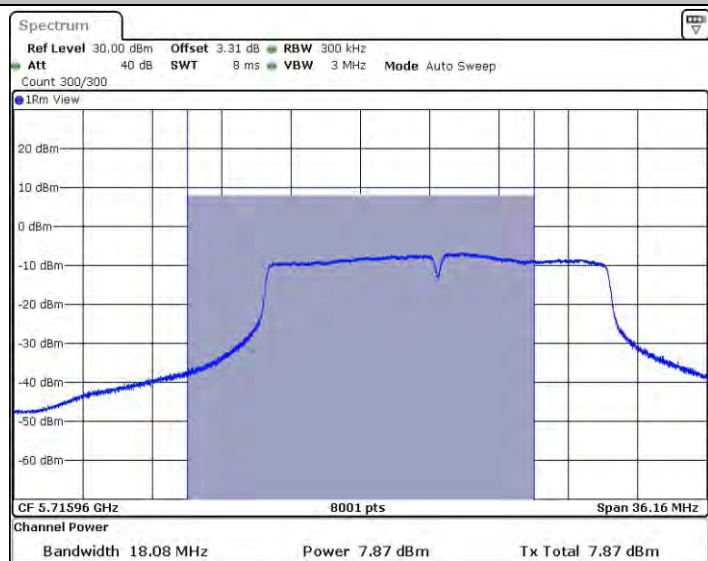
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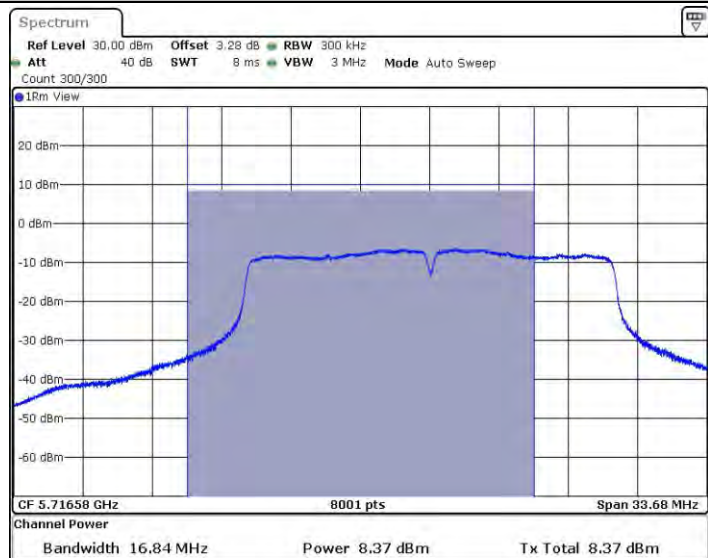
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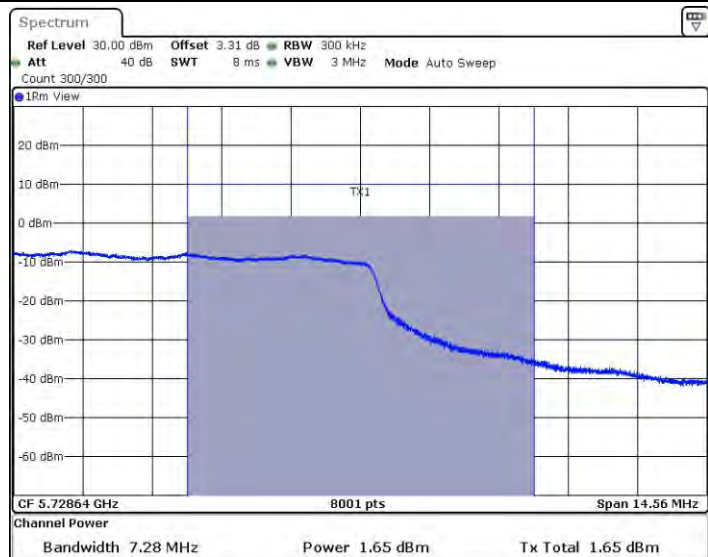
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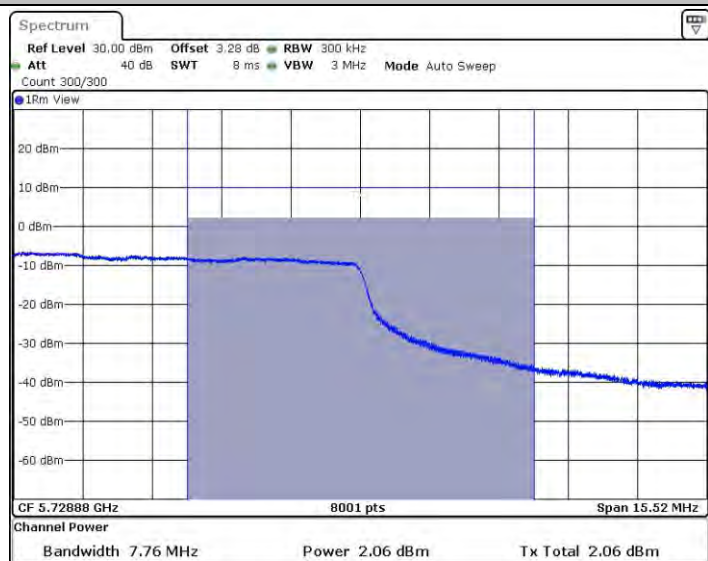
11AC20MIMO_Ant1_5720_UNII-2C



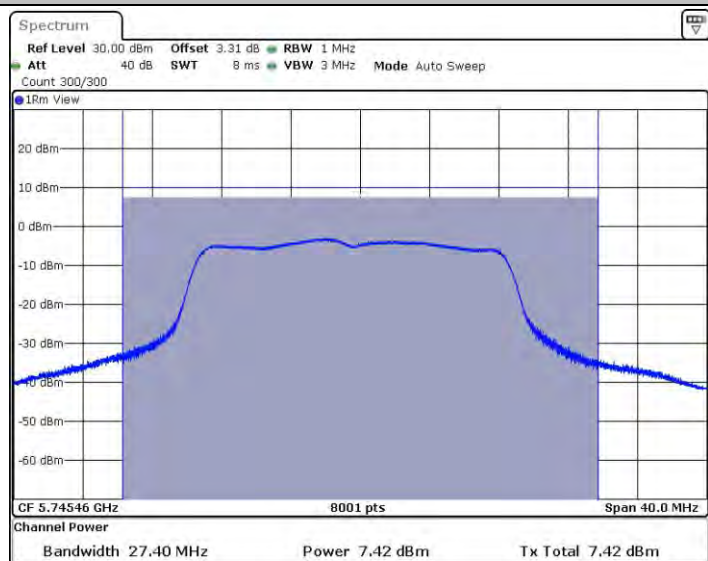
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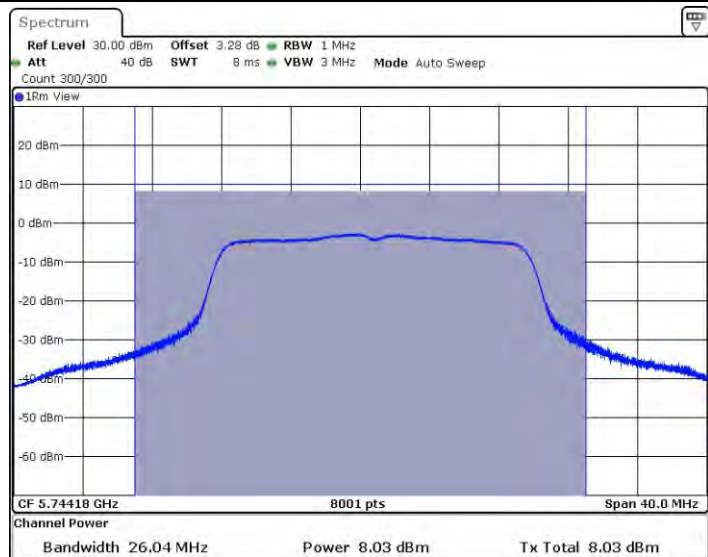
11AC20MIMO_Ant1_5720_UNII-3



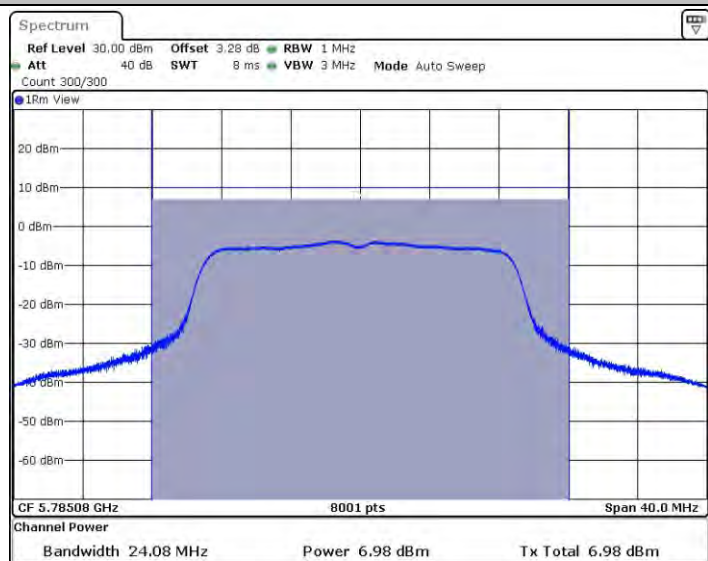
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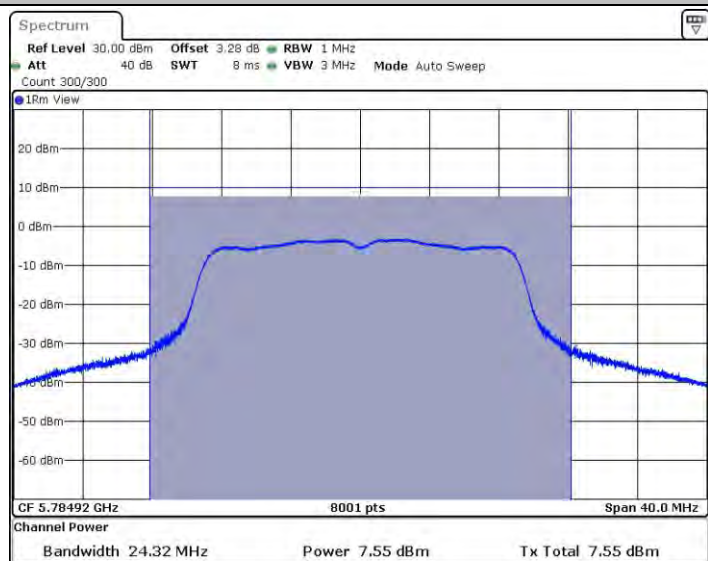
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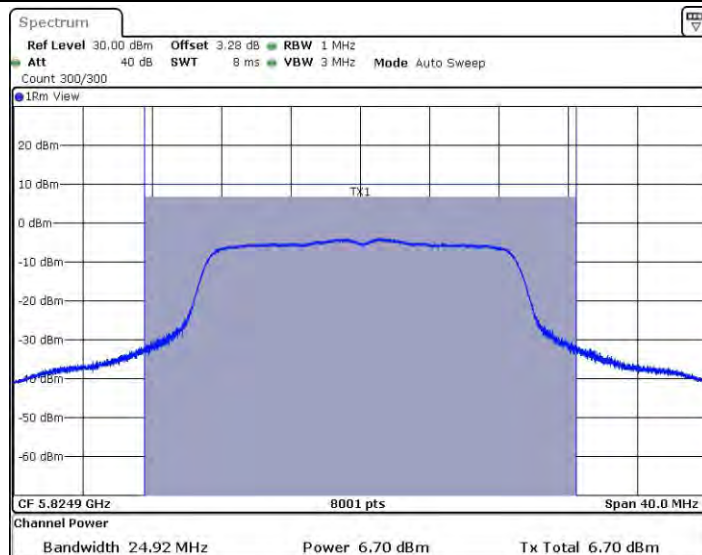
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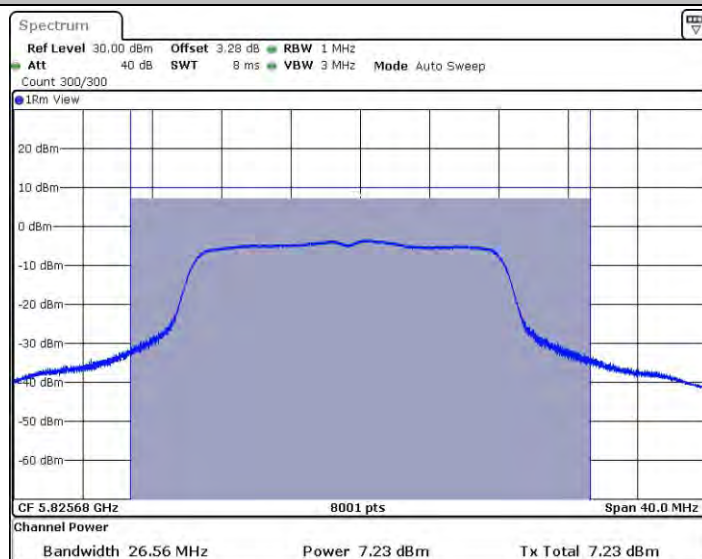
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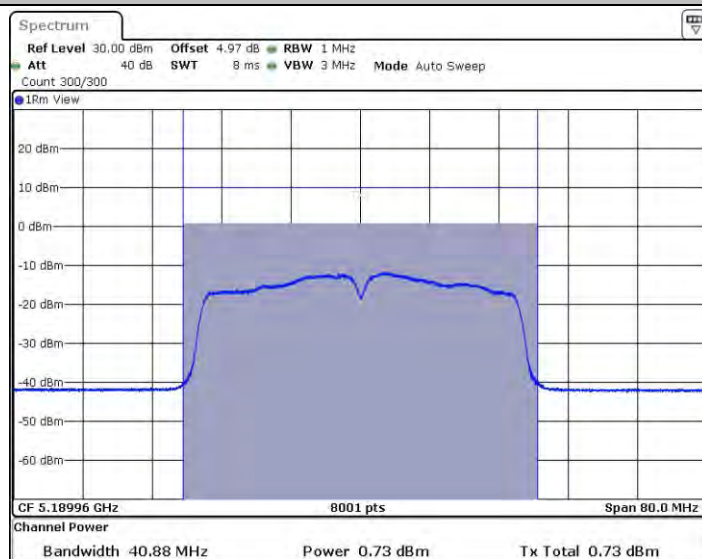
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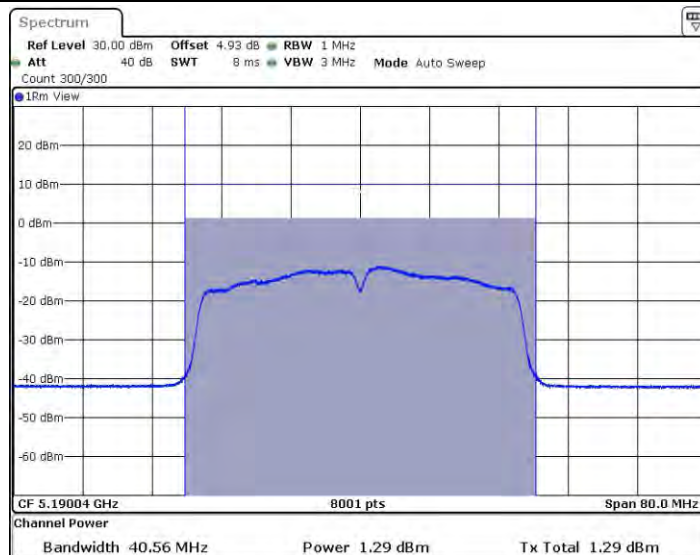
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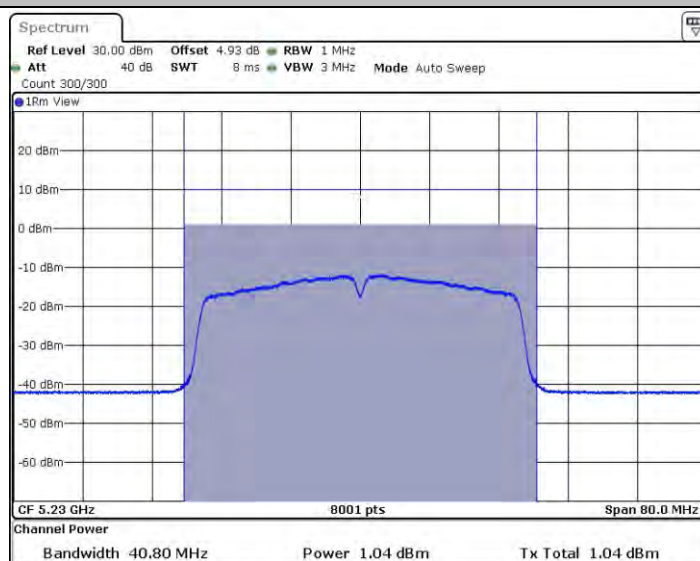
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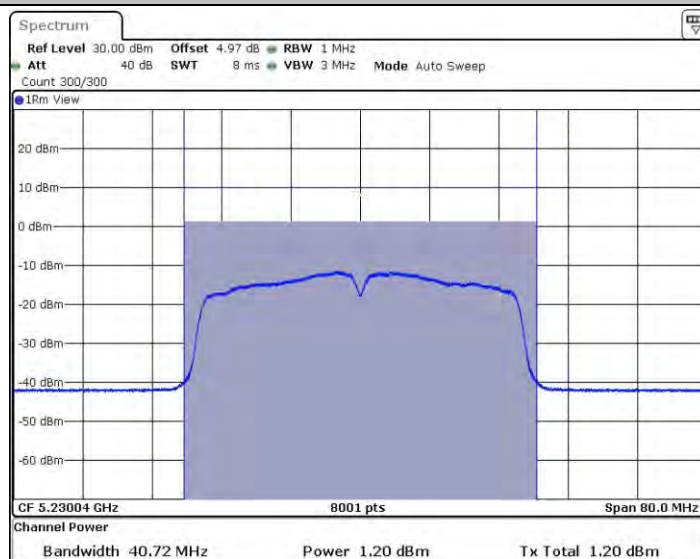
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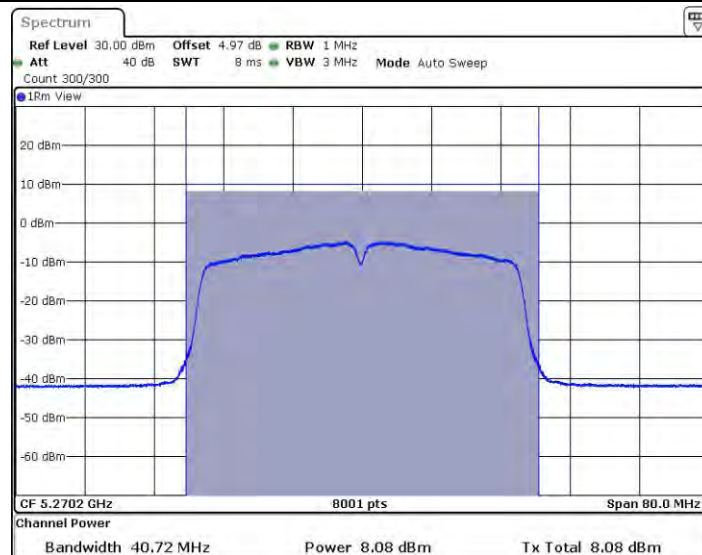
11AC40MIMO_Ant2_5190



11AC40MIMO_Ant1_5230



11AC40MIMO_Ant2_5230



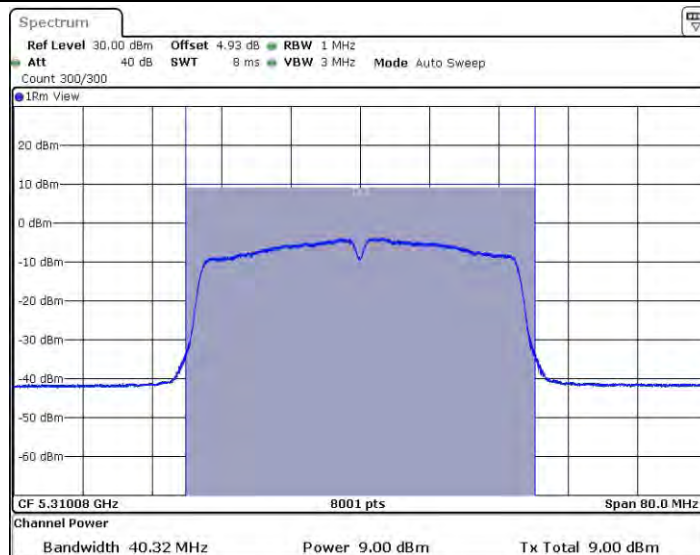
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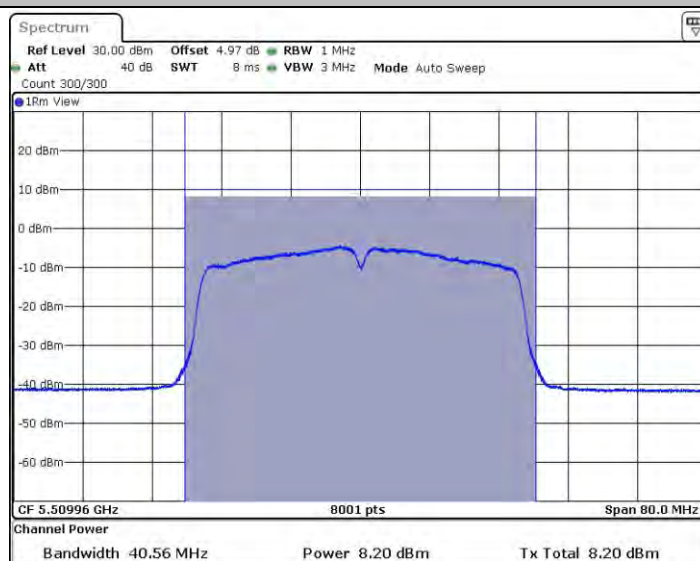
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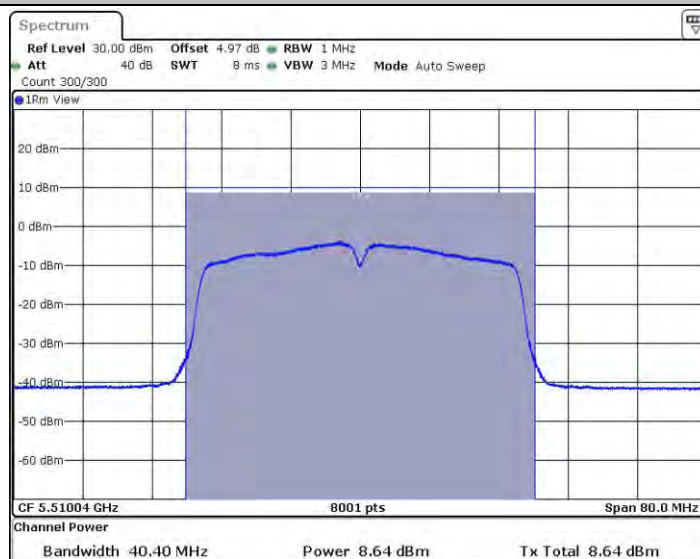
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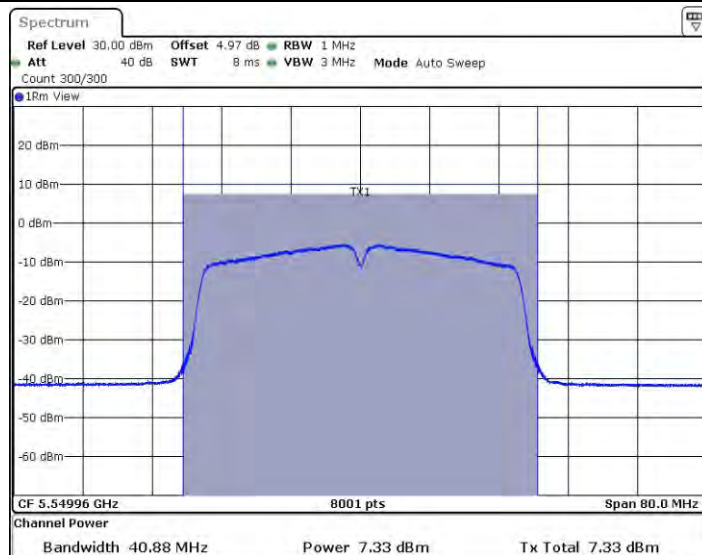
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11AC40MIMO_Ant1_5510



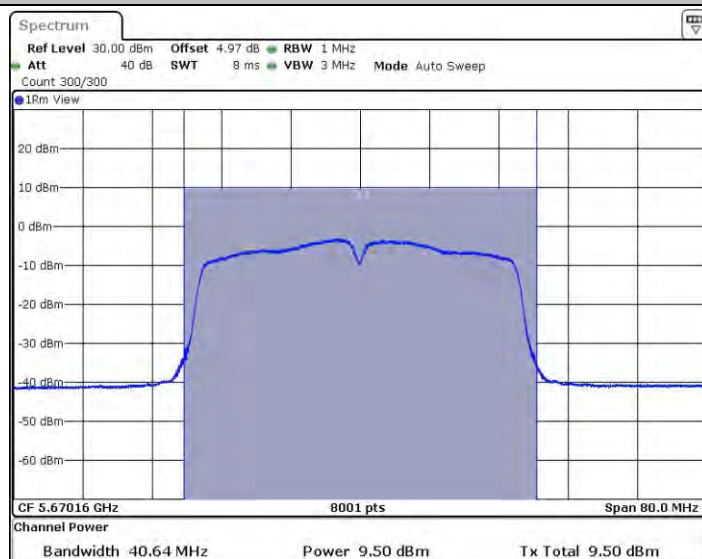
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11AC40MIMO_Ant1_5550



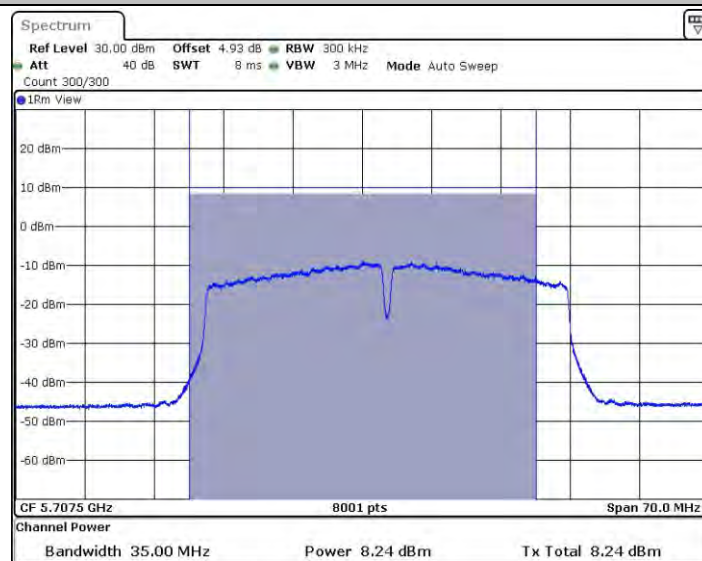
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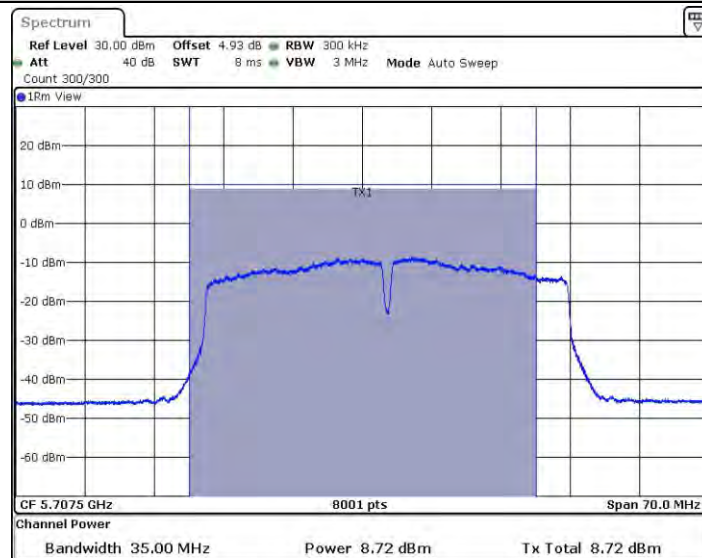
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11AC40MIMO_Ant2_5670



11AC40MIMO_Ant1_5710_UNII-2C



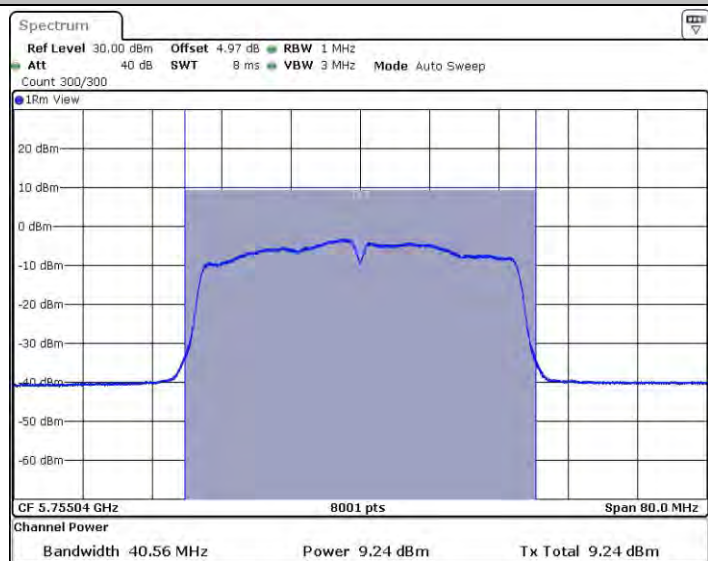
11AC40MIMO_Ant2_5710_UNII-2C



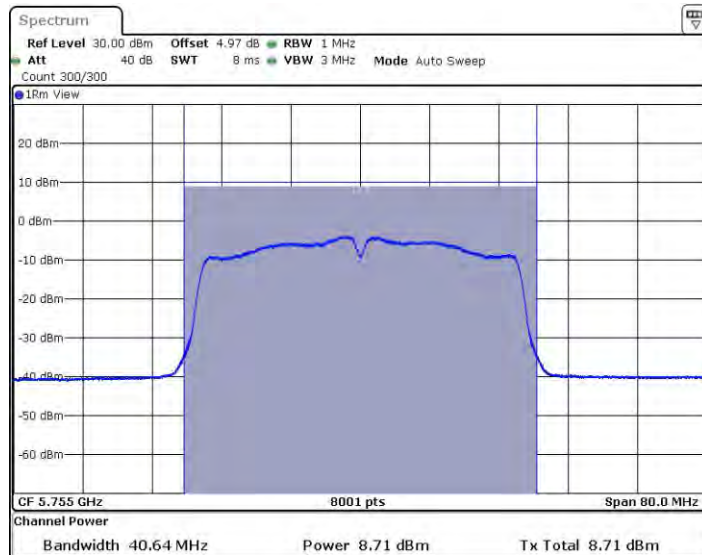
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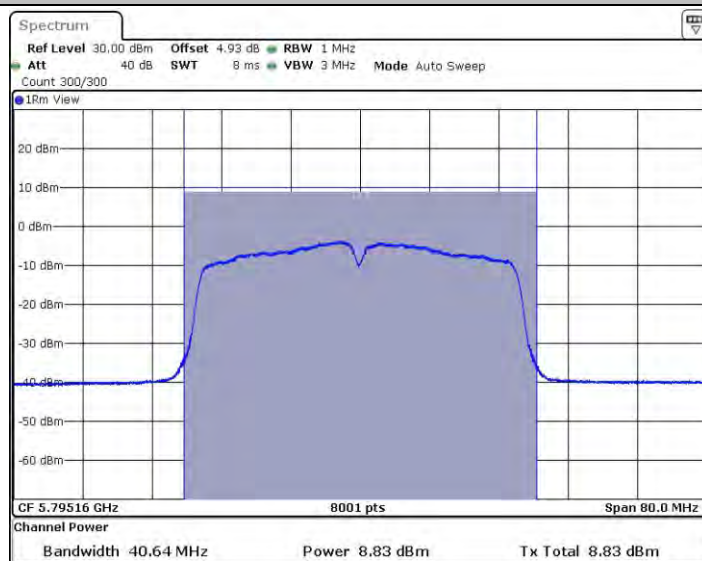
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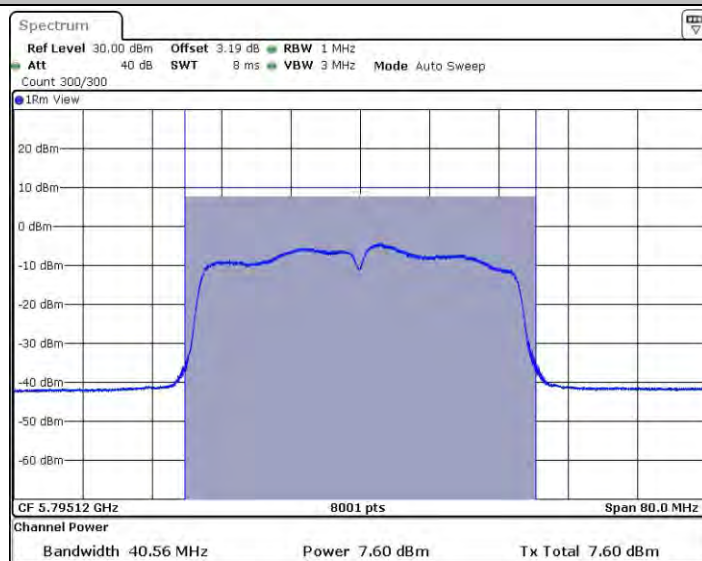
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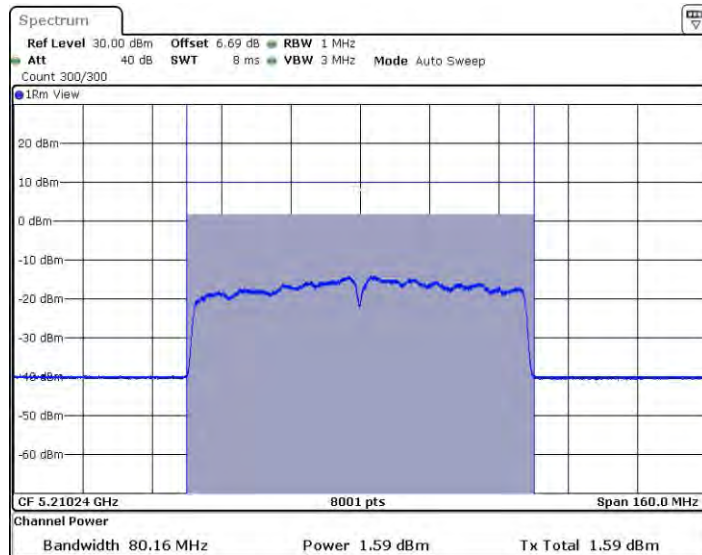
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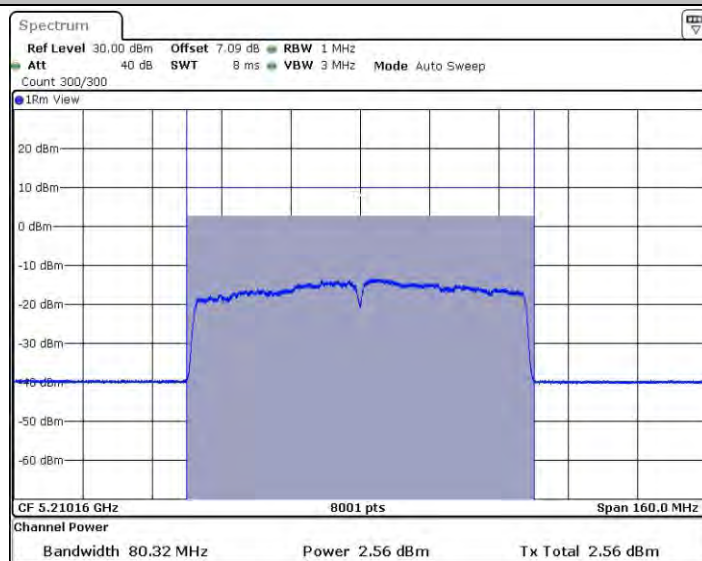
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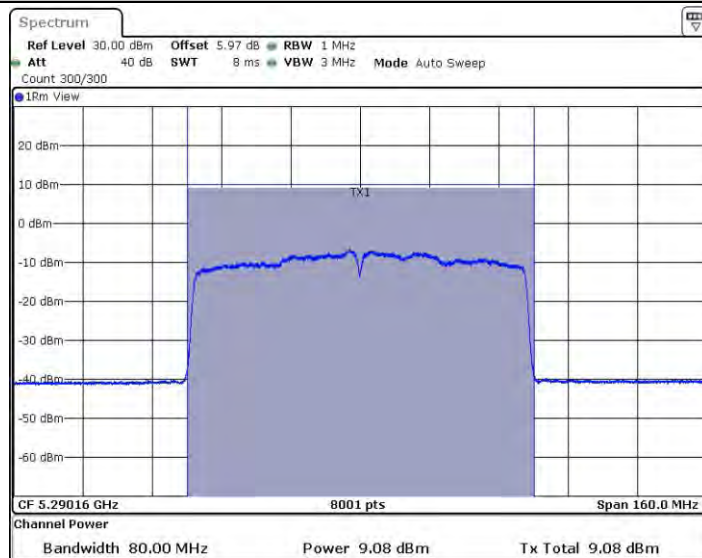
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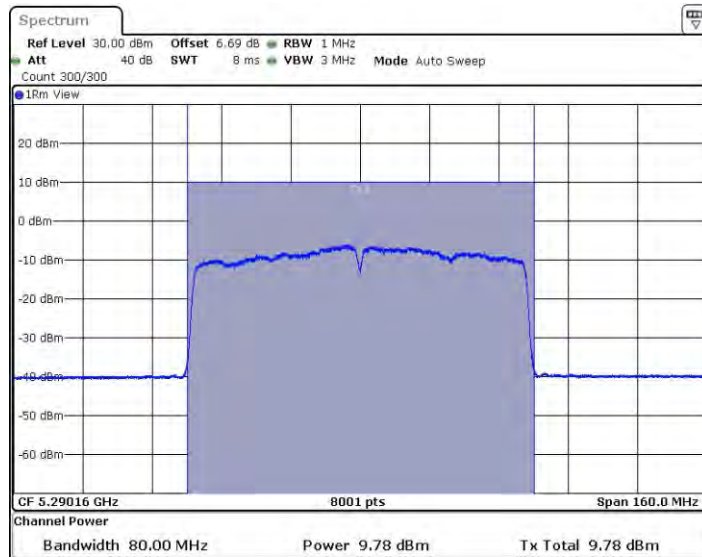
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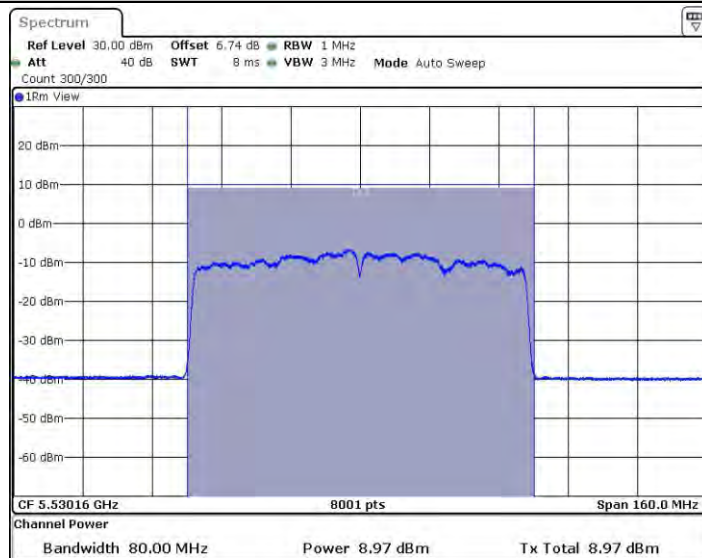
11AC80MIMO_Ant2_5210



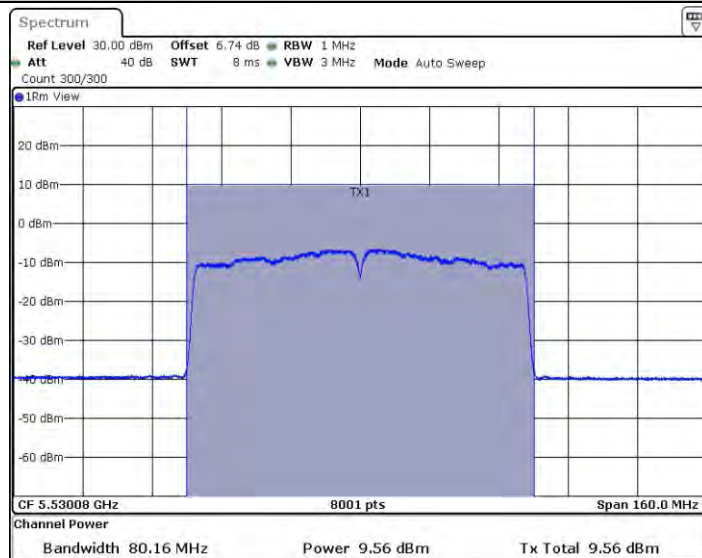
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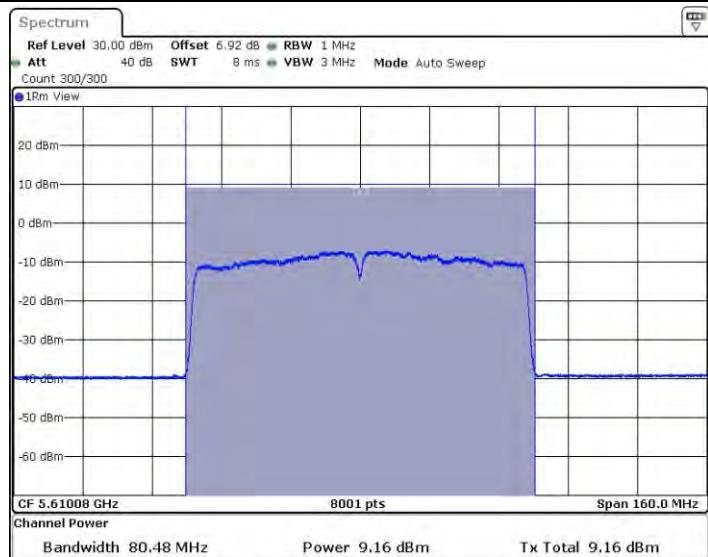
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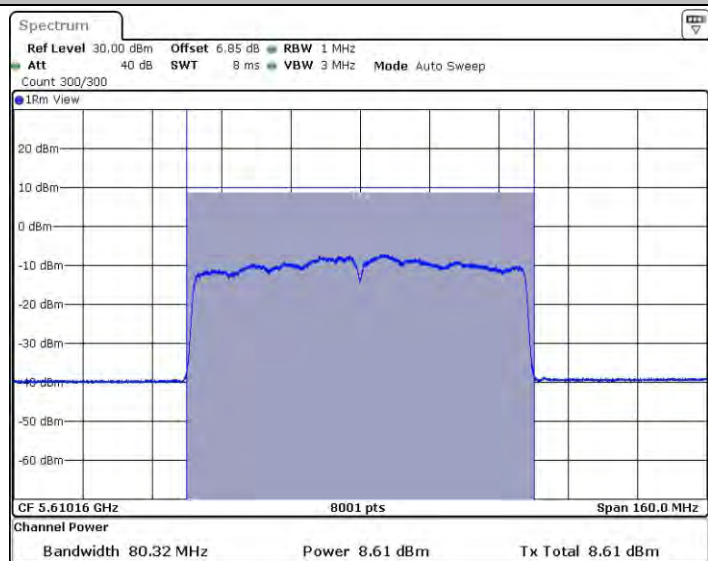
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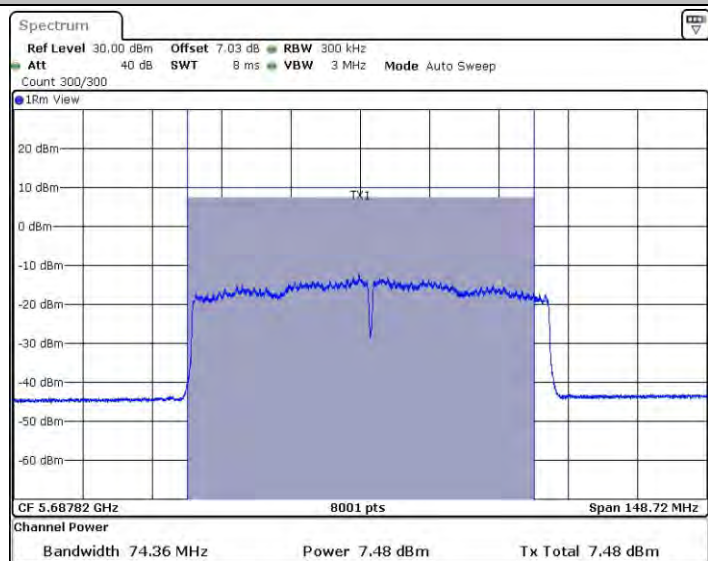
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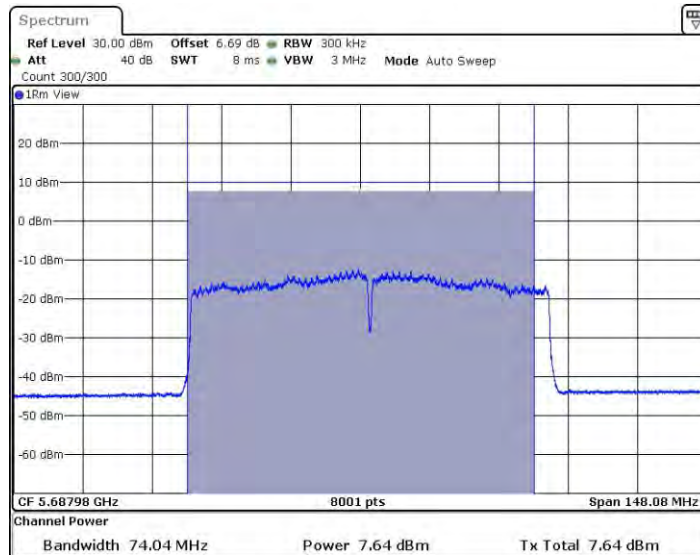
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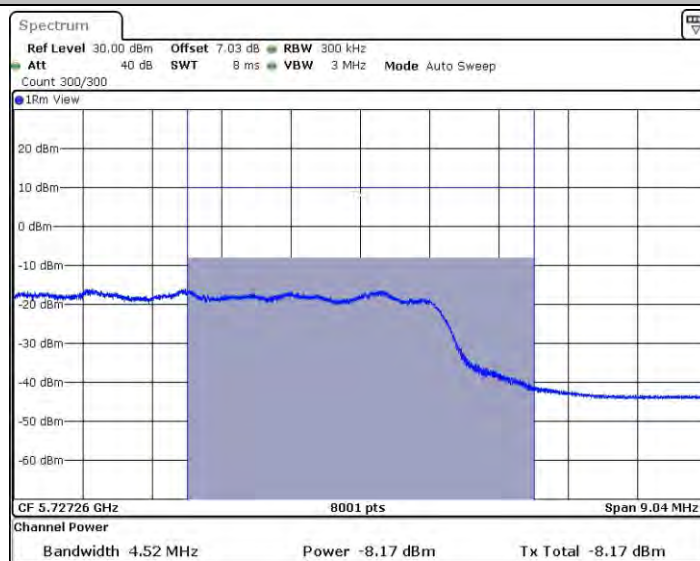
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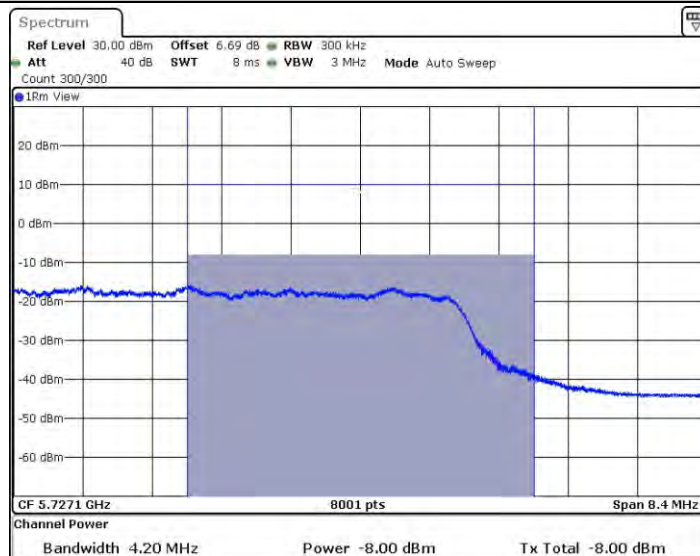
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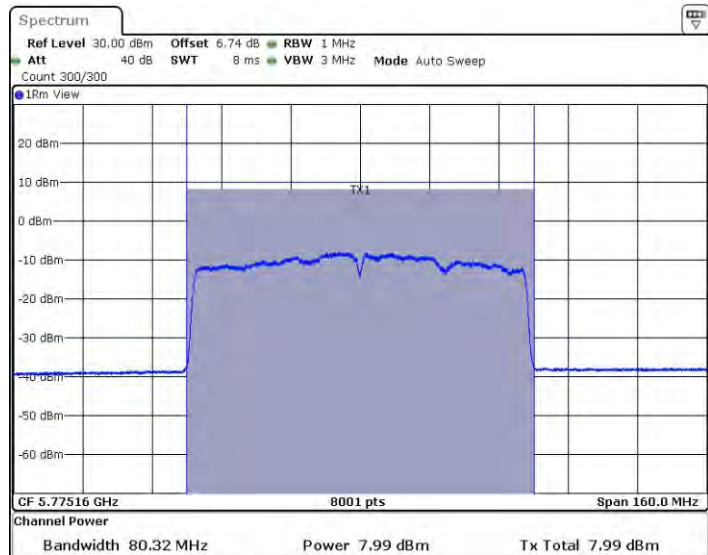
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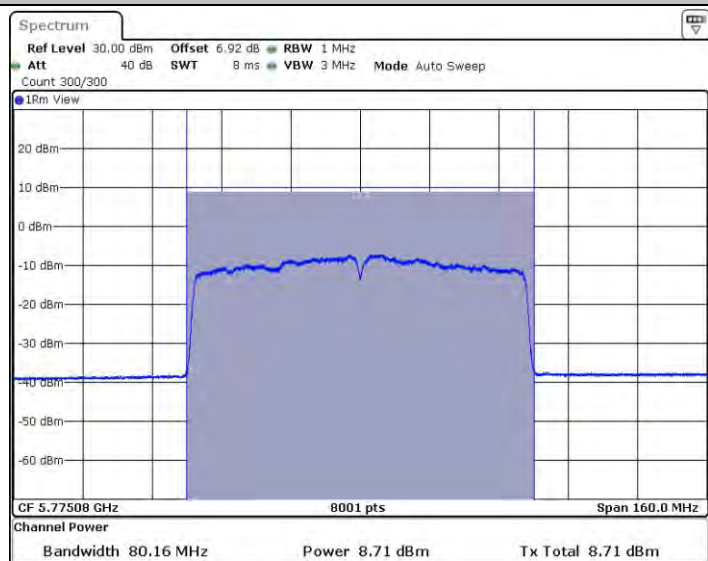
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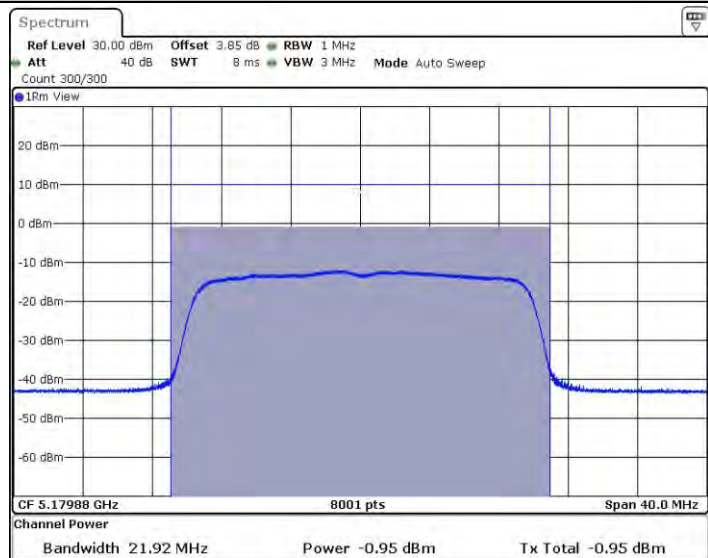
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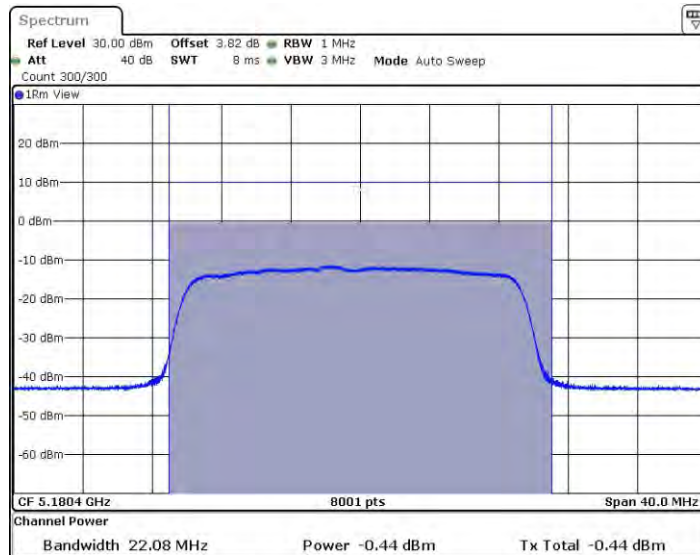
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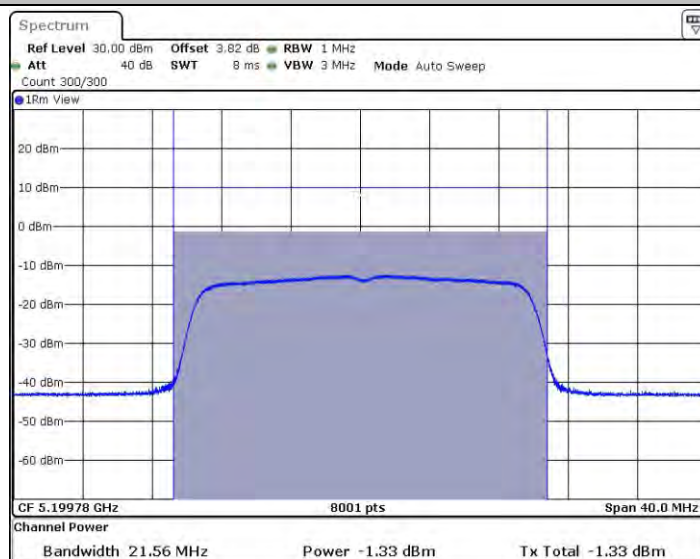
11AC80MIMO_Ant2_5775



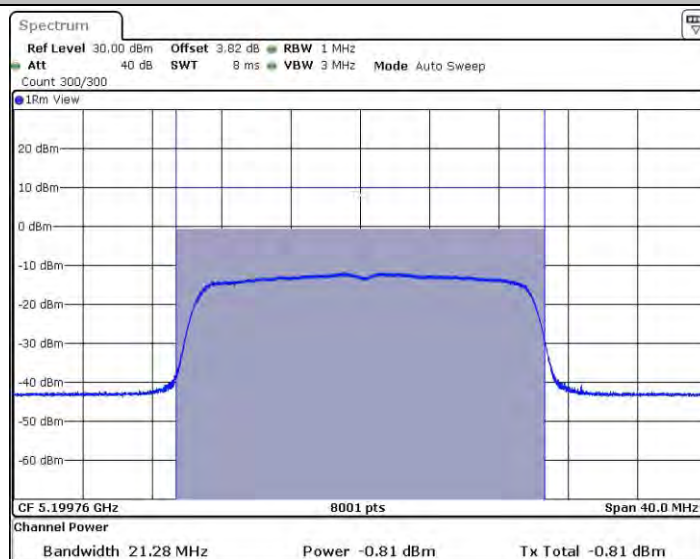
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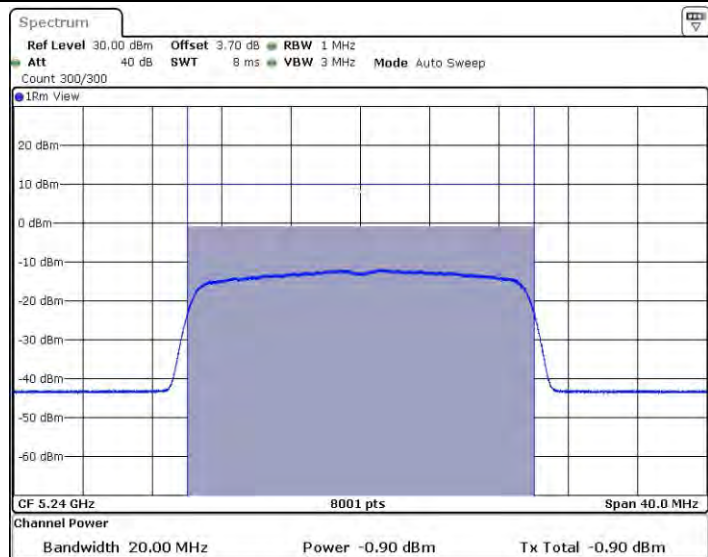
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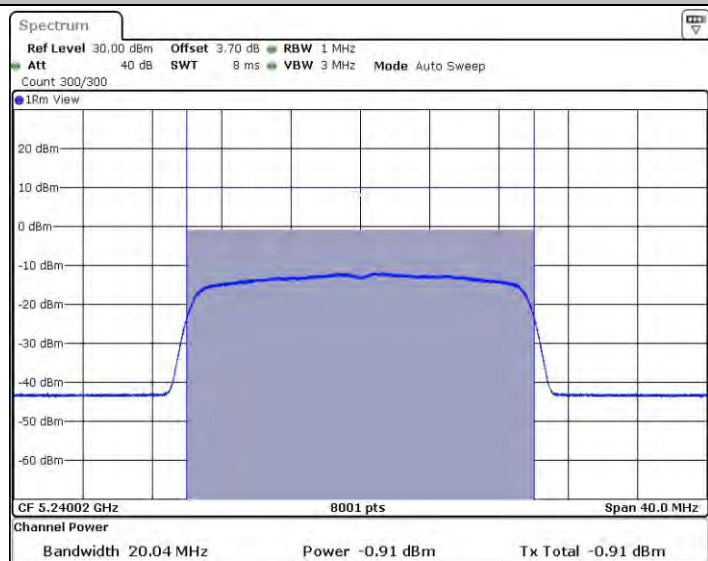
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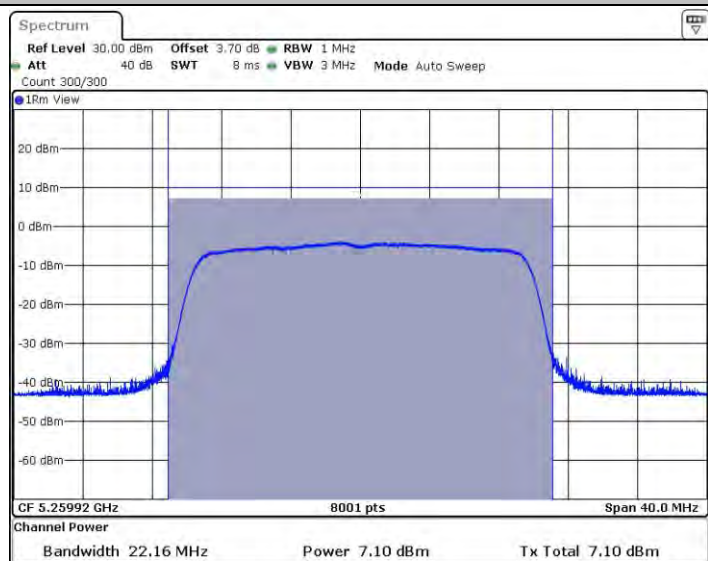
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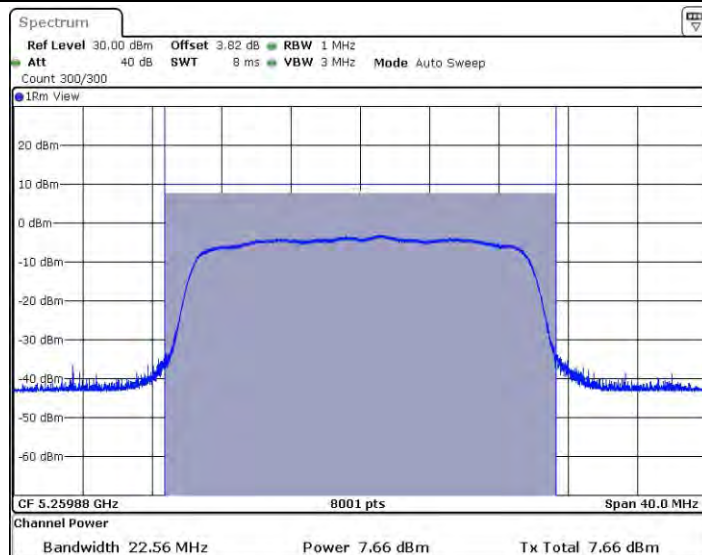
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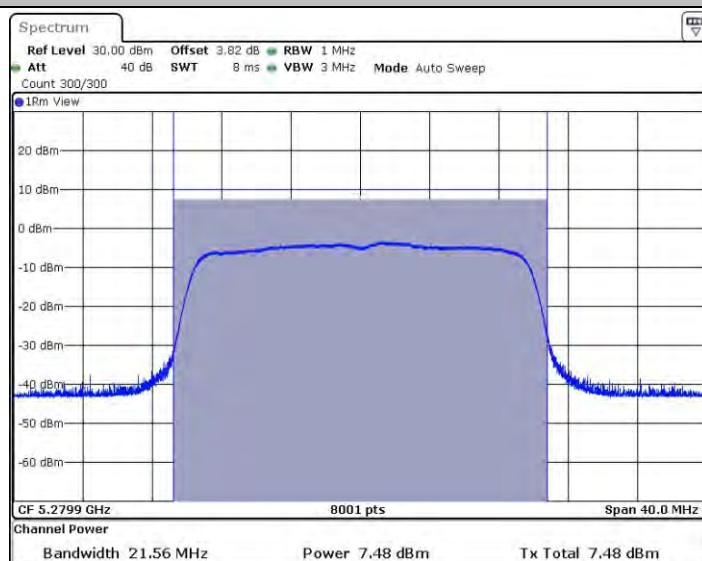
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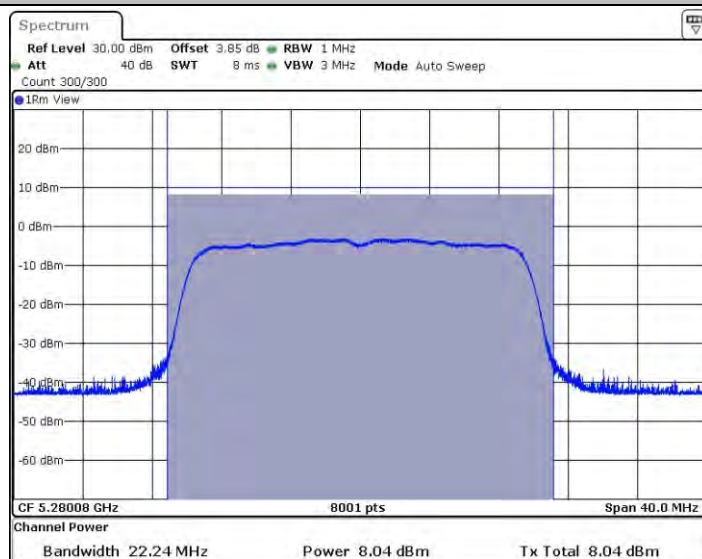
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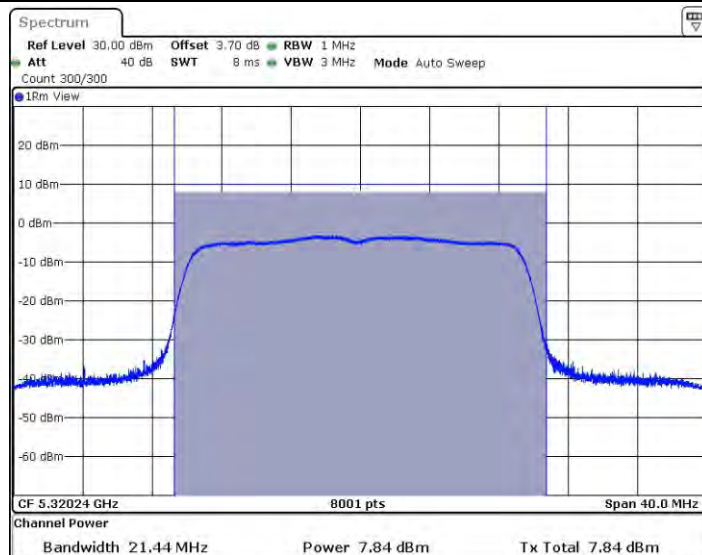
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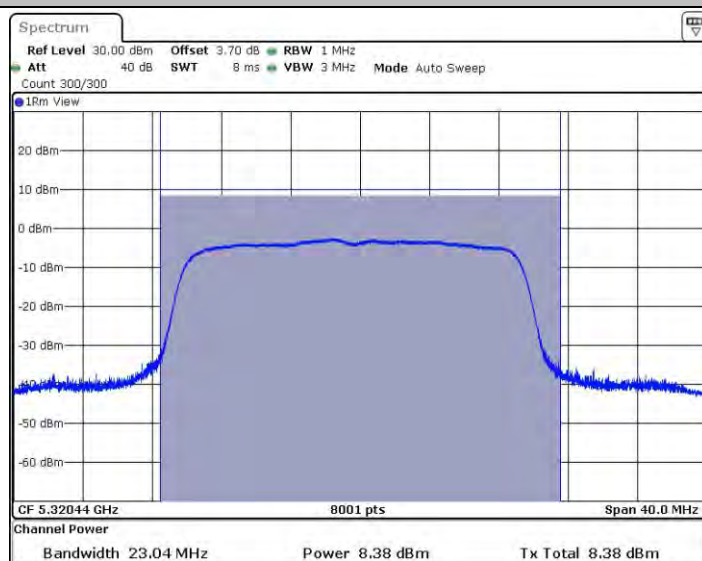
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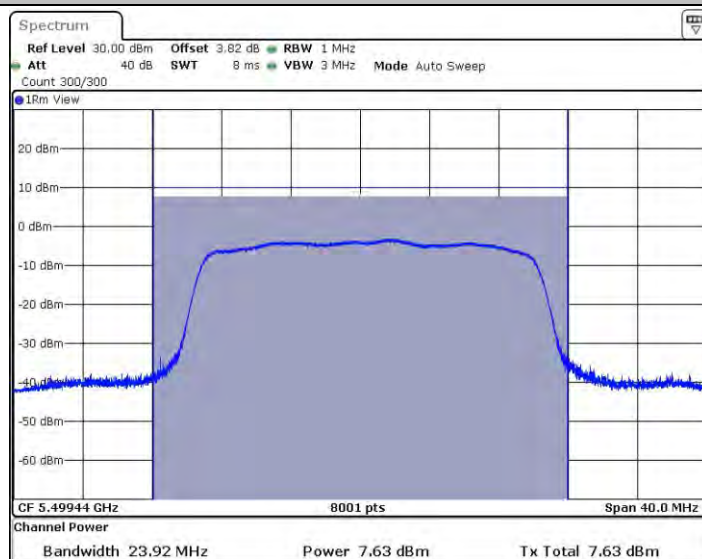
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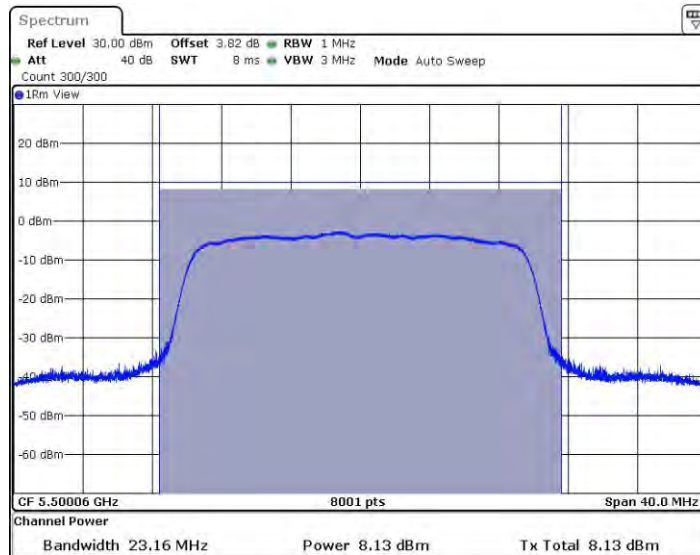
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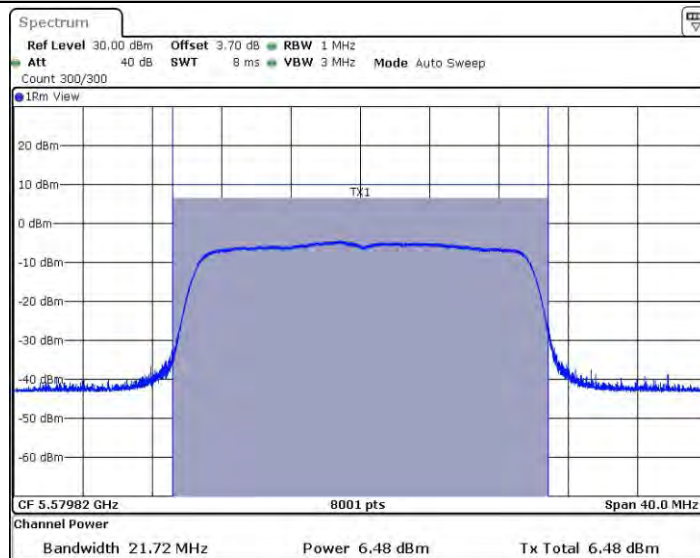
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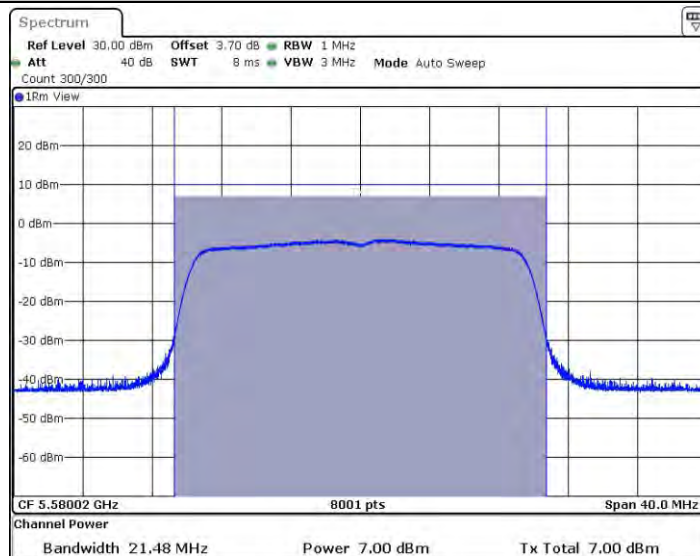
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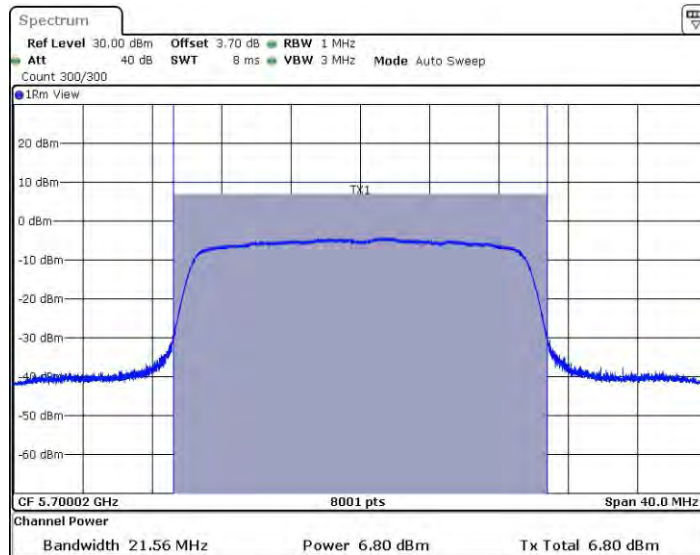
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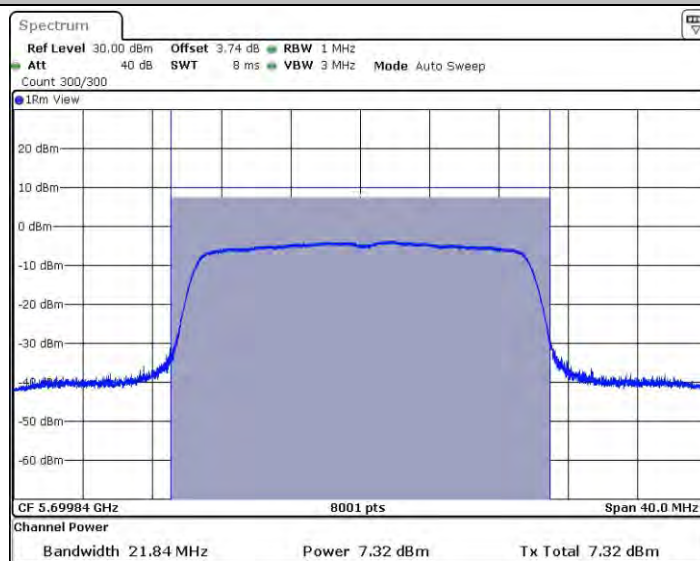
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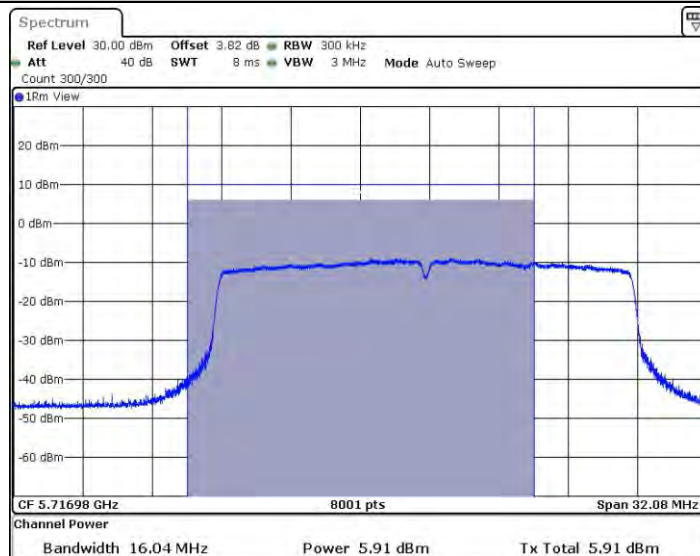
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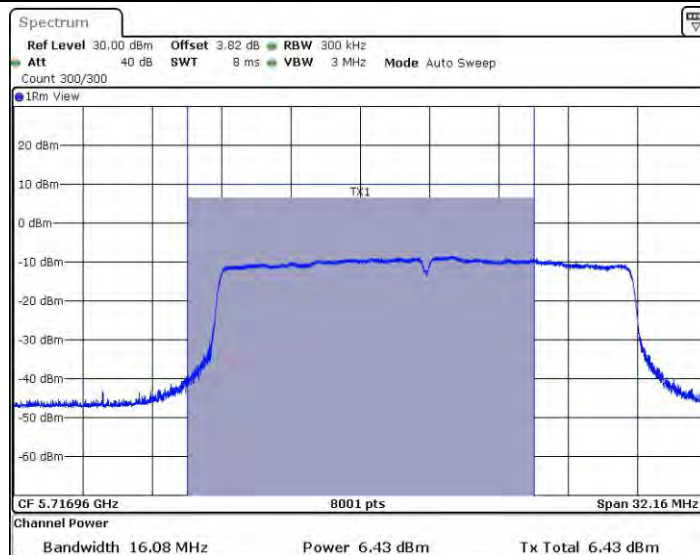
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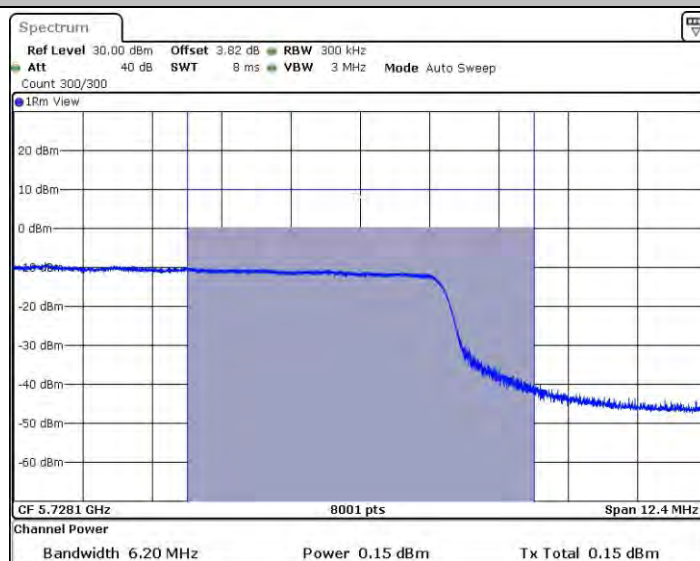
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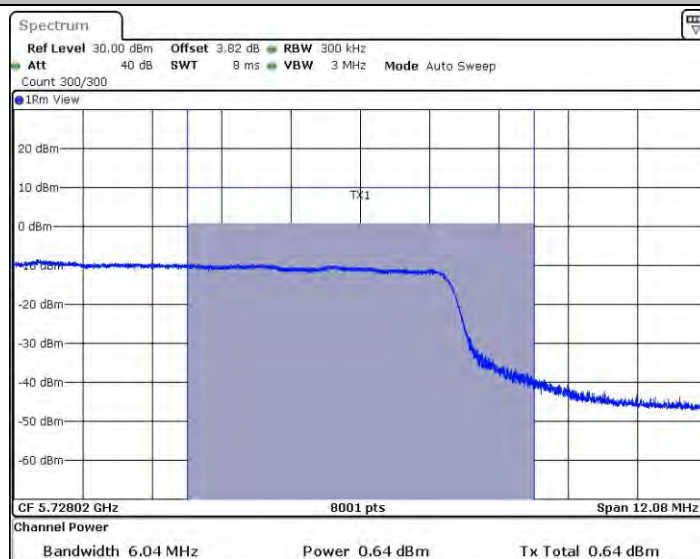
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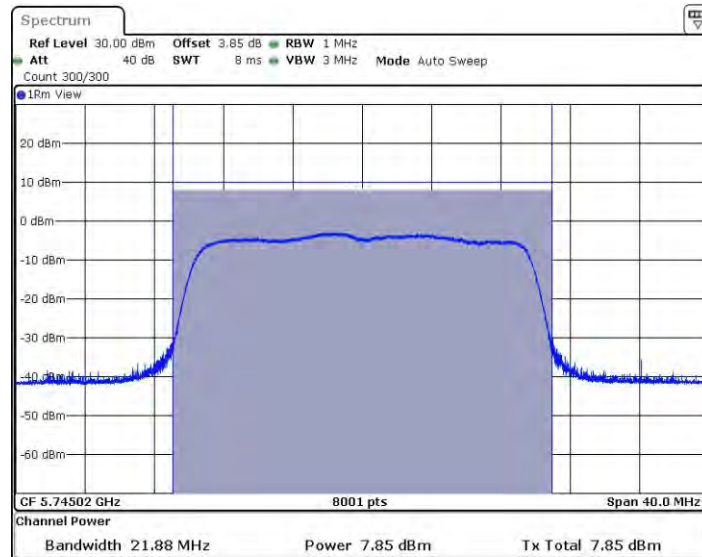
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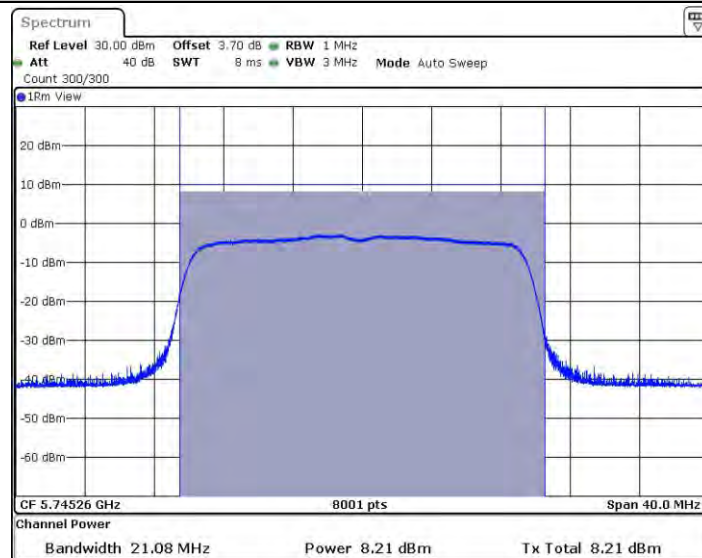
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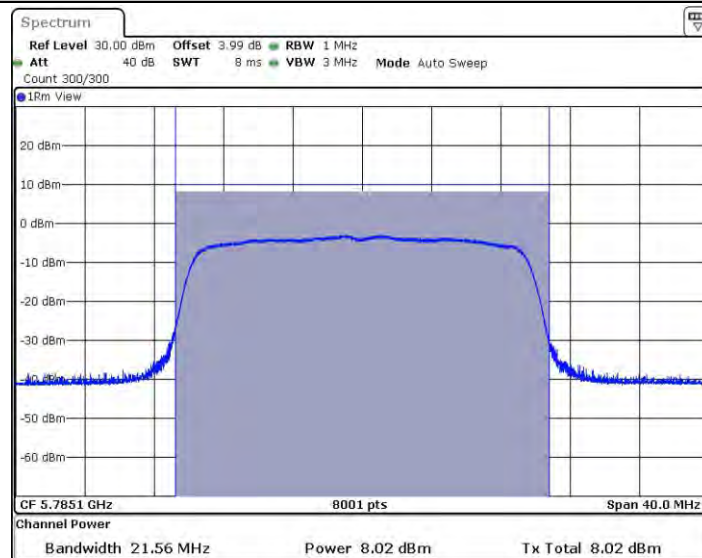
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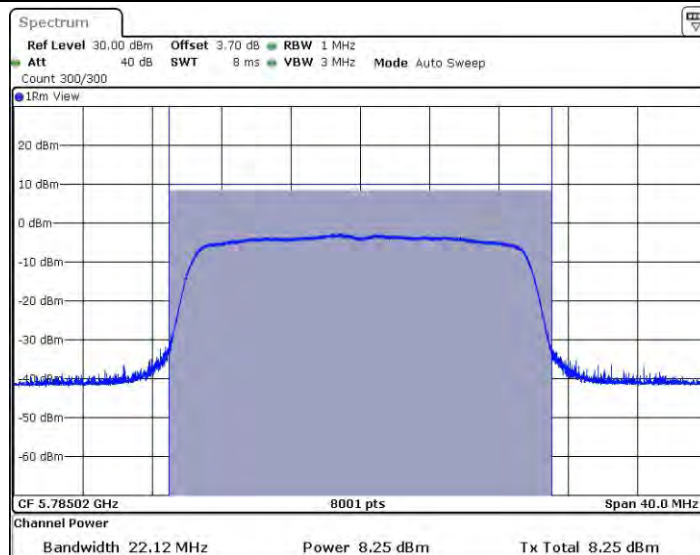
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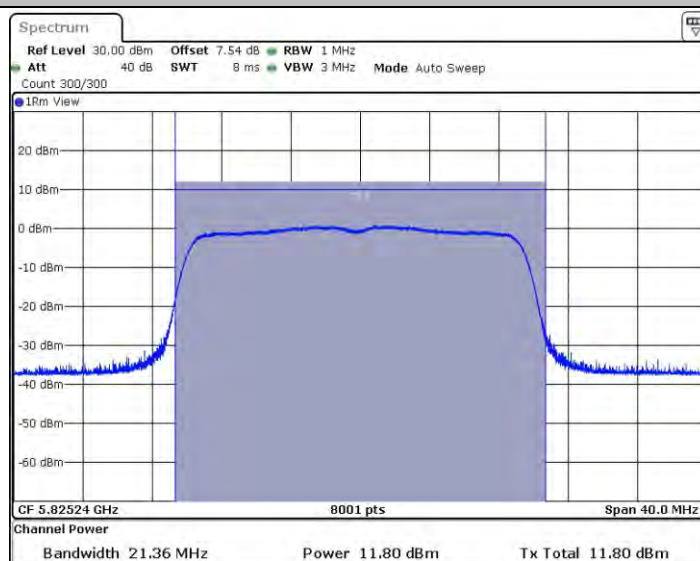
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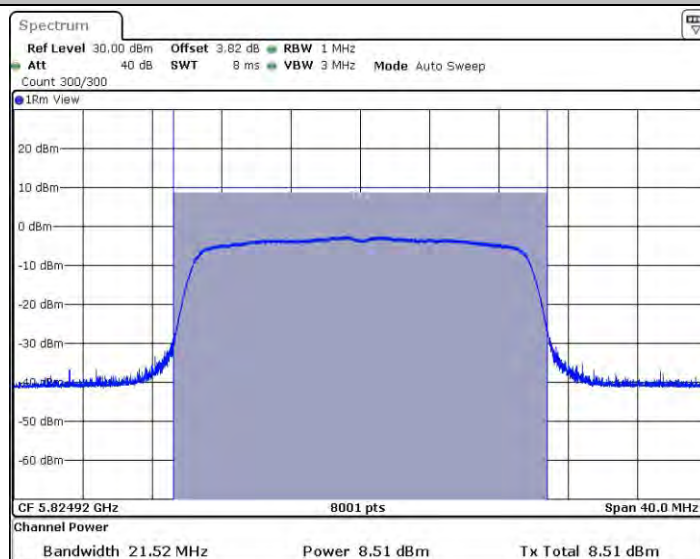
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11AX20MIMO_Ant2_5785



11AX20MIMO_Ant1_5825



11AX20MIMO_Ant2_5825