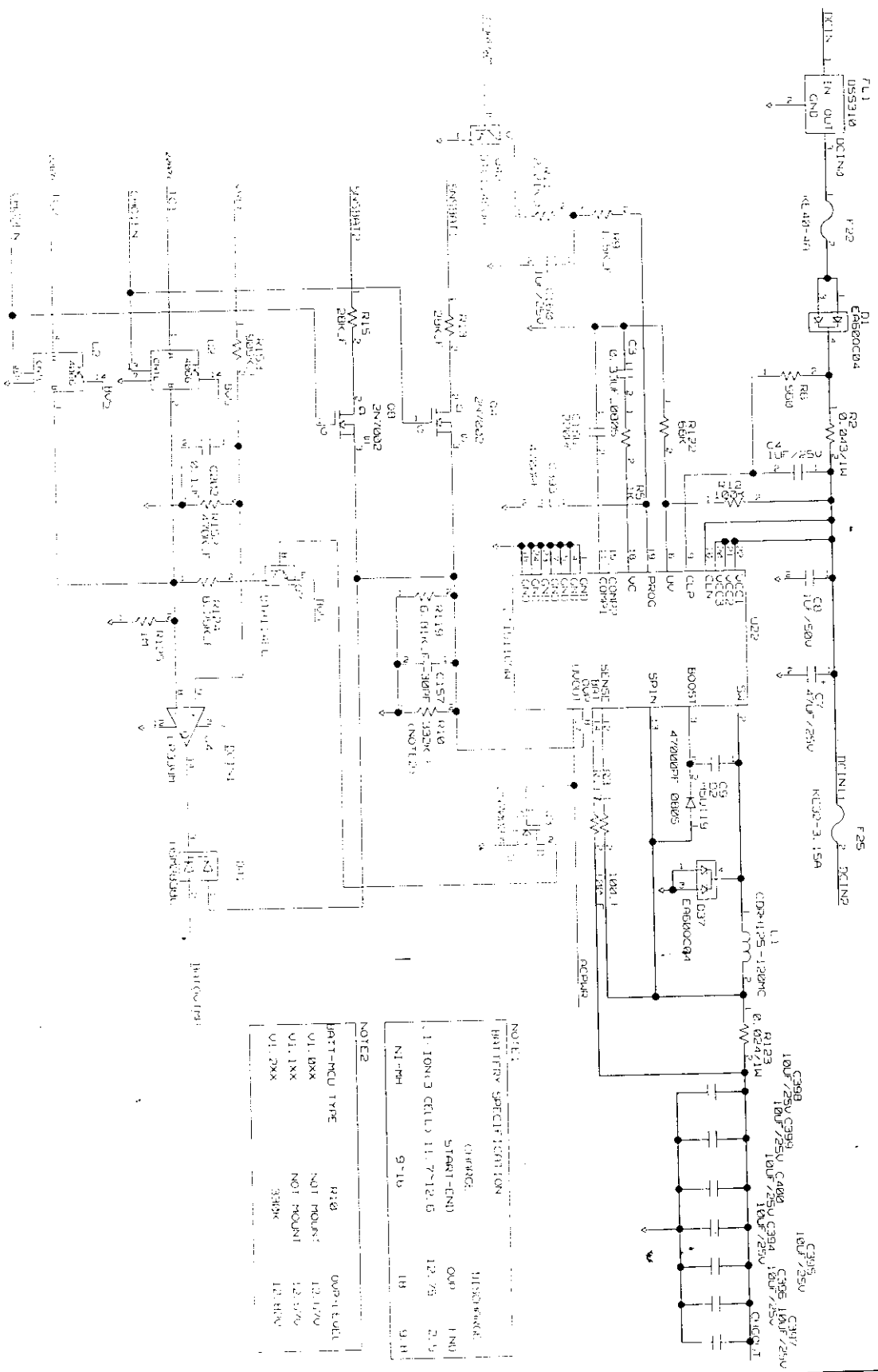


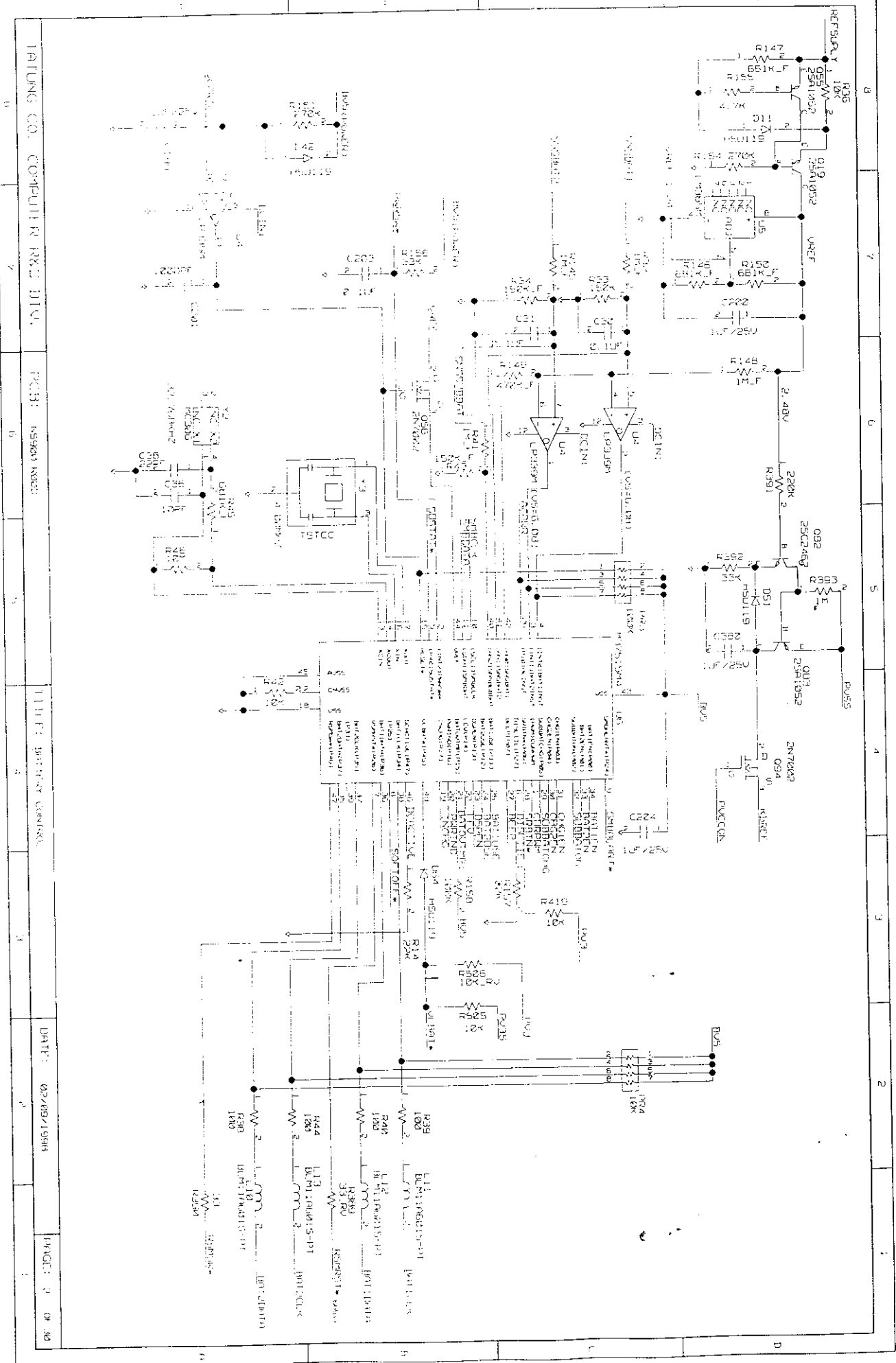


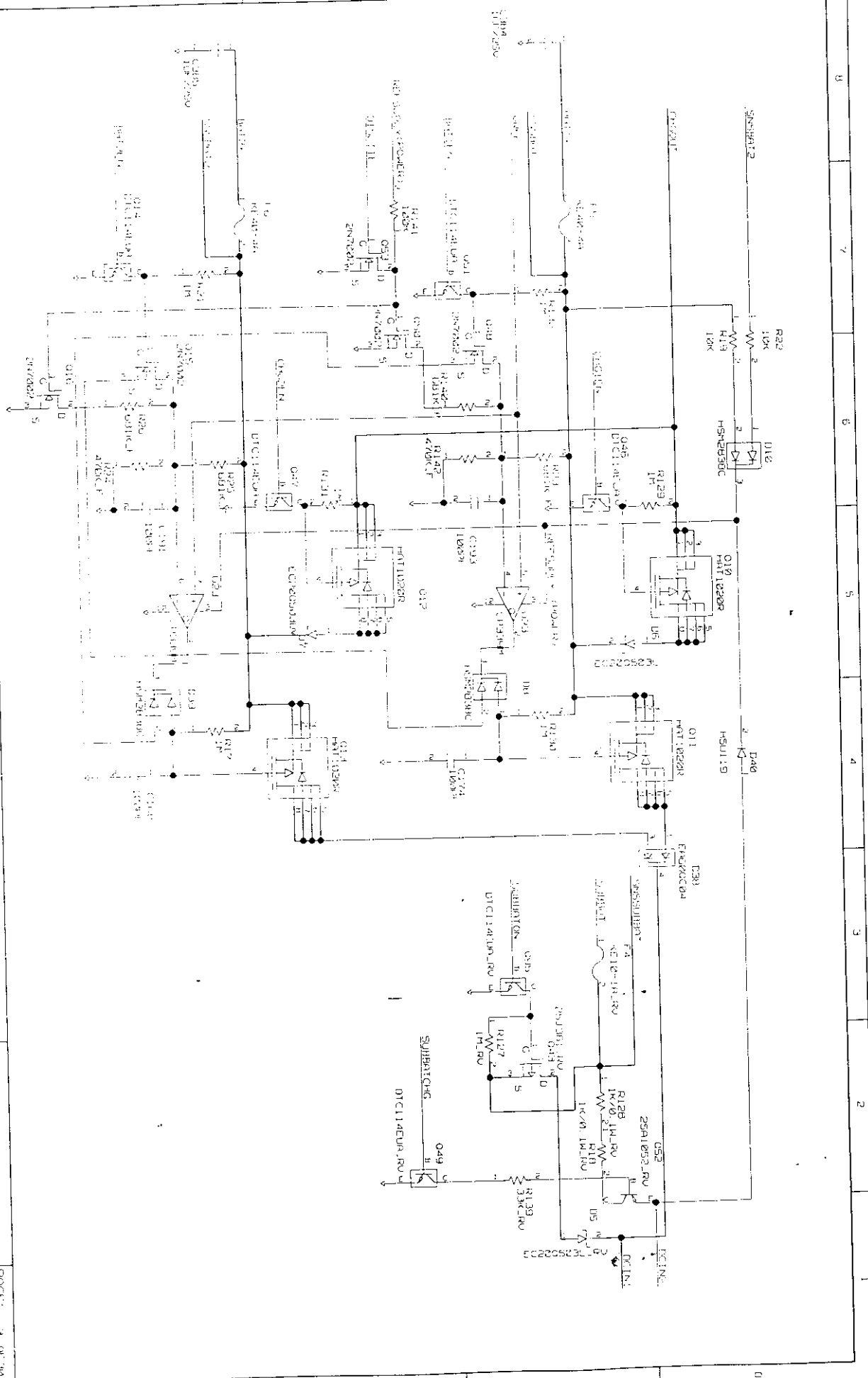
NOTE:

BATTERY SPECIFICATION	CHARGE	DISCHARGE
START-END	12.17V	11.7V
1.10V (CELL)	12.75	2.5
NI-MH	9.10	1.8

NOTE:

BATT-PCU TYPE	W/O	W/PCU
VI.1XX	NOT POINT	12.17V
VI.1XX	NOT POINT	12.75V
VI.2XX	300K	12.17V





ROUND CO. COMPUTER R&D DIV.

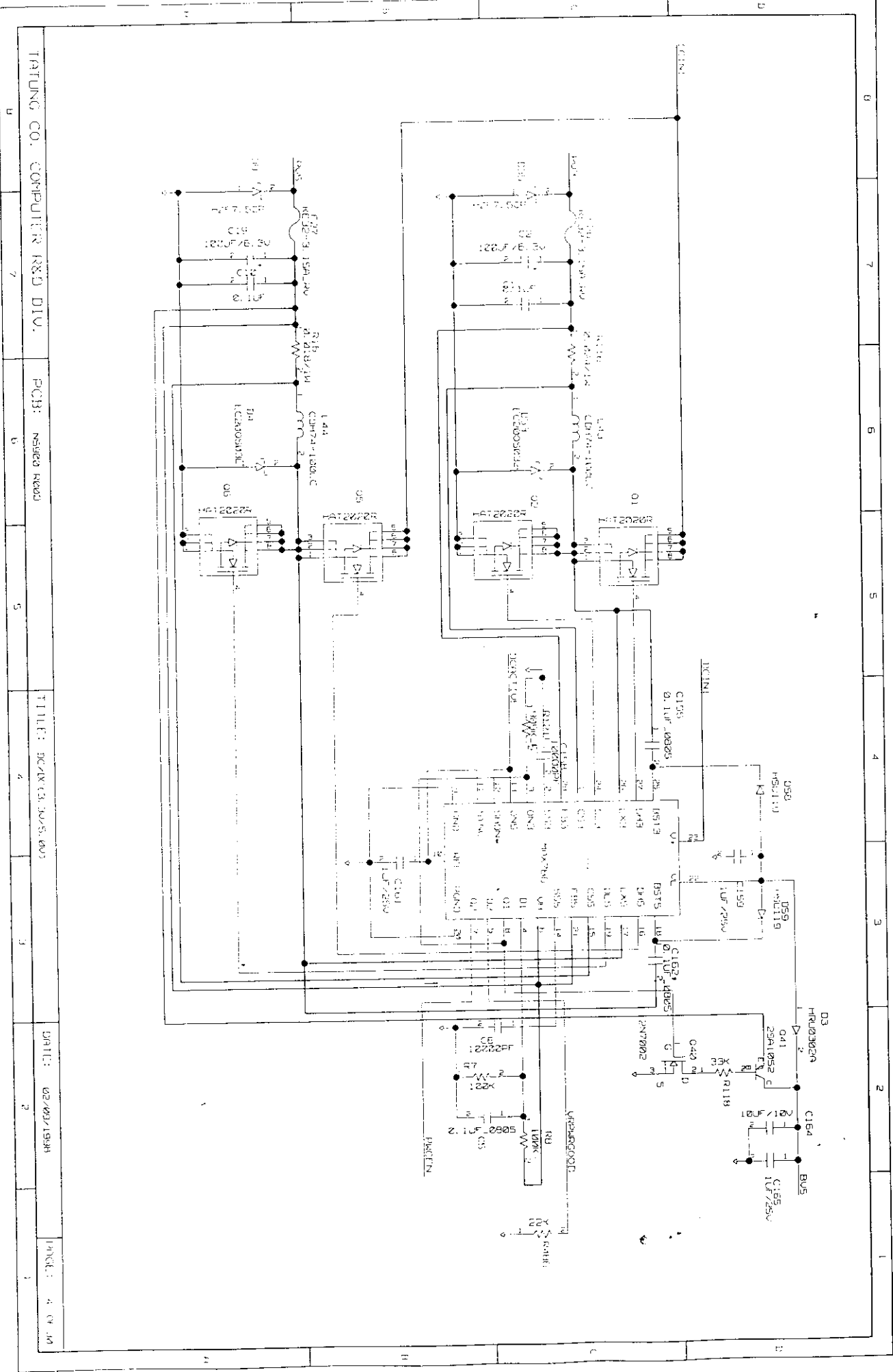
PCB: REVISED

TITLE: BATTERY SWITCH

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DATE: 02/05/1983

PAGE: 3 OF 30



TATUNG CO. COMPUTER R&D DIV.

PCB: NS800 R&D

TITLE: DC/DC/3.3V/5.0V

DATE: 02/03/1998

PAGE: 4 of 10

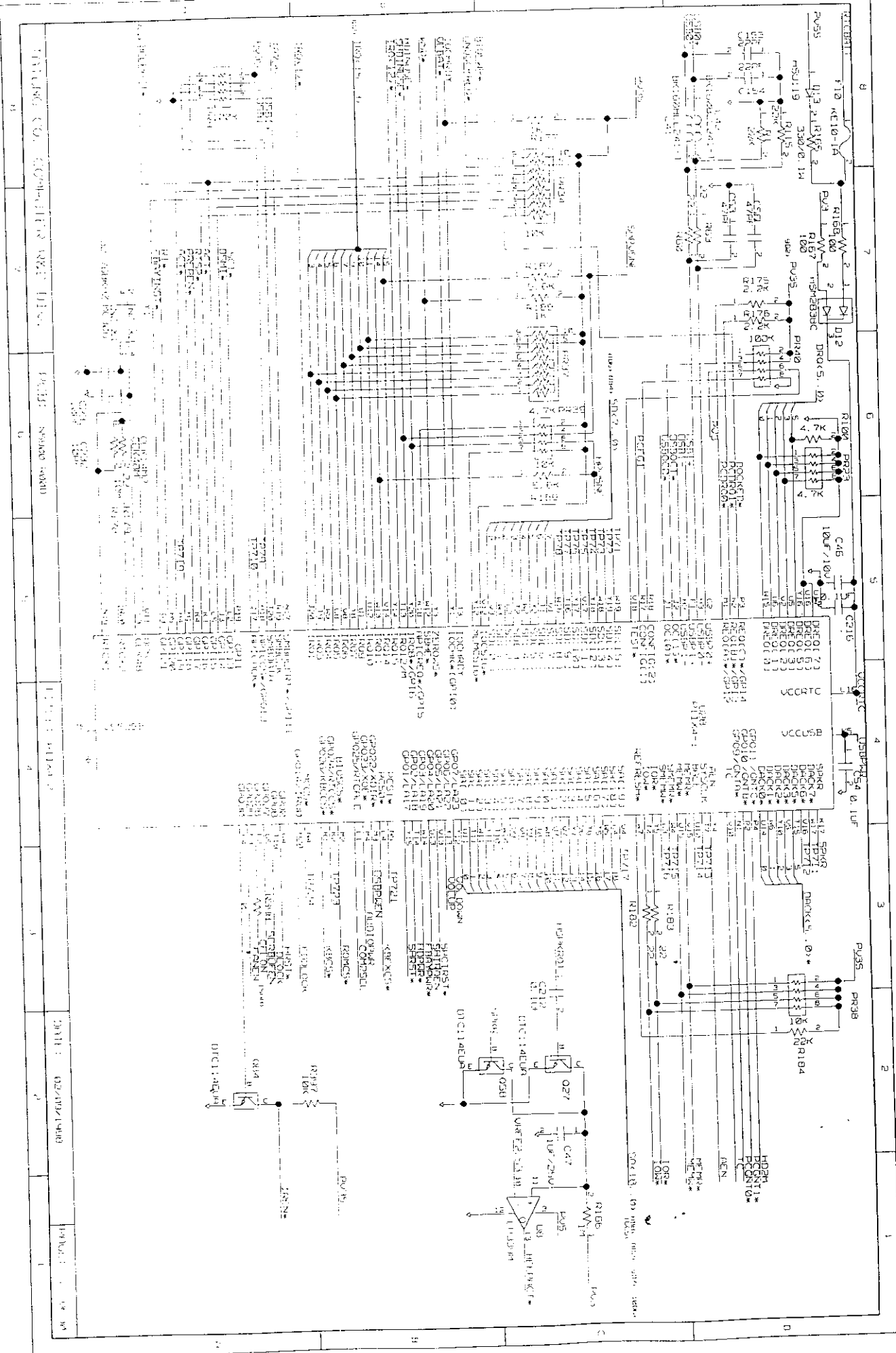


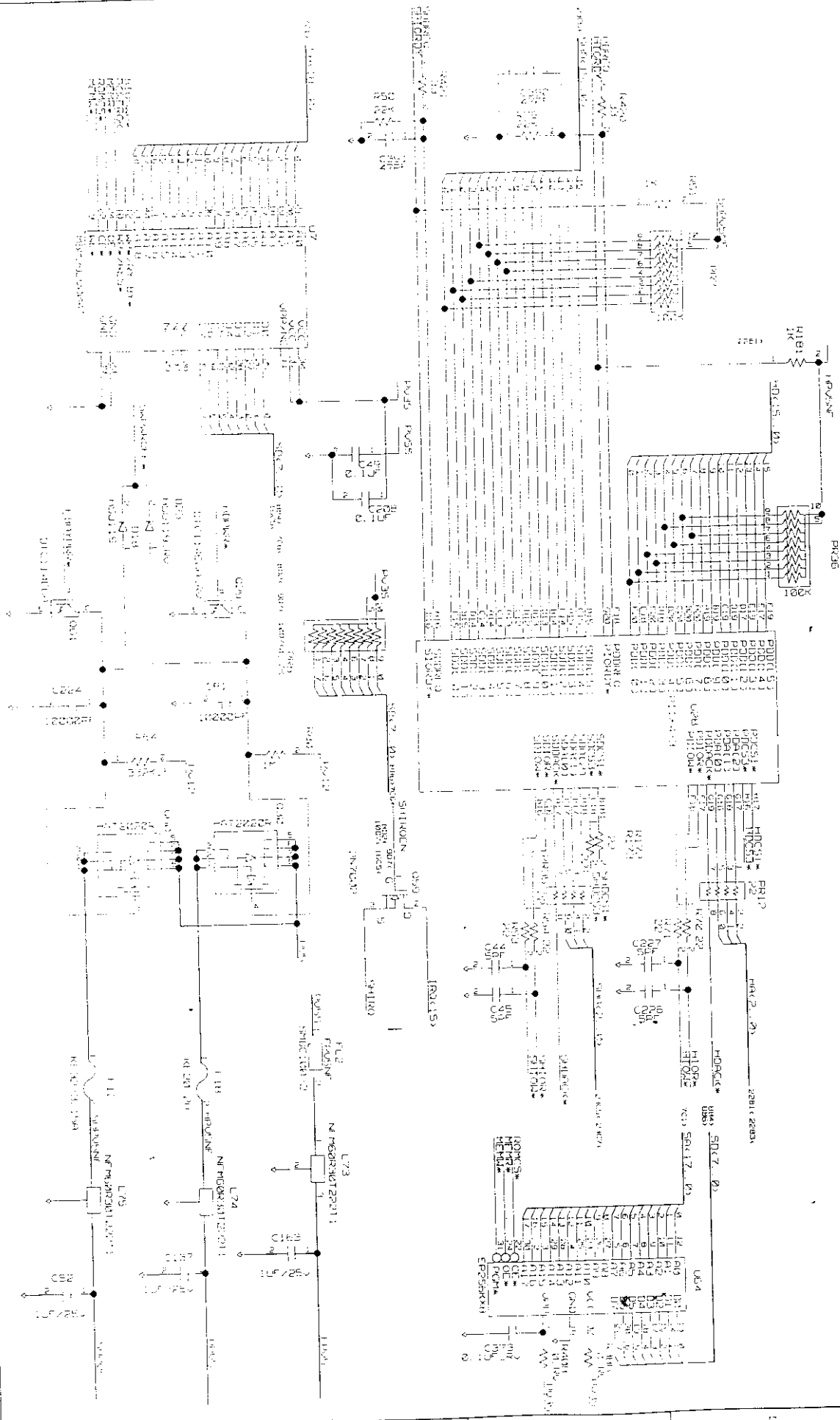
TABLE 10. COMPONENT DATA

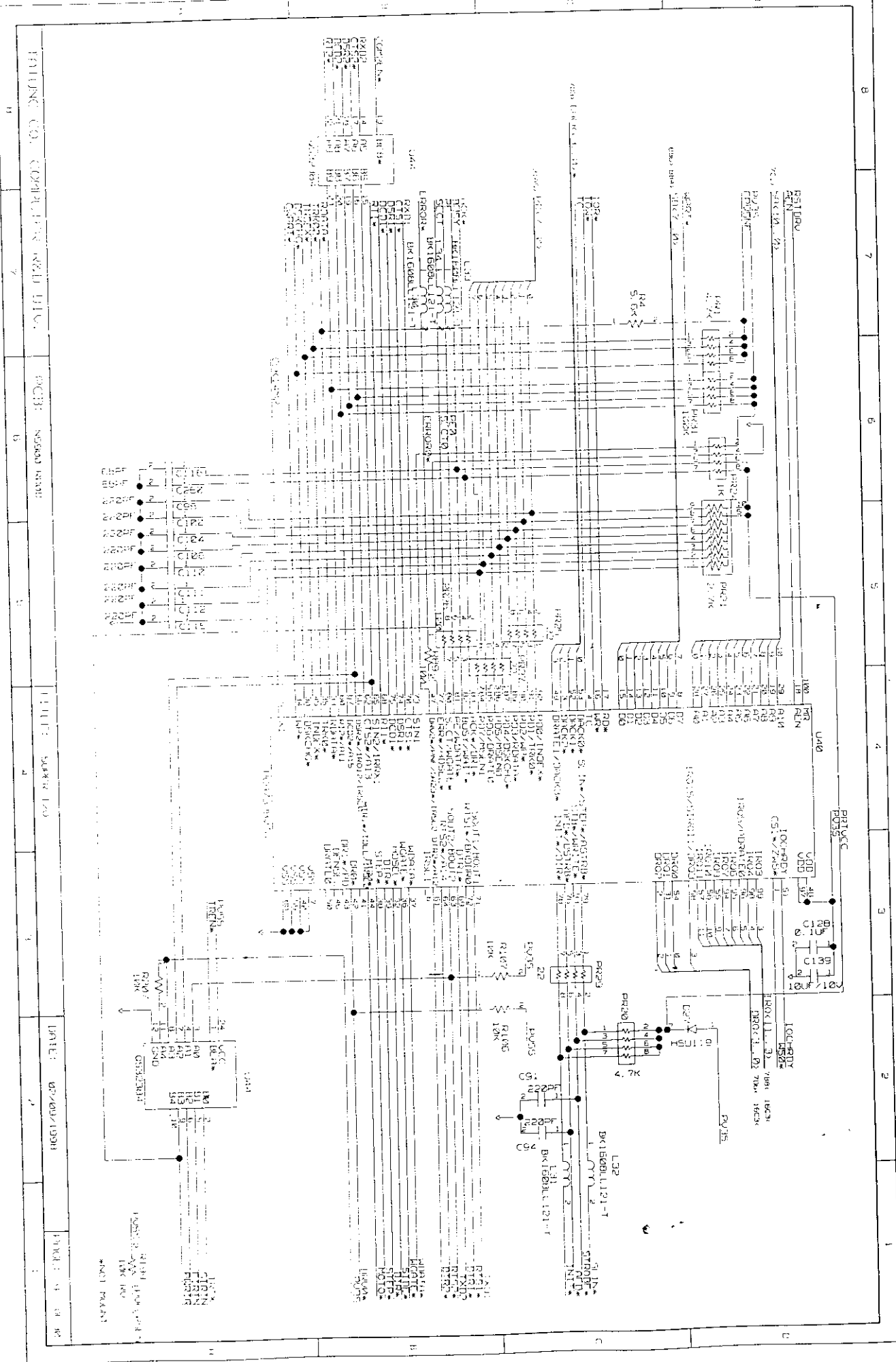
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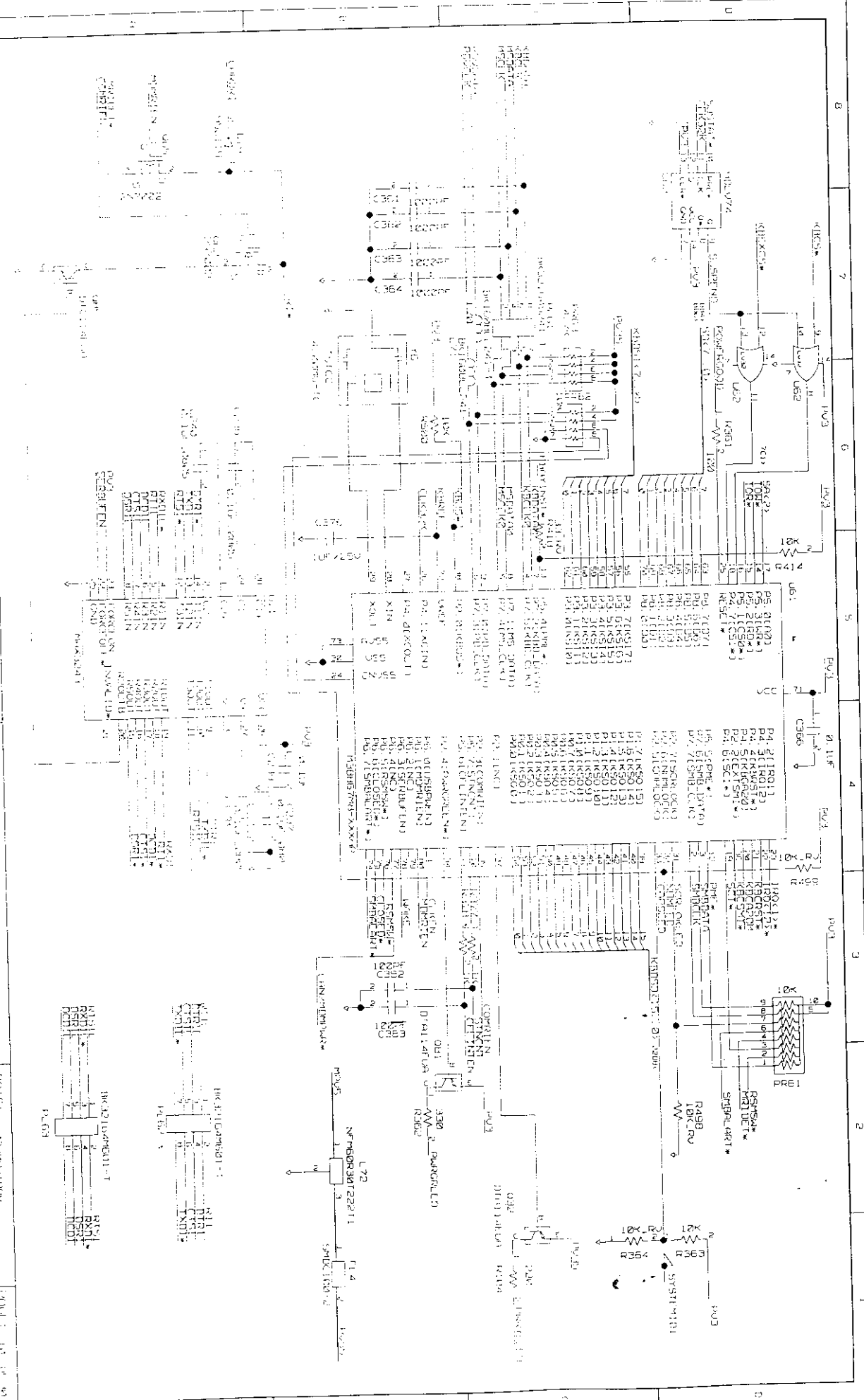
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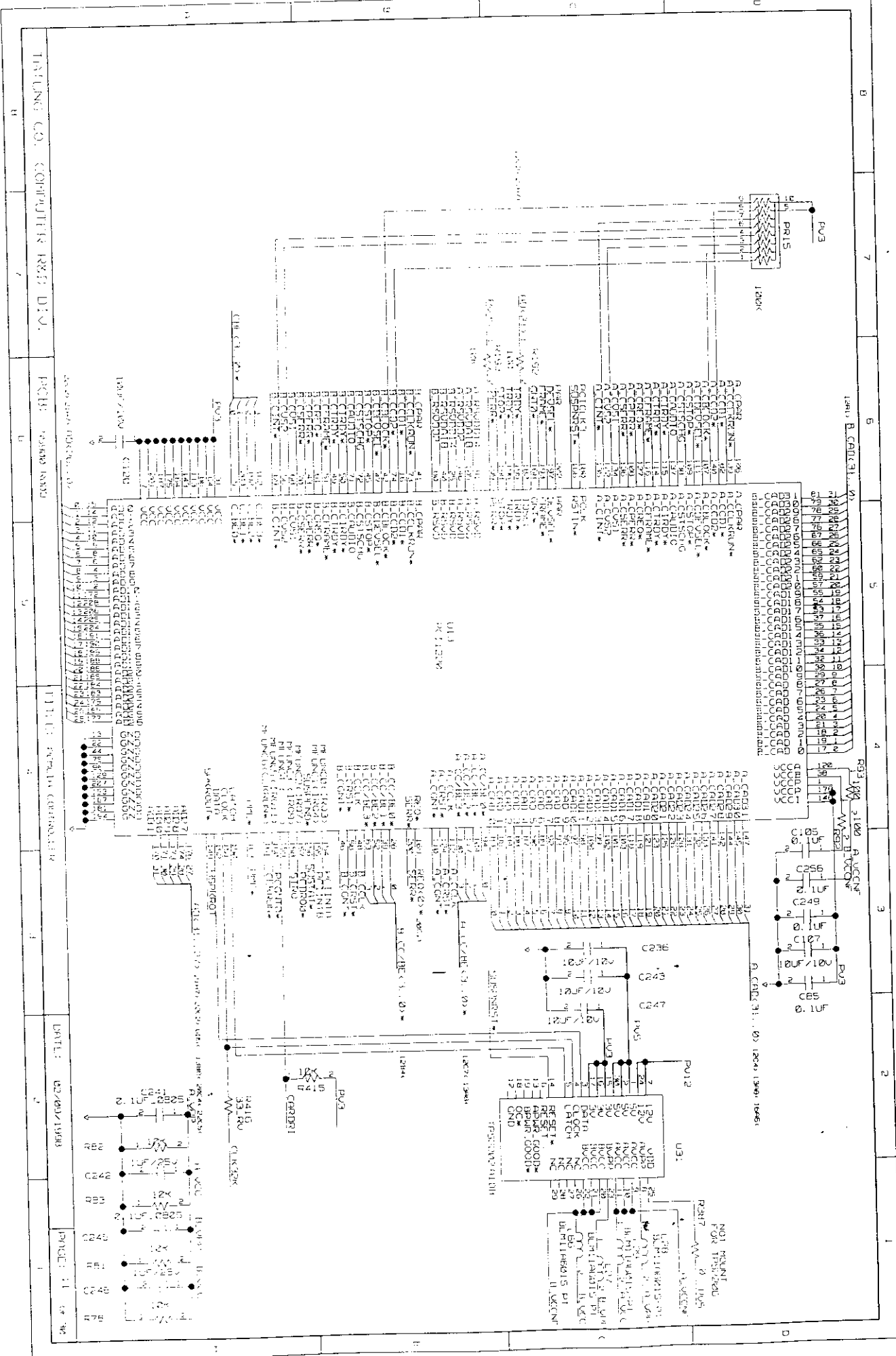
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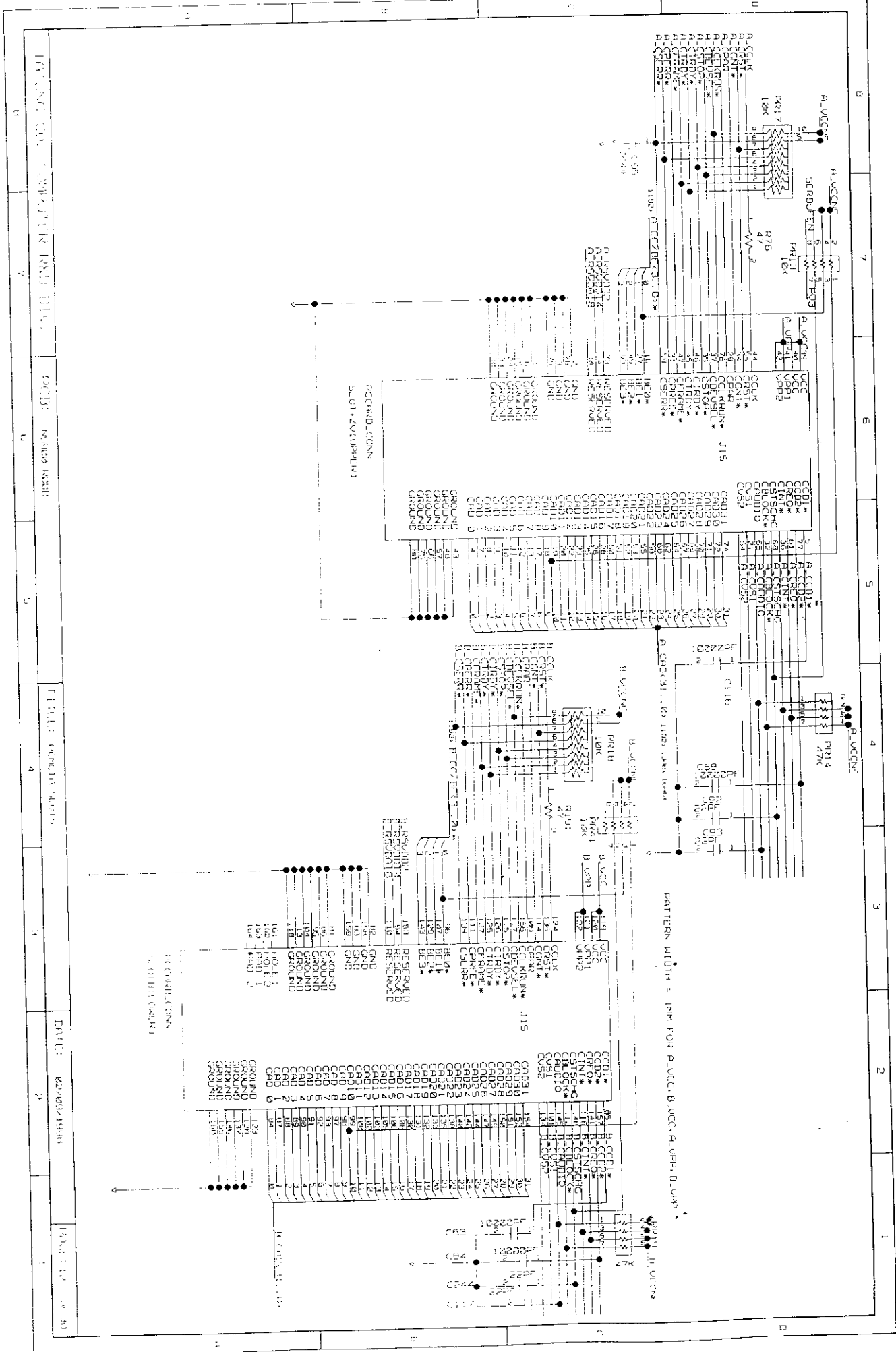
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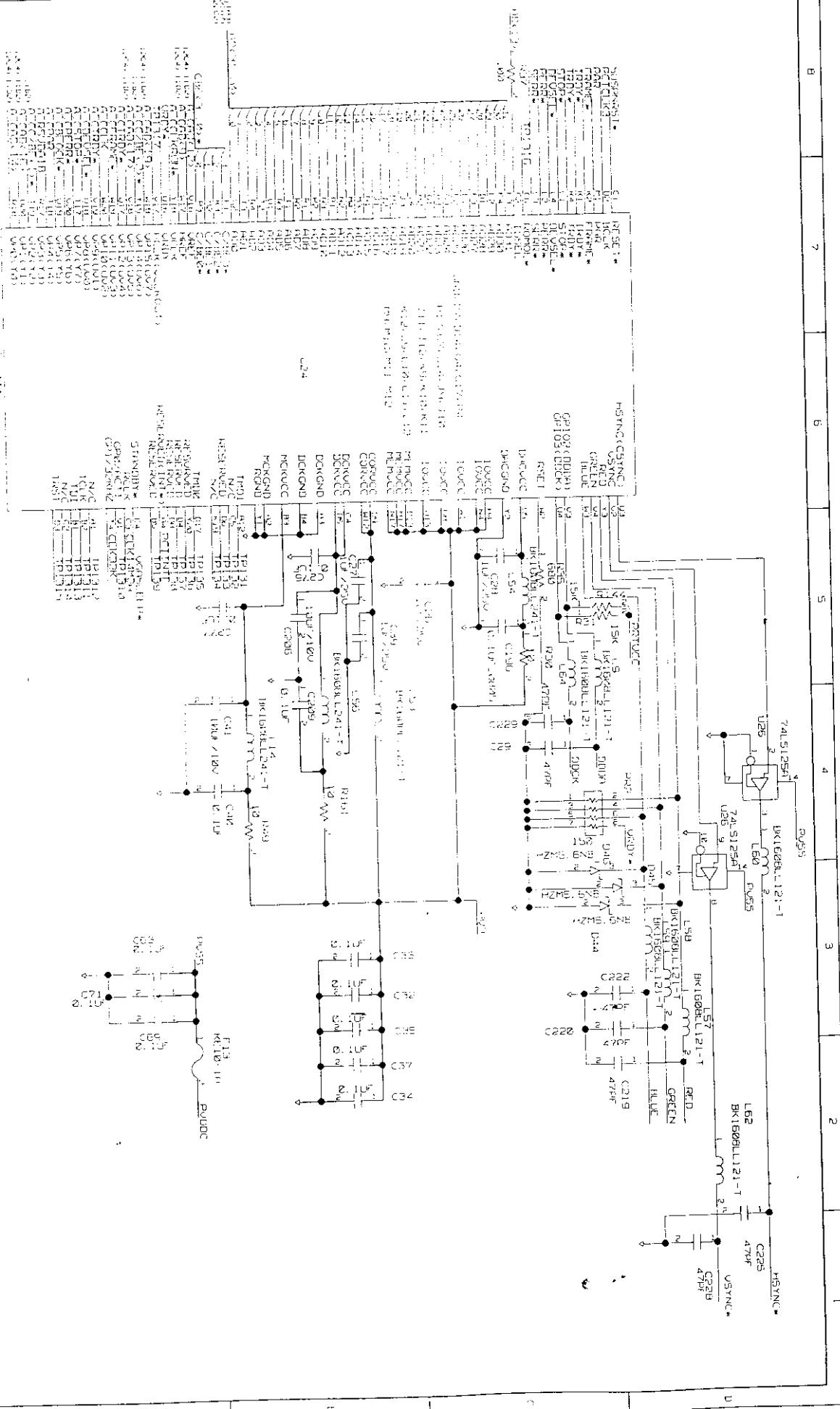












PCB: 1000000000

DATE: 02/03/1984

FILE: 0000000000

PCB: 1000000000

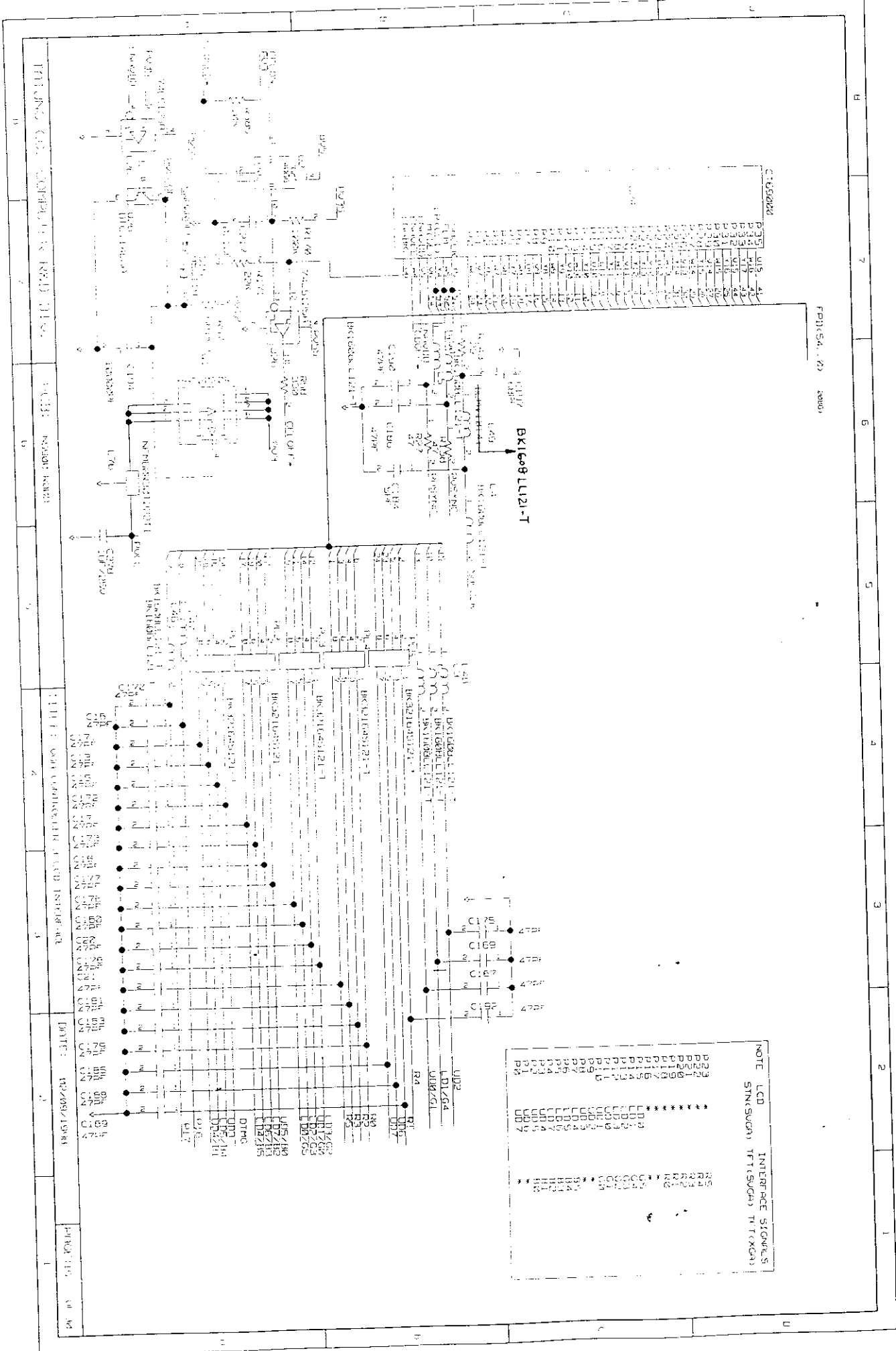
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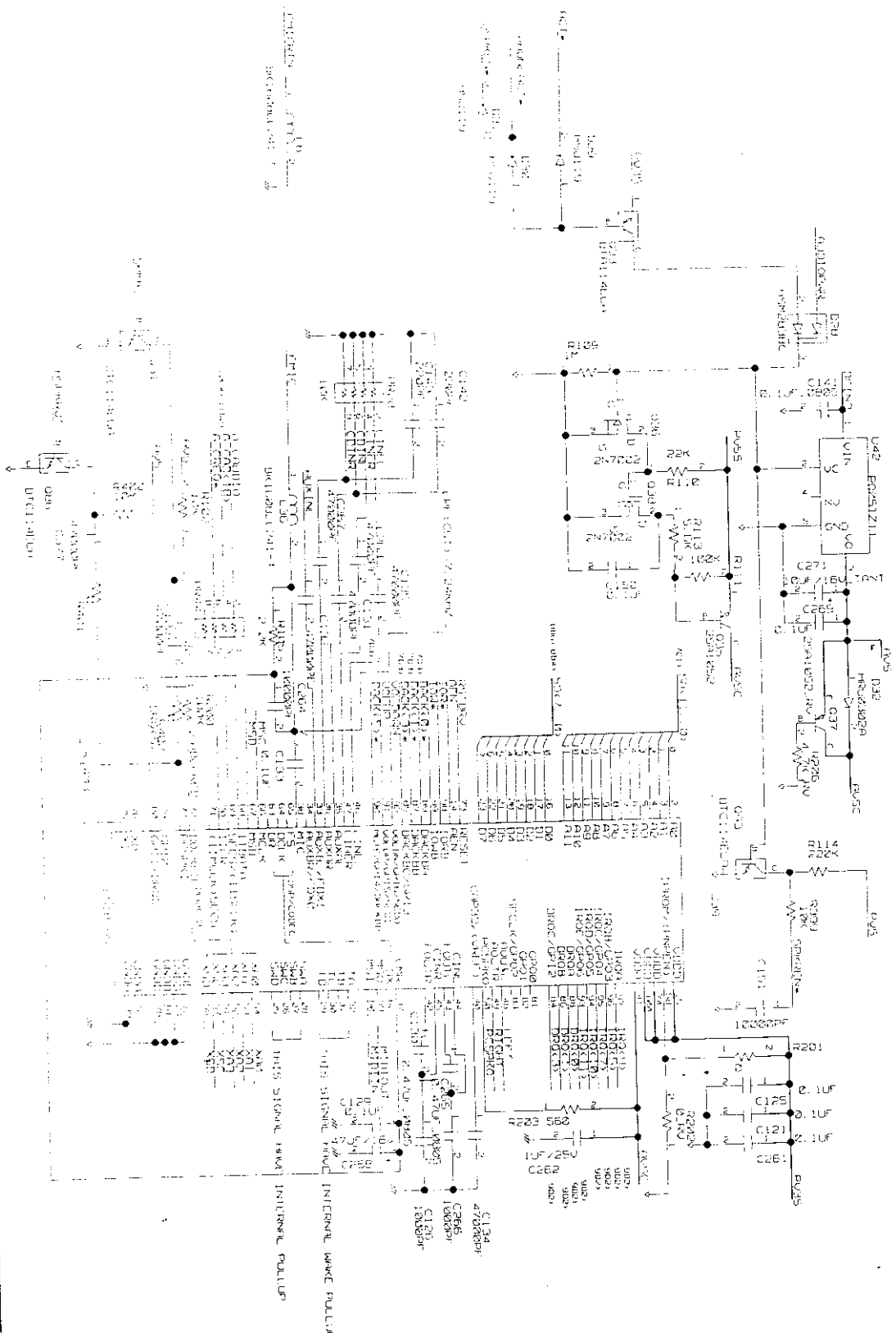
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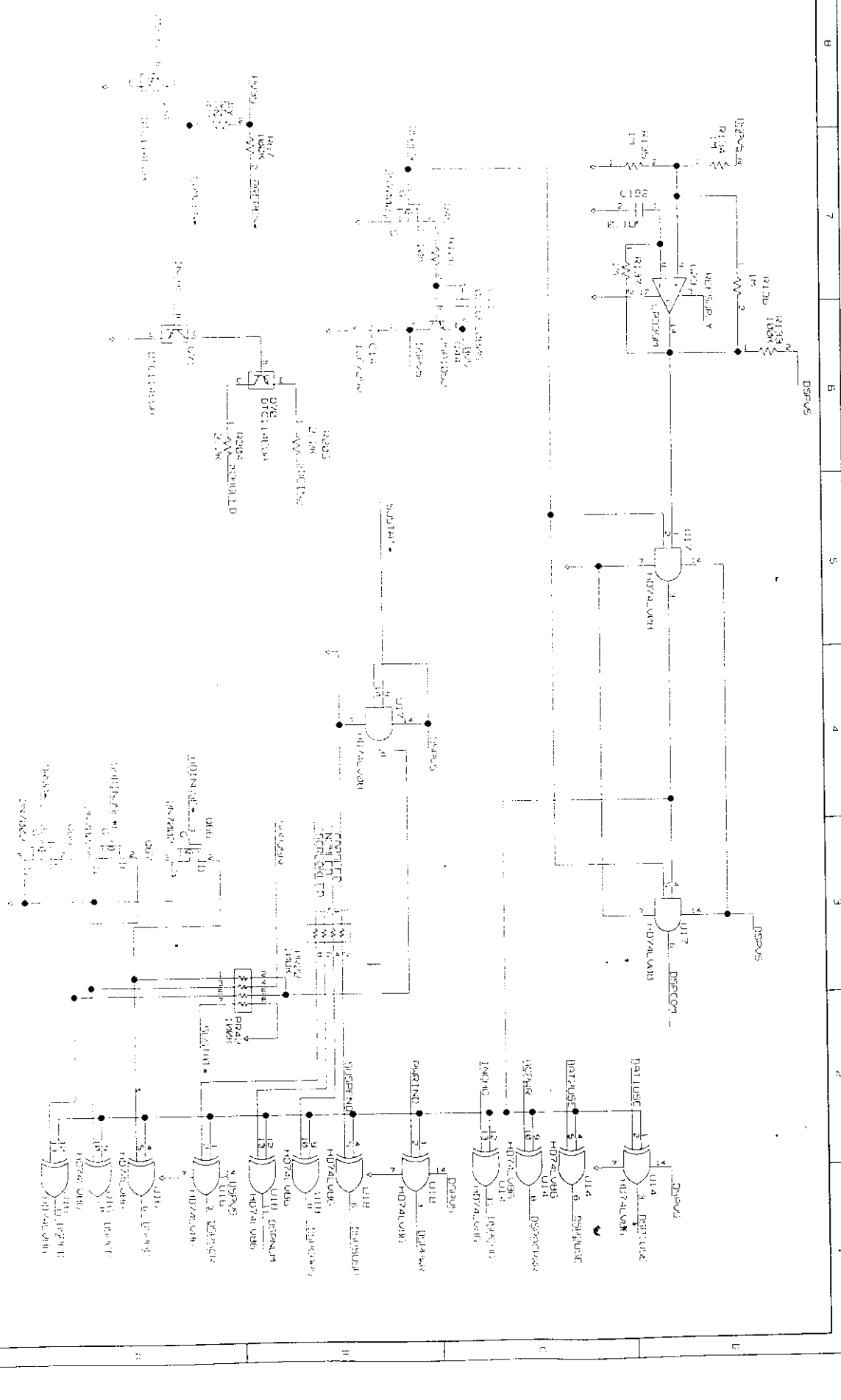
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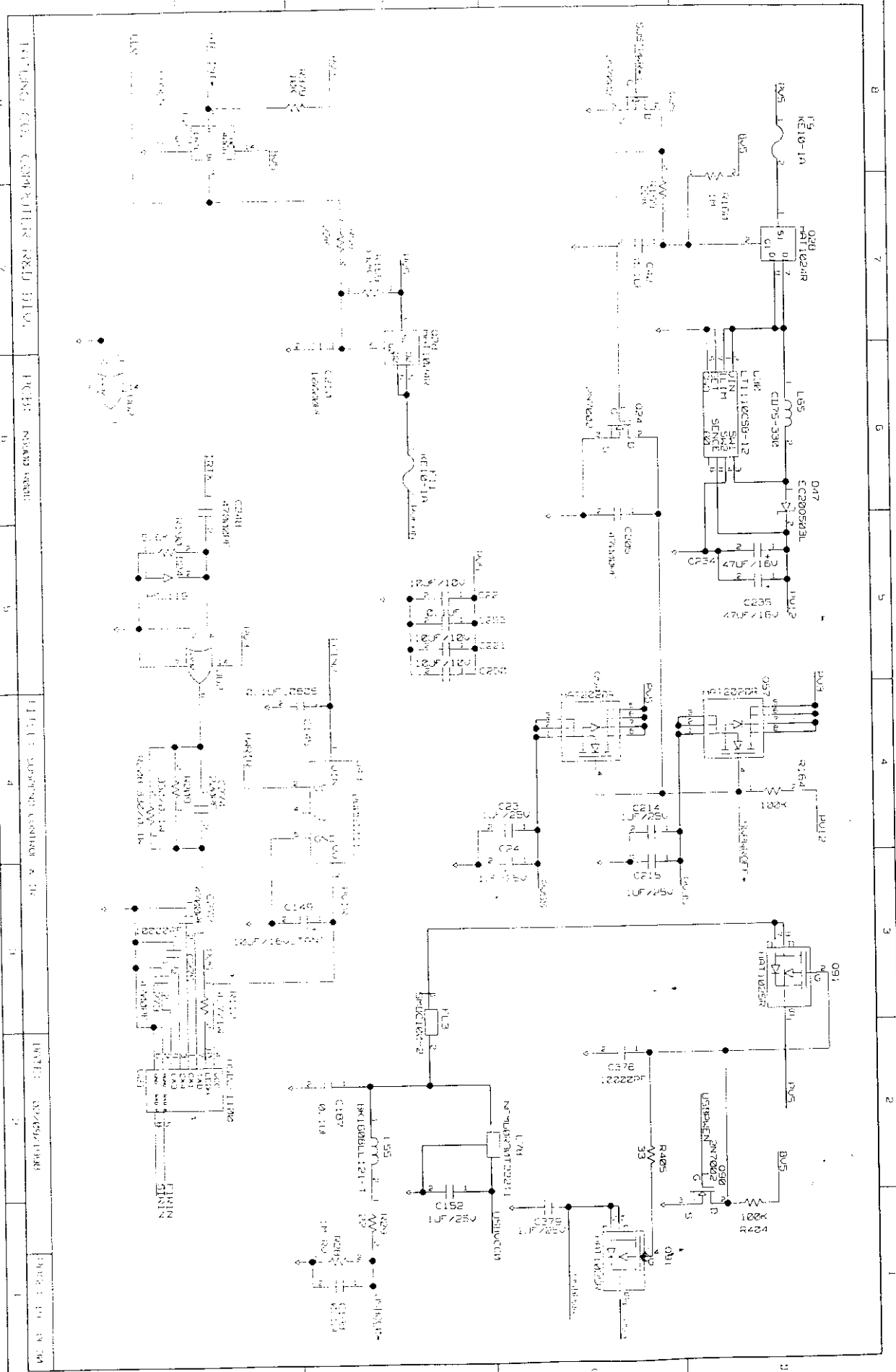
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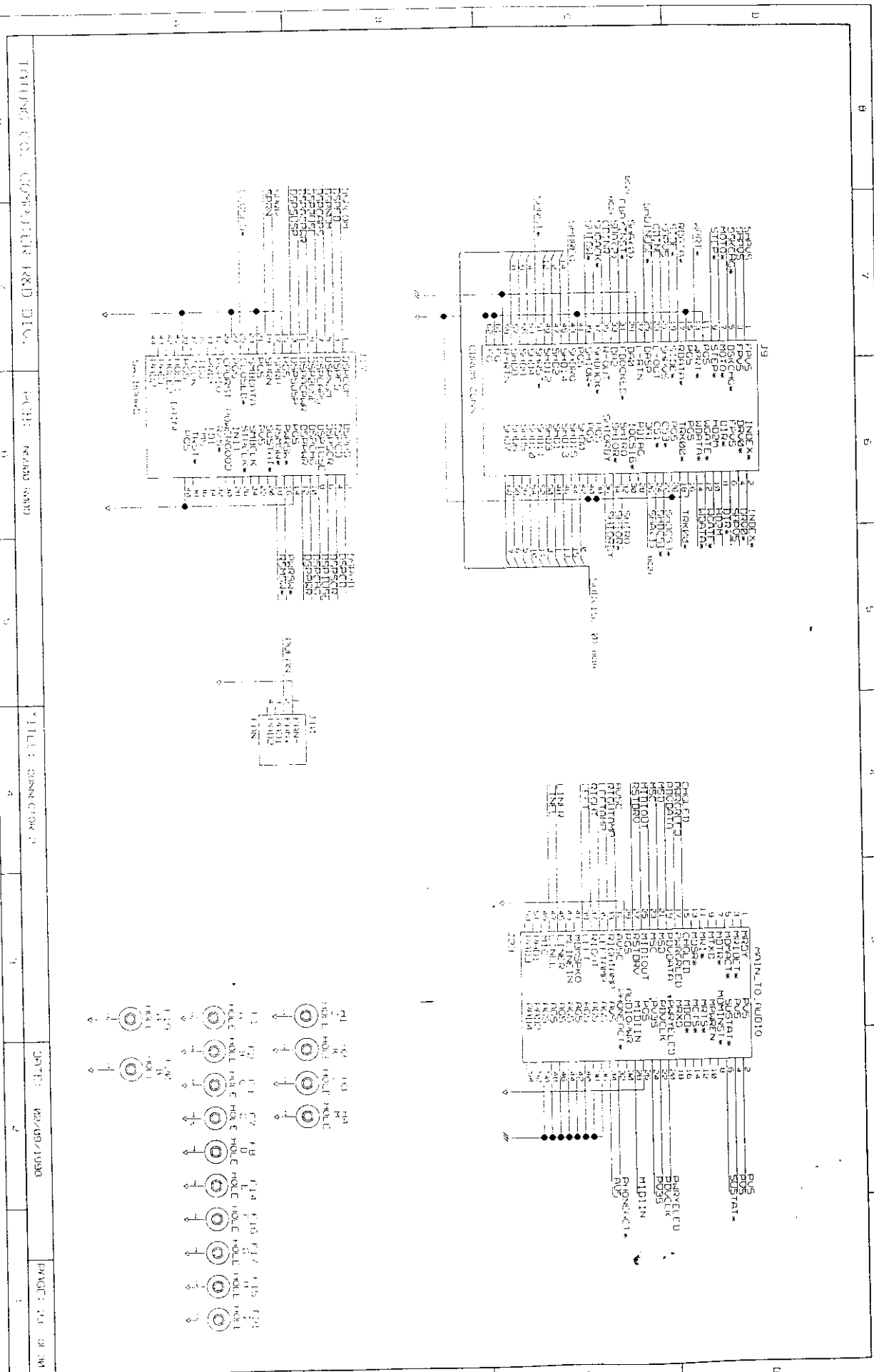
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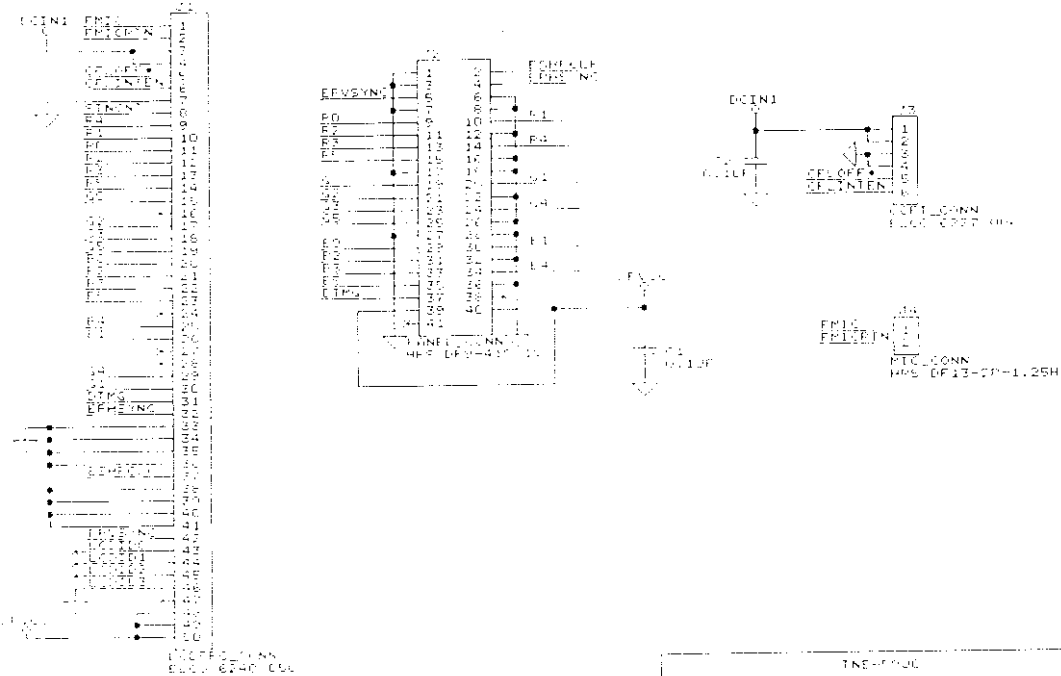
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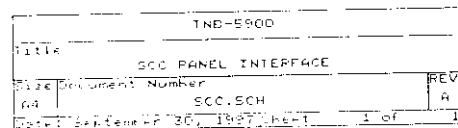
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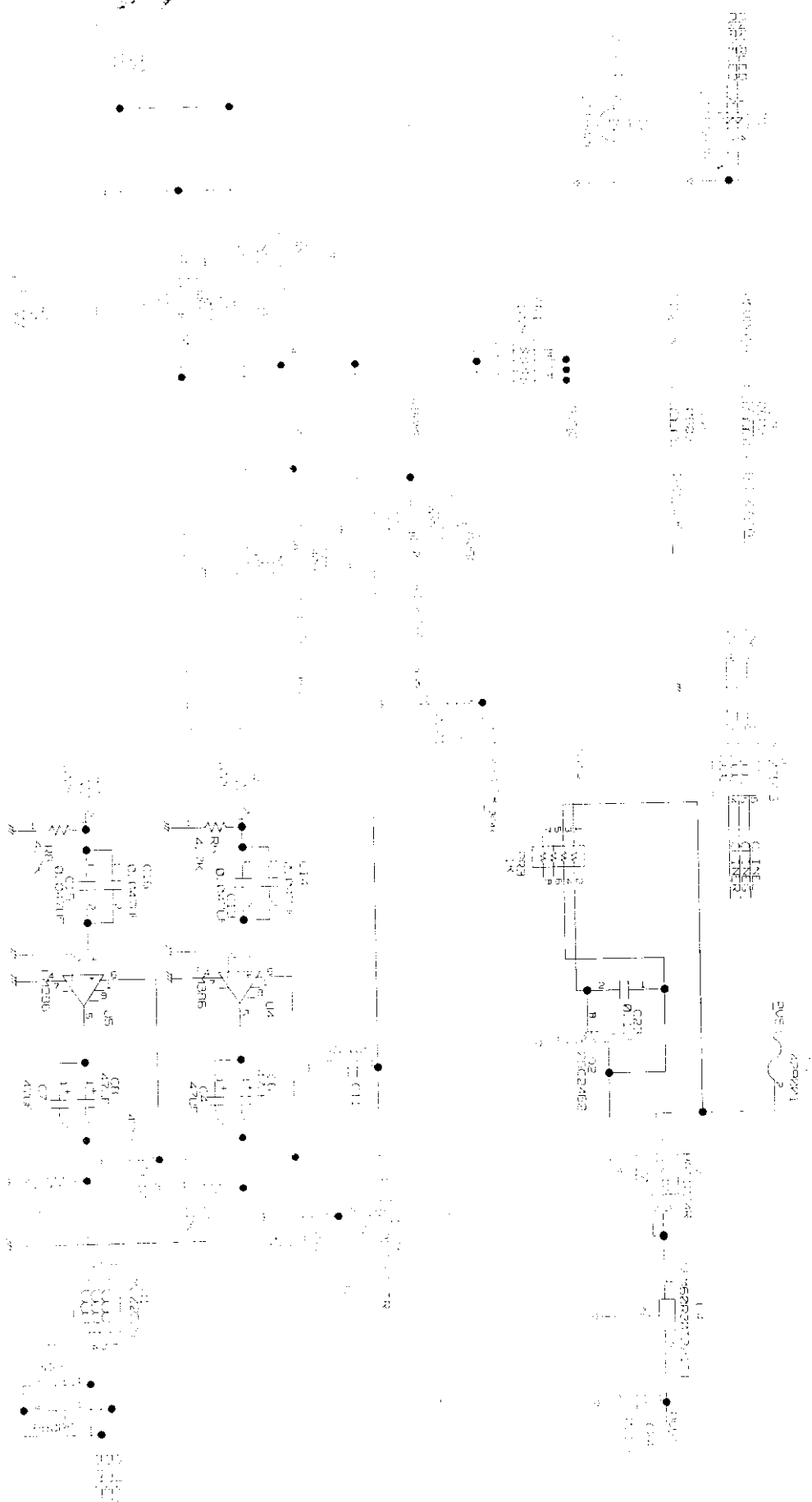
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N5900F2T R.OUD
12.1" TFT LCD TOP FPC

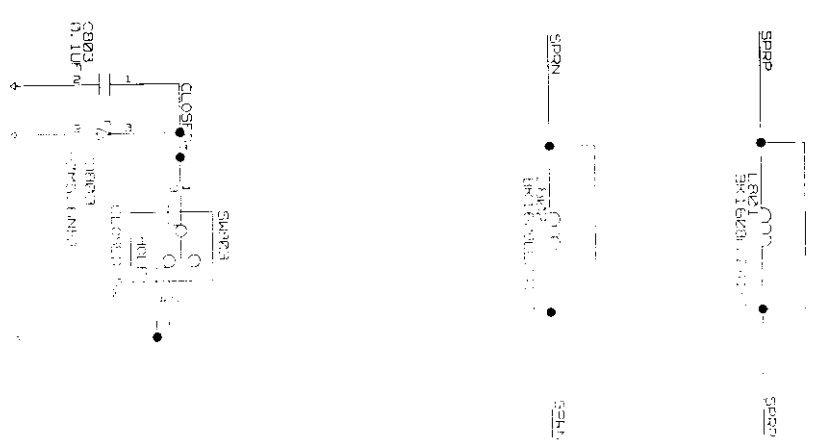
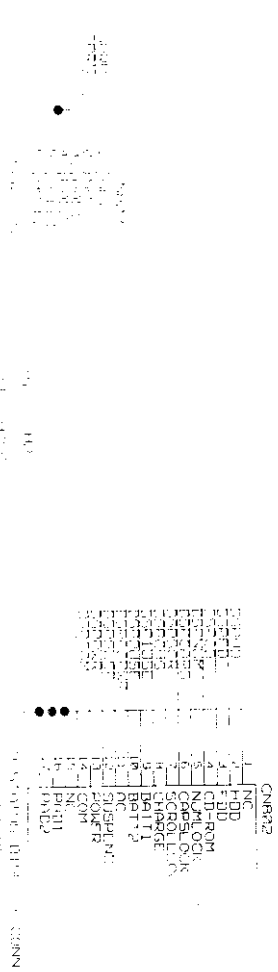
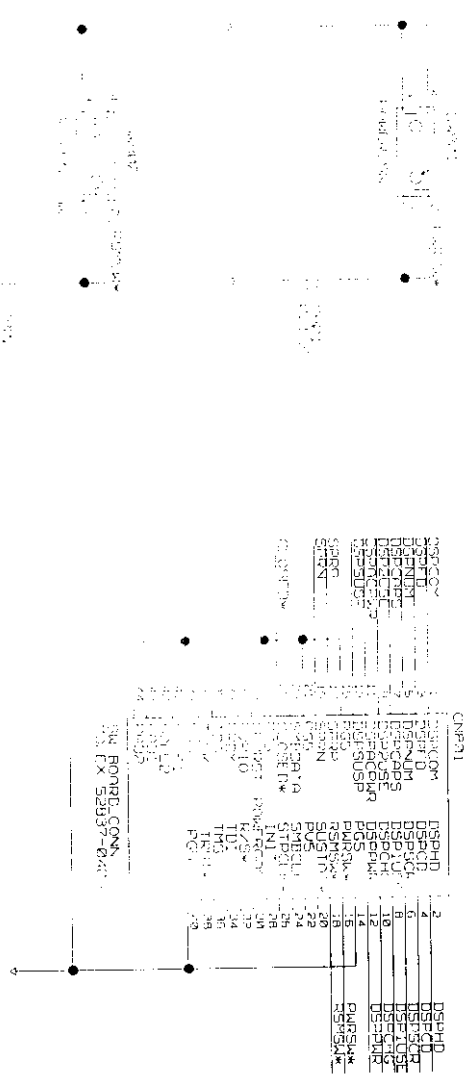


12.1" SCC LCD TOP FPC

[illegible]



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69, Ting-Hu 1st Street
Kwi-Shan Hsiang,
Tao-Yuan Hsien, Taiwan



Tel: (03) 328-7799
Fax: (03) 397-7590

Product Status Update
MODEL: D-1156NVC/R1

Availability:

ES : Sept, 97
Pilot-Run : Jan. 98
Mass-P : Mar. 98

Homologation :

US

FCC Part 68 Feb. 98

UL

Canada DOC Feb. 98

Japan JATE Feb. 98

Brief Spec:

1. Chip Set -RCV56HCF (TQFP)
2. Fax -14.4K
-V.17, V.29, V.27ter, V.21 Ch2
-TIA/EIA 578 class1 Fax
3. Data - 56K bps ITU upgradable
-V.34 Extended rate 33600bps, V.32bis

4. Data Comp. -V.42bis / MNP5
5. Error cort. -V.42 / MNP2,3,4
6. OS -Win 95, NT4.0 (Not ready)
7. Others -Plug and Play
-ACPI Power management comply

8. Interface:

- 1) DATA INTERFACE: Plug and Play PCI bus interface.
- 2) LINE INTERFACE: Modular line connector, one RJ11C phone jace.

Up to 300,1200,2400,4800,9600,14400,19200,28800,38400,57600,
115200 bps.

(2) LINE DATA RATE:

NORMAL MODE: 300/1200/2400/4800/9600/14400/16800/19200/21600/
24000/26400/28800/33600 bps and 1200/75 bps.

6. INTERFACE:

- 1) DATA INTERFACE: PCI Bus interface.
- 2) LINE INTERFACE: Modular line connector.

7. EMI & SAFTY DESIGN STANDARD: Compliance with FCC part 15, part 68 and DOC.

8. DIALING CAPABILITY

- Touch-Tone and rotary-dial pulse dialing.

9. MODEM FEATURES -

- 1) Command buffer support 40 characters.
- 2) Auto dial and auto answer.
- 3) support 1 profile.
- 4) Wake up on ring.

10. TEST AND DIAGNOSTICS FACILITIES

- Remote digital loop and remote digital loop self test.
- Analog loop and analog loop self test.
- Digital loop test.

9/11

Specification of D-1156NVC/R1

APPROVED BY:

MAIN FEATURES:

.Data/Fax
.56K bps ITU upgradable
.Support Win95 and (NT4.0 modes.)

.ACPI Power management comply

INTERFACE:

- 1) DATA INTERFACE: Plug and Play PCI Bus interface.
- 2) LINE INTERFACE: Modular line connector,

FAX FUNCTION -

1. OPERATION SPEC

- TRANSMISSION WAY: Half duplex.
- G3 FAX MODES: V.17 (14400bps) send/receive.
V.29 (9600/7200bps) send/receive.
V.27 ter(4800/2400bps) send/receive.
V.21 Channel 2(300bps) used only for signaling.

2. TRANSMIT/RECEIVE SPEC

- CARRIER FREQUENCY: V.17/ transmit carrier 1700 or 1800 Hz.
V.29 transmit carrier 1700 Hz.
V.27 ter transmit carrier 1800 Hz.

3. EIA 578 CLASS 1 COMPATIBLE

- MODEM FUNCTION -

1. OPERATION SPEC

- TRANSMISSION WAY: 56000 download string 33600 Upload string.
- DATA SPEED: 56000(download)/33600/31200/28800/26400/24000/21600/
19200/16800/14400/9600/4800/ 2400/1200/300 bps and 1200/75 bps.
- DATA LENGTH: 10 bits, including start, stop, parity.

2. ERROR CORRECTION: V.42 AND MNP 2, 3, 4.

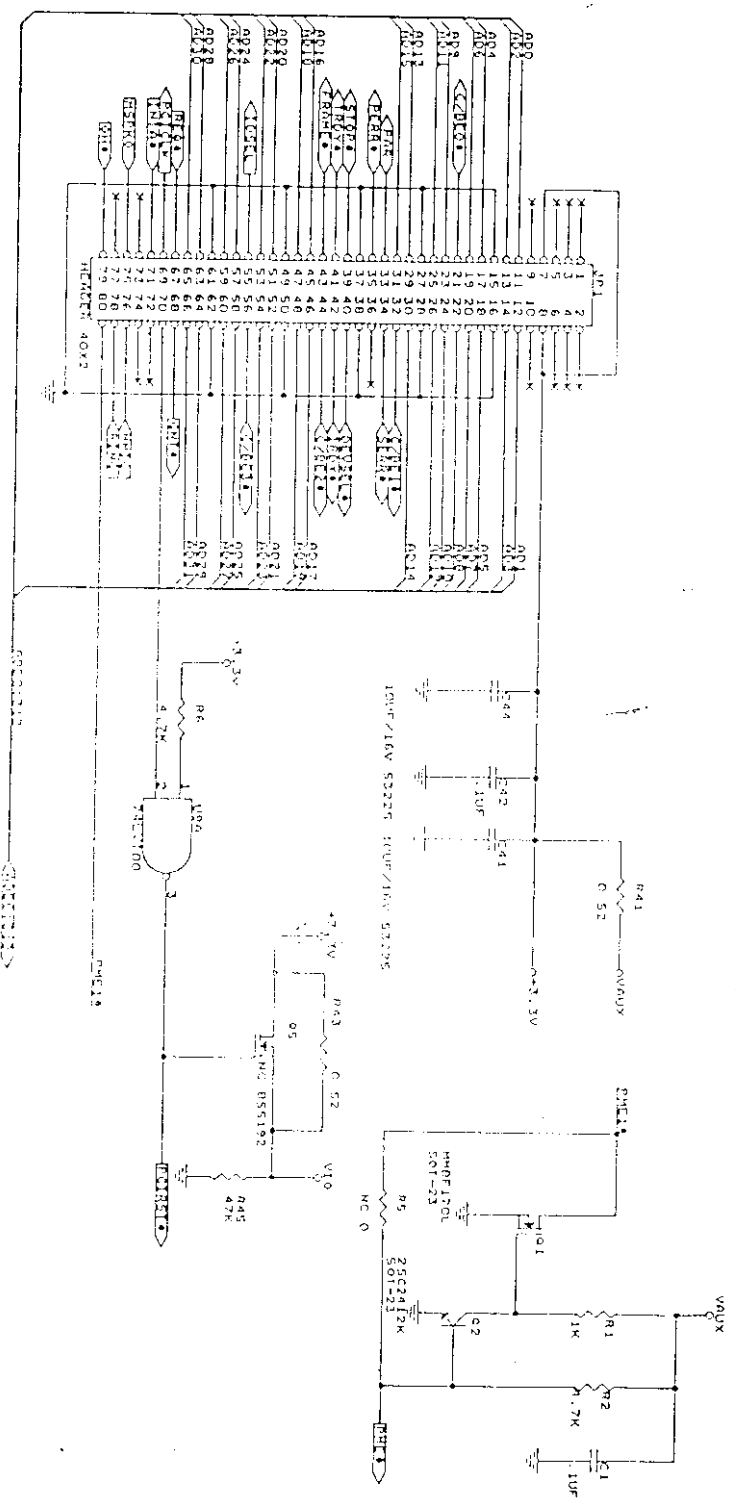
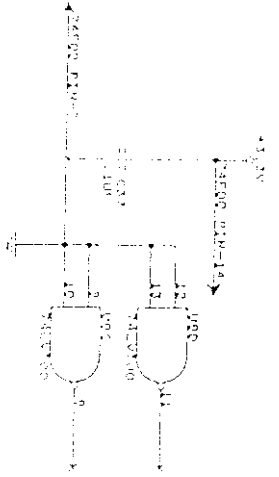
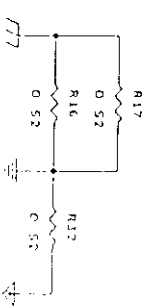
3. DATA COMPRESSION: V.42bis AND MNP 5.

4. ENHANCED "AT" COMMAND SET.

5. ASYNCHRONOUS COMMUNICATION SPECIFICATION

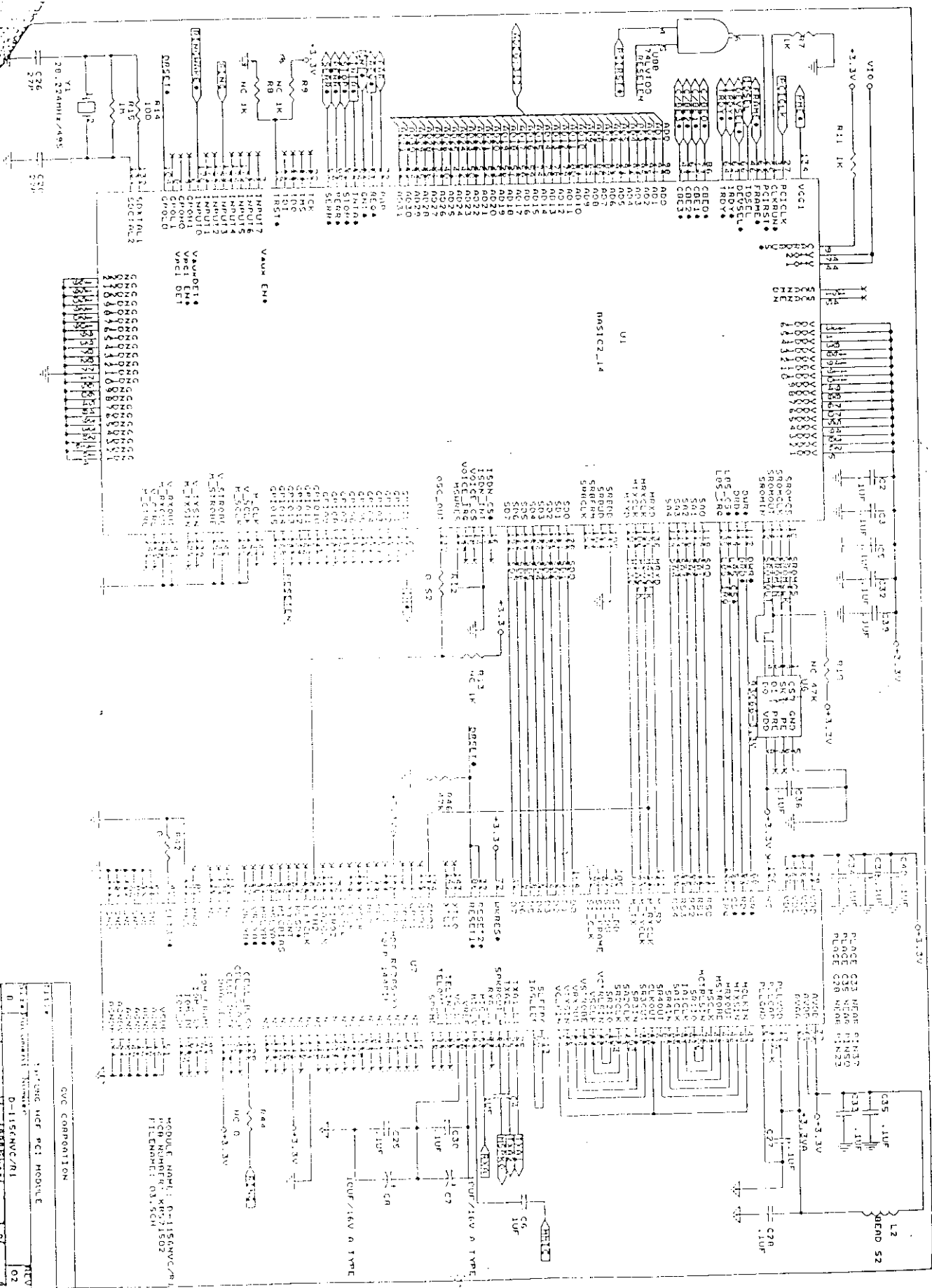
*BUAD RATE:

(1) DTE TO MODEM DATA RATE:

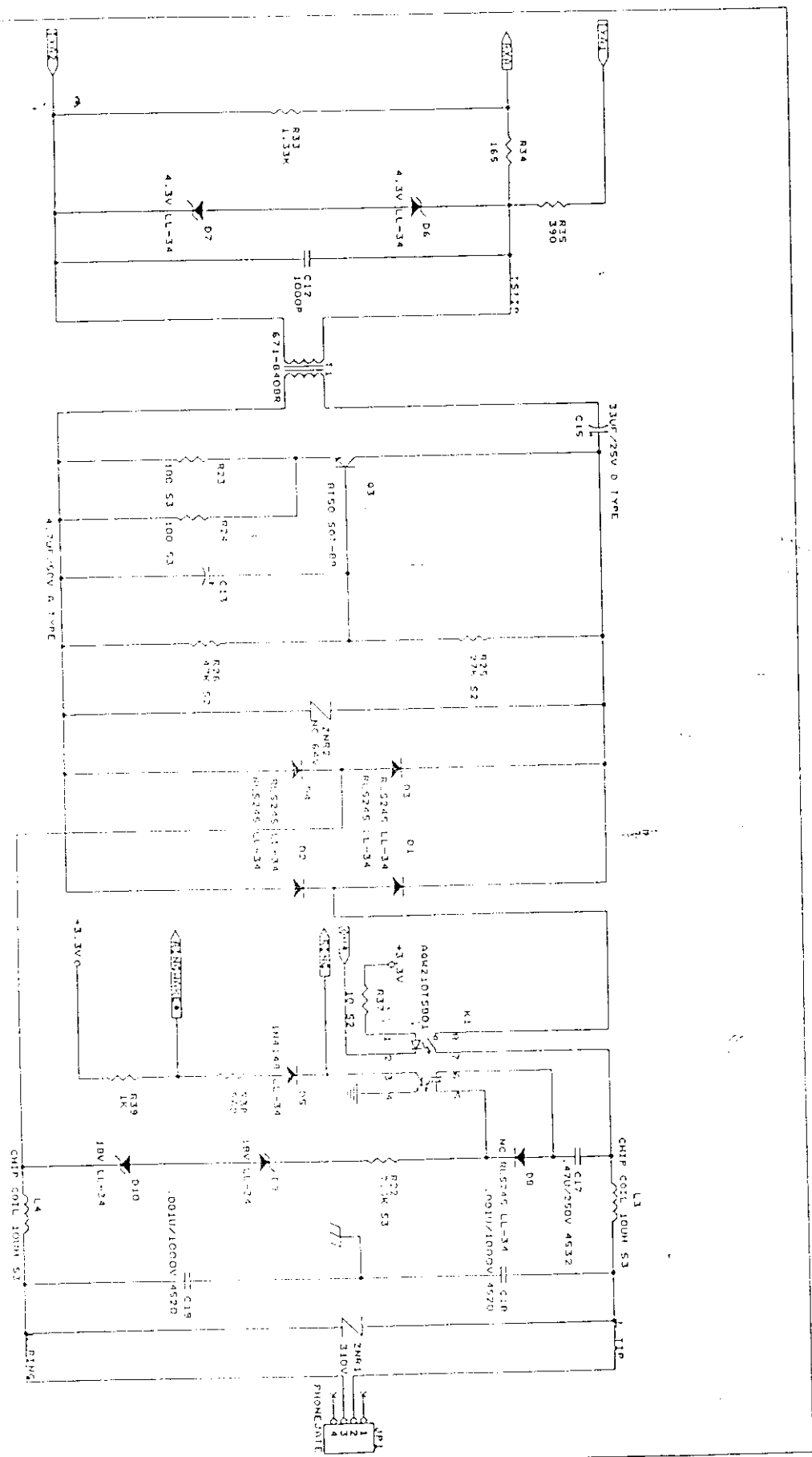


MODULE NAME: D-115AWC/R1
PCB NUMBER: K851502
FILENAME: D115CW.R1

111	CVC CORPORATION
112	FAUING HCF PCI MODULE
113	Document Number: K851502
114	REV: 01/15/90



8/3



MODEL NAME: D-1556N/C/R1
 PART NUMBER: 40571002
 FILE NAME: 04.501

CVC CORPORATION			
DATE	REVISION	DESCRIPTION	BY
01/15/01	01	INITIAL REV. FOR PCB MODULE	REV
02/15/01	02	D-1556N/C/R1	02
03/15/01	03	REVISION 15	03

3/4

1997/9/30

D-Link

100M Fast Ethernet PCI-BUS Note-Book Module with one phone-jack connector

Product Specification

1.0 Product Feature:

- Support 32-bit PCI Local Bus Master high speed operation.
- Complies with the Ethernet/IEEE 802.3 100Base-TX and 10BASE-T Industrial standard.
- Plug-and-Play--simply insert module into a Note-book and it will Automatically be configured by the Note-book PC BIOS.
- One RJ-45 connector: Auto detection of 10M Ethernet and 100M Fast Ethernet.
- Support Auto-negotiation
- 100 or 10Mbps Ethernet data transfer rate.
- Support half and full duplex operation.
- DEC 21143 32-bit Intelligent Ethernet controller.
- Supports PCI clock speed up to 33MHz, independent of network clock.
- Built-in FIFO buffers to eliminate external memory.
- Low Ethernet Command Processing Overhead.
- Support Diagnostics Program
- 32-bit Memory addressing(4GB) over the PCI bus.
- Complete drivers for Windows 95 Windows NT
- Support Magic Pack/ Wake-On Lan function of ACPI Spec.
- VCCI- class B EMI spec.

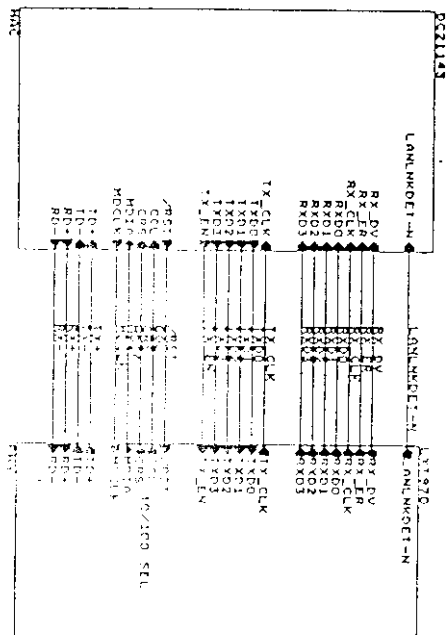
2.0 Product Specification

1. Network type : 100M/10M Ethernet.
2. Compliant with industry standard : IEEE 802.3u 100BASE-TX
3. Host Interface : PCI Spec. Revision 2.1 (Bus Master) as 80 pin connector
4. Hardware Jumperless and support Auto-negotiation
5. Media interface : RJ-45 Full shield phone jack
6. LAN Chip Set :
 - Ethernet Interface Controller : DEC DC21143 TC
 - Ethernet Transceiver interface : Level One LXT970
8. Transfer rate : 100/10Mbps.
9. Transmission Distance :
 - Twisted-Pair : Maximum 100 meters to HUB
10. EMI
 - VCCI Class B (Tested /Applied by customer)
11. AC Noise,
 - Device designed by customer.
12. Power consumption : 1.05 W (max)
13. Operating temperature : 0 - 55 degree Celsius
14. Humidity : 10% - 90% non-condensing
15. PCB Layer : 6 layers
16. Dimension: 41.5 * 80 mm
17. Weight: 36 gram

3.0 Software Support

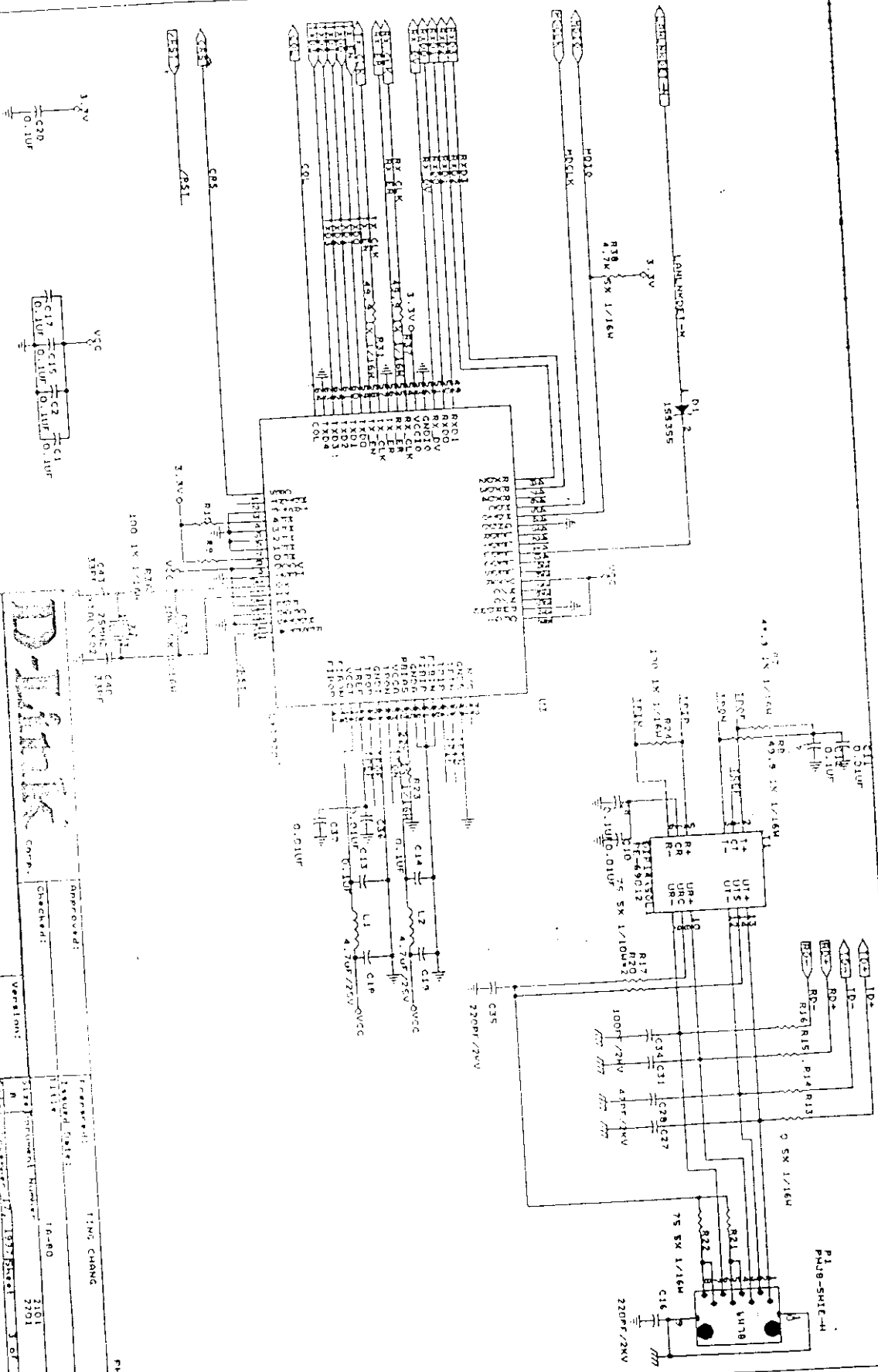
- Microsoft Windows 95
- Microsoft Windows NT4.0

T.D.O. D



		Approved: _____ Checked: _____	
Doc. No.: _____		Issued: _____ Title: _____	
Version: _____		Issued Date: _____ 10-80	
Doc. Number: _____ 8		2101 2201	
Date: November 11, 1987		REV of 01	

 $\frac{8}{5} \times \frac{1}{2} =$



Bill of Materials

QTY	DESCRIPTION	UNIT
1	IC1	IC
1	IC2	IC
1	IC3	IC
1	IC4	IC
1	IC5	IC
1	IC6	IC
1	IC7	IC
1	IC8	IC
1	IC9	IC
1	IC10	IC
1	IC11	IC
1	IC12	IC
1	IC13	IC
1	IC14	IC
1	IC15	IC
1	IC16	IC
1	IC17	IC
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1	IC32	IC
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1	IC97	IC
1	IC98	IC
1	IC99	IC
1	IC100	IC

QTY	DESCRIPTION	UNIT
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