Spectrum plots showing compliance at the lower and upper bandedges of PCS frequency blocks

Purpose

This report provides spectrum plots(conducted) showing compliance at the lower and upper bandedges of PCS frequency blocks.

PCS frequency blocks A thru F

Block	Frequency Assignment (MHz)	
	PCS TX	PCS RX
A	1850-1865	1930-1945
B	1870-1885	1950-1965
C	1895-1910	1975-1990
D	1865-1870	1945-1950
E	1885-1890	1965-1970
F	1890-1895	1970-1975

PCS1900 Block	Lower Edge Channel Freq (MHz)	Lower Edge Channel Number	Upper Edge Channel Freq (MHz)	Lower Edge Channel Number
A	1850.2	512	1864.8	585
D	1865.2	587	1869.8	610
B	1870.2	612	1884.8	685
E	1885.2	687	1889.8	710
F	1890.2	712	1894.8	735
C	1895.2	737	1909.8	810

Test setup

1) EUT was setup to transmit at maximum power level(+30dBm) at the frequency of the lower and upper bandedges of PCS frequency blocks as listed above into a spectrum analyzer. Hewlett Packard HP8498A 30 dB attenuator was used at the input of the spectrum analyzer to protect against overloading

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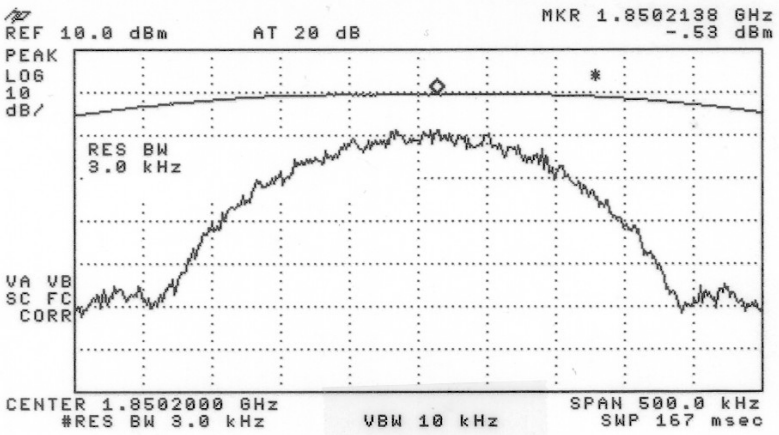
2) The spectrum analyzer was configured according to the settings recommended as the following:
   Dual trace functions(A & B) was used.

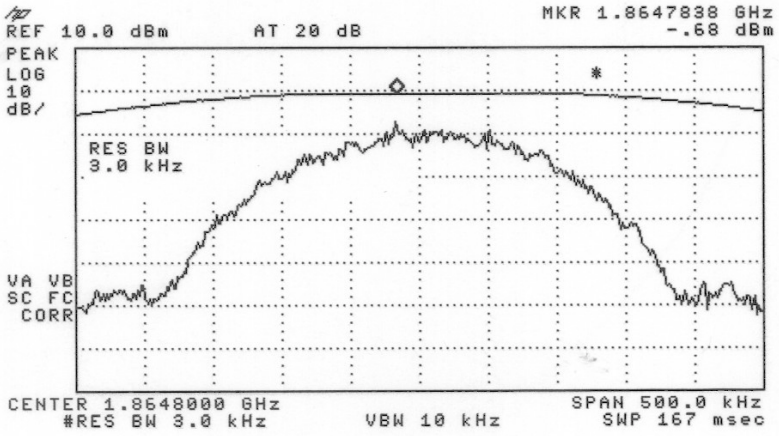
	Trace A	Trace B
RBW	3 KHz	300 KHz
VBW	10 KHz	1 MHz
Span	500 KHz	500 KHz

3) Each plot contains two spectrum sub-plots using the settings mentioned in 2). The smoother flat spectrum plot was generated using 300 KHz RBW and the plot immediately below was obtained using 3KHz RBW.

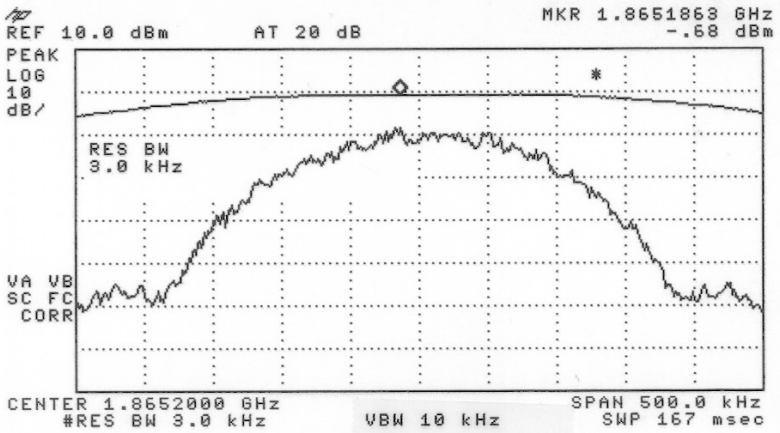
Plots

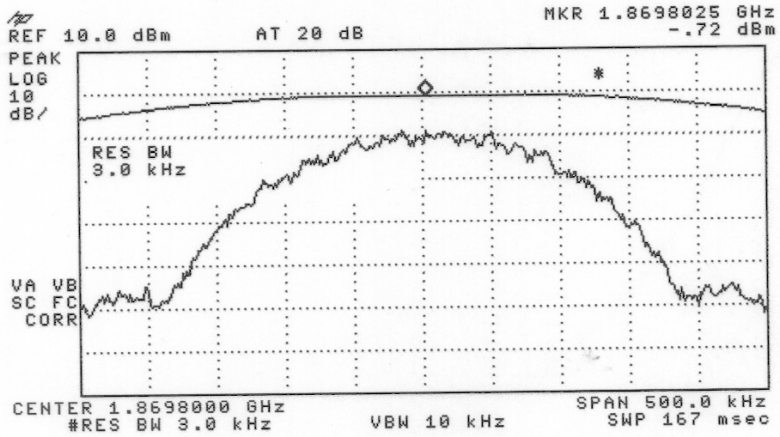
1) Spectrum plots of lower and upper bandedges at Block A



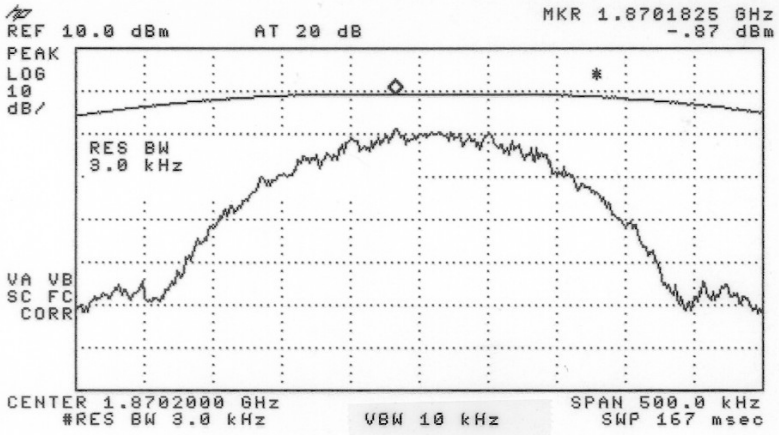


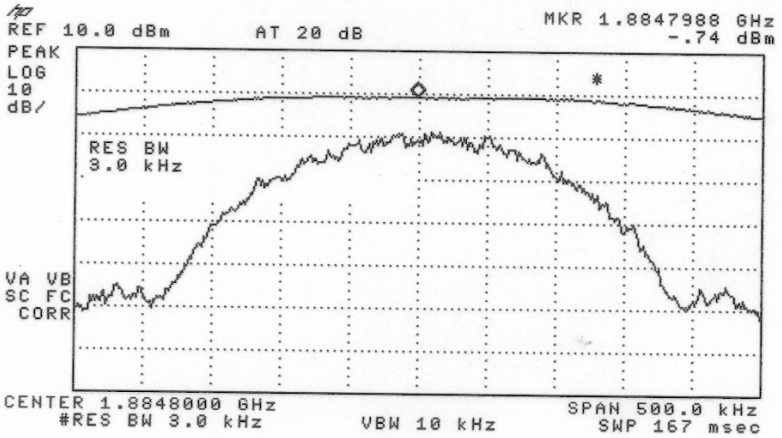
2) Spectrum plots of lower and upper bandedges at Block D



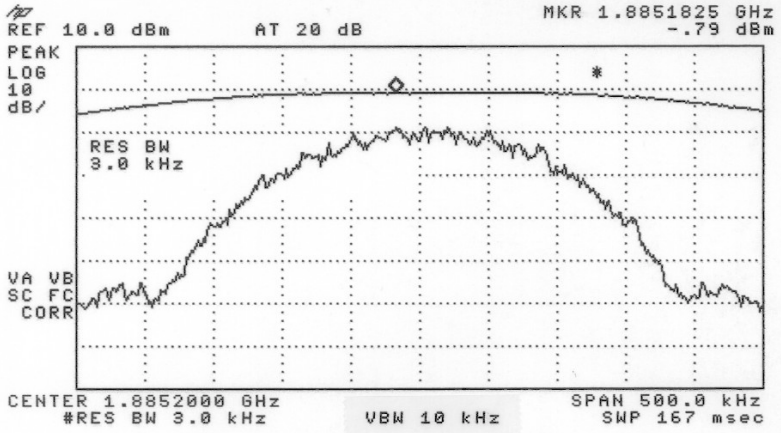


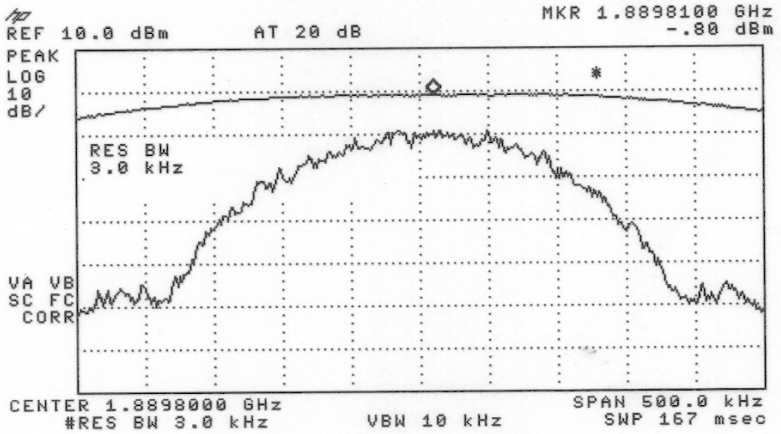
3) Spectrum plots of lower and upper bandedges at Block B



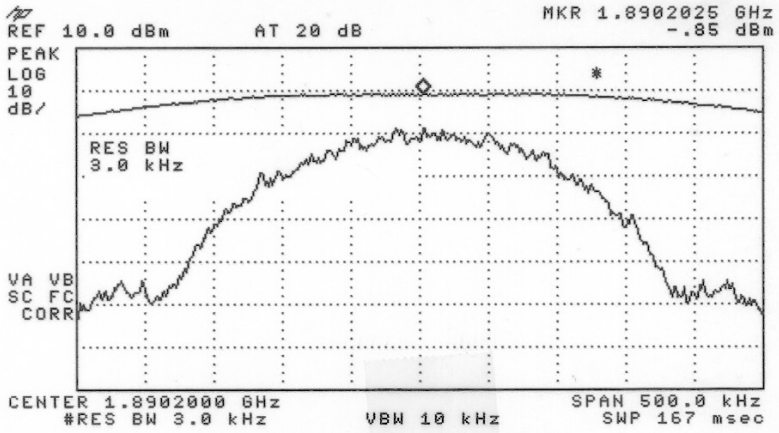


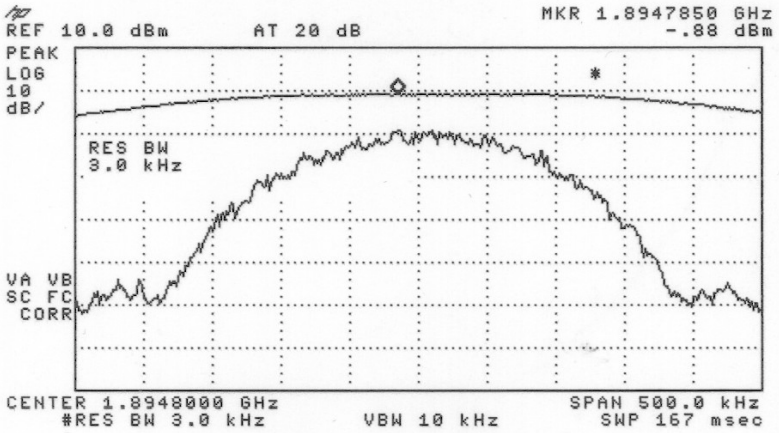
4) Spectrum plots of lower and upper bandedges at Block E





5) Spectrum plots of lower and upper bandedges at Block F





6) Spectrum plots of lower and upper bandedges at Block C