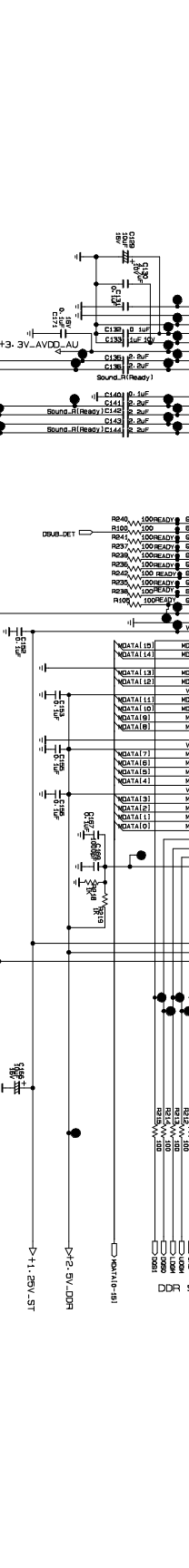
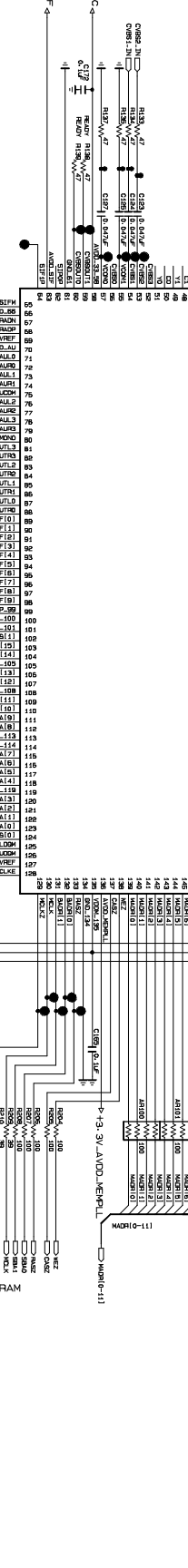
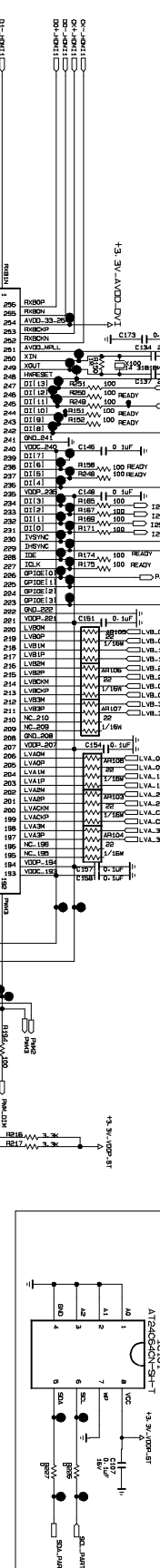
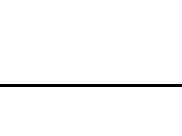
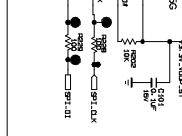
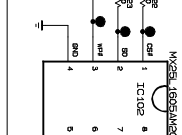
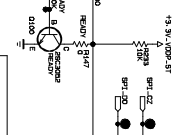
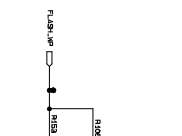
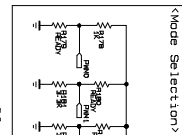
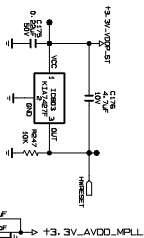


EAX61350002 MSTAR LMO2A

1. MSTAR



THE Δ SYMBOL MARK OF THIS SCHEMATIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FILTR AND ELECTRICAL SHOCK HAZARDS. WHEN SERVICING IT IS ESSENTIAL THAT ONLY MANUFACTURES SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE Δ SYMBOL MARK OF THE SCHEMATIC.

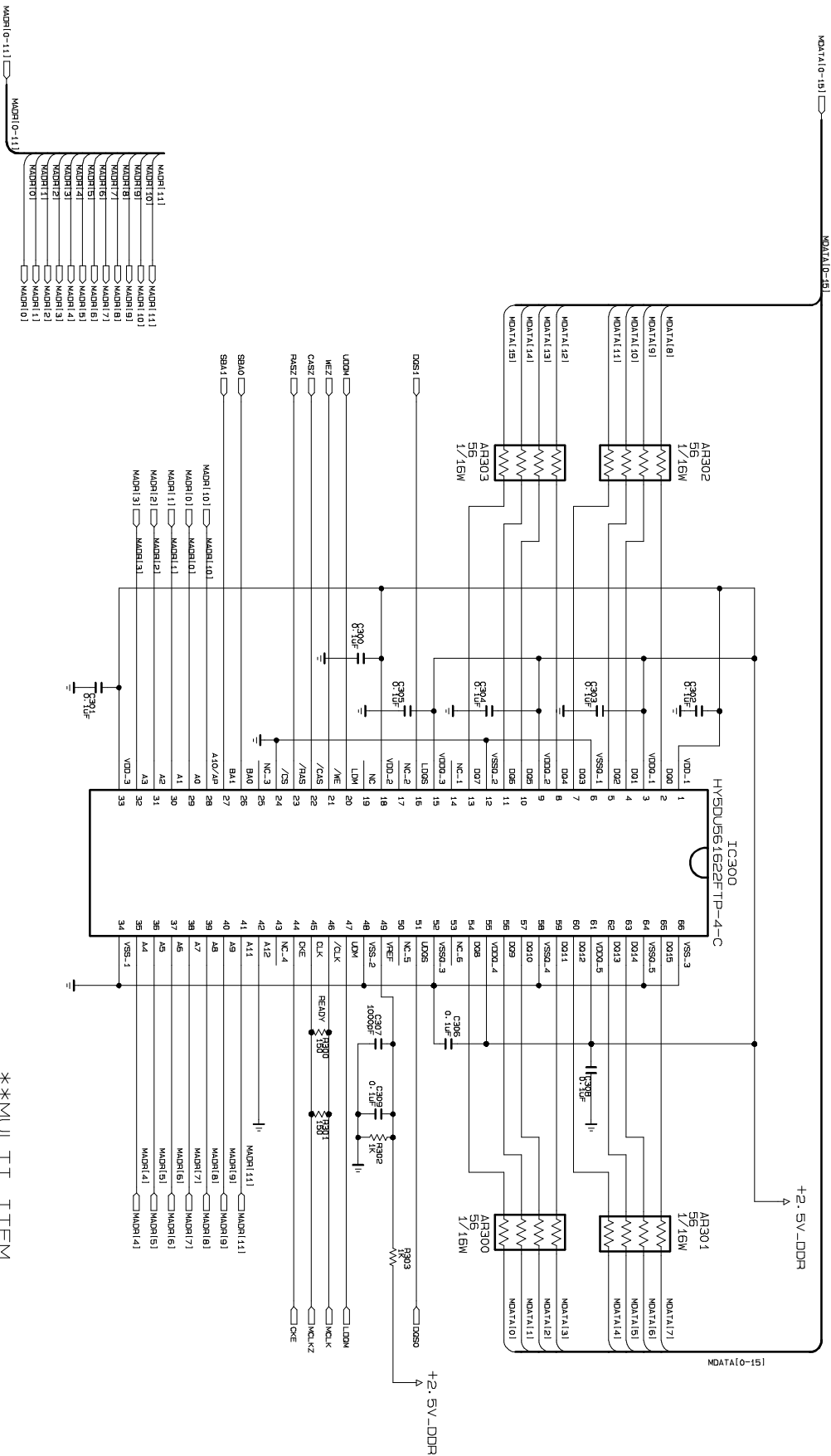
SECRET

LGElectronics

LG ELECTRONICS

MODEL SECURITY MNT DATE 2009/09/09
BLOCK MSTAR SHEET 1/7

#2. DDR



MAIN - HYNIX : EAN31729202

SECRET
LG ELECTRONICS

MODEL SECURITY MNT DATE 2009/09/09
BLOCK DDR SHEET 2 / 7

THE SYMBOL MARK OF THIS SCHEMATIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FILDE AND ELECTRICAL SHOCK HAZARDS. WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFACTURES SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE SYMBOL MARK OF THE SCHEMATIC.

Module

CONTROL Key

IR & LED

Zener Diode is close to wafer

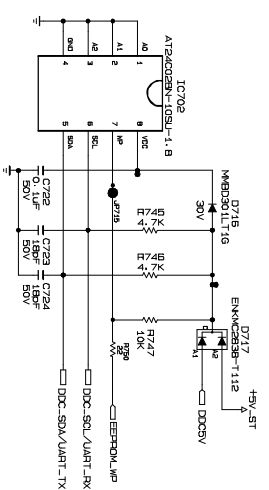
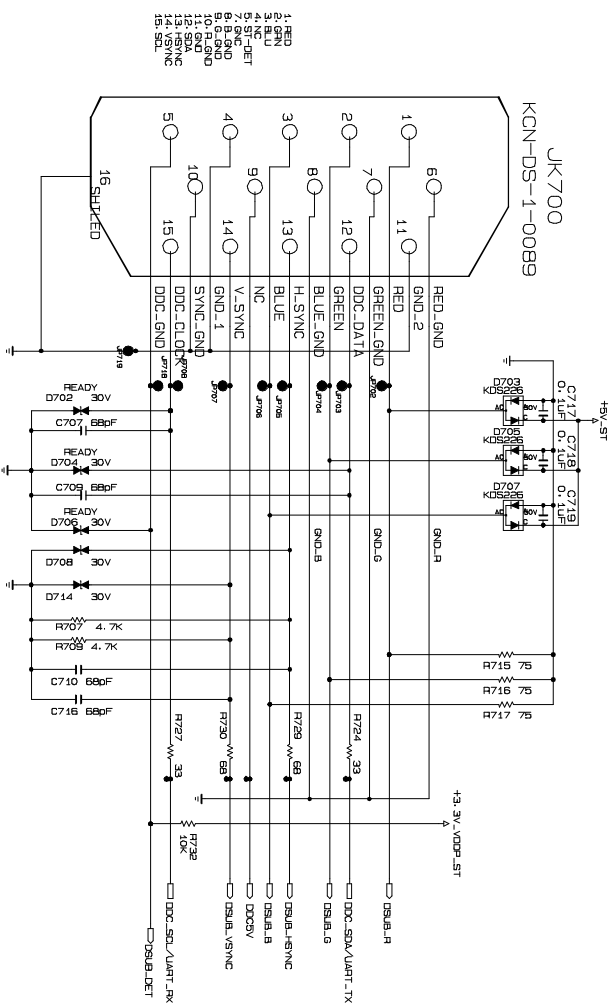
The diagram illustrates the internal circuitry of the 1207MH-07L P404 package. It features a central IC with pins 1 through 8. The circuit includes a +3.3V VDDP-ST supply connected to pin 1 via a 3.3k resistor (R408) and a 100nF capacitor (C404). A +3.3V LED supply is connected to pin 2 via a 4.7k resistor (R405) and a 100nF capacitor (C405). A +5V-ST supply is connected to pins 3 through 8 via 4.7k resistors (R406-R411) and 100nF capacitors (C406-C412). The package also contains several diodes (D401, D402, D403, D404, D405, D406, D407, D408, D409, D410, D411, D412) and capacitors (C401, C402, C403, C404, C405, C406, C407, C408, C409, C410, C411, C412).

SECRET

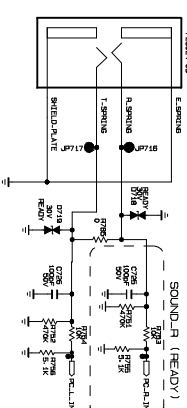
LG Electronics

MODEL	SECURITY MNT	DATE	2009/09/09
BLOCK	Module&Ct1	SHEET	3 / 7

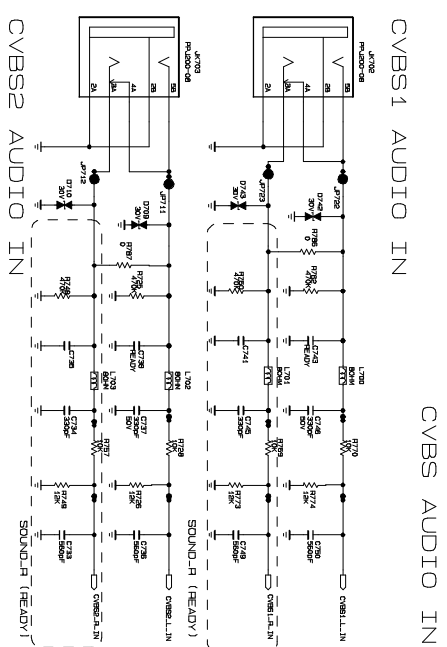
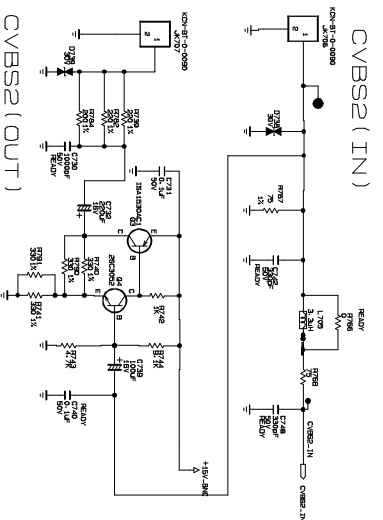
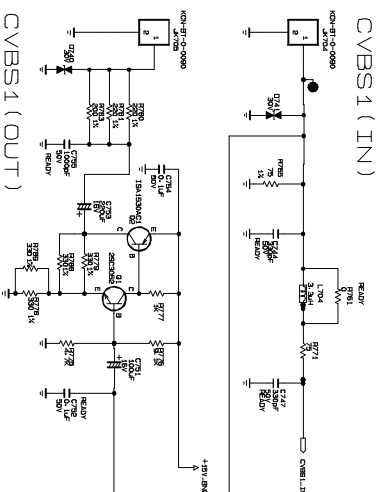
Form



PC AUDIO IN



CVBS



THE Δ SYMBOL MARK OF THIS SCHEMATIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION, FILTRER AND ELECTRICAL SHOCK HAZARDS. WHEN SERVISING IF IS ESSENTIAL THAT ONLY MANUFACTURERS SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE Δ SYMBOL MARK OF THE SCHEMATIC.

SECRET

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 LG ELECTRONICS

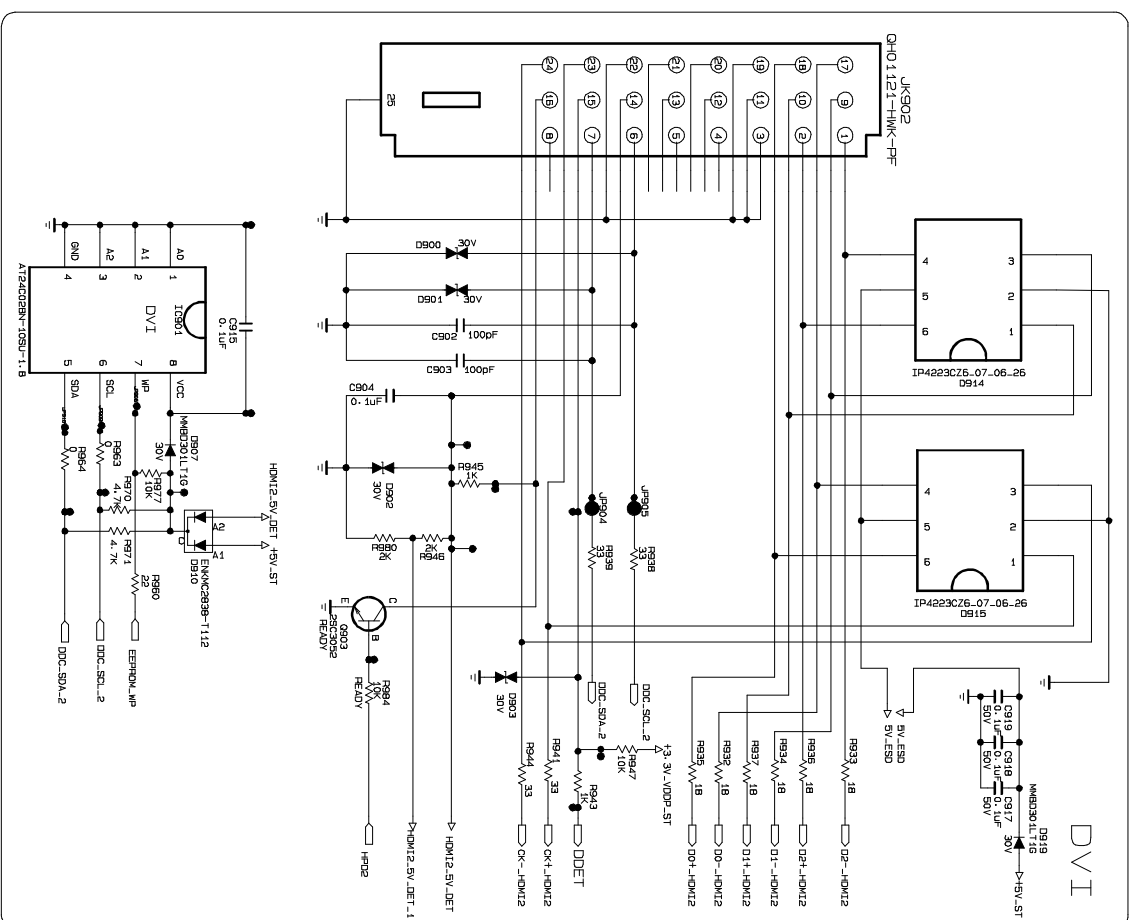
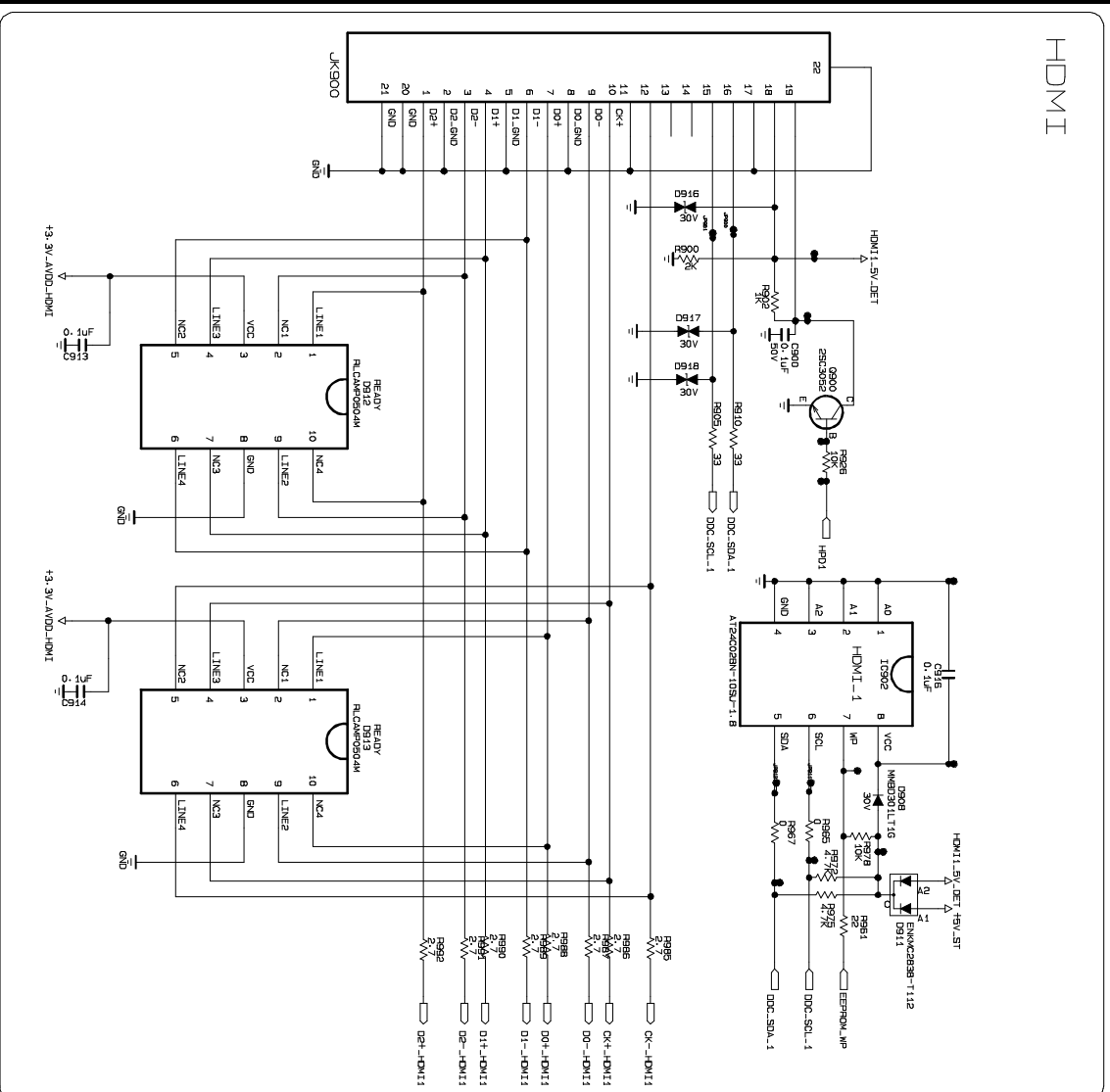
MODEL	SECURITY MNT	DATE
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BLOCK Interface SHEET

2009/09/05

$$\frac{4}{7}$$

HDMI & DVI INPUT



THE  SYMBOL MARK OF THIS SCHEMATIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION, FILRE AND ELECTRICAL SHOCK HAZARDS. WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFACTURERS SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE  SYMBOL MARK OF THE SCHEMATIC.

SECRET

LG Electronics



LG ELECTRONICS

MODEL

SECURITY MNT

DATE _____

2009/09/09

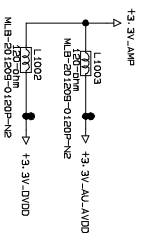
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HDMI/DVI

SHEET

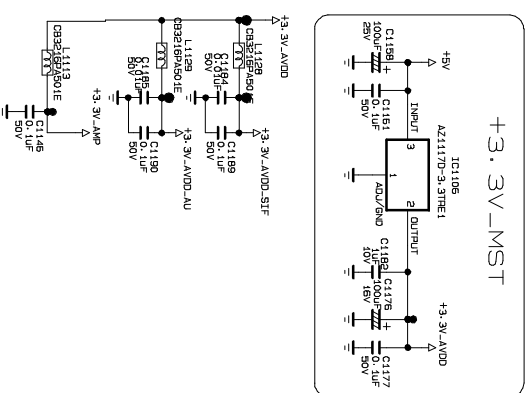
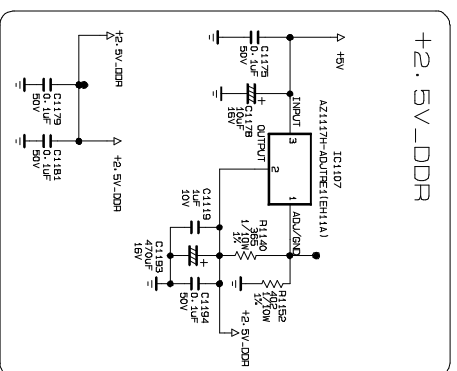
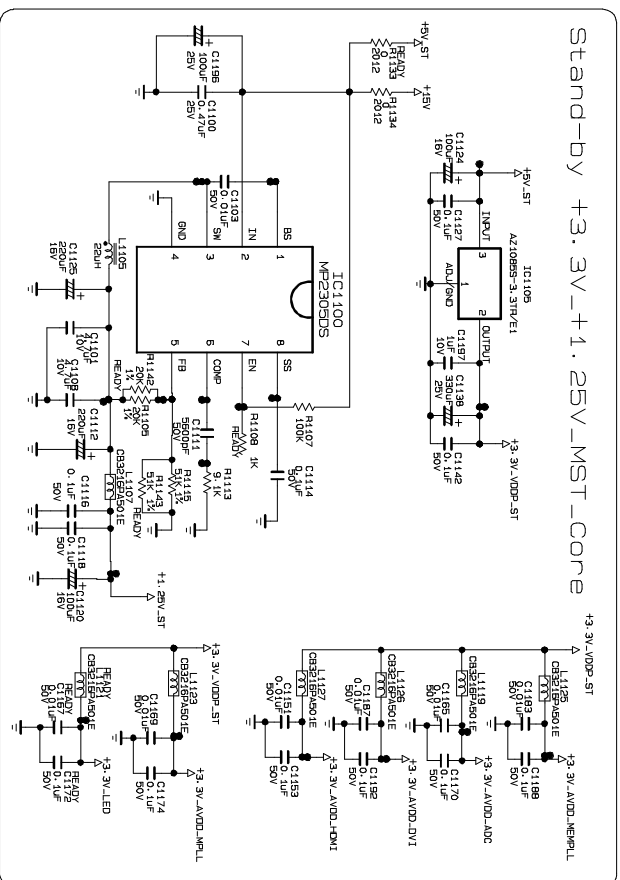
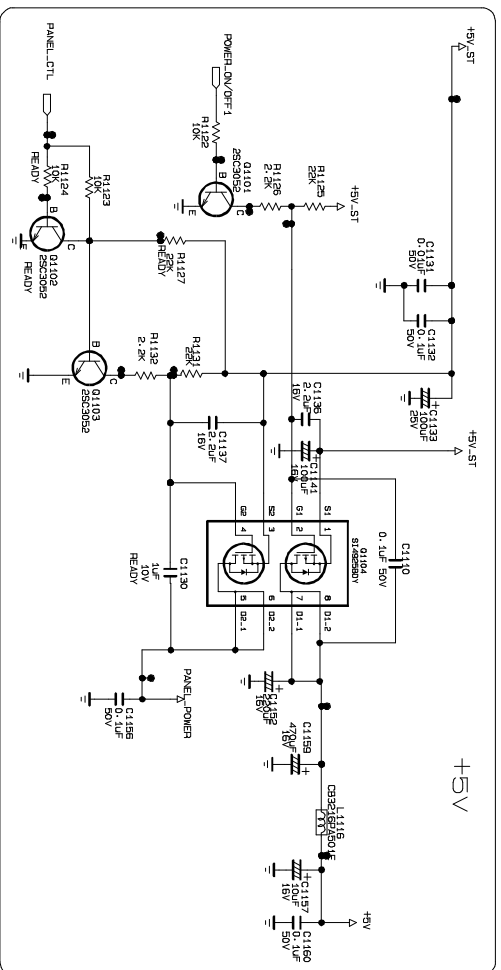
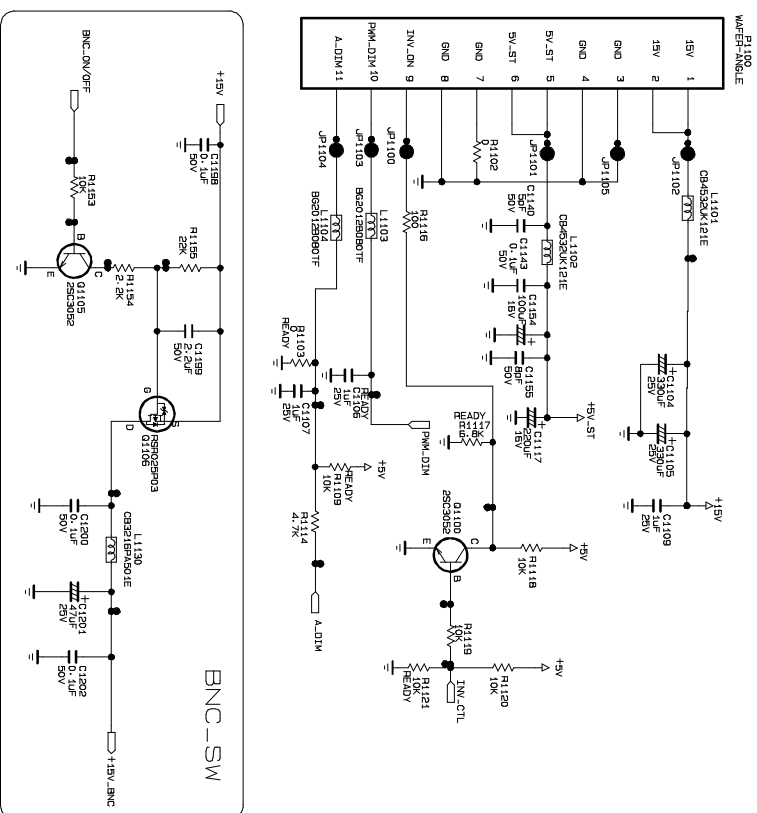
7/

Separate DGND and AVSS



MODEL	SECURITY MNT	DATE	2009/09/09
BLOCK	AMP	SHEET	6 / 7

FROM LIPS & POWER B/D



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SECRET

LG Electronics



MODEL	SECURITY MNT	DATE
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SECURITY MNT	DATE	2009/09/09
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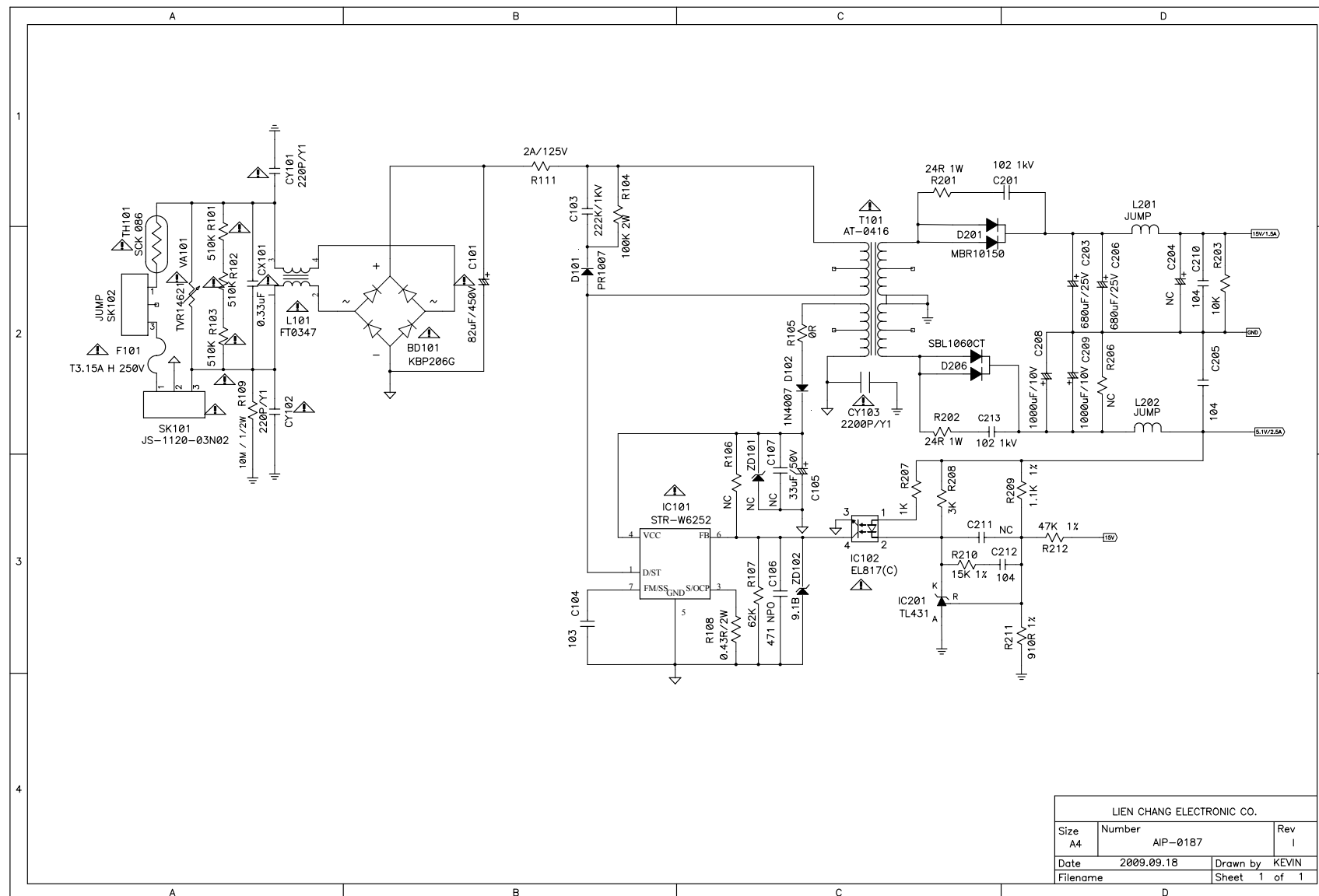
WIK

Power

SHEE

777

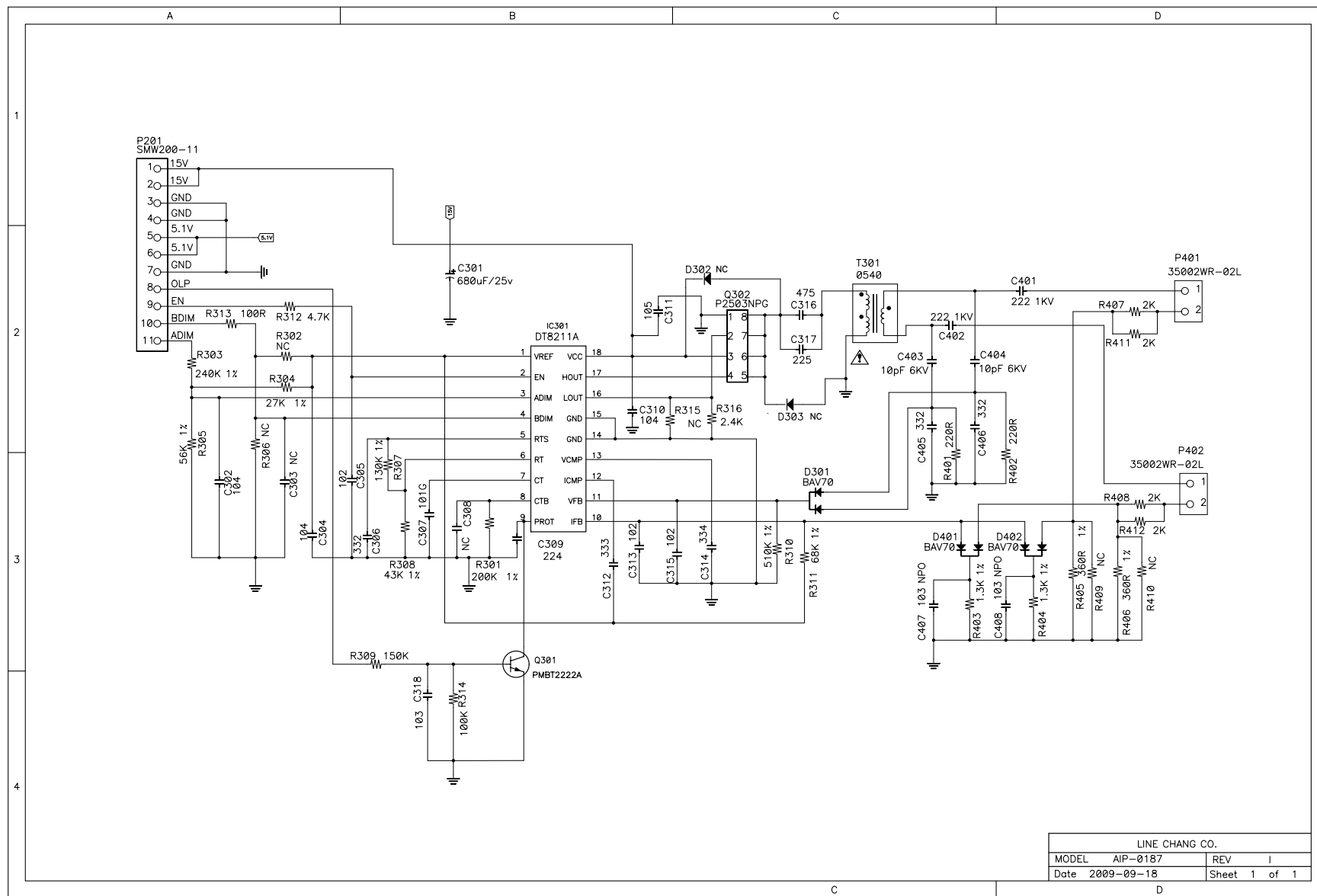
1. POWER CIRCUIT DIAGRAM :



MODEL:AIP-0187~A(5805/5806) Sepc Rev.No.:9.0

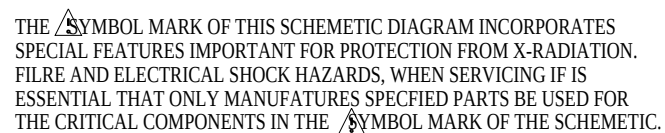
33/87

2. INVERTER CIRCUIT DIAGRAM :

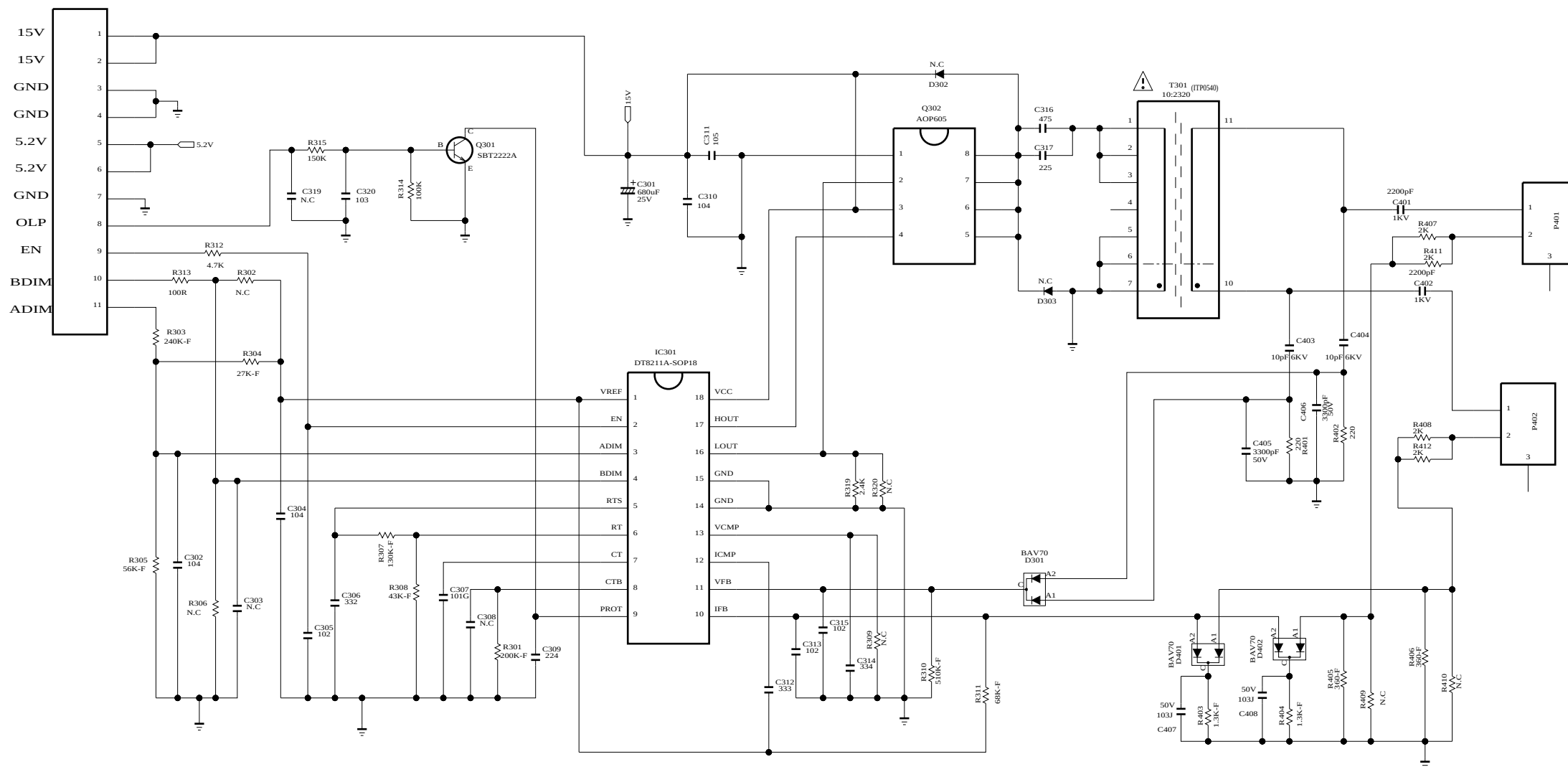


MODEL:AIP-0187~A(5805/5806) Sepc Rev.No.:9.0

34/87

 LG INNOTEK

MODEL	PLLM-M803A/B	REV. DATE	
BLOCK	Power	SHEET	1 / 2



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REG. DATE : 2009.09.11

 LG INNOTEK

LGE P/NO: EAY48196102/3

MODEL	PLLM-M803A/B	REV. DATE	
BLOCK	Inverter	SHEET	2 / 2