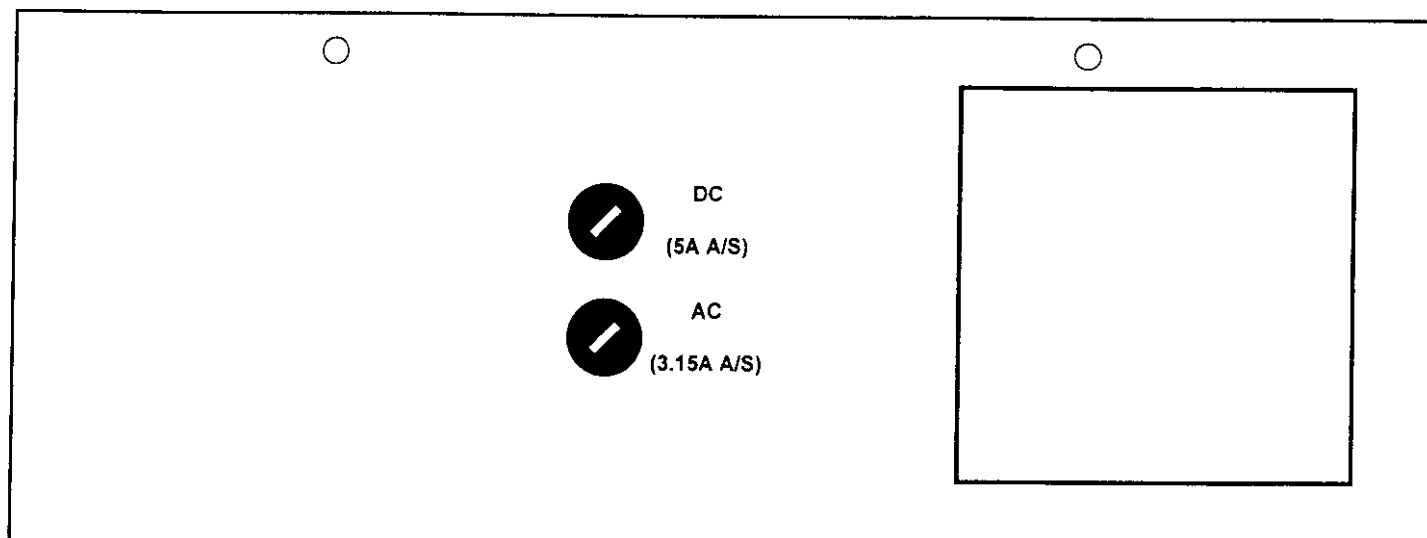
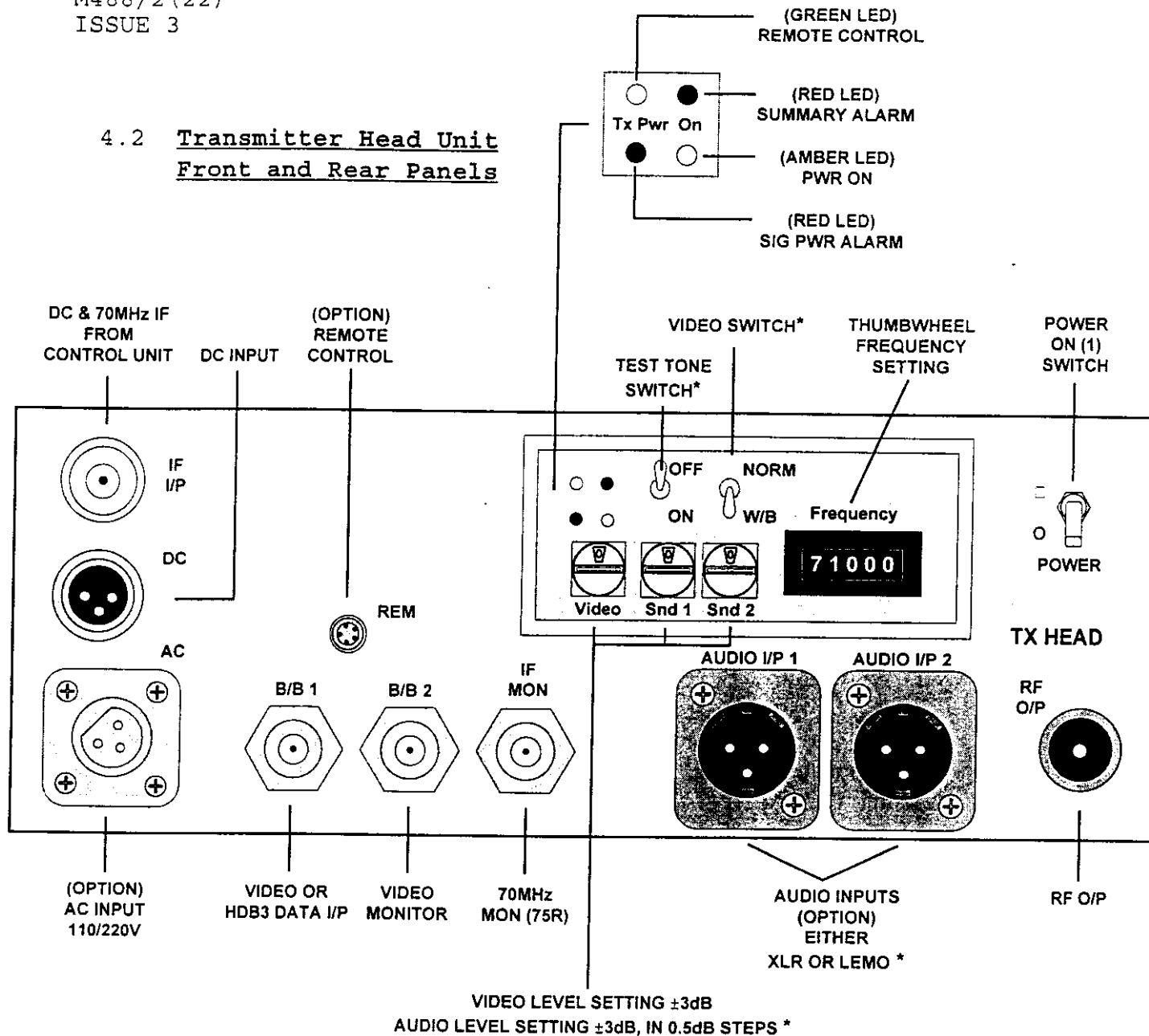
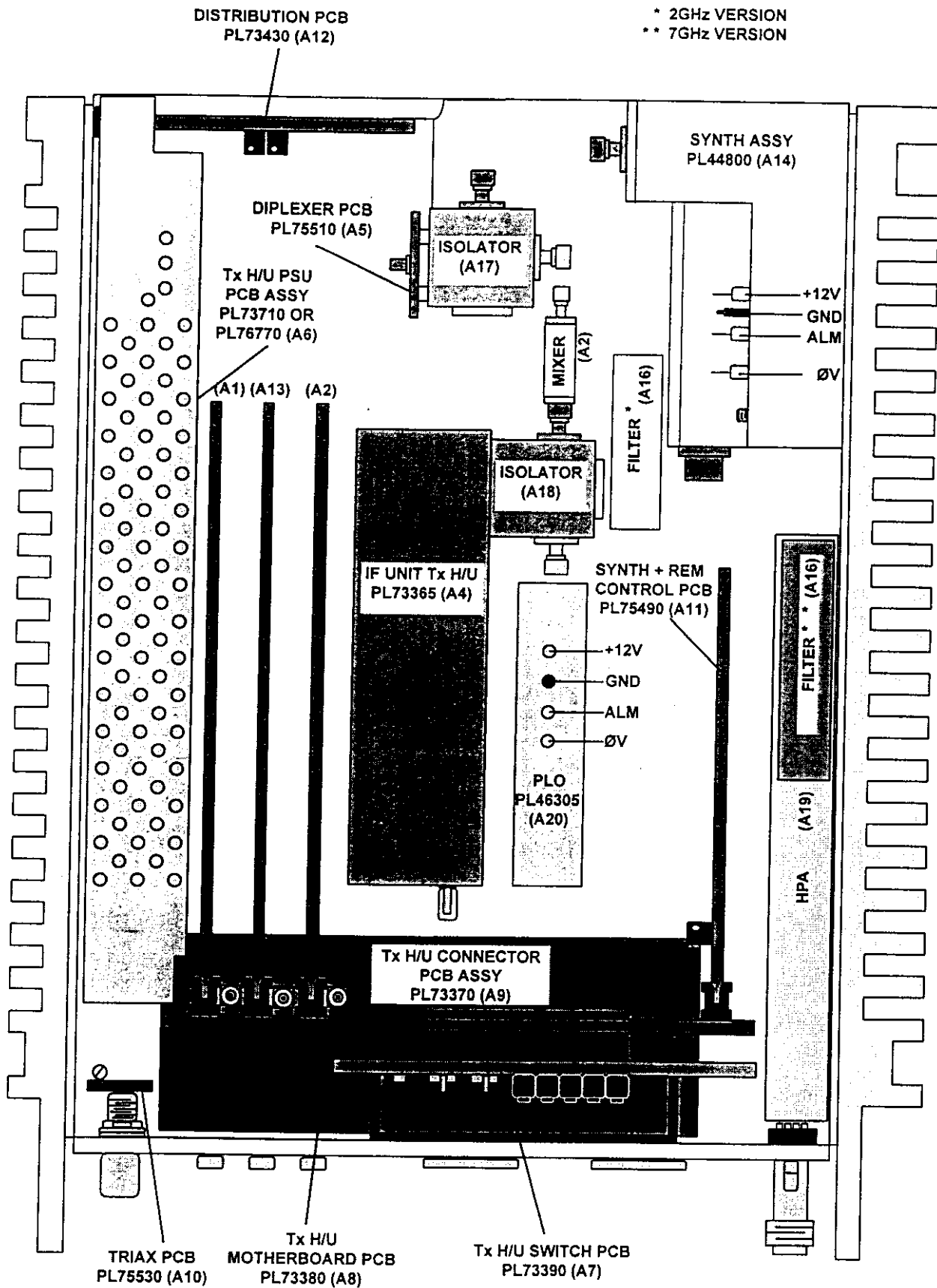


4.2 Transmitter Head Unit Front and Rear Panels



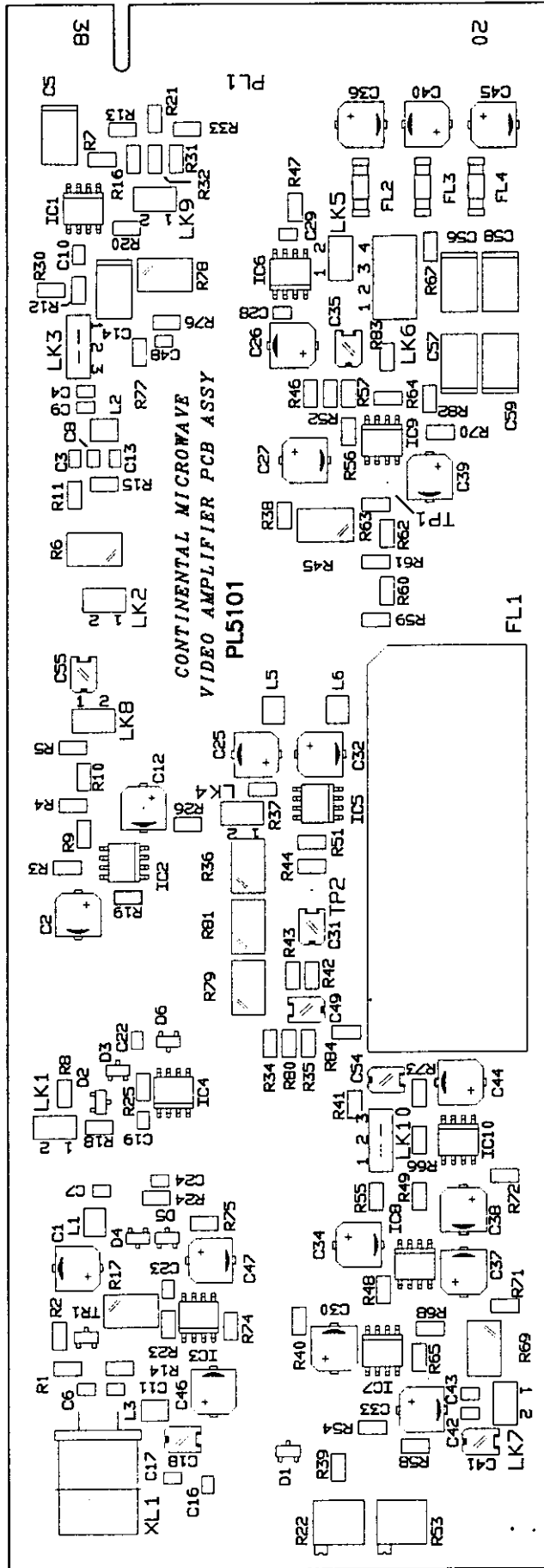
14PX488.DOC

Fig. 20 Transmitter Head Unit Front and Rear Panels

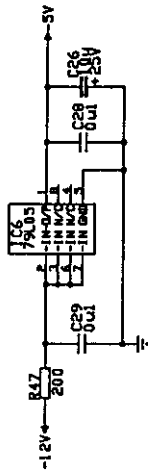
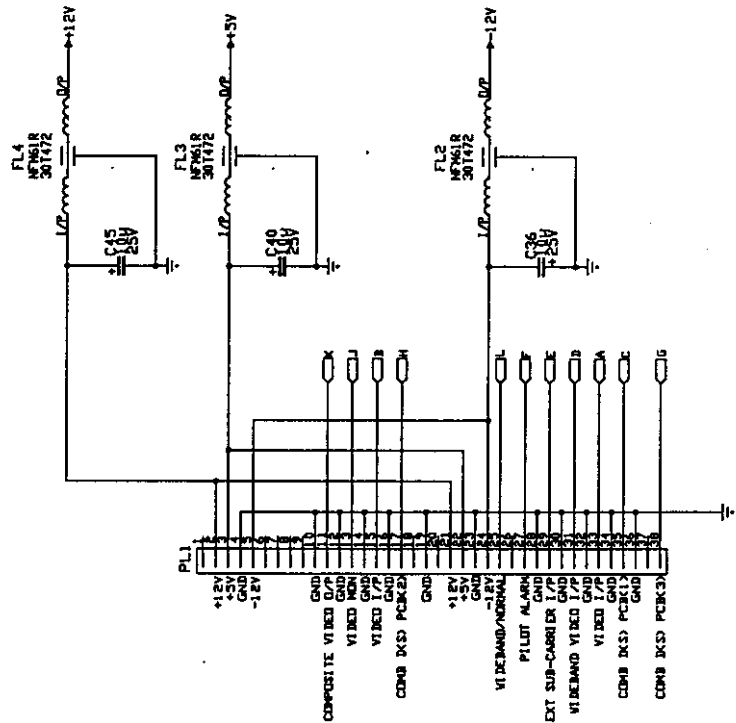


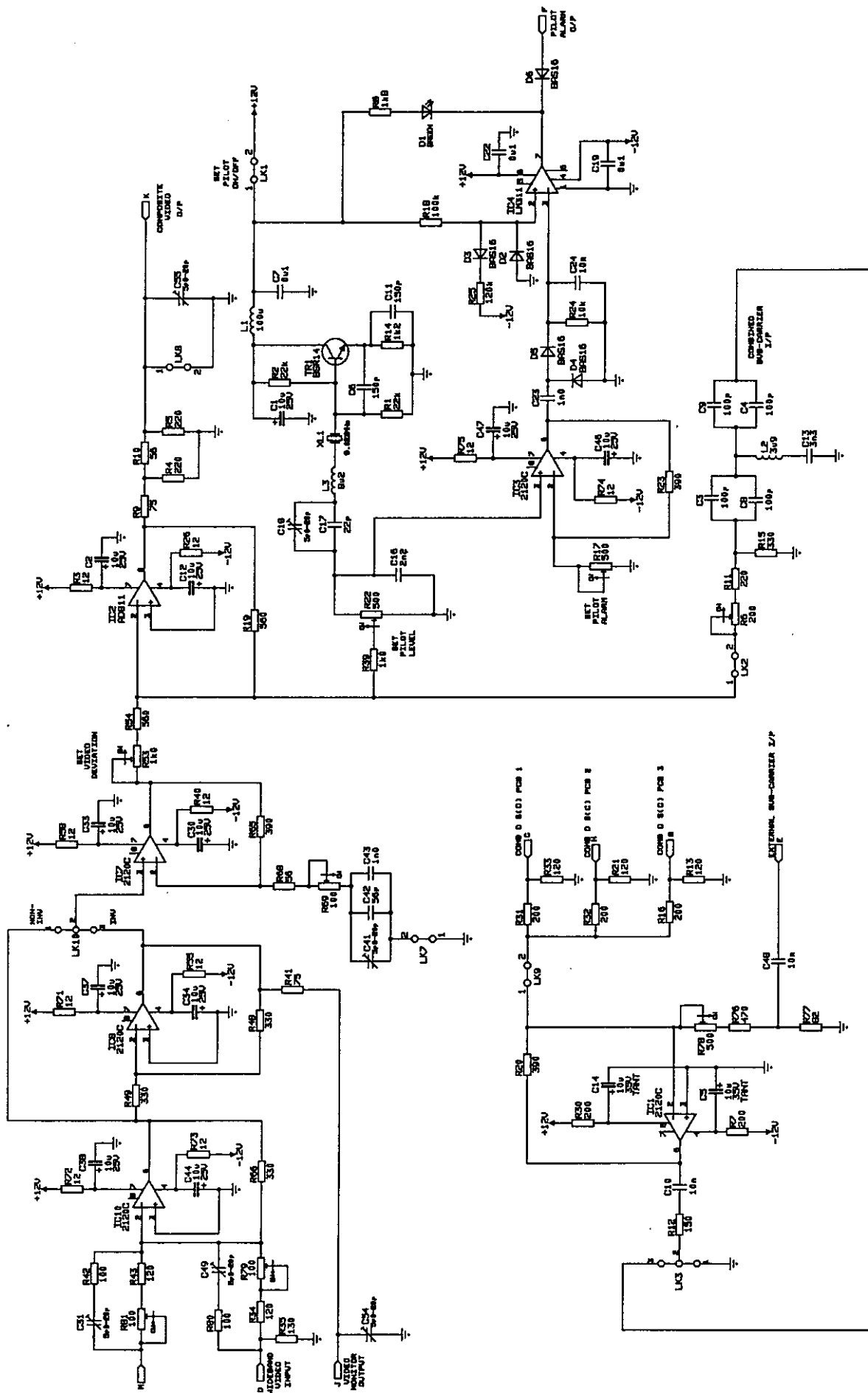
6PX488.DOC

Fig.24 Transmitter Head Unit Top View

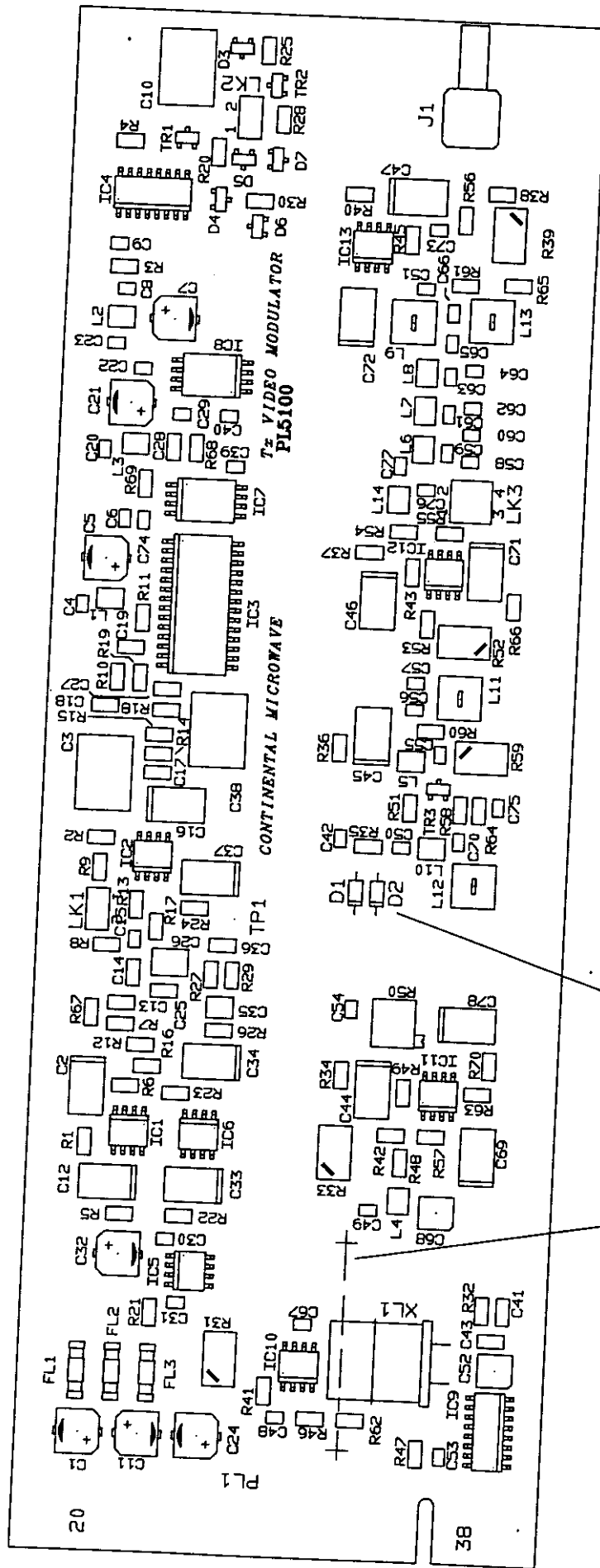


RF TECHNOLOGY, INC. COMMERCIAL IN CONFIDENCE		INNOVATIONS IN MICROWAVE COMMUNICATIONS	
TOLERANCES XX - 01 XXX - 005 XXXX - 0005 SUGLT - 1% SUGLT - 0.5%		COMPTON LAYOUT TRANSMITTER VIDEO AMP ACL TRANSMITTER	
DATE 7/29/96		MA5101	
MA5101.DWG			





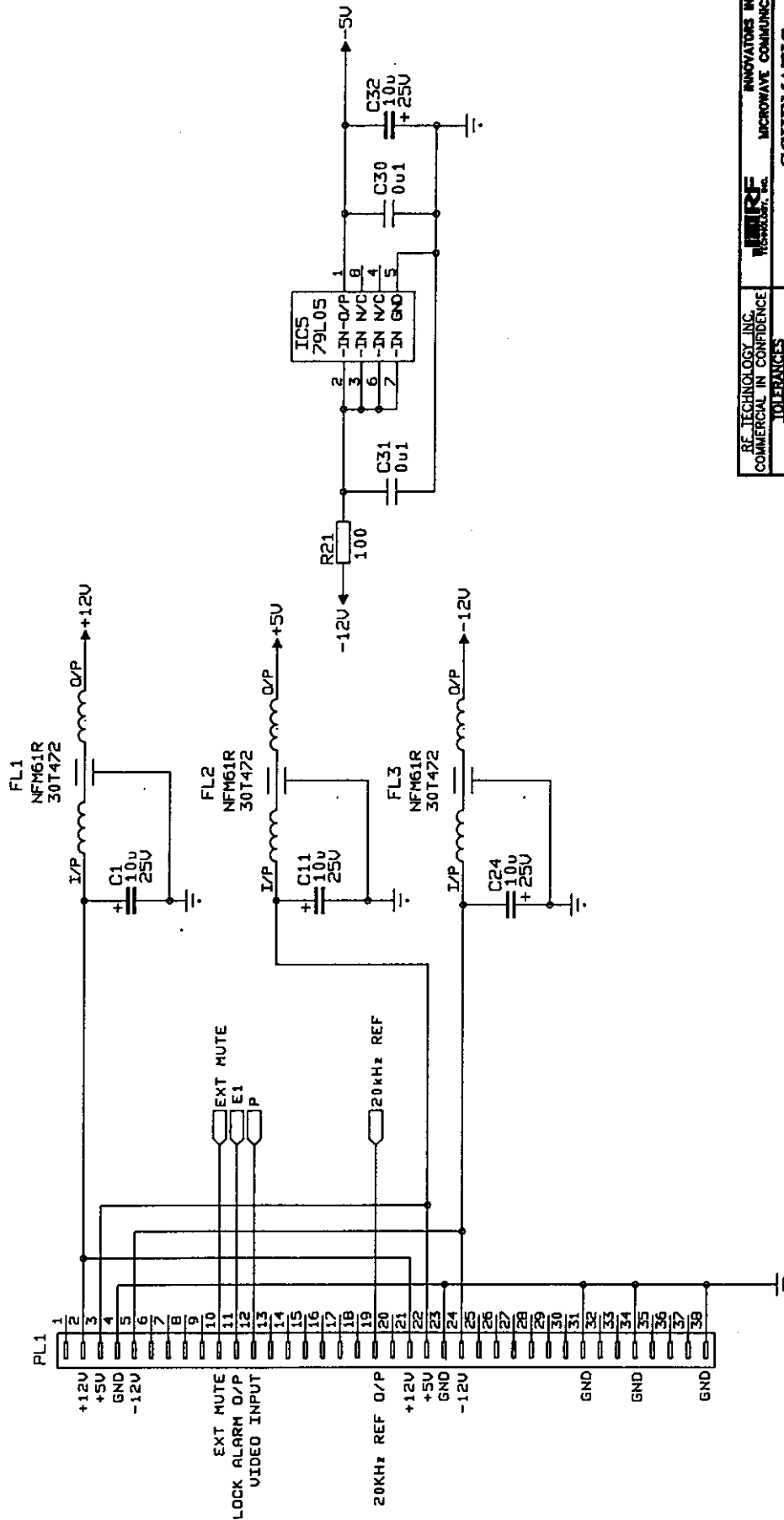
RF TECHNOLOGY INC.		INNOVATORS IN	
COMMERCIAL IN CONFIDENCE		MICROWAVE COMMUNICATIONS	
TOLERANCES		SCHEMATIC	
XX = .01		TRANSMITTER VIDEO AMP	
XXX = .005		ACL TRANSMITTER	
XXXX = .0005		DEM PF MW	
SCALE = .001		SM5101	
UNIT = INCHES		272	
DATE		7/29/96	
DRAWN		MA5101-1.DWG	



SOLDER SEMI-RIGID CABLE
TO UNDERSIDE OF PCB IN
EXACT POSITION SHOWN

BEND LEGS OF D1 & D2 TO
SOLDER ONTO PADS

RF TECHNOLOGY, INC. COMMERCIAL IN CONFIDENCE		INNOVATORS IN MICROWAVE COMMUNICATIONS	
TOLERANCES		COMPONENT LAYOUT VIDEO MODULATOR PCB	
XX	XXX	XXXX	ANGLE
TOLERANCES		ACL TRANSMITTER	
MA5100.DWG		MA5100	
7/29/96		7/29/96	



RF TECHNOLOGY INC.
COMMERCIAL IN CONFIDENCE

INNOVATORS IN
MICROWAVE COMMUNICATIONS

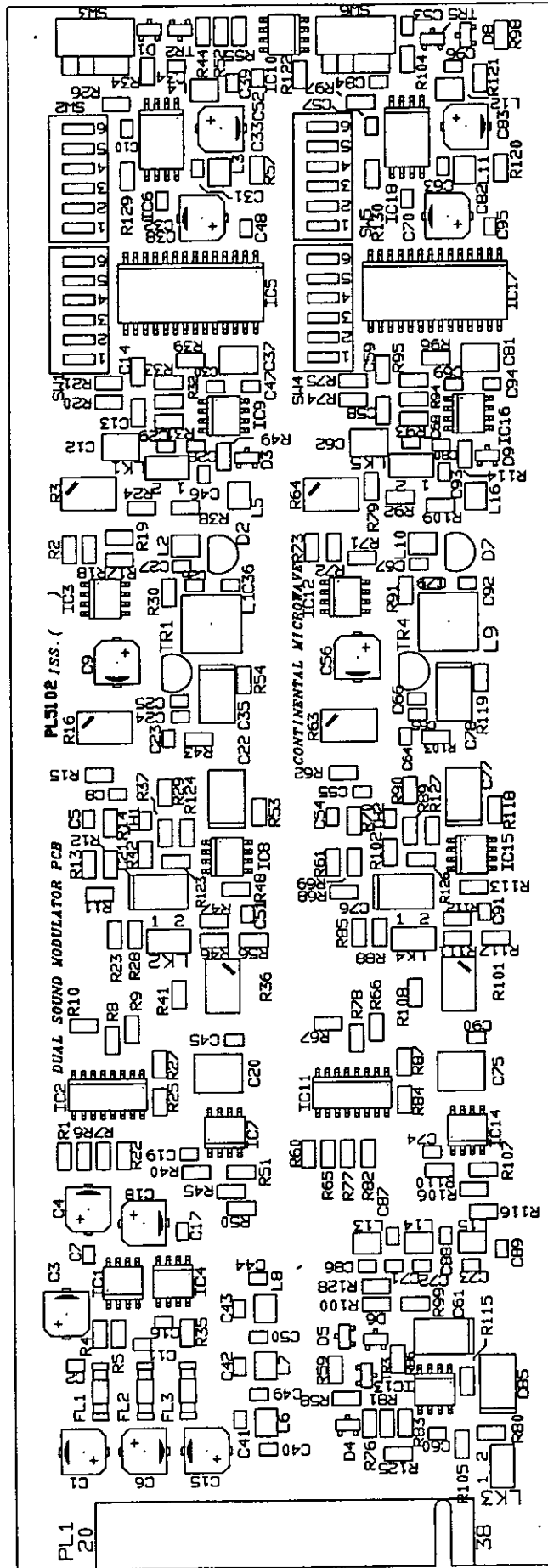
TOLERANCES

XX = .01
XXX = .005
XXXX = .0005
ANGLE = 0°
DIM. DECIMAL INCHES

SCHEMATIC
X-MITTER VIDEO MODULATOR
ACL TRANSMITTER

DATE 7/29/96
DRAWN BY PF MW
CHECKED BY MAS100
REV 1

MAS100-1.DWG

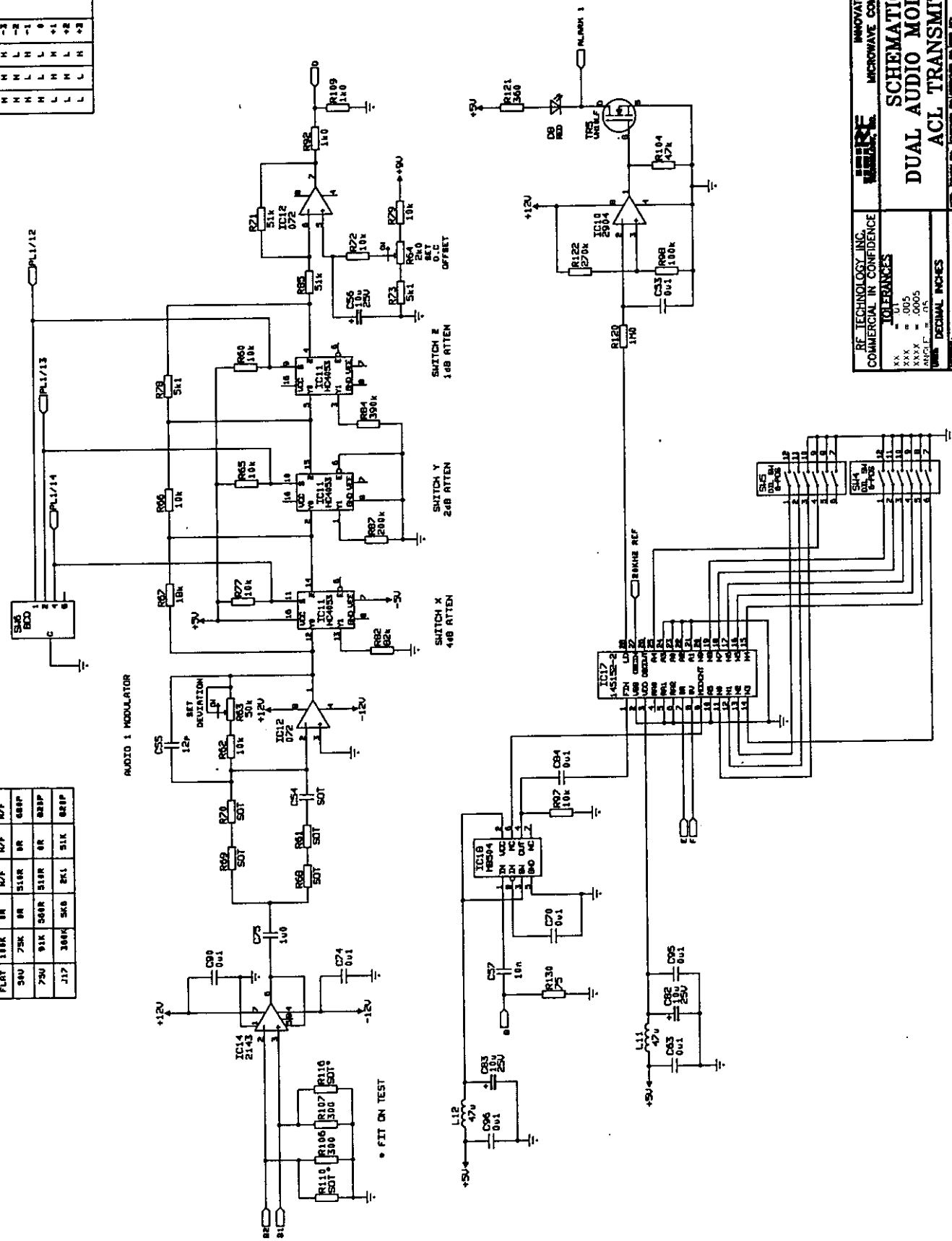


NOTE:-- SOLDER SEMI-RIGID CABLE (ITEM No.7000) TO THE UNDERSIDE OF THE PCB IN POSITIONS SHOWN.

RF TECHNOLOGY INC COMMERCIAL IN CONFIDENCE	INNOVATORS IN MICROWAVE COMMUNICATIONS
TOLERANCES XX - .010 XXX - .005 XXXX - .0005 XXXXX - .0001	COMPONENT LAYOUT DUAL AUDIO MODULATOR ACL TRANSMITTER
UNITS DECIMAL INCHES	
DATE 7/29/96	DRAWN BY PF MW
PROJECT MA5102.DWG	MA5102

PRE-EMPHASIS				
DPP	R59	R79	R61	C54
FLAT	100K	0R	N/F	N/F
340	75K	0R	510R	0R0P
750	91K	500R	510R	0R
J17	300K	5K0	5K1	51K

AUDIO LEVEL ATTENUATION				SUM POS'n
4	2	1	10	
M	M	H	-3	1
M	M	L	-2	2
M	M	H	-1	3
M	L	L	0	4
L	M	L	+1	5
L	M	L	+2	6
L	L	H	+3	



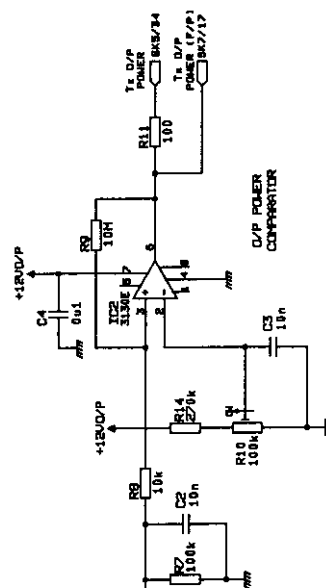
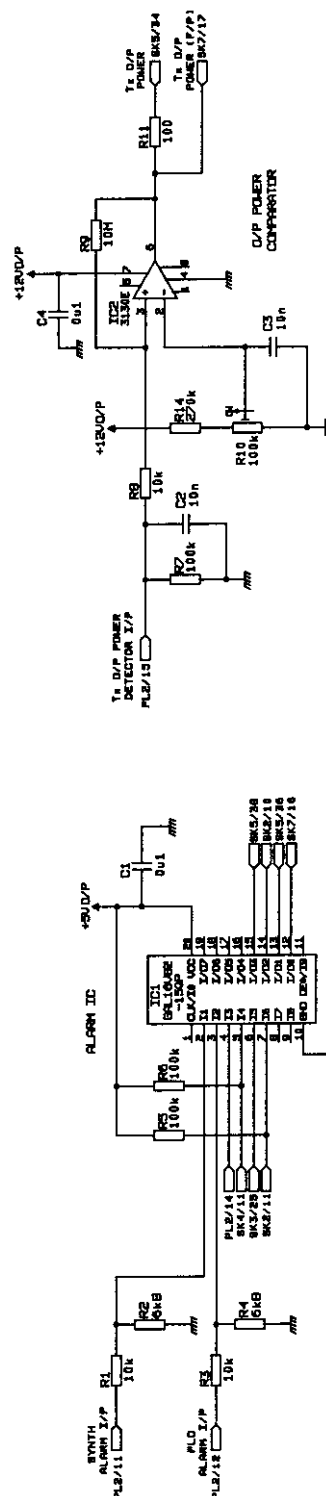
RF TECHNOLOGY INC.
COMMERCIAL IN CONFIDENCE

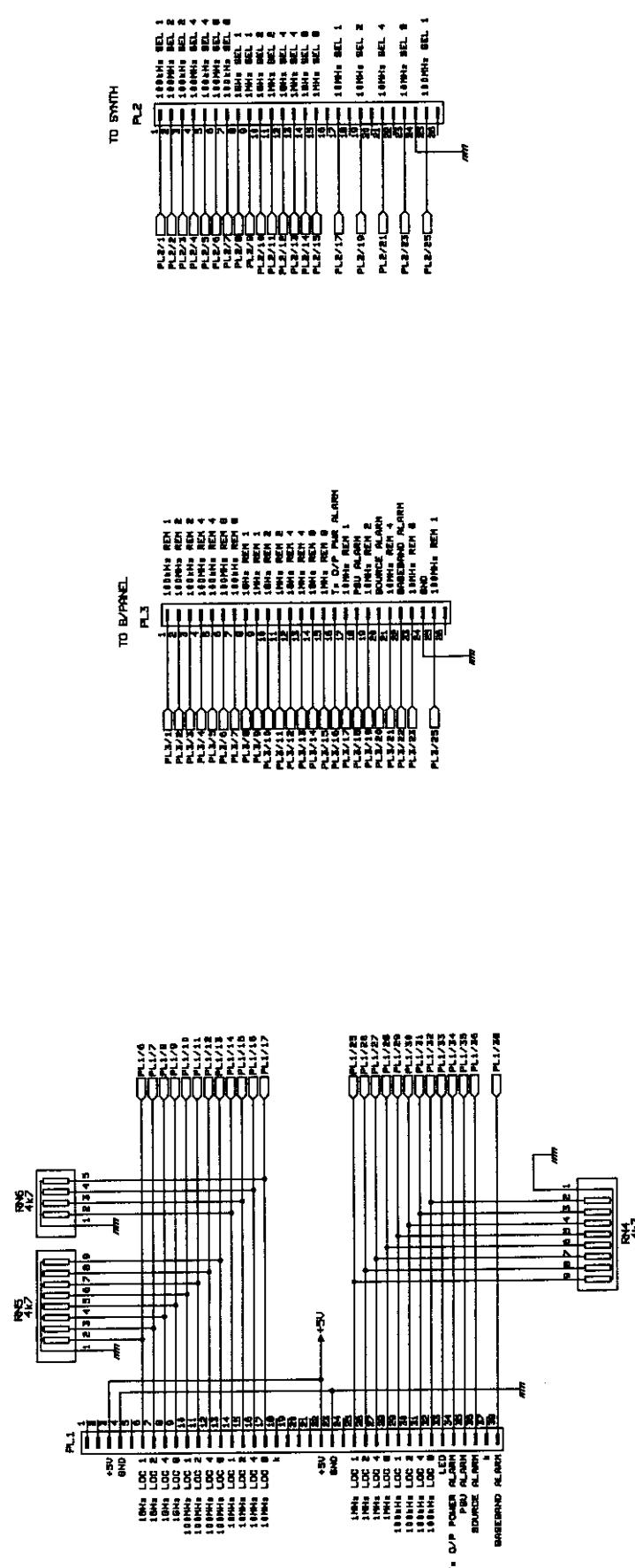
**INNOVATORS IN
MICROWAVE COMMUNICATIONS**

SCHEMATIC DUAL AUDIO MODULATOR ACL TRANSMITTER

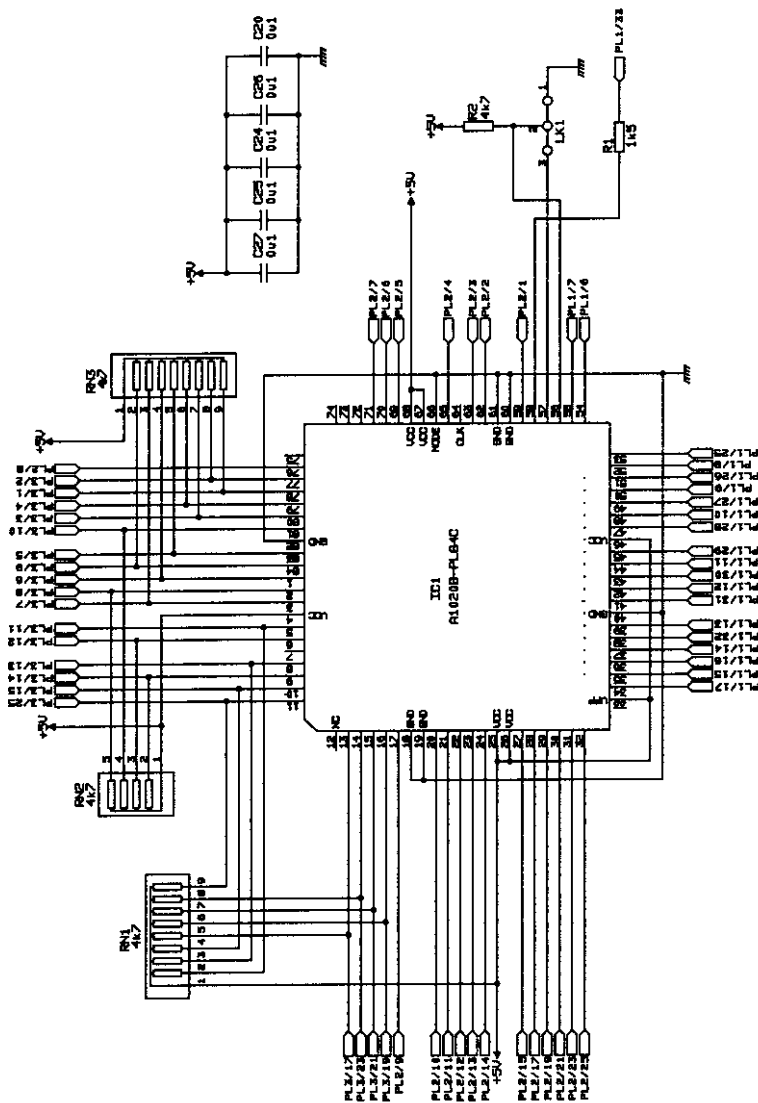
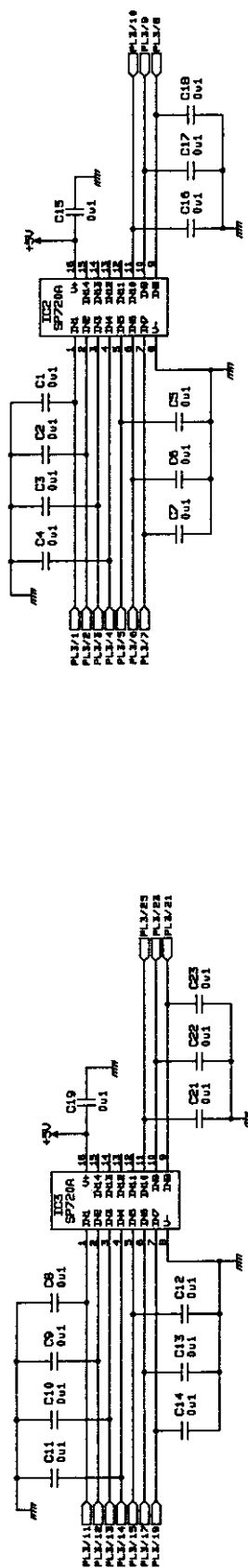
DATE	ISSUED ON	ORDERS ON	APPROVED BY	ISSUE NO.	REV
	DEM	PF	MW	MA5102	174
SOLD	DATE 7/29/96		ISSUE #	ISSUE 40423.PLT	

MA5102-1.DWG

[illegible]



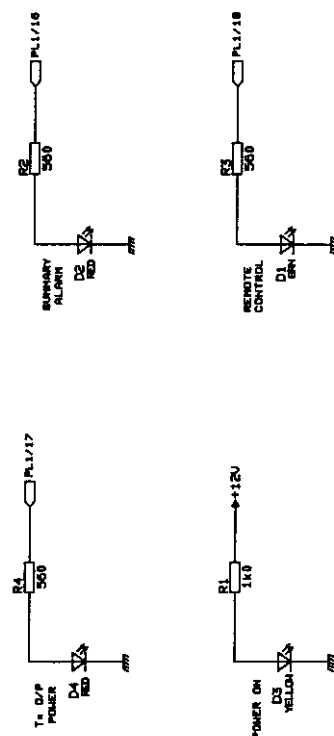
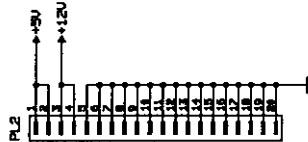
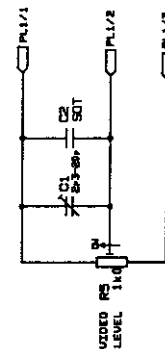
DATE	10/10/10	REV	1
DESIGNER	J. L. L. L.	CHECKED	J. L. L. L.
APPROVED	J. L. L. L.	DATE	10/10/10
THIS DRAWING IS THE PROPERTY OF CONTINENTAL ELECTRONICS AND IS NOT TO BE REPRODUCED OR TRANSMITTED IN ANY FORM OR BY ANY MEANS, ELECTRONIC OR MECHANICAL, INCLUDING PHOTOCOPYING, RECORDING, OR BY ANY INFORMATION STORAGE AND RETRIEVAL SYSTEM.			
CONTINENTAL			
TITLE: SYNTH & REM CONTROL PCB CIRCUIT DIAGRAM			
DESIGN NO.	E73403	SCALE	A
SHEET	1	OF	2
FIG.			

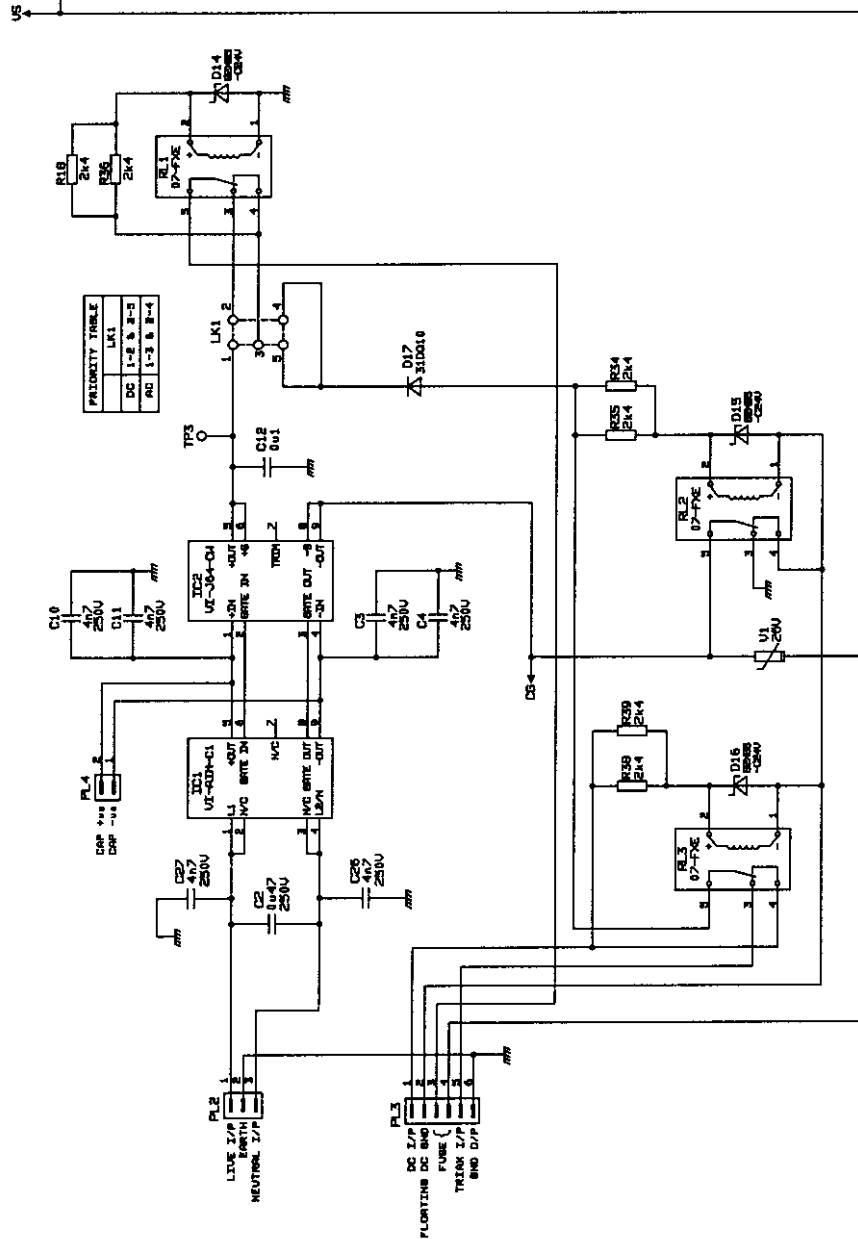


REV	DATE	BY	CHKD	APPD
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2	10/1/73	JMS	JMS	JMS
3	10/1/73	JMS	JMS	JMS
4	10/1/73	JMS	JMS	JMS
5	10/1/73	JMS	JMS	JMS
6	10/1/73	JMS	JMS	JMS
7	10/1/73	JMS	JMS	JMS
8	10/1/73	JMS	JMS	JMS
9	10/1/73	JMS	JMS	JMS
10	10/1/73	JMS	JMS	JMS

CONTINENTAL	
ELECTRONIC COMPONENTS	
TITLE SYNTH & REM CONTROL	
PCB CIRCUIT DIAGRAM	
REV	DATE
1	10/1/73
2	10/1/73
3	10/1/73
4	10/1/73
5	10/1/73
6	10/1/73
7	10/1/73
8	10/1/73
9	10/1/73
10	10/1/73

REV	DATE	BY	CHKD	APPD
1	10/1/73	JMS	JMS	JMS
2	10/1/73	JMS	JMS	JMS
3	10/1/73	JMS	JMS	JMS
4	10/1/73	JMS	JMS	JMS
5	10/1/73	JMS	JMS	JMS
6	10/1/73	JMS	JMS	JMS
7	10/1/73	JMS	JMS	JMS
8	10/1/73	JMS	JMS	JMS
9	10/1/73	JMS	JMS	JMS
10	10/1/73	JMS	JMS	JMS

[illegible]

[illegible]

ISSUED	DATE	PERIOD	QTR
P. METHODIZATION			11.4.88

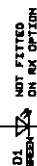
THIS ENGLISH ON PART THIRTY MAY NOT BE EXPLOYED OR REPRODUCED WITHOUT PRIOR WRITTEN PERMISSION

CONTINENTAL

ITEM# PSU PCB (UCL)

CIRCUIT DIAGRAM

E73413	C
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CONTINENTAL

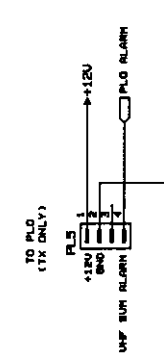
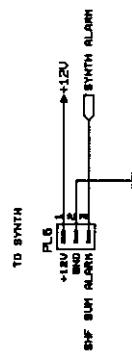
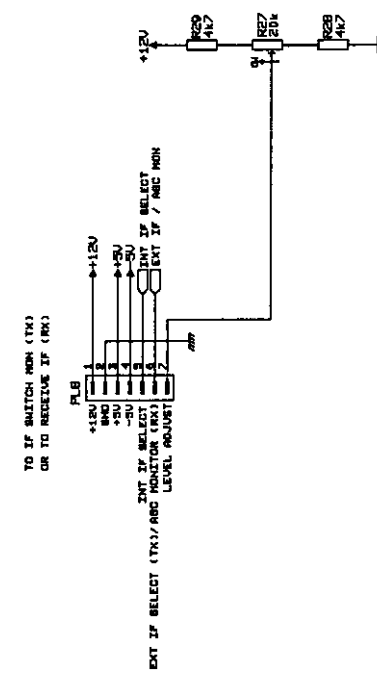
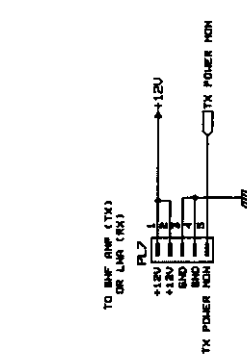
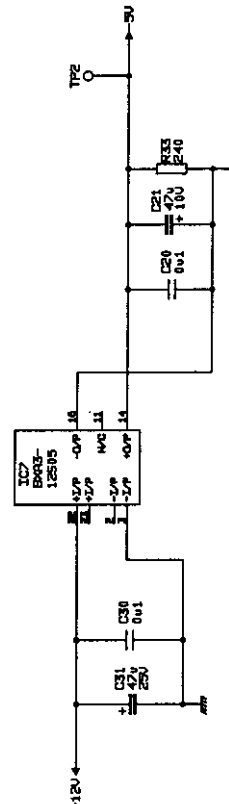
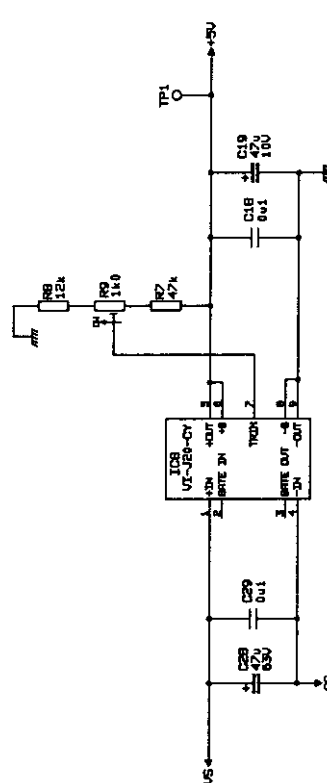
PSU PCB (UCL)
CIRCUIT DIAGRAM

777417

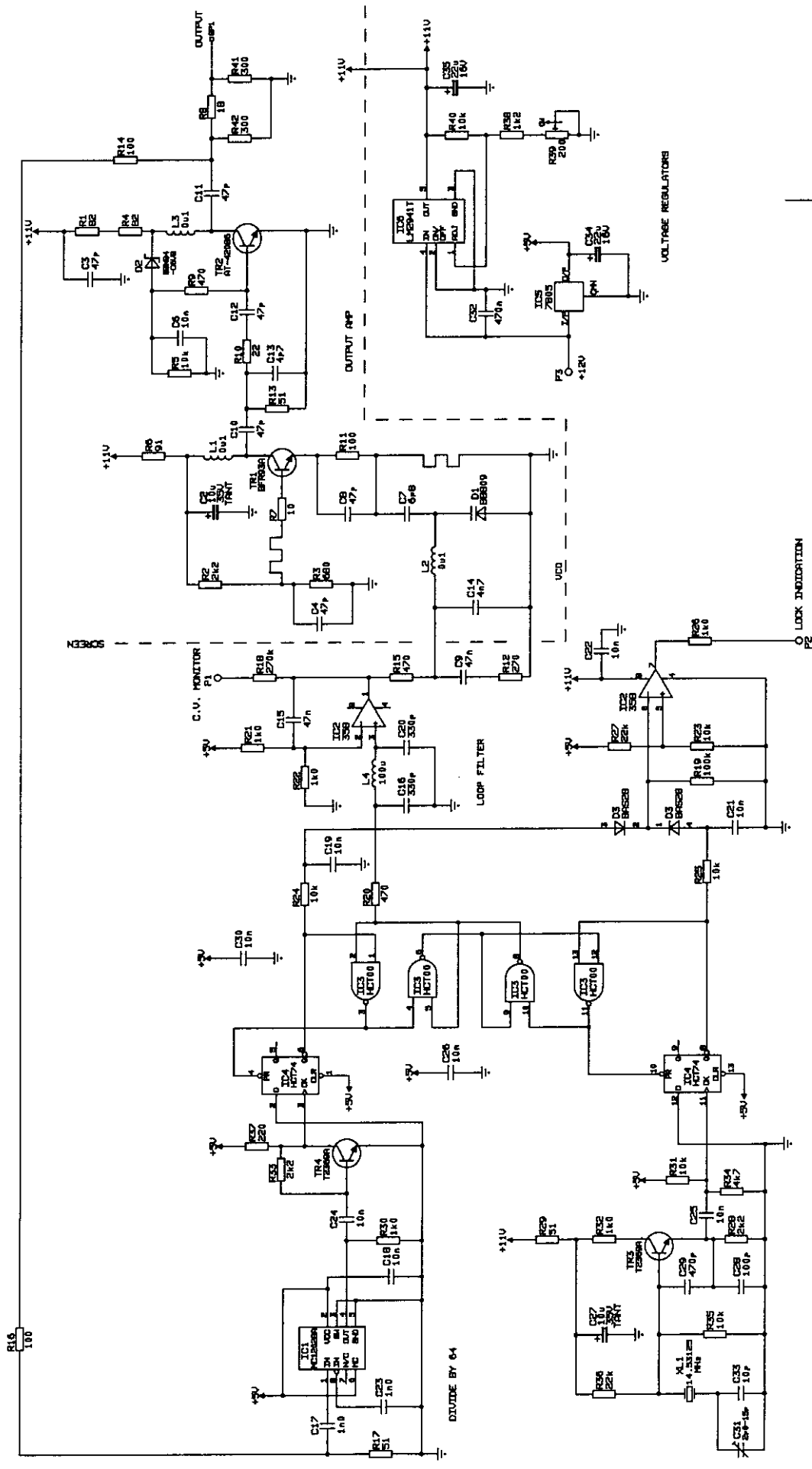
CIRCULI DIAGRAM

E73413

DATE	3	0-000000
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THIS DRAWING IS THE PROPERTY OF CONTINENTAL ELECTRONICS CORPORATION. IT IS TO BE USED ONLY FOR THE PURPOSES SPECIFIED HEREIN.			
CONTINENTAL ELECTRONICS CORPORATION			
PSU PCB (UOL)			
CIRCUIT DIAGRAM			
FIGURE NO.	E73413	REV	1
SHEET	1	OF	1
			AL



REV	DATE	BY	CHKD	APPD	DESCRIPTION
1	11/11/81	WJ	WJ		INITIAL DESIGN
2	11/11/81	WJ	WJ		REVISION
3	11/11/81	WJ	WJ		REVISION
4	11/11/81	WJ	WJ		REVISION
5	11/11/81	WJ	WJ		REVISION
6	11/11/81	WJ	WJ		REVISION
7	11/11/81	WJ	WJ		REVISION
8	11/11/81	WJ	WJ		REVISION
9	11/11/81	WJ	WJ		REVISION
10	11/11/81	WJ	WJ		REVISION

DESIGNED BY: WJ

DATE: 11/11/81

PROJECT: PLL 938MHz PCS

CIRCUIT DIAGRAM

REVISION: 1

DATE: 11/11/81

BY: WJ

CHKD: WJ

APPD: WJ

CONTINENTAL

PLD 938MHz PCS

CIRCUIT DIAGRAM

REVISION: 1

DATE: 11/11/81

BY: WJ

CHKD: WJ

APPD: WJ

FILE: E49579

DESIGNER: C

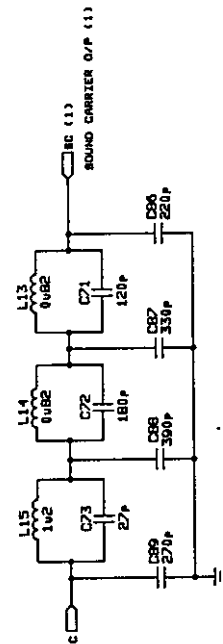
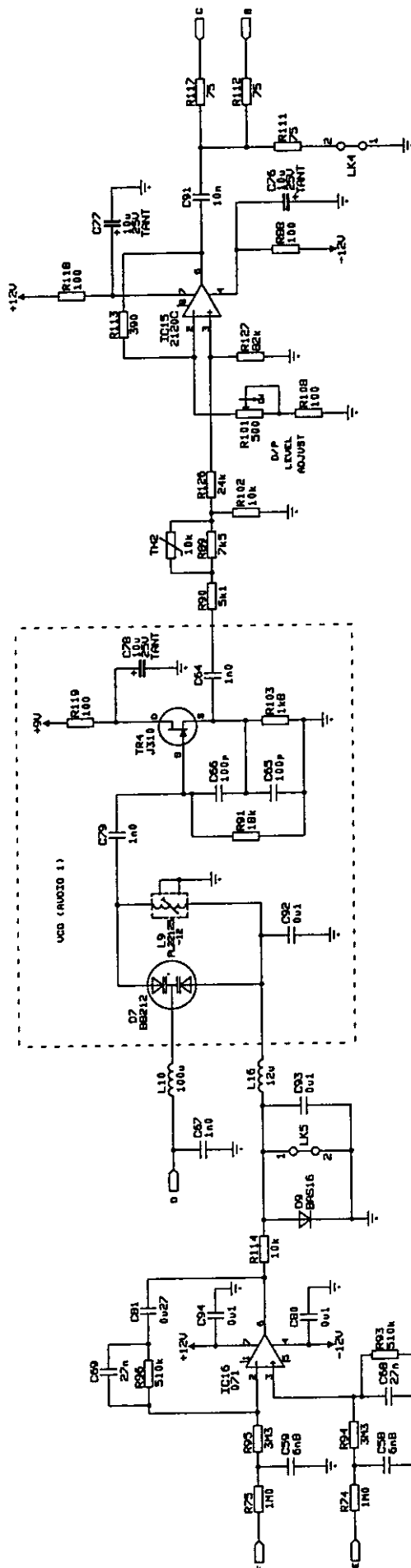
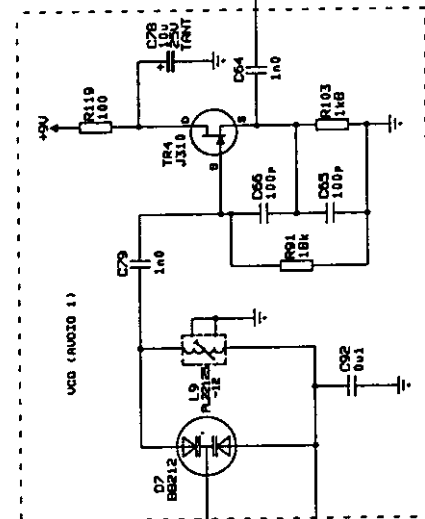
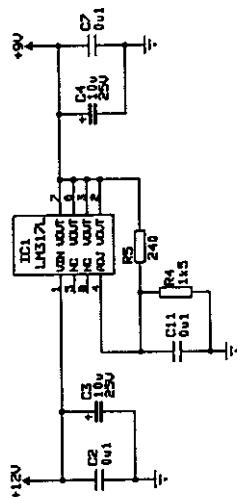
REVISION: 1

DATE: 11/11/81

BY: WJ

CHKD: WJ

APPD: WJ



RF TECHNOLOGY INC.
COMMERCIAL IN CONFIDENCE

INNOVATORS IN
MICROWAVE COMMUNICATIONS

TOLERANCES

XX = 0.1
XXX = 0.05
XXXX = 0.005
XXXXX = 0.001
MFG. = 0.001

SCHEMATIC
DUAL AUDIO MODULATOR
ACL TRANSMITTER

DATE 7/29/96

DESIGNER PF MW

PROJECT NA5102

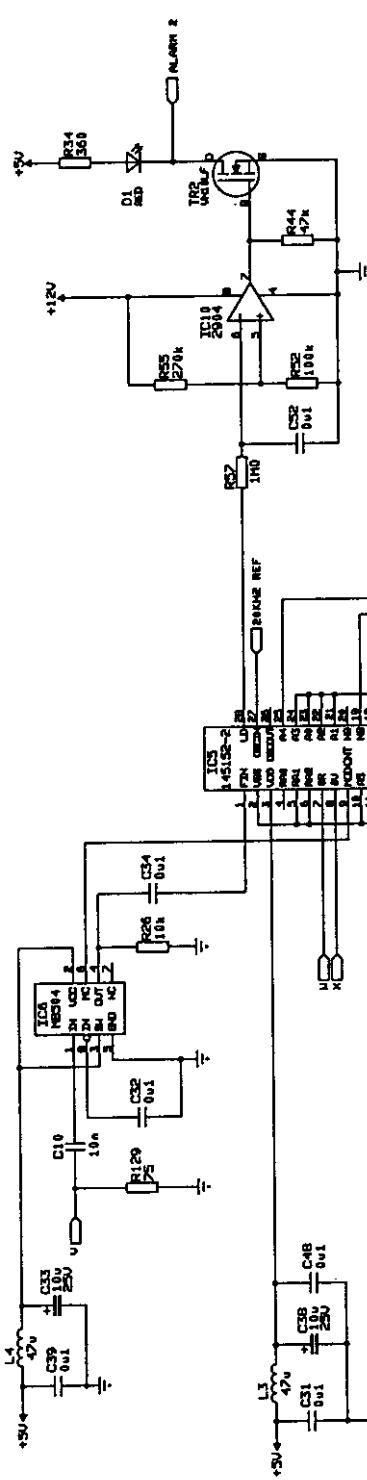
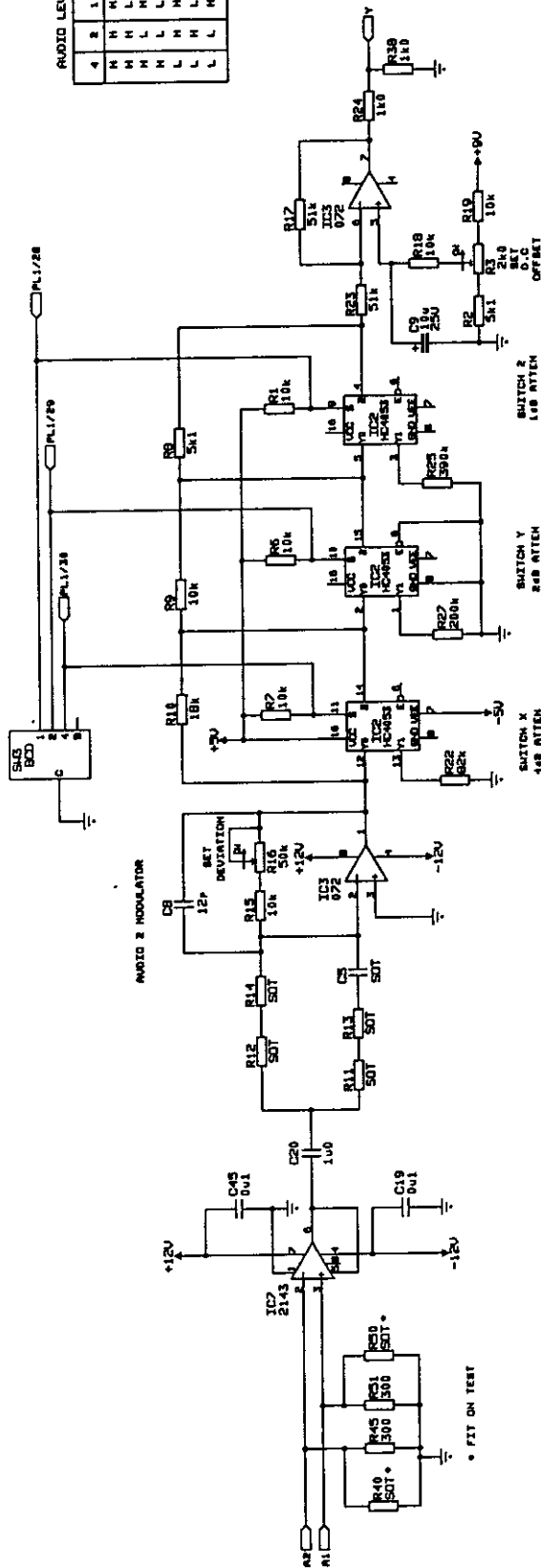
REVISION 274

NA5102-2.DWG

AUDIO LEVEL ATTENUATION

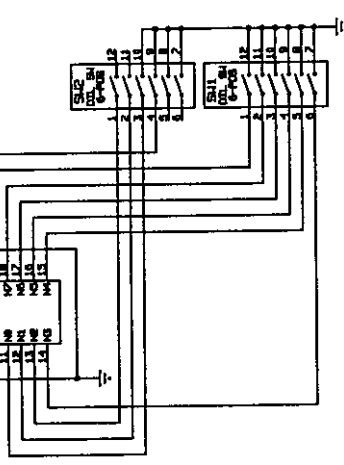
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M	M	M	-3	1	1
M	M	M	-2	2	2
M	M	M	-1	3	3
L	M	M	0	4	4
L	M	M	+1	5	5
L	M	M	+2	6	6

AUDIO 2 MODULATOR



PRE-EMPHASIS

OPP	R12	R14	R11	R13	C5
FLAT	10K	8K	M/F	M/F	M/F
5K	70K	8K	51K	8K	800P
750	81K	500K	51K	8K	800P
J17	300K	50K	5K1	51K	800P



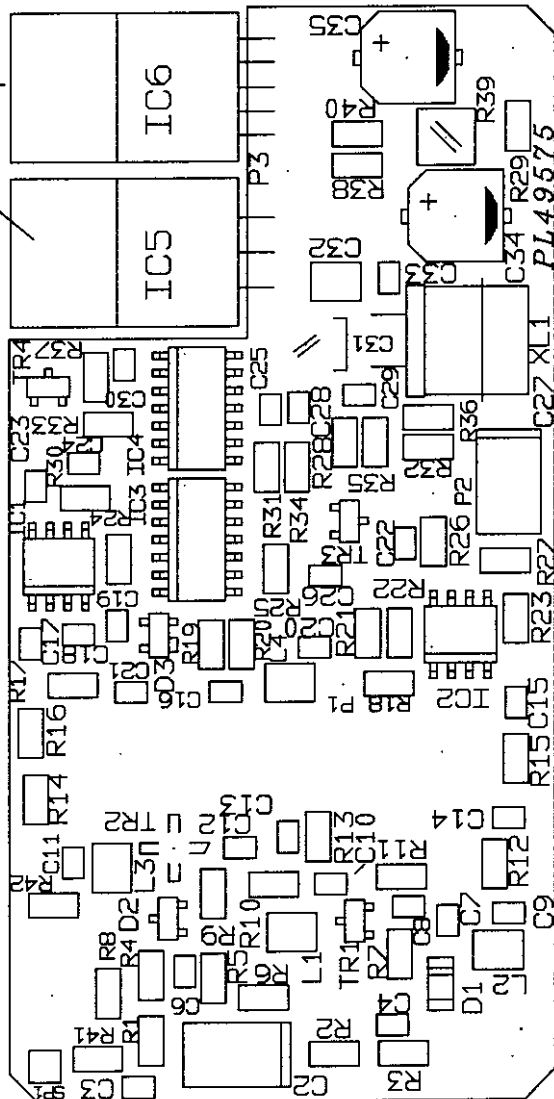
RF TECHNOLOGY INC. INNOVATORS IN COMMERCIAL IN CONFIDENCE

SCHEMATIC DUAL AUDIO MODULATOR ACL TRANSMITTER

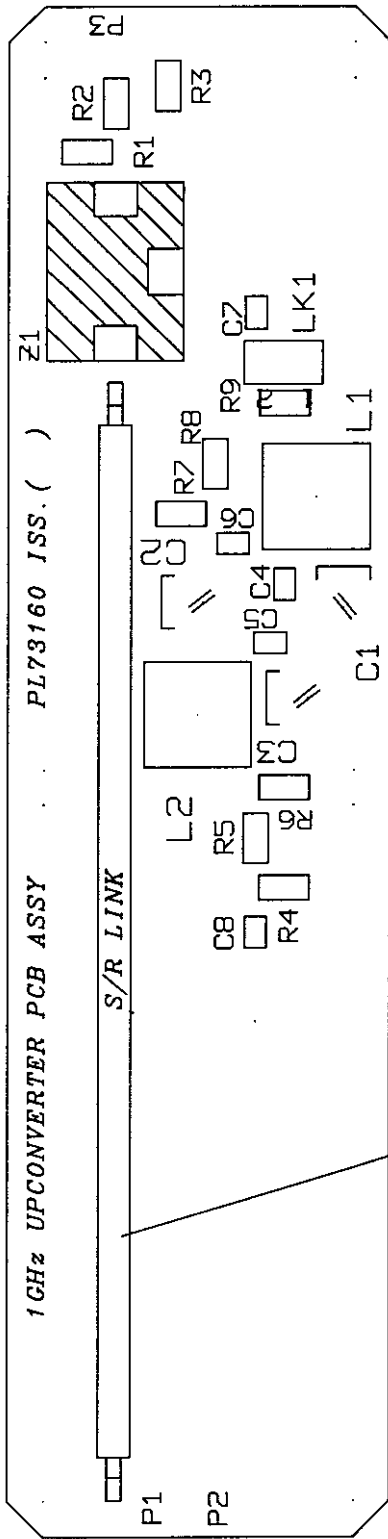
DEM PF MW MAS102 574

7/29/86

MAS102-3.DWG



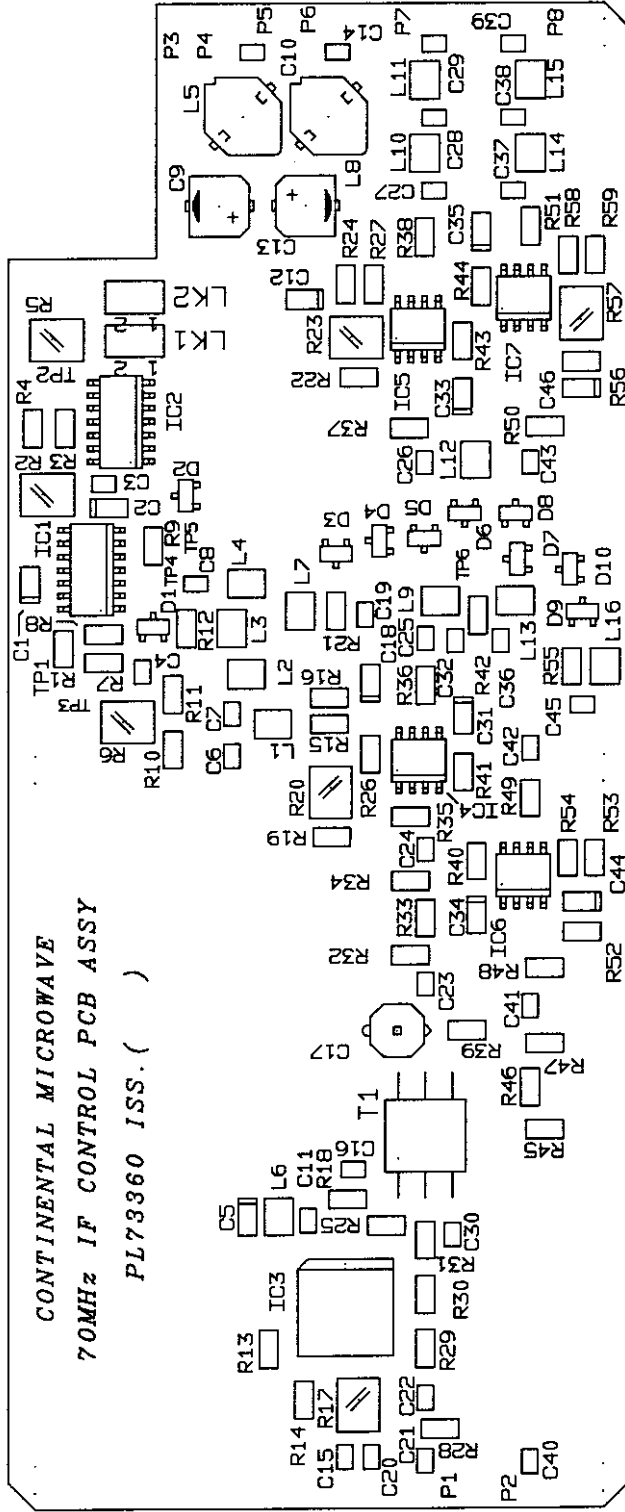
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B	DATE	NO. 0781	NO	24. 7. 94
A	DATE	NO. 1364	NO	25. 8. 94
TABLE	ITEM NO. / REVISION	DATE	DATE	DATE
TELEPHONE				
REVISED				
X	1	1/2 - 1/4	1/2 - 1/4	1/2 - 1/4
X	2	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	3	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	4	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	5	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	6	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	7	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	8	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	9	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	10	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	11	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	12	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	13	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	14	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	15	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	16	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	17	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	18	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	19	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	20	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	21	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	22	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	23	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	24	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	25	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	26	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	27	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	28	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	29	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	30	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	31	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	32	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	33	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	34	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	35	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	36	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	37	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	38	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	39	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	40	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	41	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	42	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	43	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	44	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	45	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	46	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	47	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	48	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	49	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	50	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	51	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	52	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	53	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	54	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	55	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	56	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	57	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	58	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	59	1/2 - 1/2	1/2 - 1/2	1/2 - 1/2
X	60	1/2 - 1/2	1/2 - 1/2	1/2



FIT S/R CABLE (IT.400) IN EXACT POSITION AS SHOWN

A				PL 19.4.96			
TABLE	EDN NO.	REVISION	DATE	TABLE	EDN NO.	REVISION	DATE
TOLERANCES				TOLERANCES			
DIMENSIONS IN MM/IN				DIMENSIONS IN MM/IN			
P. HETTERINGSTON				P. HETTERINGSTON			
DATE				DATE			
19.4.96				19.4.96			
THIS DRAWING OR PART THEREOF MUST NOT BE REPRODUCED OR TRANSMITTED IN ANY FORM OR BY ANY MEANS, ELECTRONIC OR MECHANICAL, INCLUDING PHOTOCOPYING, RECORDING, OR BY ANY INFORMATION STORAGE AND RETRIEVAL SYSTEM, WITHOUT PERMISSION IN WRITING FROM THE CONTINENTAL MICROELECTRONICS CORPORATION.				THIS DRAWING OR PART THEREOF MUST NOT BE REPRODUCED OR TRANSMITTED IN ANY FORM OR BY ANY MEANS, ELECTRONIC OR MECHANICAL, INCLUDING PHOTOCOPYING, RECORDING, OR BY ANY INFORMATION STORAGE AND RETRIEVAL SYSTEM, WITHOUT PERMISSION IN WRITING FROM THE CONTINENTAL MICROELECTRONICS CORPORATION.			
CONTINENTAL				CONTINENTAL			
MICROELECTRONICS				MICROELECTRONICS			
TITLE 1GHz UPCONVERTER				TITLE 1GHz UPCONVERTER			
PCB ASSEMBLY DRAWING				PCB ASSEMBLY DRAWING			
DRAWING NO. D73160				DRAWING NO. D73160			
SHEET 1 OF 1 SHEETS				SHEET 1 OF 1 SHEETS			
A				A			

CONTINENTAL MICROWAVE
70MHZ IF CONTROL PCB ASSY
PL73360 ISS. ()

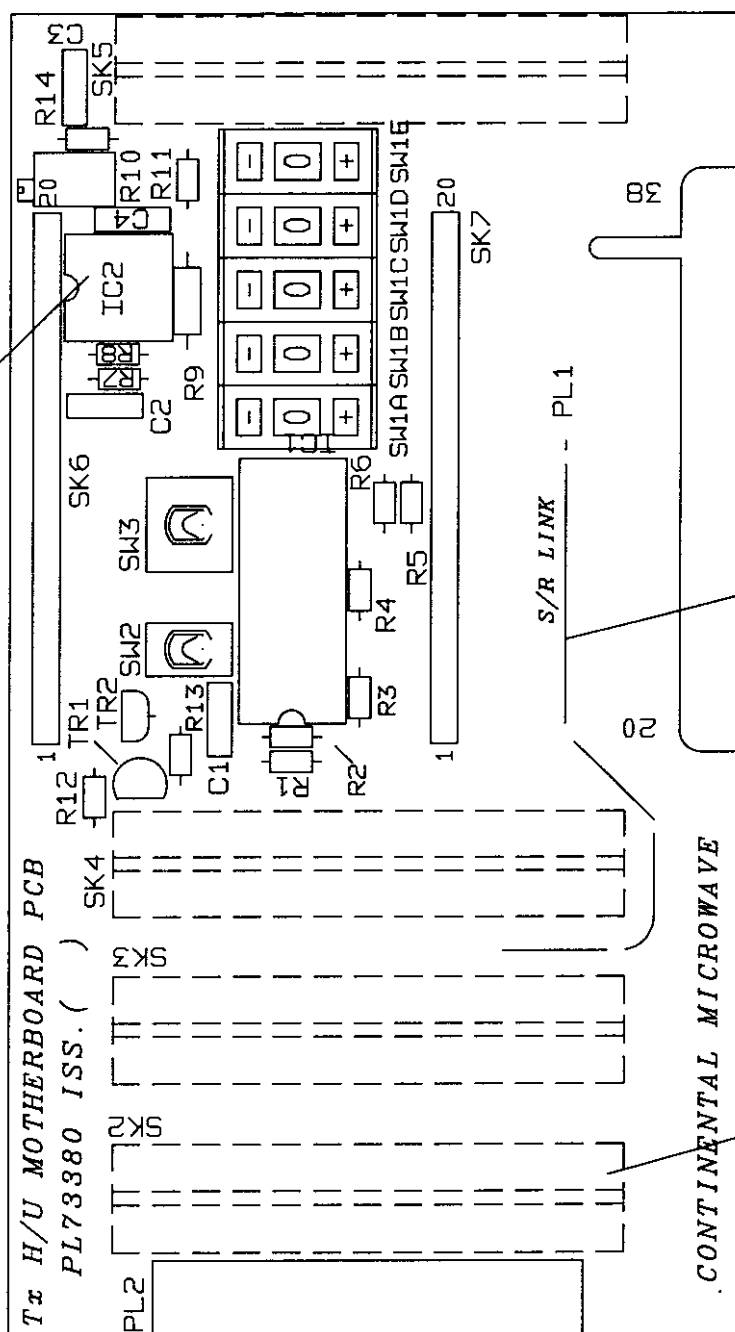


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CONTINENTAL
MICROWAVE

TITLE	70MHZ IF CONTROL PCB ASSEMBLY DRAWING
DRAWING No.	D73360
ISSUE	A
SHEET	1 OF 1 SHEETS
REV	A2

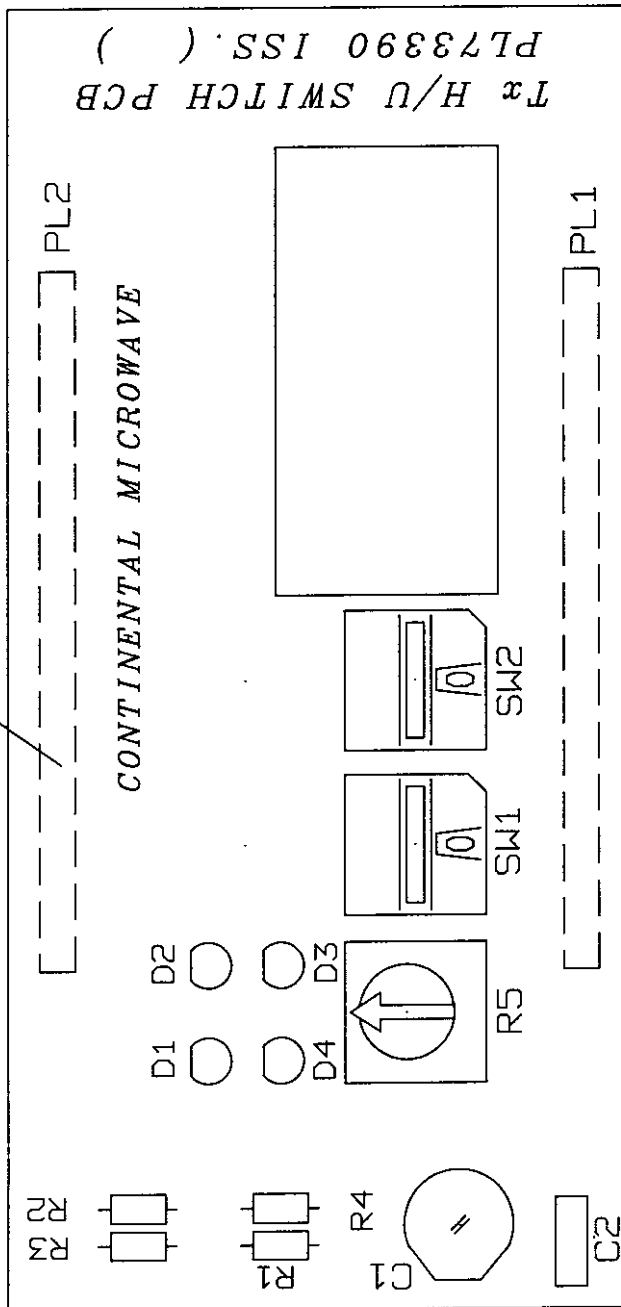
FIT IC SOCKETS UNDER ALL IC's



SOLDER S/R CABLE IN EXACT POSITION AS SHOWN ON COMPT SIDE

SOLDER SK2-SK5 TO UNDERSIDE OF PCB (FIT POLARIZING KEYS IN POSNS 18-37)

B	DCLS 1003	PH 12.7.96
A	DATA 2229	PH 21.5.96
DATE	EDN NO.	EDN DATE
METHOD	TOLERANCES	APPROVAL
X	1/2 - 0.4	FRACTIONS 1/2 - 1/64
X	1/2 - 0.2	X.001 1/2 - 0.010
X	1/2 - 0.1	X.004 1/2 - 0.005
X	1/2 - 0.05	X.0005 1/2 - 0.001
X	1/2 - 0.01	X.00005 1/2 - 0.0001
FILES	1/2 - 0.01	1/2 - 0.001
DISPENSING IN INVTAS	1/2 - 0.01	1/2 - 0.001
DATE	DATE	DATE
P. HETHERINGTON	21.5.96	21.5.96
THIS DRAWING OR PART THEREOF MUST NOT BE COPIED OR REPRODUCED WITHOUT PRIOR WRITTEN PERMISSION		
CONTINENTAL		
SAIC/PCB/AS/VE		
TITLE Tx H/U MOTHERBOARD		
PCB ASSEMBLY DRAWING		
DRAWING NO.	073380	TABLE
SHEET	1	OF 1
SHEETS	1	OF 1

[illegible]

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NOTES

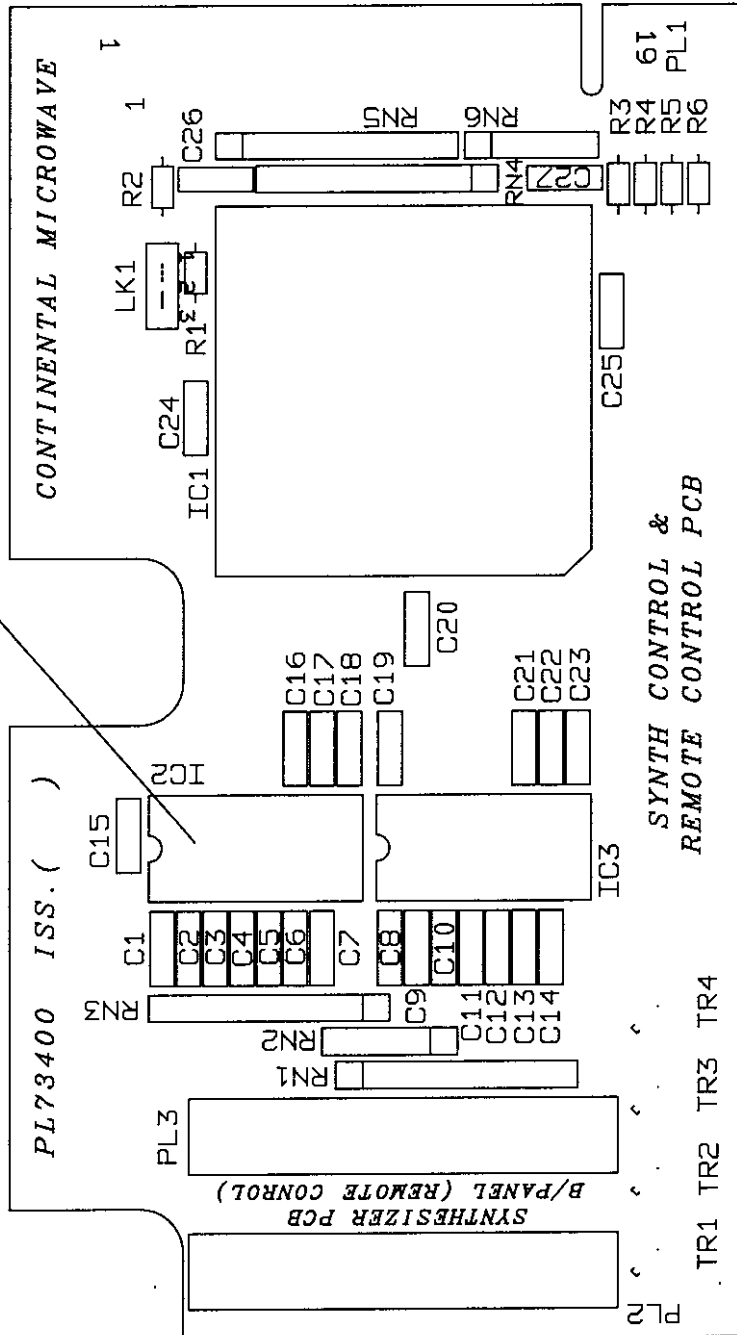
CONTINENTAL

TITLE	Tx H/U SWITCH PCB ASSEMBLY DRAWING	DRAWING No.	TABLE
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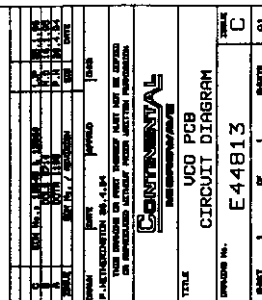
073390

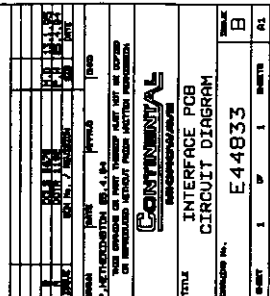
SHEET	1 OF 1	SLEETS	A2
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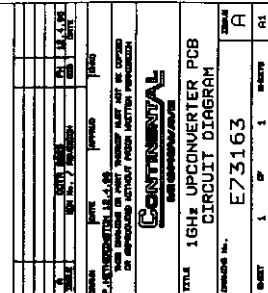
FIT IC SOCKETS UNDER ALL IC's

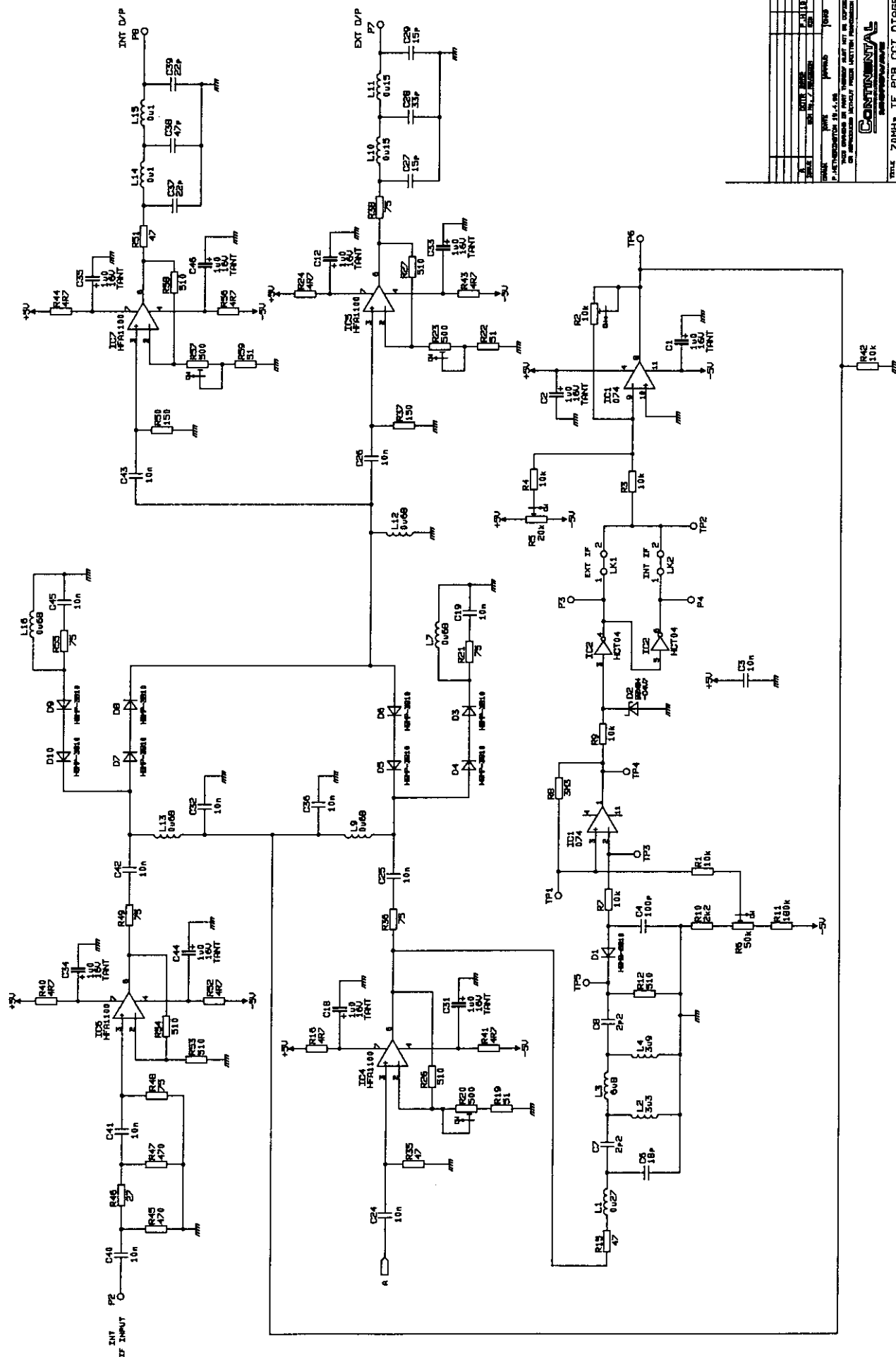


REVISIONS		DATE	BY	CHKD
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2	0019 1018	21.5.96	PH	1.8.96
3	0020 1018	21.5.96	PH	1.8.96
METHOD		DATE	BY	CHKD
1	0018 1018	21.5.96	PH	1.8.96
2	0019 1018	21.5.96	PH	1.8.96
3	0020 1018	21.5.96	PH	1.8.96
DIMENSIONS IN MM/INCH		DATE	BY	CHKD
1	0018 1018	21.5.96	PH	1.8.96
2	0019 1018	21.5.96	PH	1.8.96
3	0020 1018	21.5.96	PH	1.8.96
TOLERANCES		DATE	BY	CHKD
1	0018 1018	21.5.96	PH	1.8.96
2	0019 1018	21.5.96	PH	1.8.96
3	0020 1018	21.5.96	PH	1.8.96
APPROVAL		DATE	BY	CHKD
1	0018 1018	21.5.96	PH	1.8.96
2	0019 1018	21.5.96	PH	1.8.96
3	0020 1018	21.5.96	PH	1.8.96
THIS DRAWING OR PART THEREOF MUST NOT BE COPIED OR REPRODUCED WITHOUT PRIOR WRITTEN PERMISSION		DATE	BY	CHKD
1	0018 1018	21.5.96	PH	1.8.96
2	0019 1018	21.5.96	PH	1.8.96
3	0020 1018	21.5.96	PH	1.8.96
CONTINENTAL		DATE	BY	CHKD
1	0018 1018	21.5.96	PH	1.8.96
2	0019 1018	21.5.96	PH	1.8.96
3	0020 1018	21.5.96	PH	1.8.96
TITLE SYNTH & REM CONTROL PCB ASSEMBLY DRAWING		DATE	BY	CHKD
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3	0020 1018	21.5.96	PH	1.8.96
DRAWING No. 073400		DATE	BY	CHKD
1	0018 1018	21.5.96	PH	1.8.96
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3	0020 1018	21.5.96	PH	1.8.96
SHEET 1 OF 1 SHEETS		DATE	BY	CHKD
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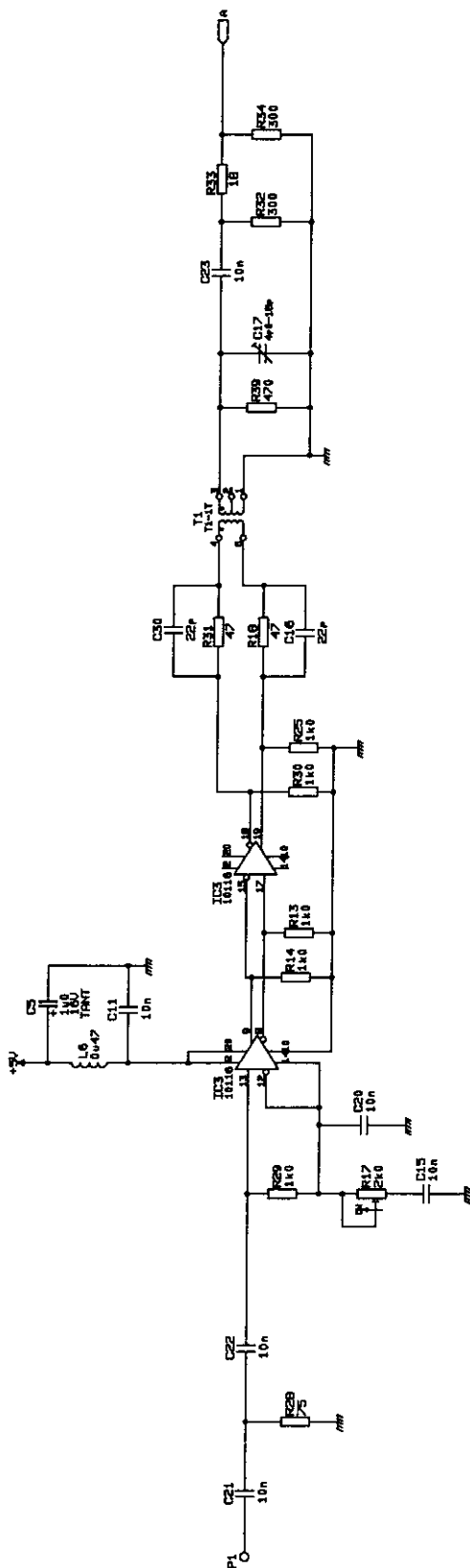
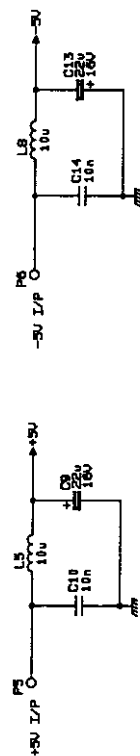








CONTINENTAL	
70MHz IF PCB GET DIAGRAM	
(CONTROL & MONITORING)	
REVISED BY	E73363
DATE	1 OF 2
BY	AL



REVISION	DATE	BY	CHKD	DATE
1	10/1/83	J. J. J.		
2	10/1/83	J. J. J.		
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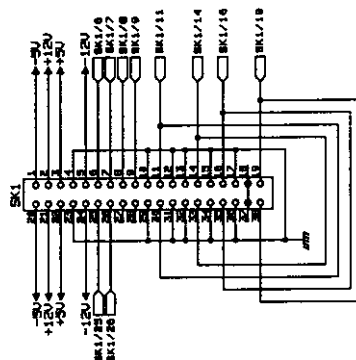
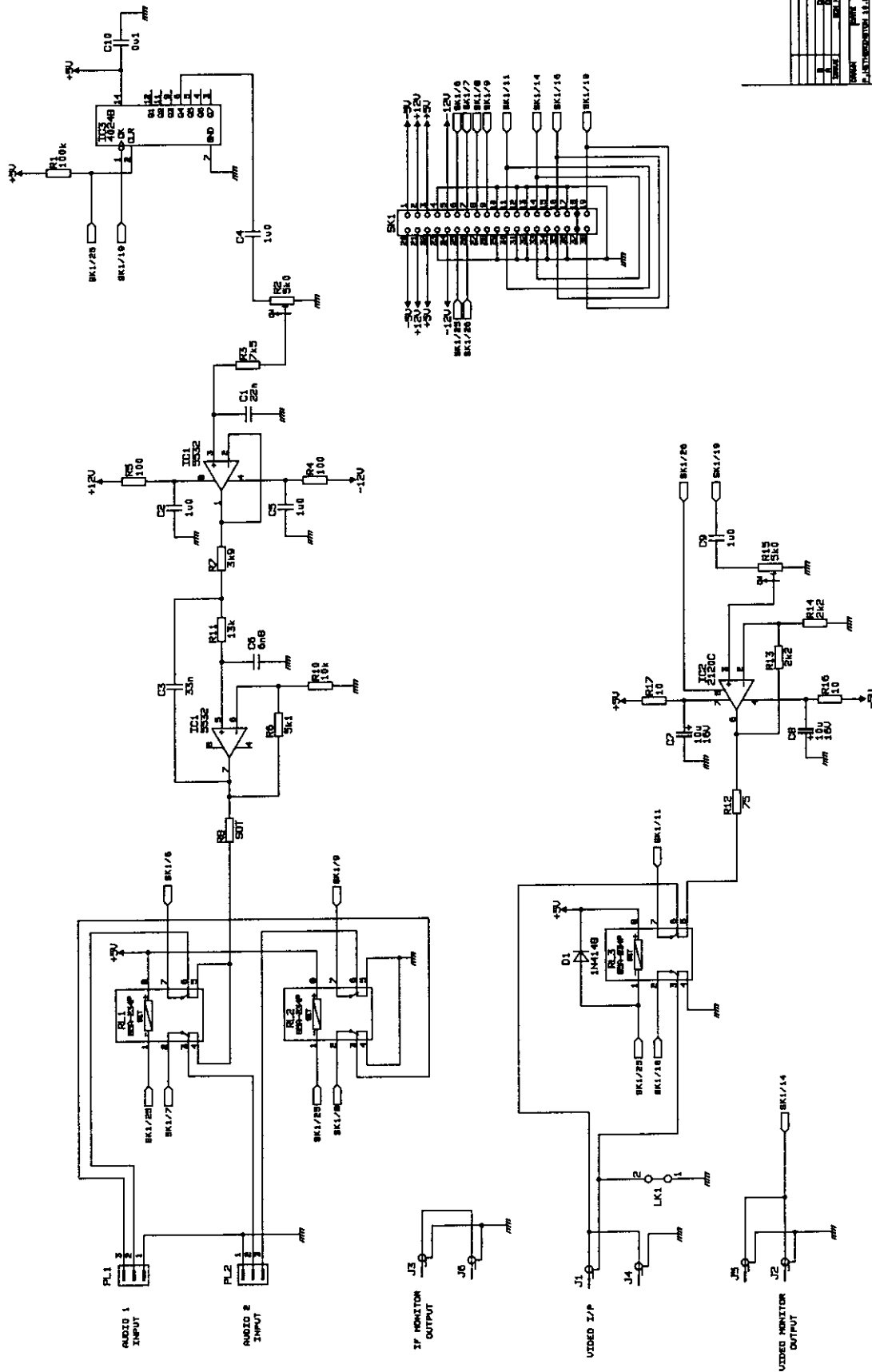
CONTINENTAL

TITLE 70MHz IF PCB COT DIAGRAM
(CONTROL & MONITORING)

REVISION No. E73363

REVISION 2 OF 2

REVISION A



REV	DATE	BY	CHKD	APPD	QTY	UNIT	PRICE	TOTAL
1	10/1/78	J. H. H.	J. H. H.	J. H. H.	1	PCB	10.00	10.00
2	10/1/78	J. H. H.	J. H. H.	J. H. H.	1	PCB	10.00	10.00
3	10/1/78	J. H. H.	J. H. H.	J. H. H.	1	PCB	10.00	10.00
4	10/1/78	J. H. H.	J. H. H.	J. H. H.	1	PCB	10.00	10.00
5	10/1/78	J. H. H.	J. H. H.	J. H. H.	1	PCB	10.00	10.00
6	10/1/78	J. H. H.	J. H. H.	J. H. H.	1	PCB	10.00	10.00
7	10/1/78	J. H. H.	J. H. H.	J. H. H.	1	PCB	10.00	10.00
8	10/1/78	J. H. H.	J. H. H.	J. H. H.	1	PCB	10.00	10.00
9	10/1/78	J. H. H.	J. H. H.	J. H. H.	1	PCB	10.00	10.00
10	10/1/78	J. H. H.	J. H. H.	J. H. H.	1	PCB	10.00	10.00

TITLE TX H/U CONNECTOR
 PCB CIRCUIT DIAGRAM
 DRAWING NO. E73373
 SHEET 1 OF 1