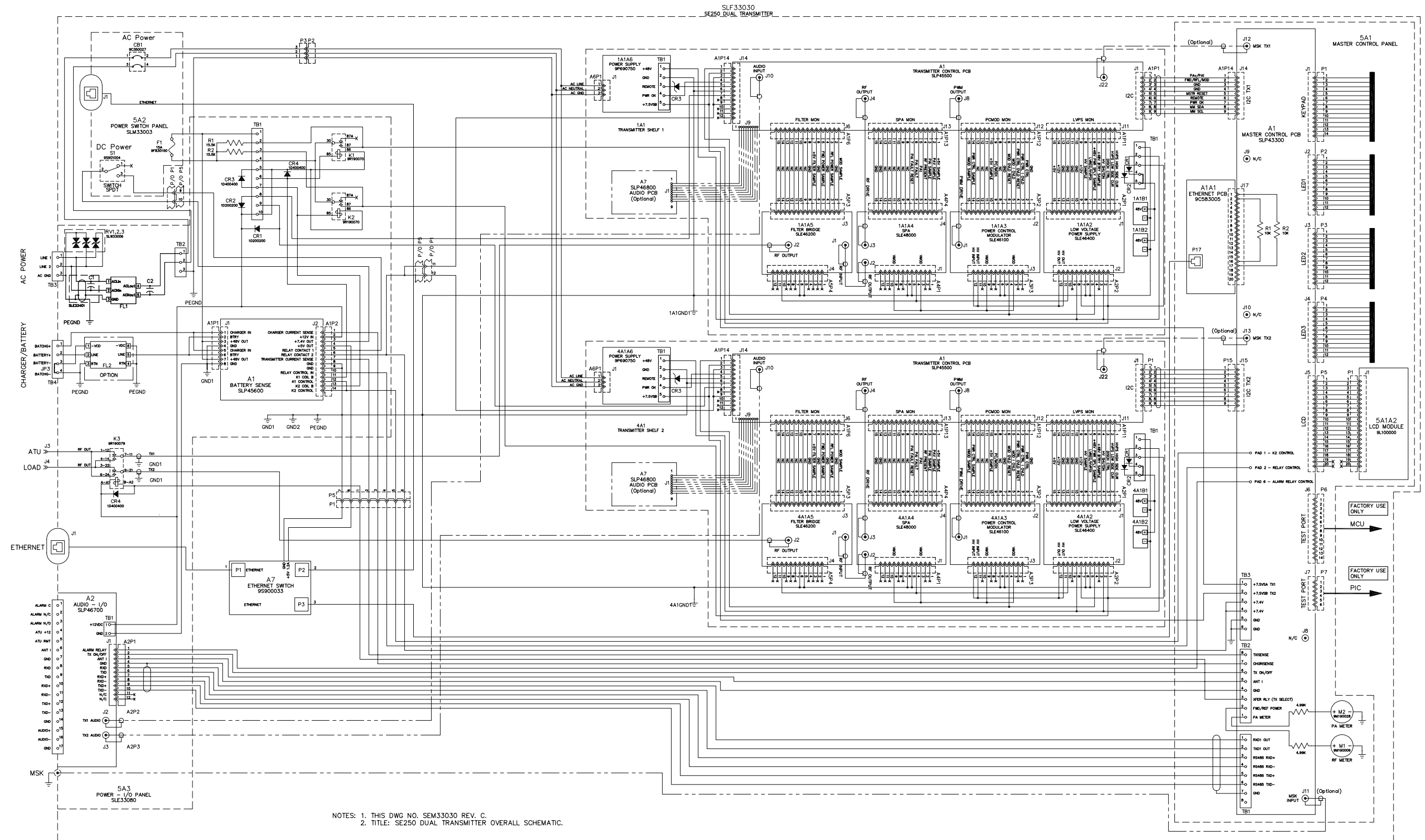
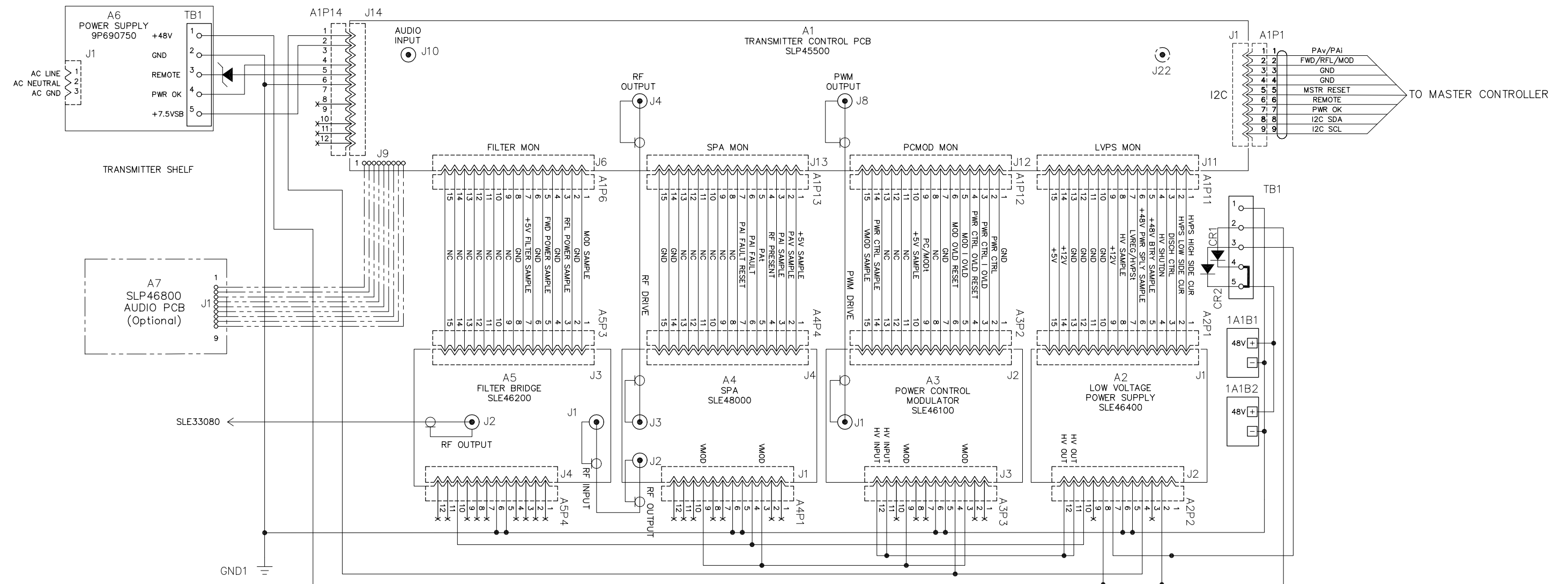


SE250 Schematics

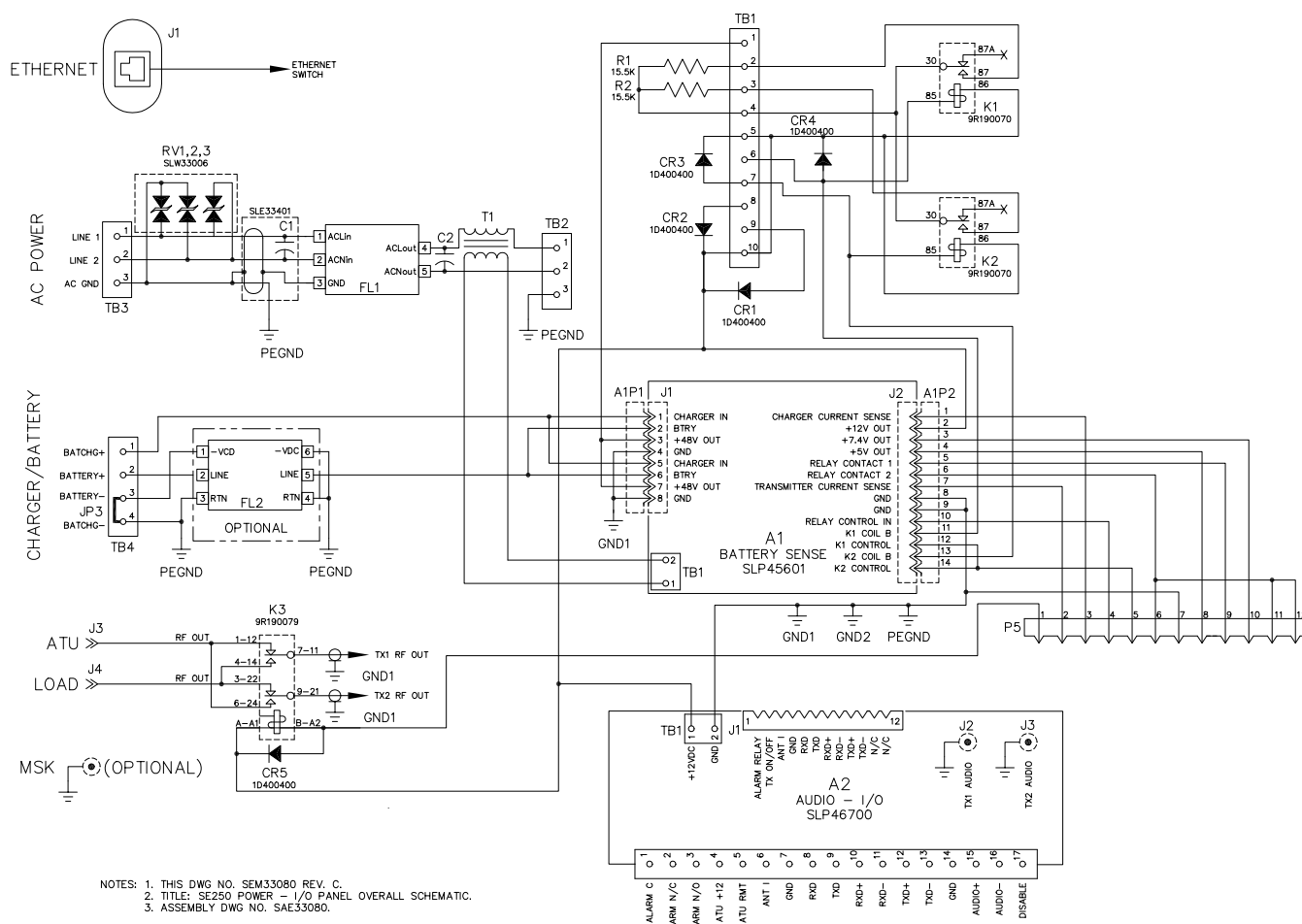
<u>Page</u>	<u>Document</u>
2	SEM33030 - SE250 overall schematic
3	SEM33008 - Transmitter Drawer Schematic
4	SEM33080 - Power Control Panel Schematic
5	SEP43300 - Master Control PCB Schematic
10	SEP45500 - Transmitter Control PCB Schematic
15	SEP46400 - LV Regulator/HV Power Supply PCB Schematic
16	SEP46100 - Power Control Modulator PCB Schematic
18	SEP48000 - SPA PCB Schematic
19	SEP46200 - Filter/Bridge PCB Schematic
21	SLP45601 - Battery Sense PCB Schematic
22	SEP46700 - Audio I/O PCB Schematic



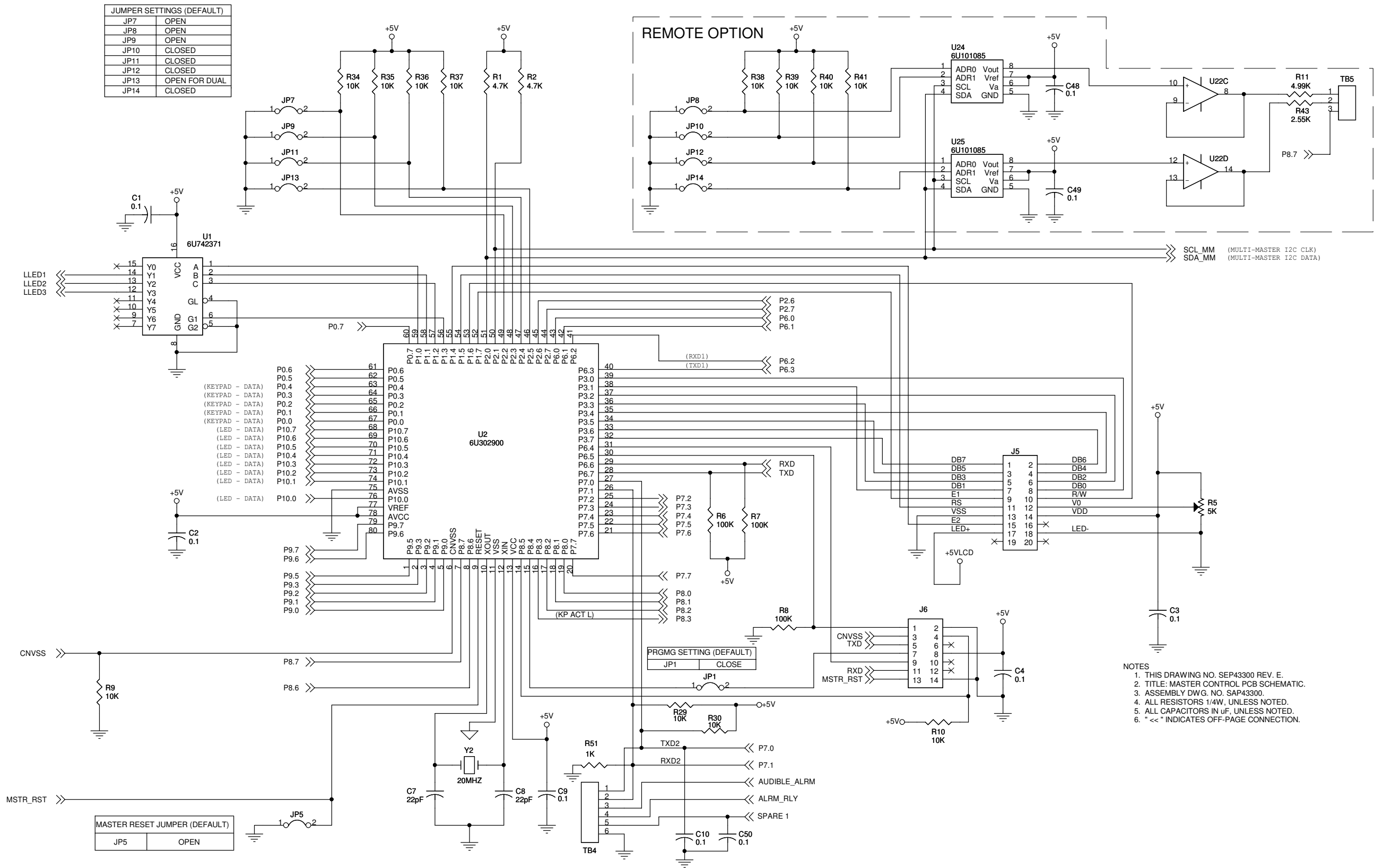
SEM33030 - SE250 OVERALL SCHEMATIC

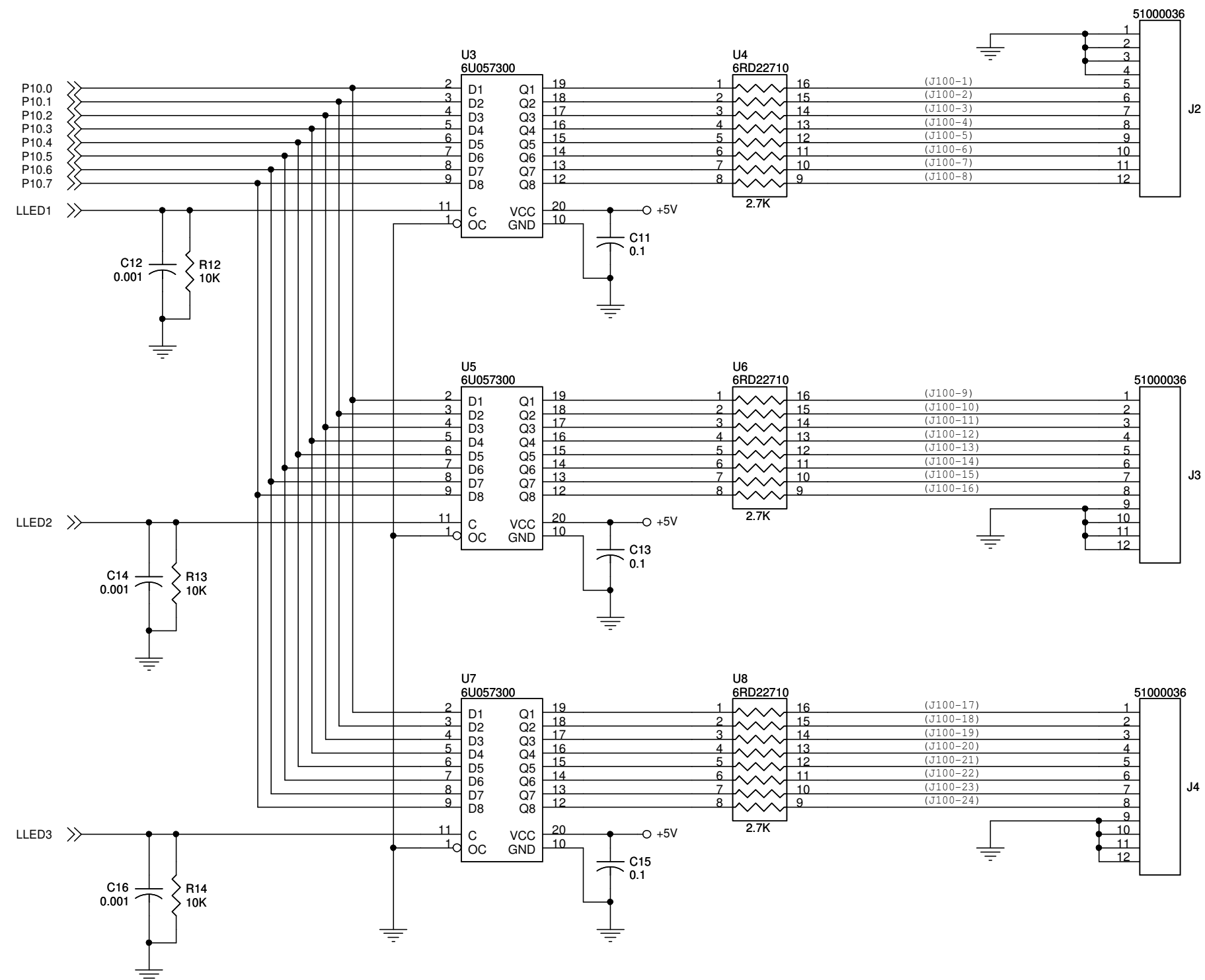


SEM33008 - SE250 TRANSMITTER ASSEMBLY SCHEMATIC



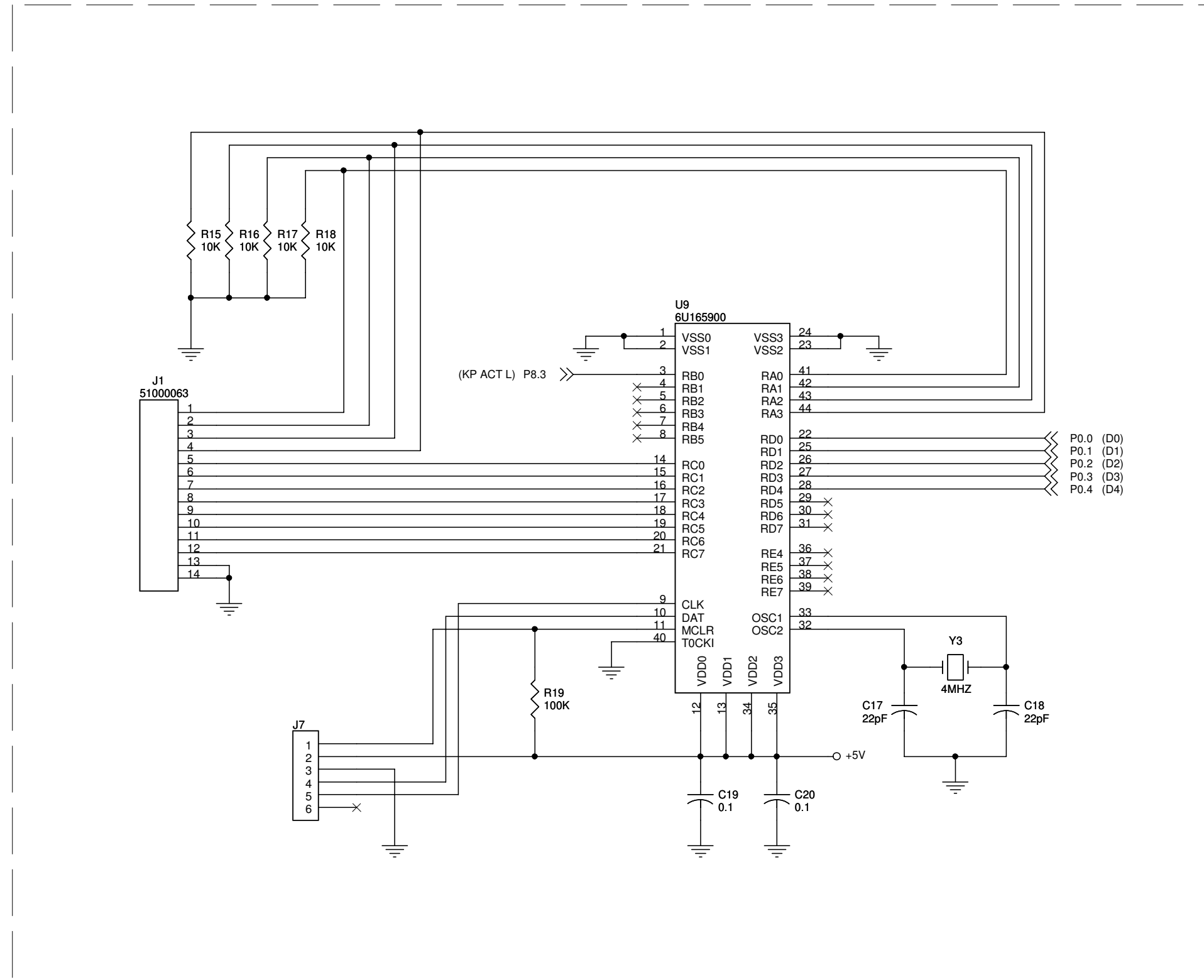
SEM33080 Rev C
Figure 2-3. Power Control panel Schematic



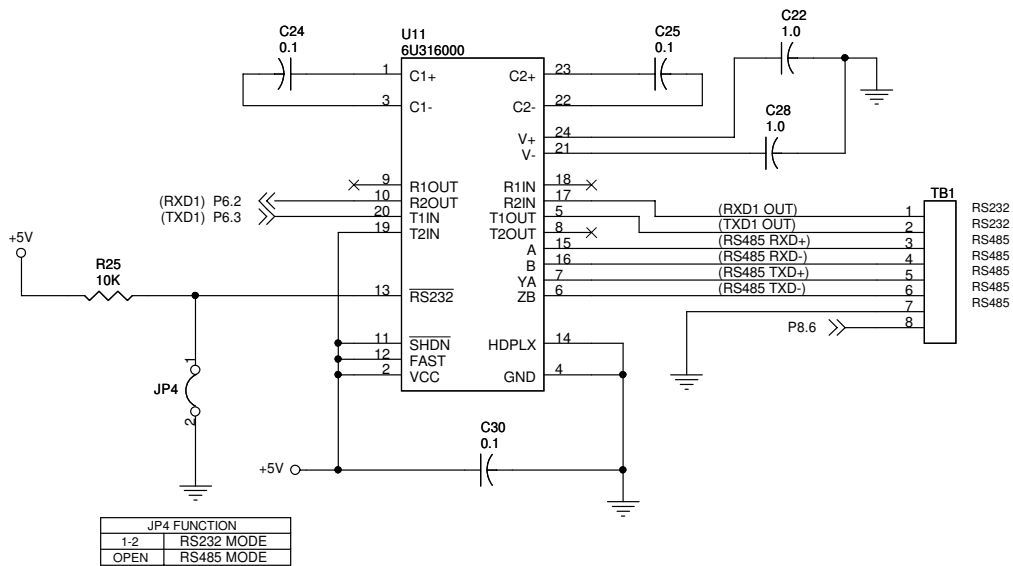


SEP43300 - MASTER CONTROL SCHEMATIC (2 OF 5)

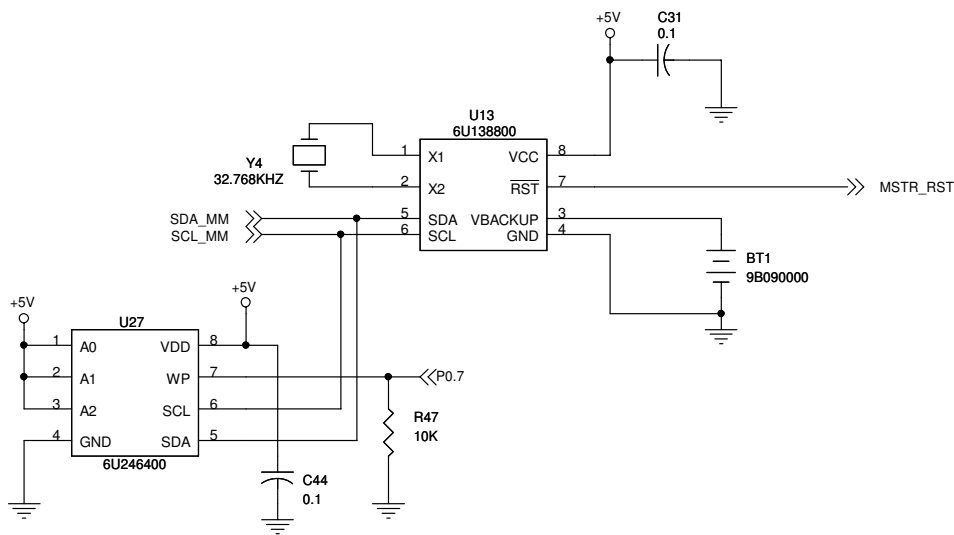
KEYPAD



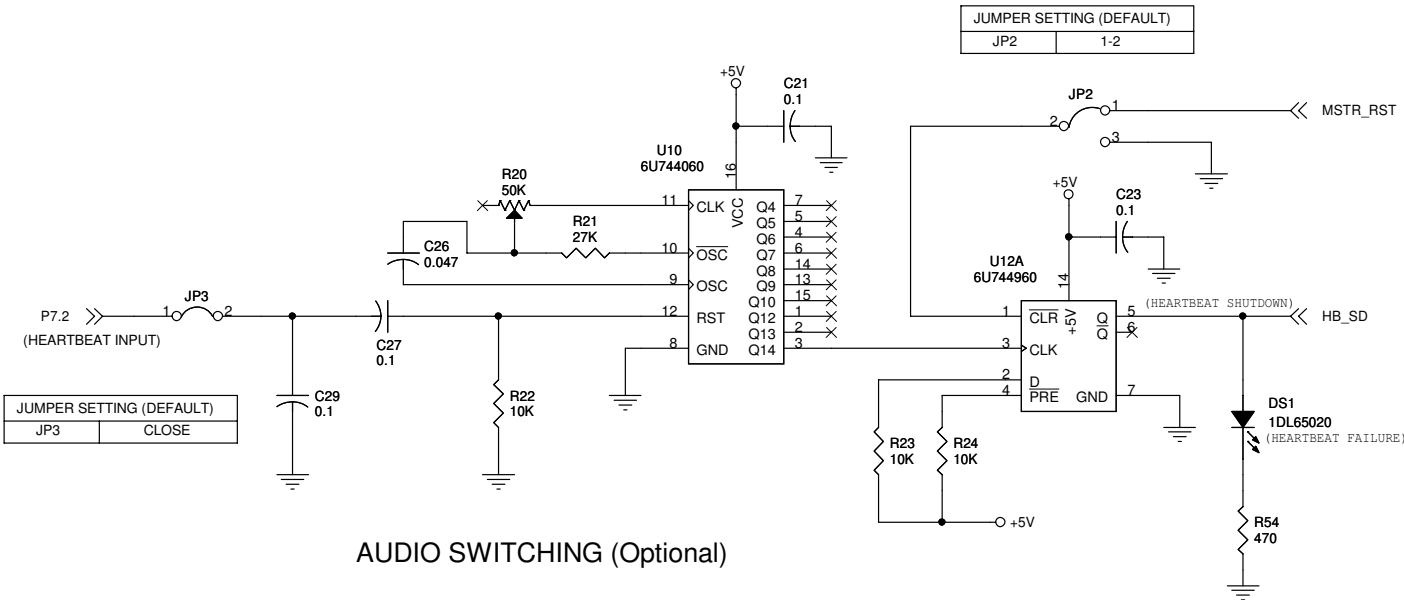
RS-232 / RS-485 INTERFACE



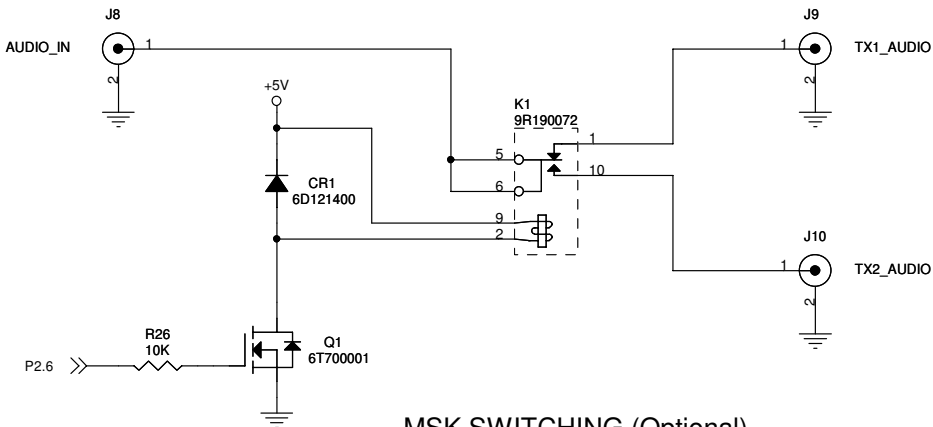
RTC / EEPROM / FRAM



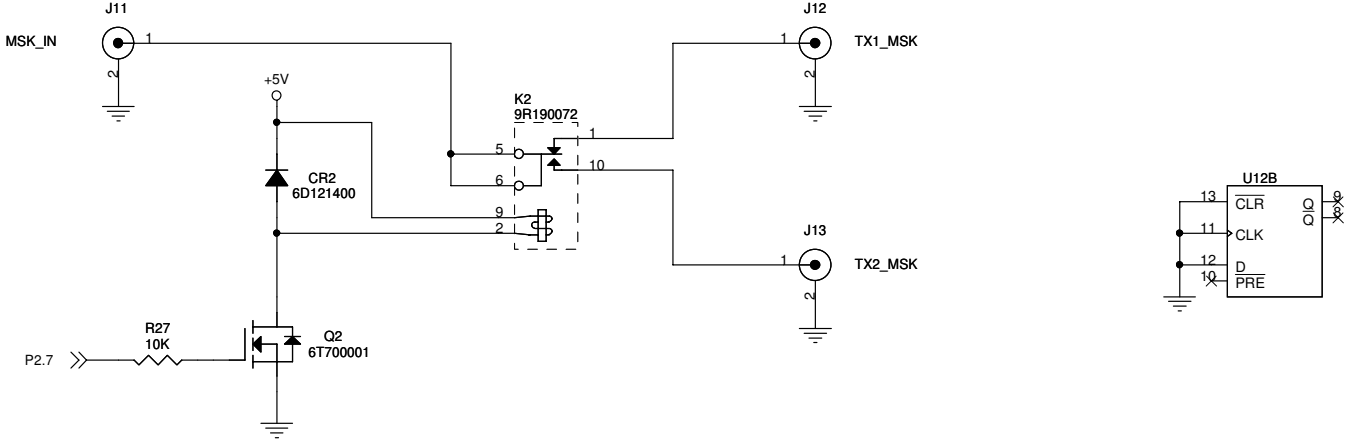
HEARTBEAT CIRCUIT

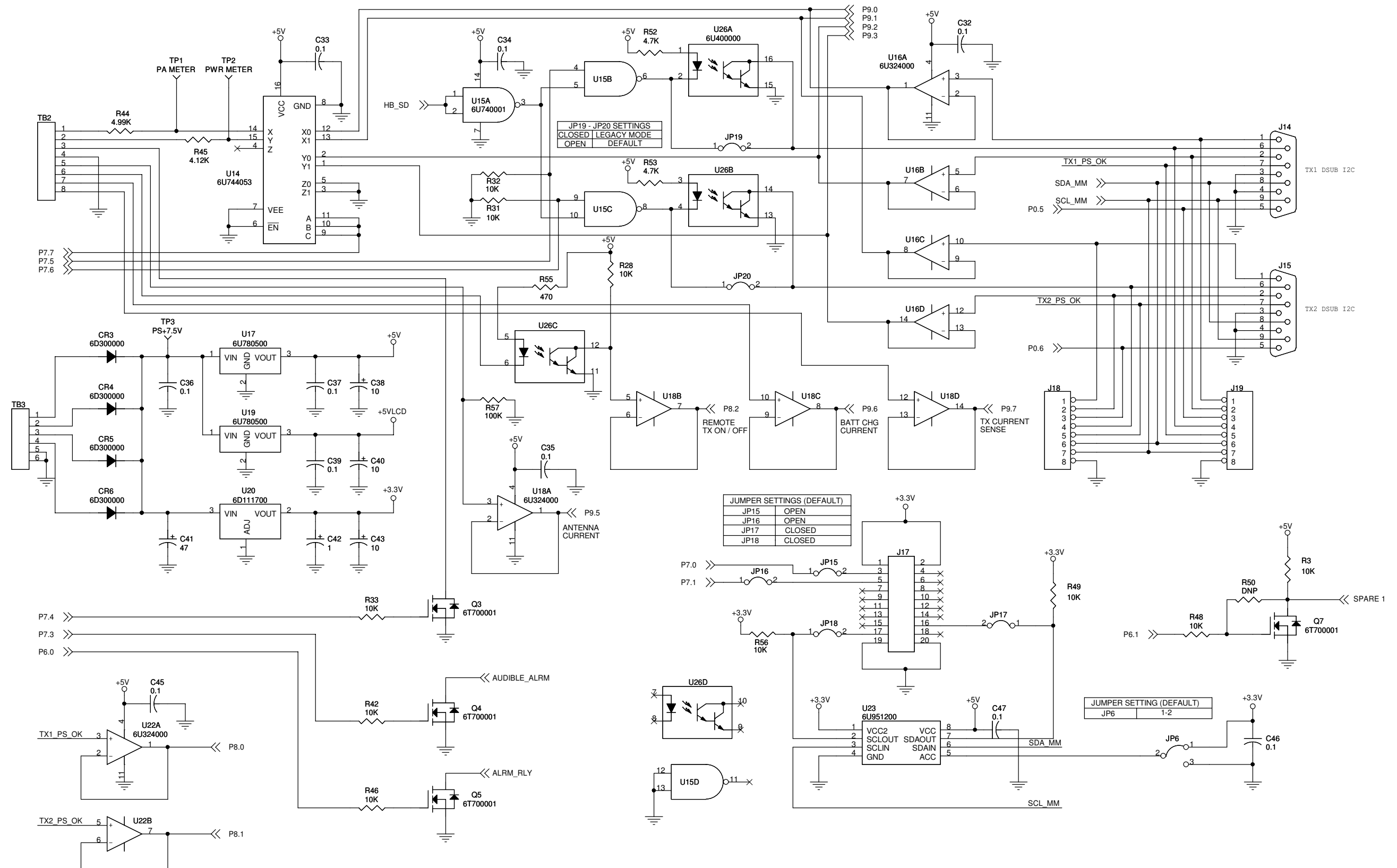


AUDIO SWITCHING (Optional)

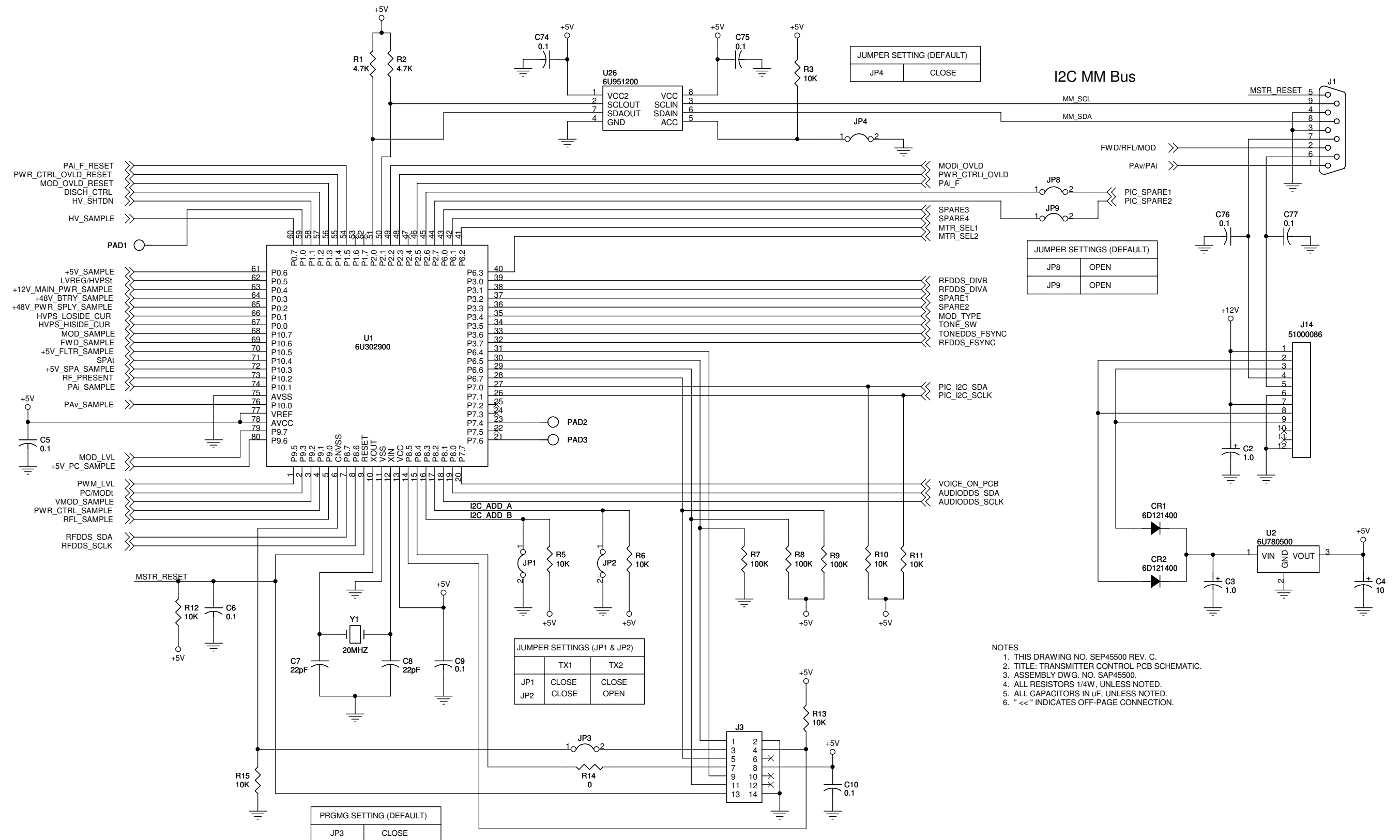


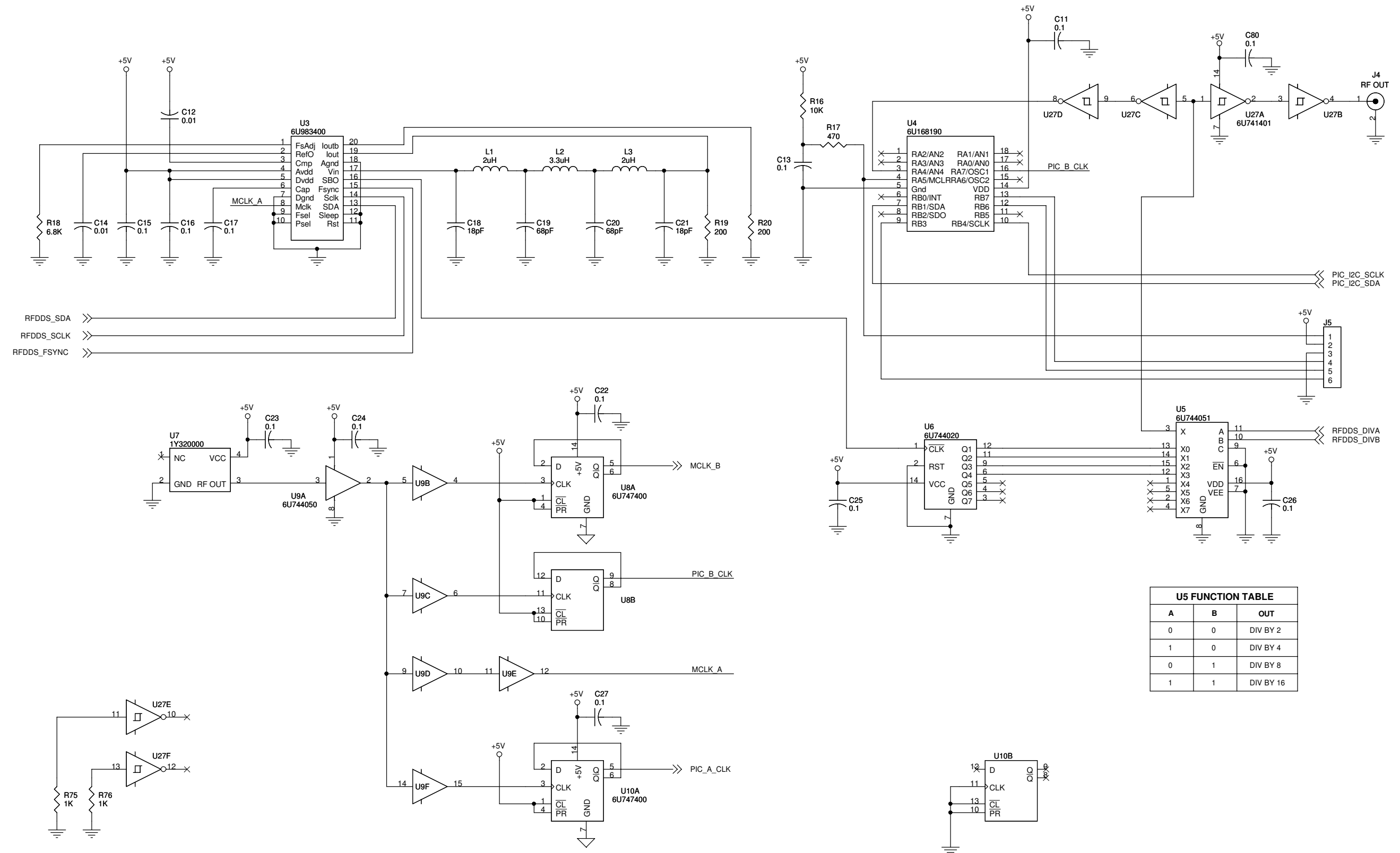
MSK SWITCHING (Optional)





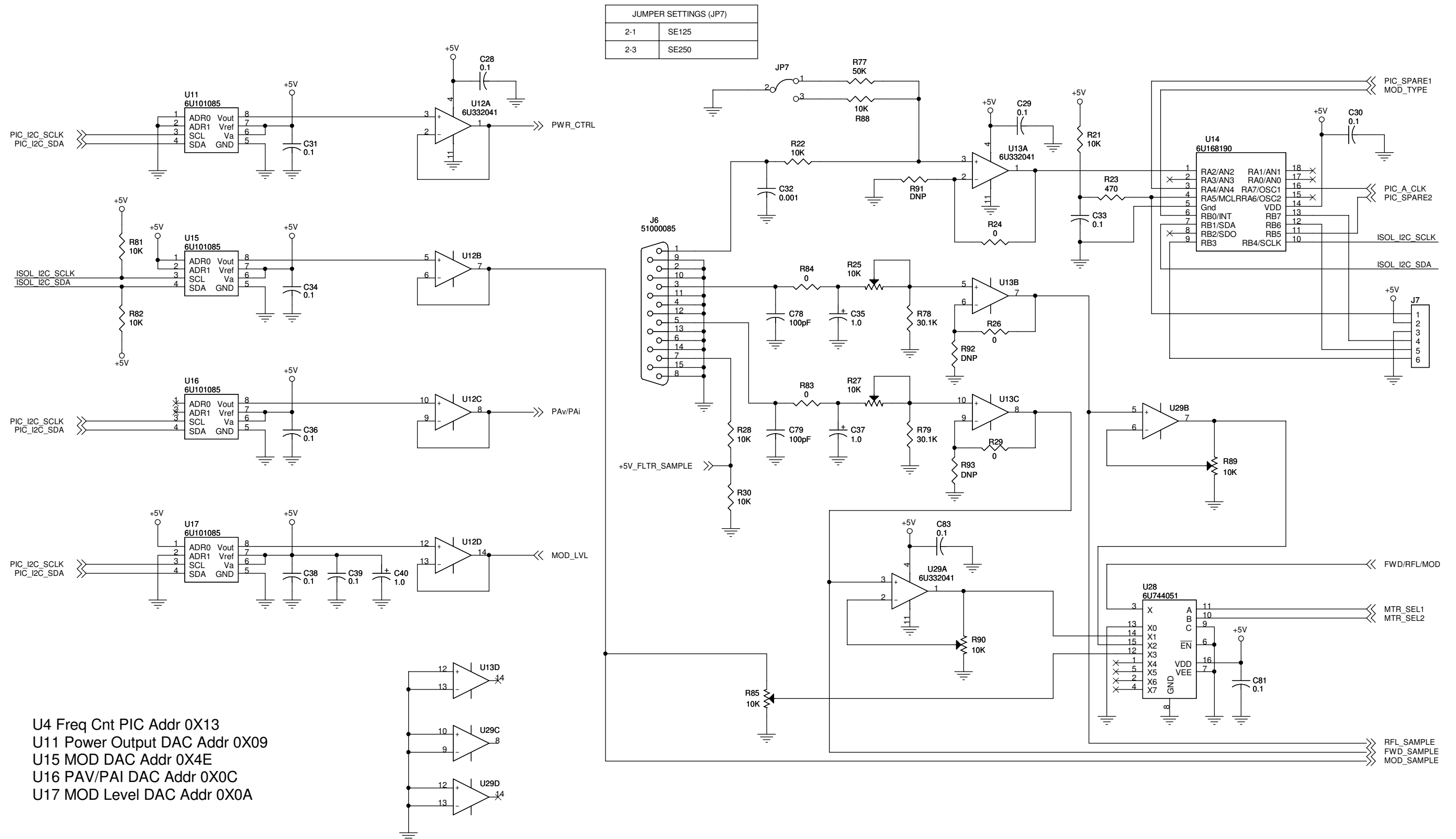
SEP43300 - MASTER CONTROL SCHEMATIC (5 OF 5)

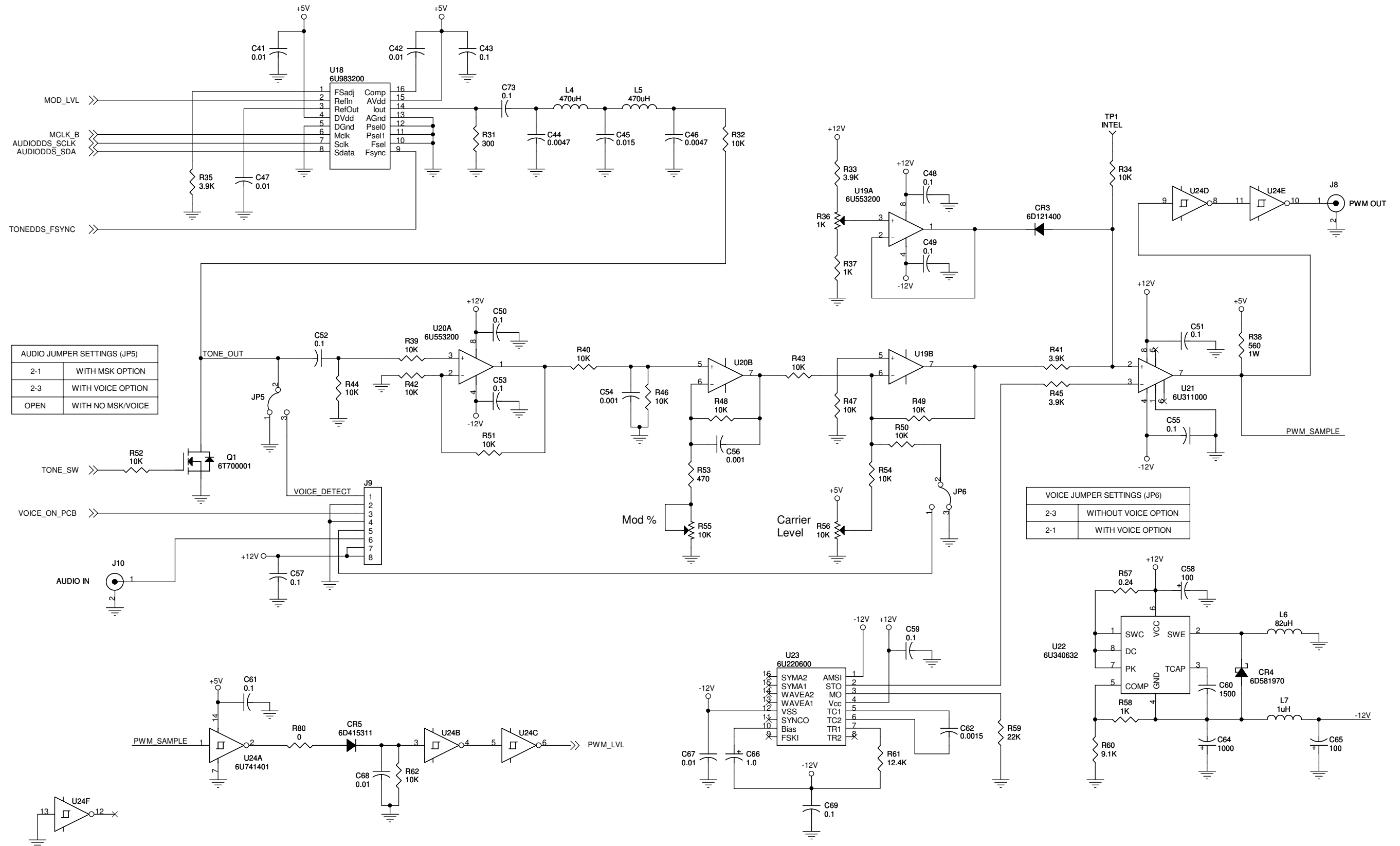




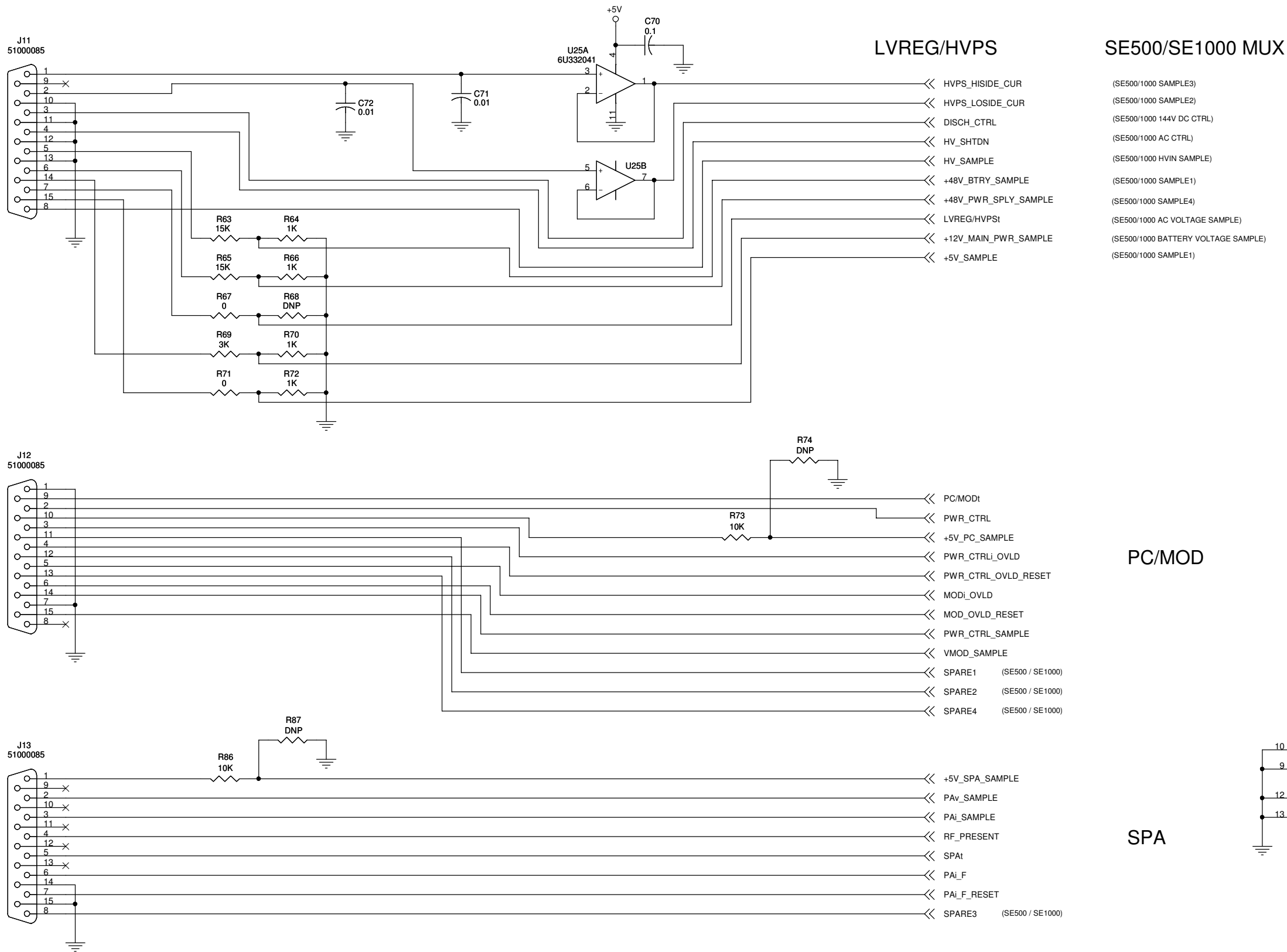
U5 FUNCTION TABLE		
A	B	OUT
0	0	DIV BY 2
1	0	DIV BY 4
0	1	DIV BY 8
1	1	DIV BY 16

SEP45500 - TRANSMITTER CONTROL SCHEMATIC (2 OF 5)

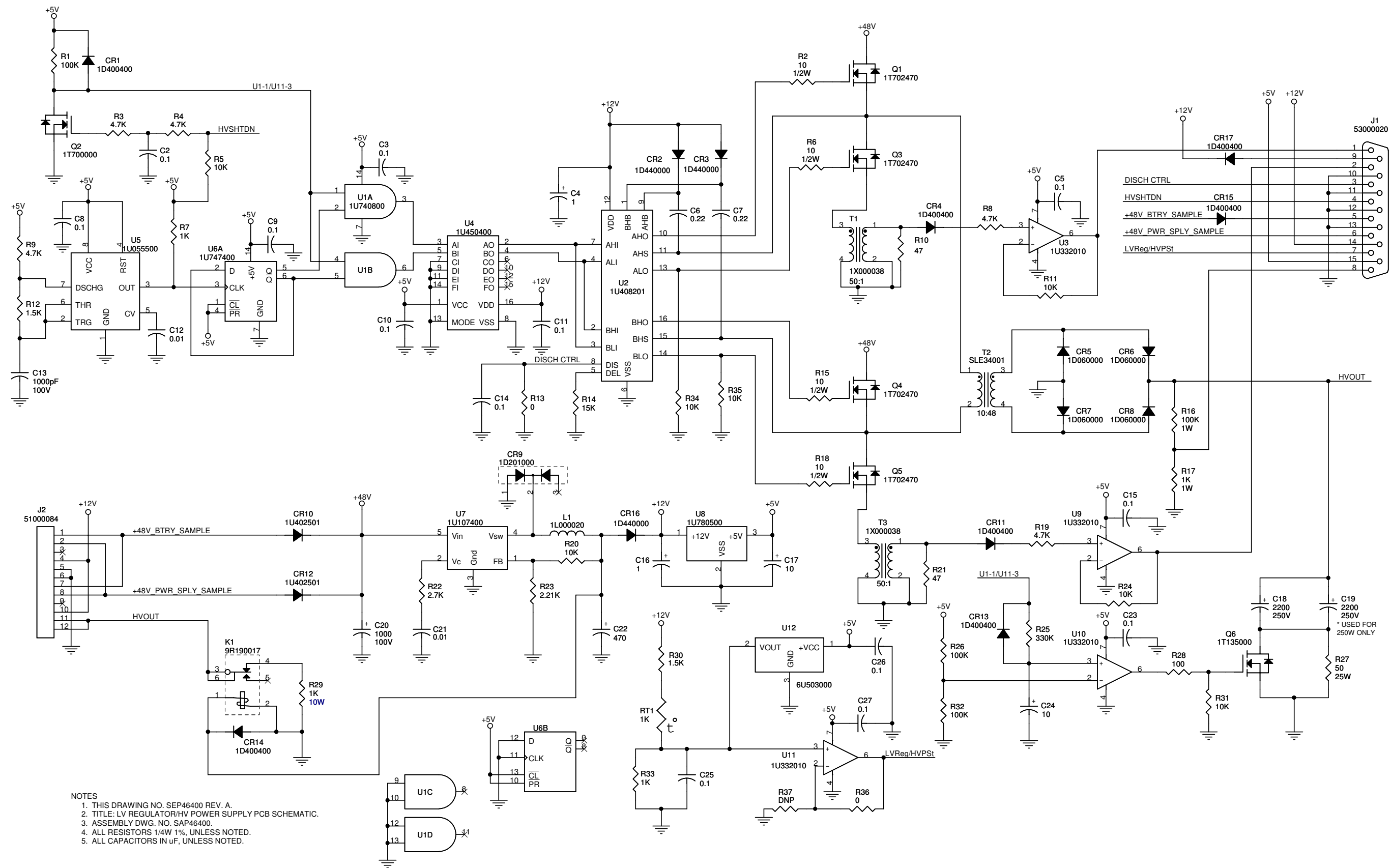




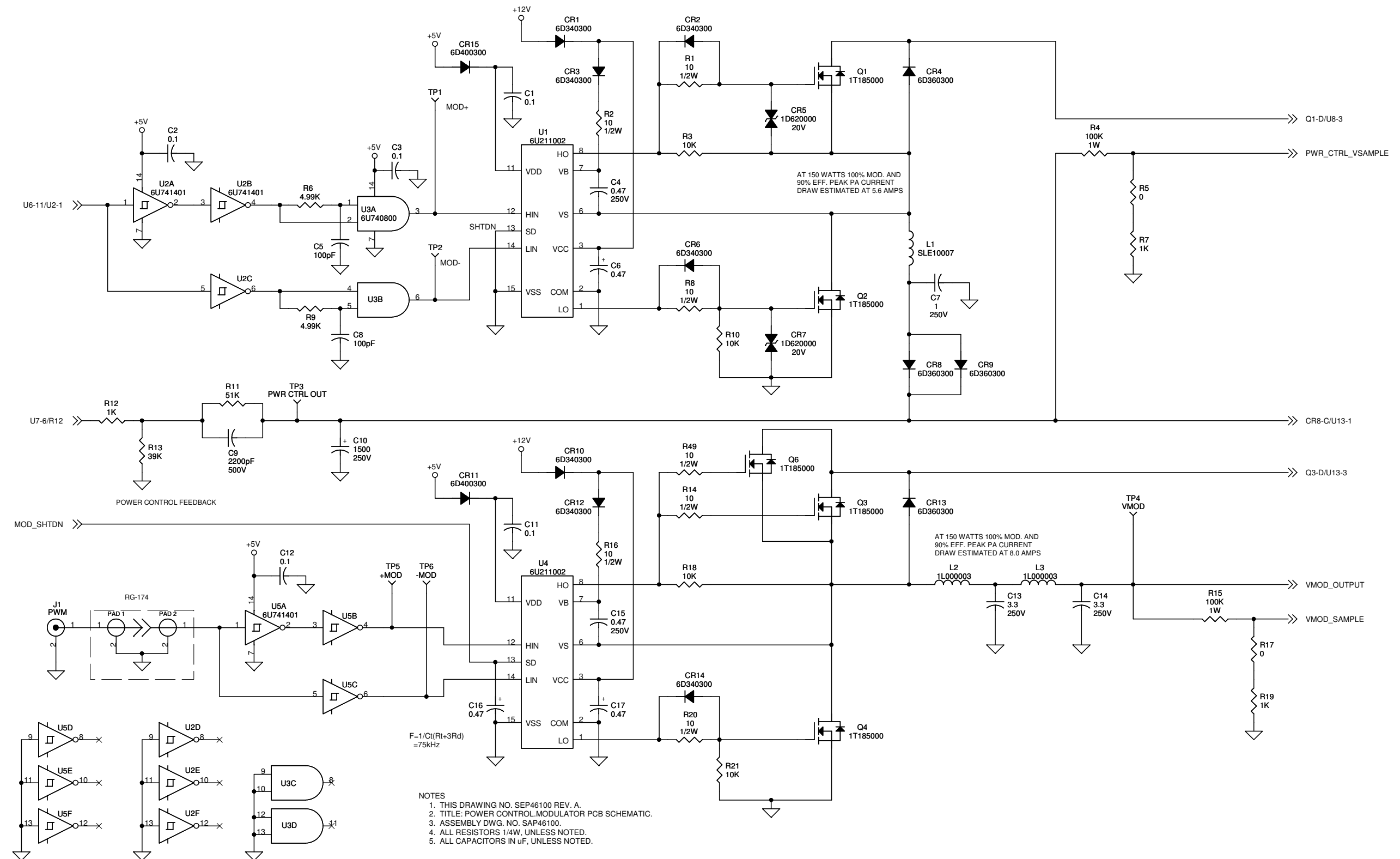
SEP45500 - TRANSMITTER CONTROL SCHEMATIC (4 OF 5)



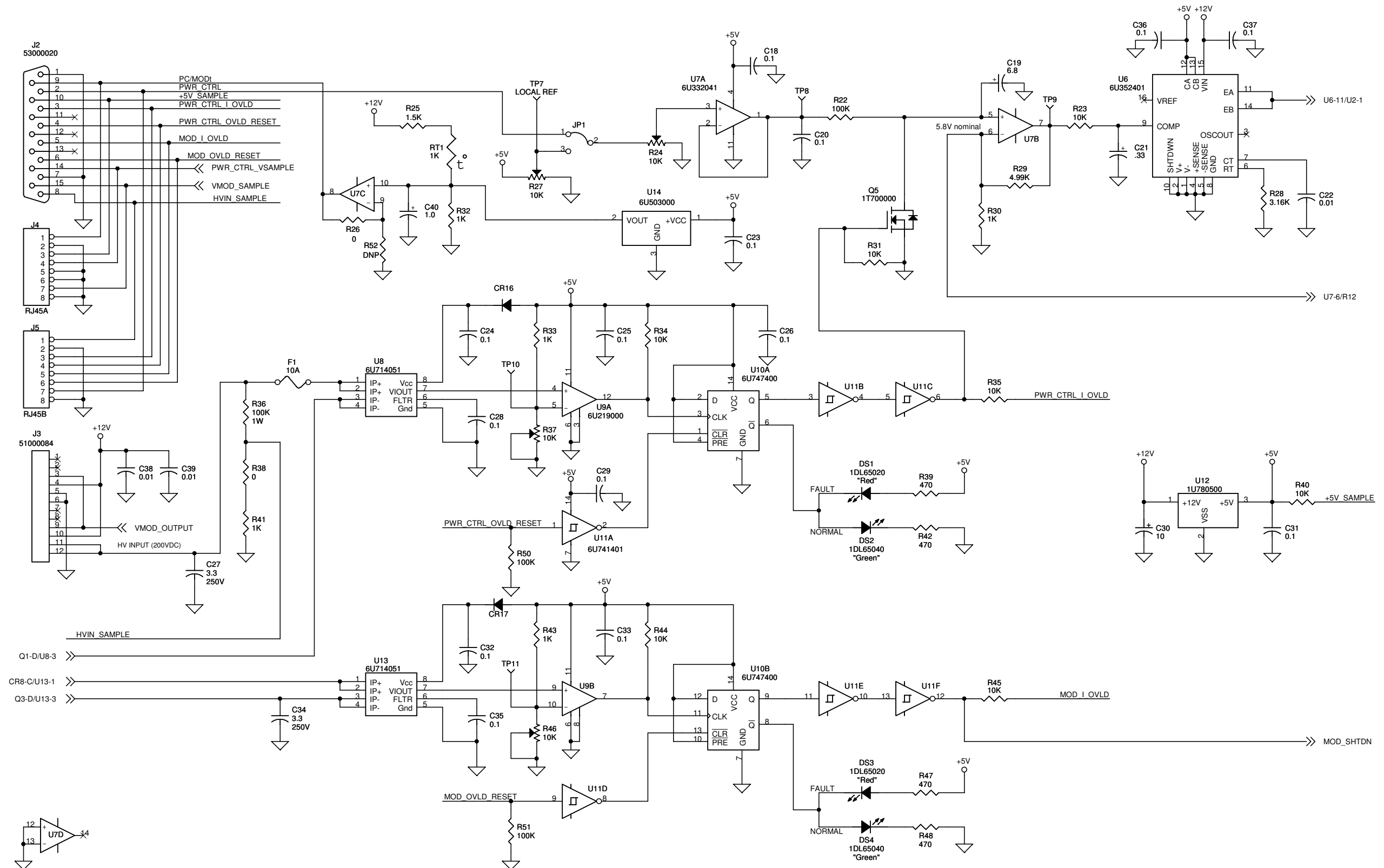
SEP45500 - TRANSMITTER CONTROL SCHEMATIC (5 OF 5)



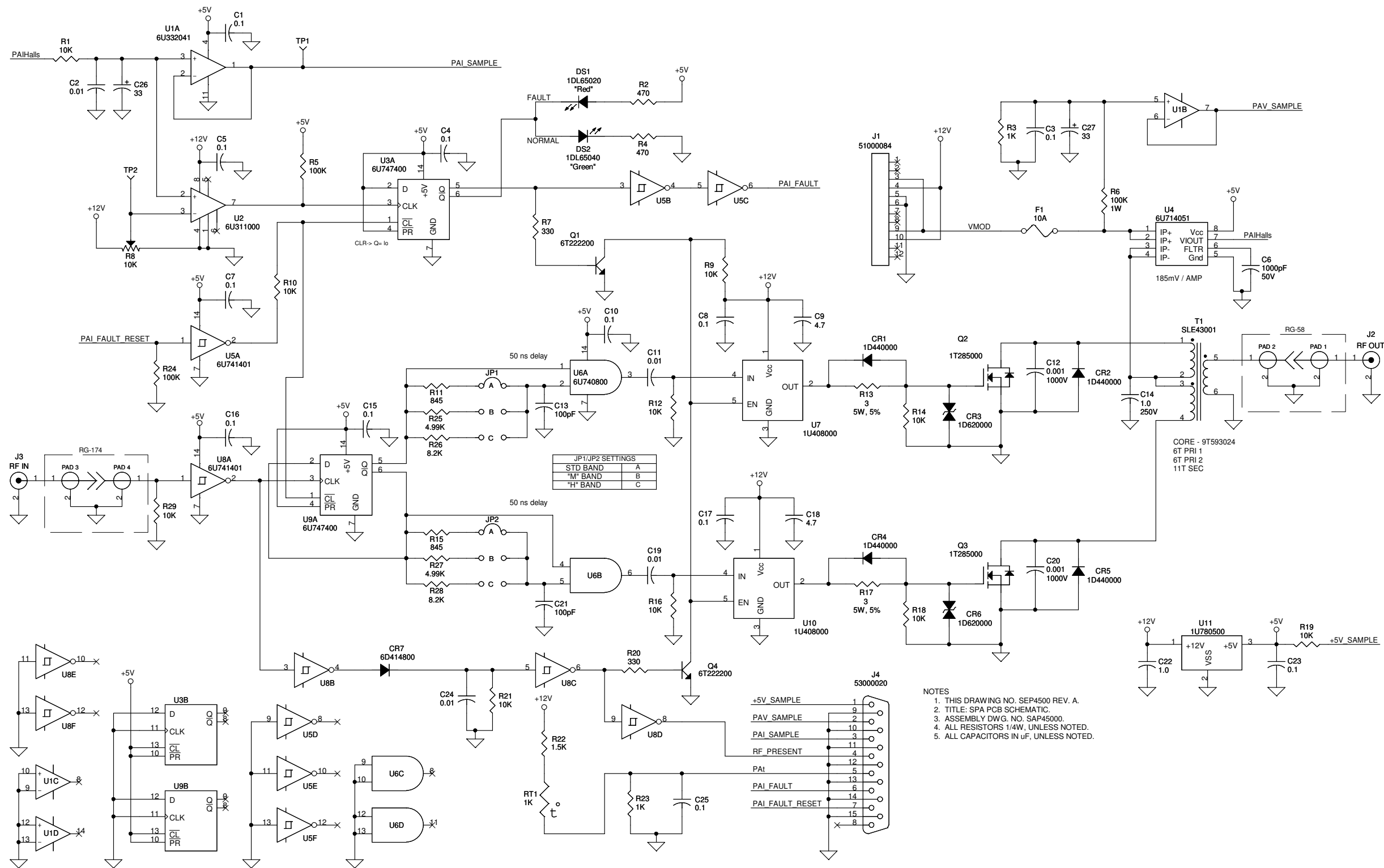
SEP46400 - LOW VOLTAGE/HIGH VOLTAGE REGULATOR SCHEMATIC



SEP46100 - POWER CONTROL MODULATOR SCHEMATIC (1 OF 2)

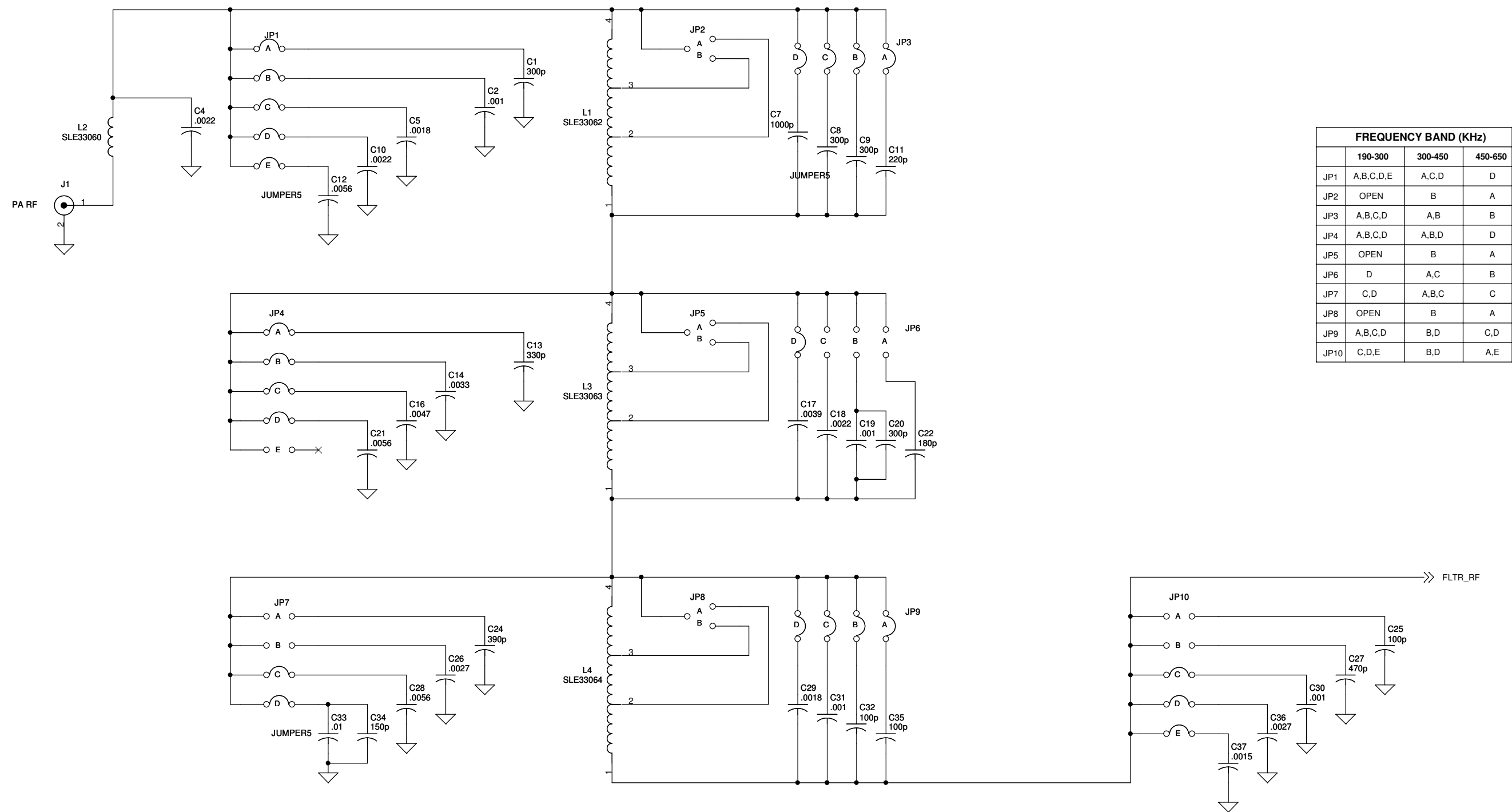


SEP46100 - POWER CONTROL MODULATOR SCHEMATIC (2 OF 2)



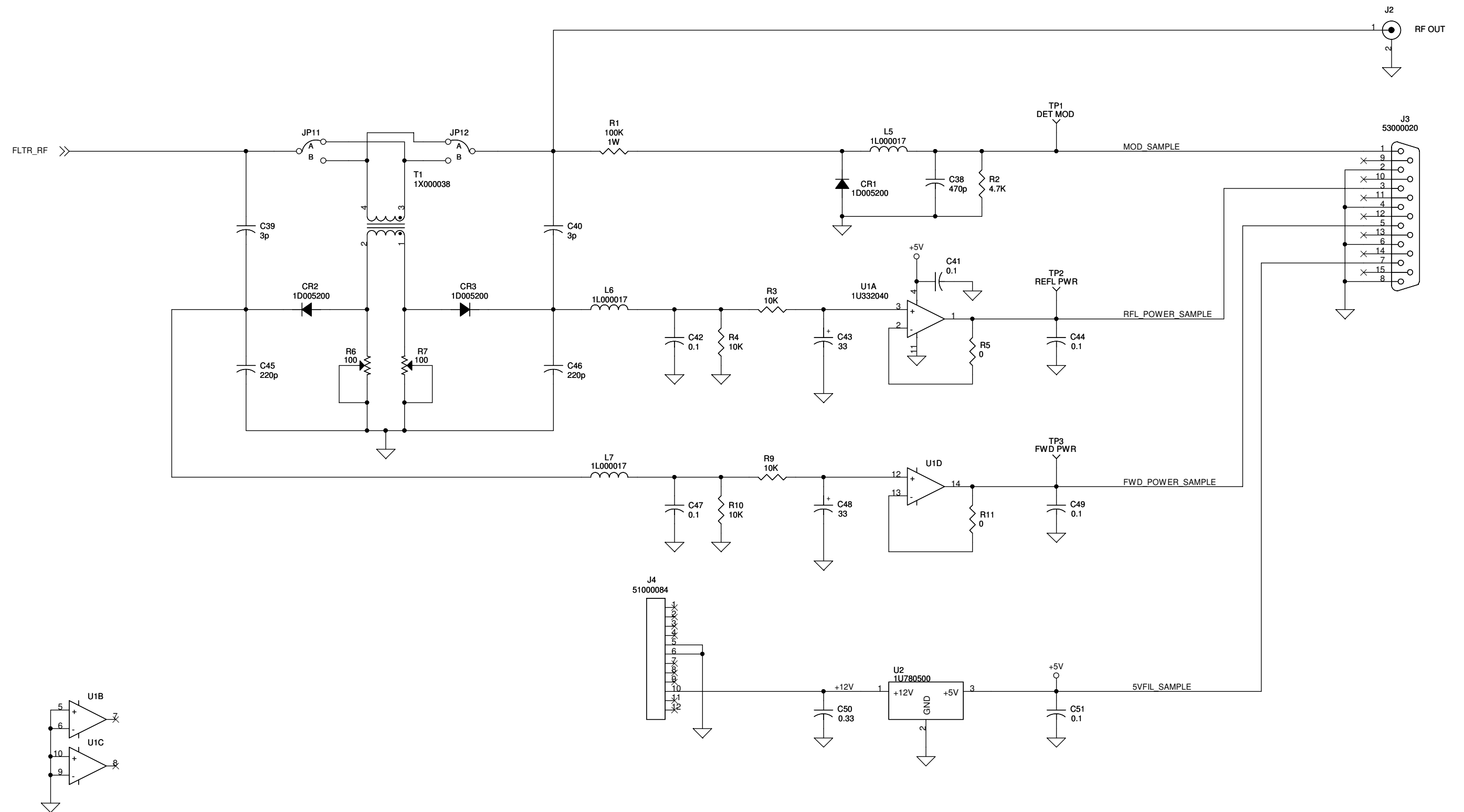
- NOTES
1. THIS DRAWING NO. SEP4500 REV. A.
 2. TITLE: SPA PCB SCHEMATIC.
 3. ASSEMBLY DWG. NO. SAP45000.
 4. ALL RESISTORS 1/4W, UNLESS NOTED.
 5. ALL CAPACITORS IN uF, UNLESS NOTED.

SEP45000 - SPA SCHEMATIC

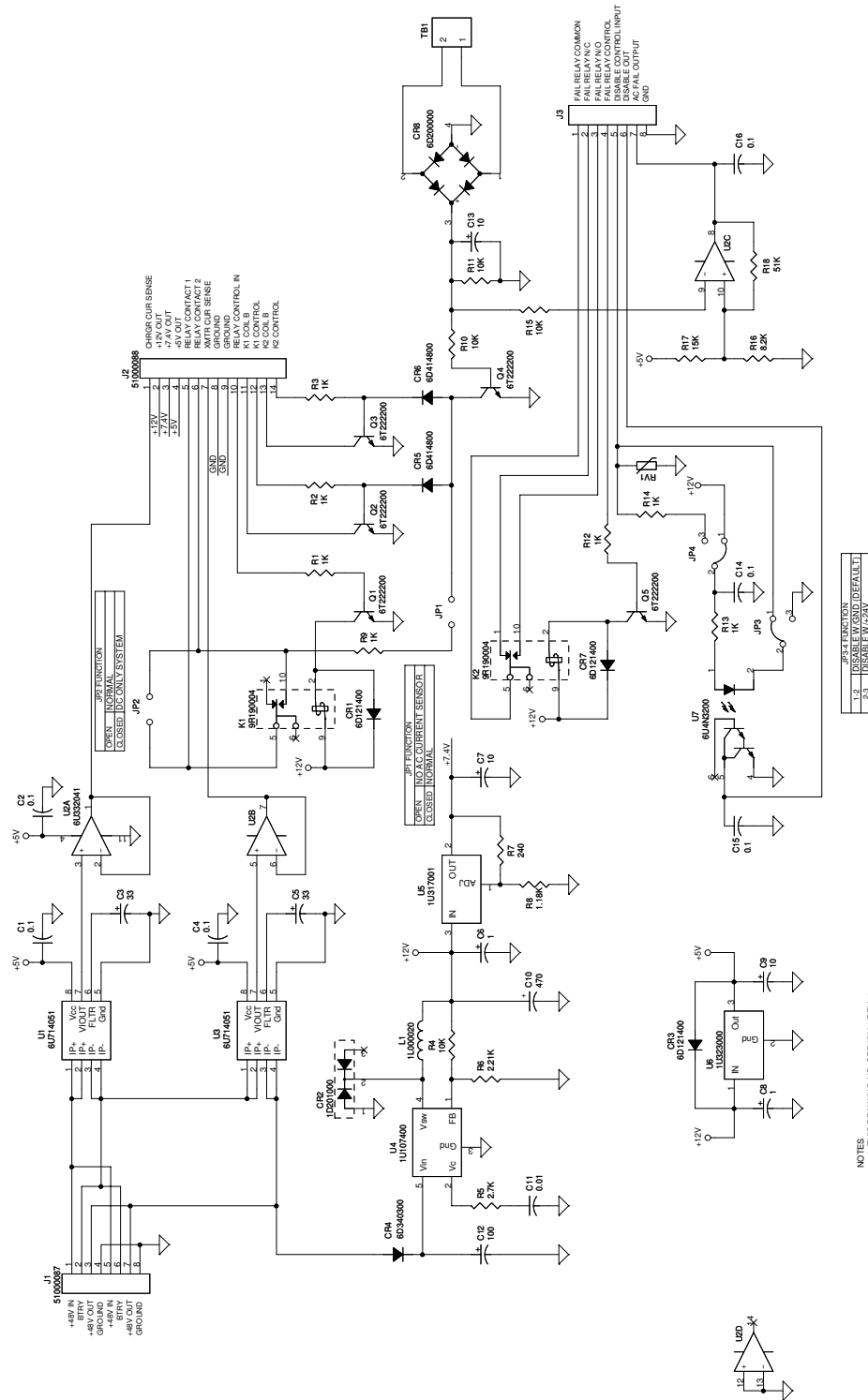


- NOTES
1. THIS DRAWING NO. SEP46200 REV A.
 2. TITLE: FILTER/BRIDGE PCB (STANDARD BAND) SCHEMATIC.
 3. ASSEMBLY DWG. NO. SAP46200.
 4. ALL RESISTORS 1/4W, 1%, UNLESS NOTED.
 5. ALL CAPACITORS IN uF, UNLESS NOTED.
 6. C3, C6, C15, C23, L1 NOT USED FOR STANDARD BAND.
 7. POLY FILM CAPS (IN uF) ARE RATED AT 1600V.
 8. SILVER MICA CAPS (IN pF) ARE RATED AT 500V.
 9. FILTER SHOWN JUMPED FOR 190-300kHz.
 10. ">>" INDICATES OFF-PAGE CONNECTION.

SEP46200 - FILTER BRIDGE SCHEMATIC (1 OF 2)

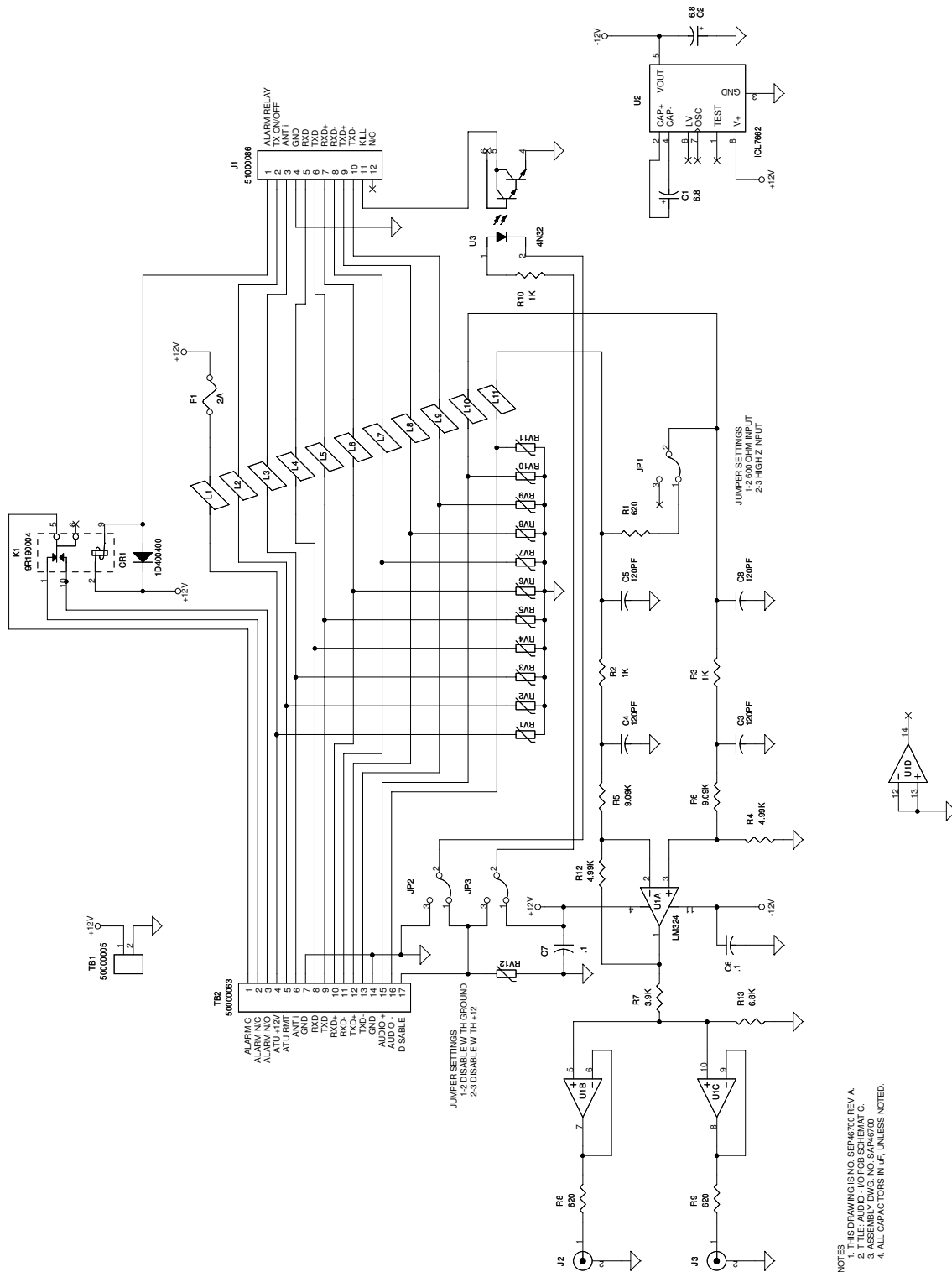


SEP46200 - FILTER BRIDGE SCHEMATIC (2 OF 2)



NOTES:
 1. THIS DRAWING NO. SEP45601 REV. 1.
 2. TITLE: BATTERY SENSE/REG PCB SCHEMATIC.
 3. DATE: 10/10/00
 4. ALL CAPACITORS IN U.F. UNLESS NOTED.

SEP45601 Rev 1
 Figure 2-37. Battery Sense PCB Schematic



SEP46700 Rev A

Figure 2-39. Audio I/O PCB schematic