

### 4.6 Controller Schematics

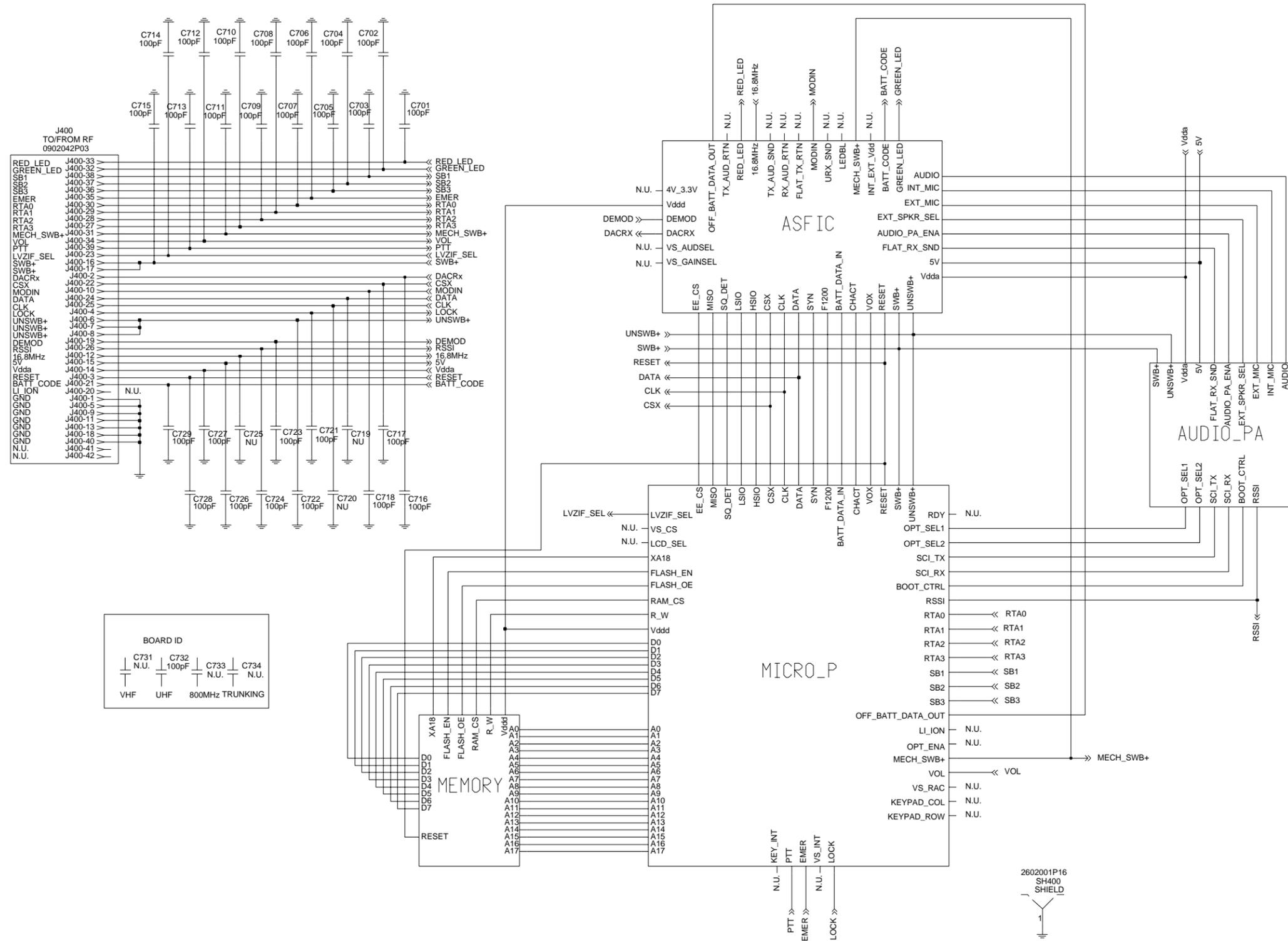


Figure 4-12: Complete Controller Schematic Diagram

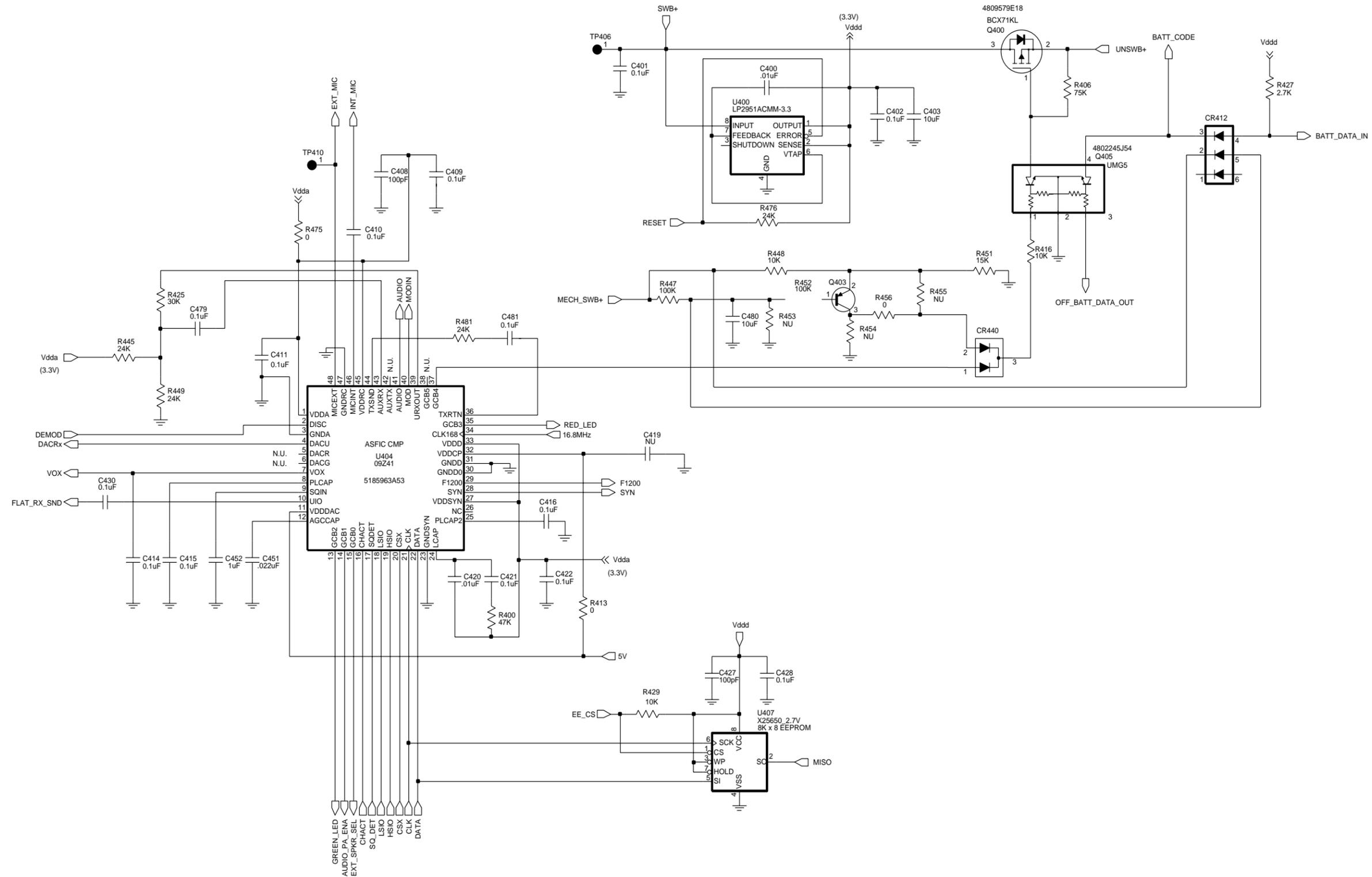


Figure 4-13: Controller ASFIC/ON\_OFF Schematic Diagram

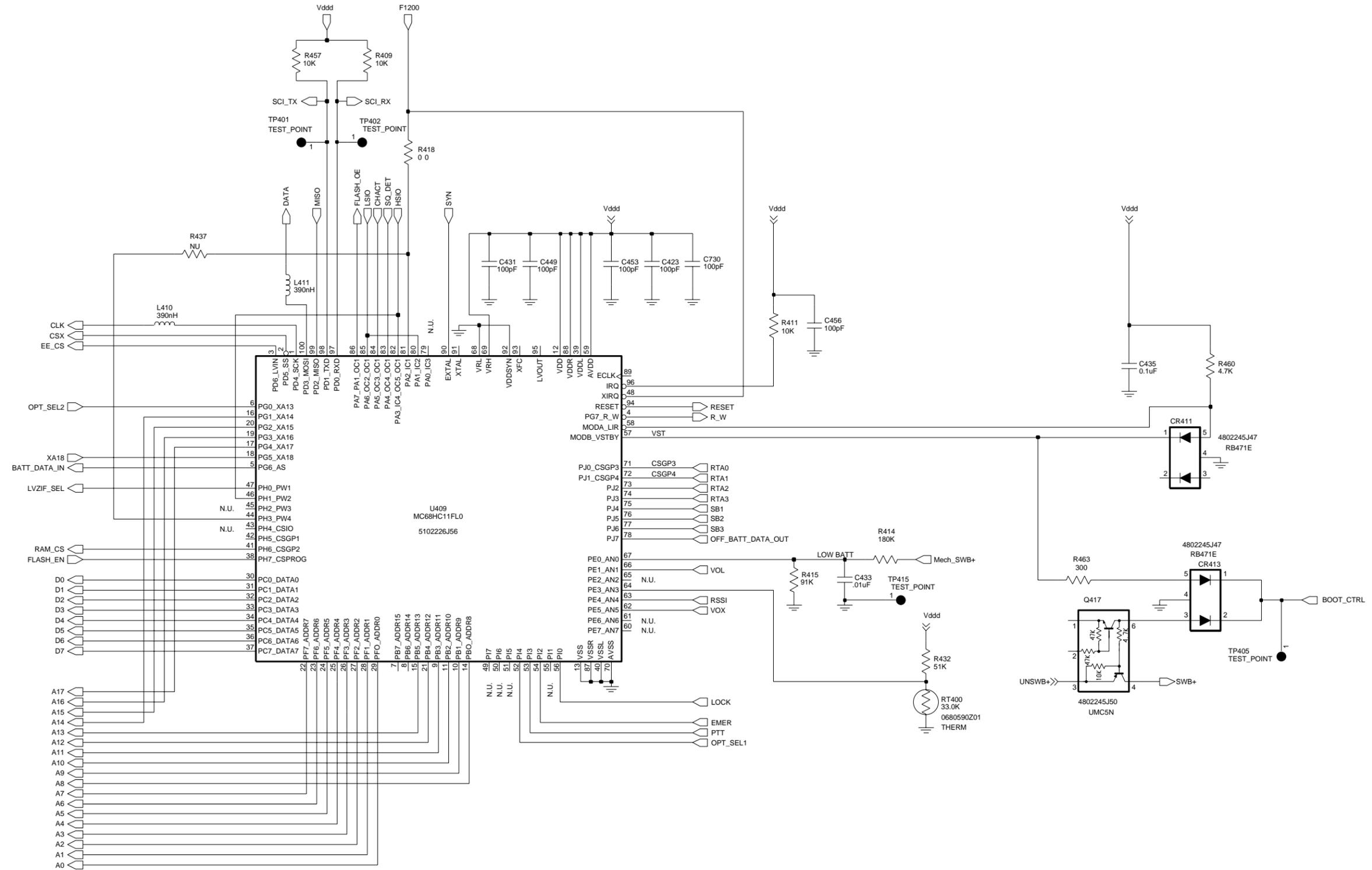


Figure 4-14: Controller Micro Processor Schematic Diagram

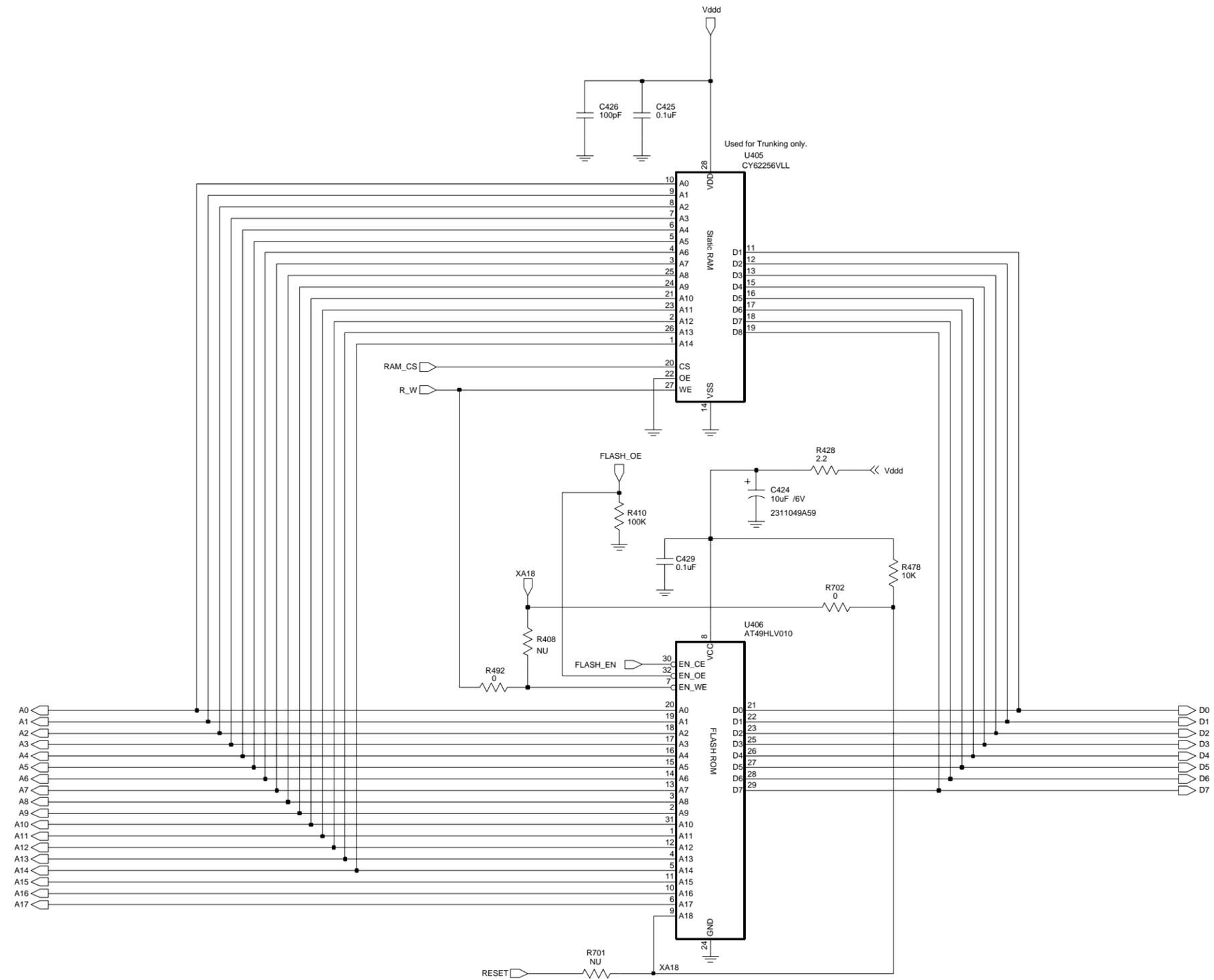


Figure 4-15: Controller Memory Schematic Diagram

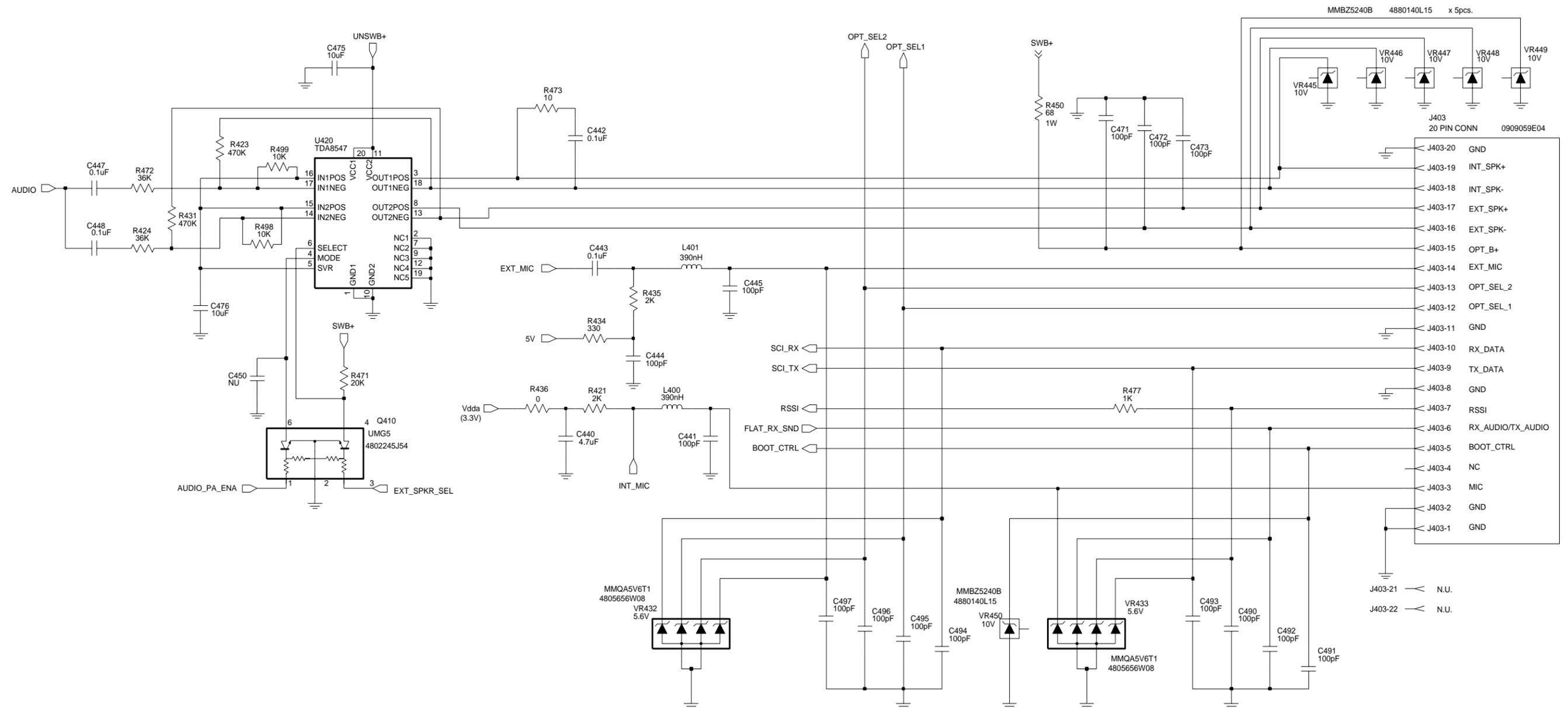


Figure 4-16: Controller Audio Power Amplifier Schematic Diagram

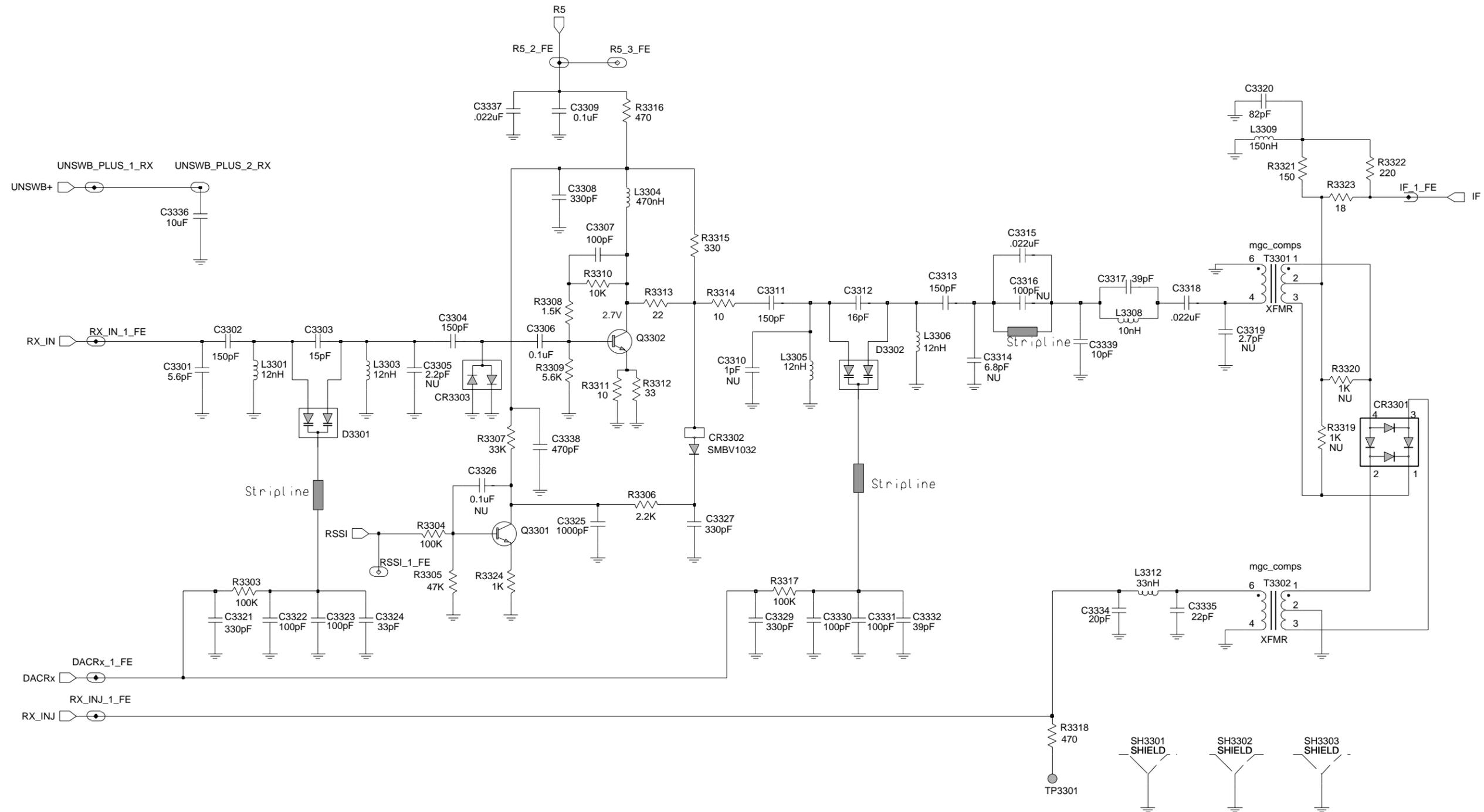


Figure 4-20: VHF Receiver Front End Schematic Diagram

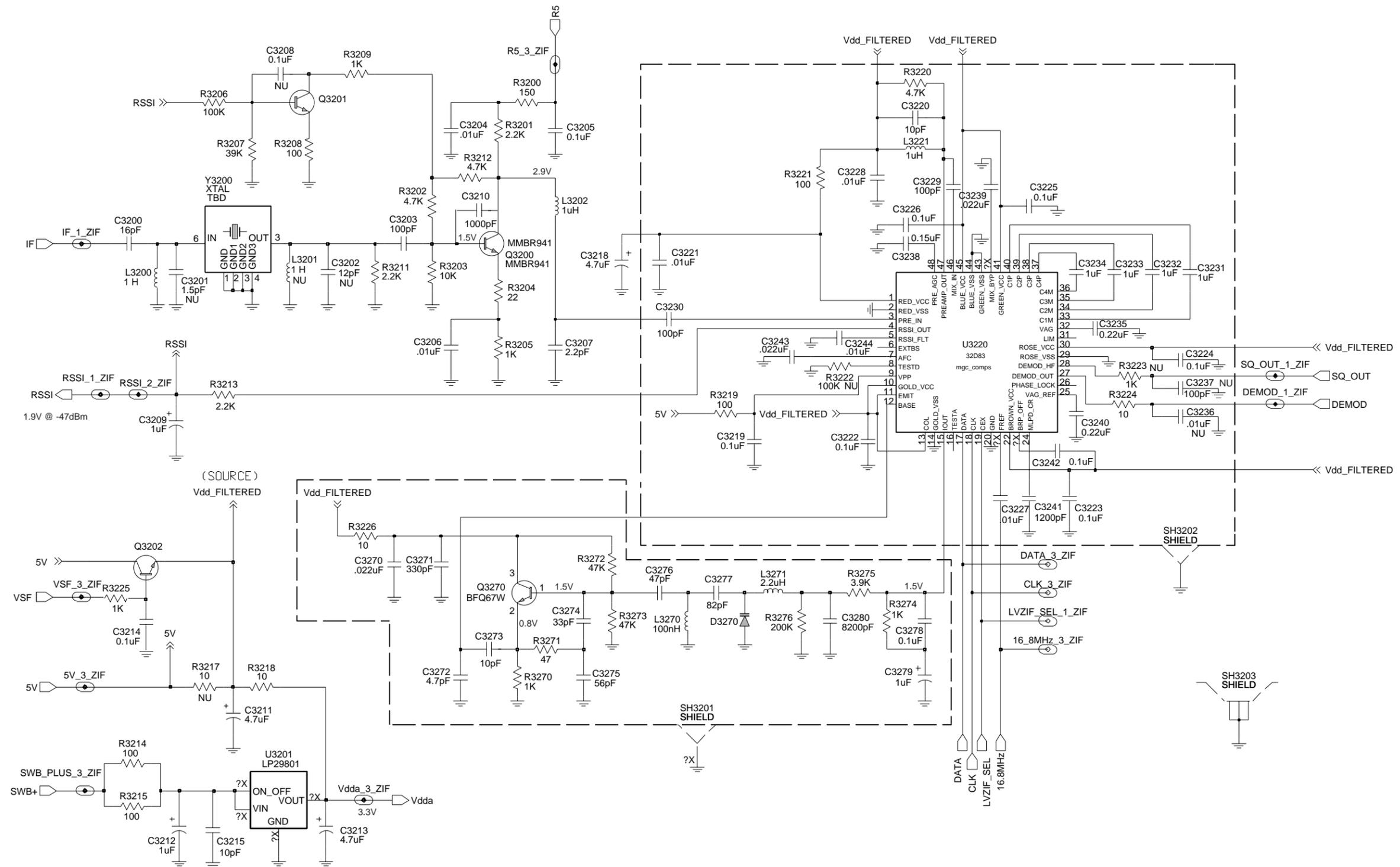


Figure 4-21: VHF Receiver Back End Schematic Diagram

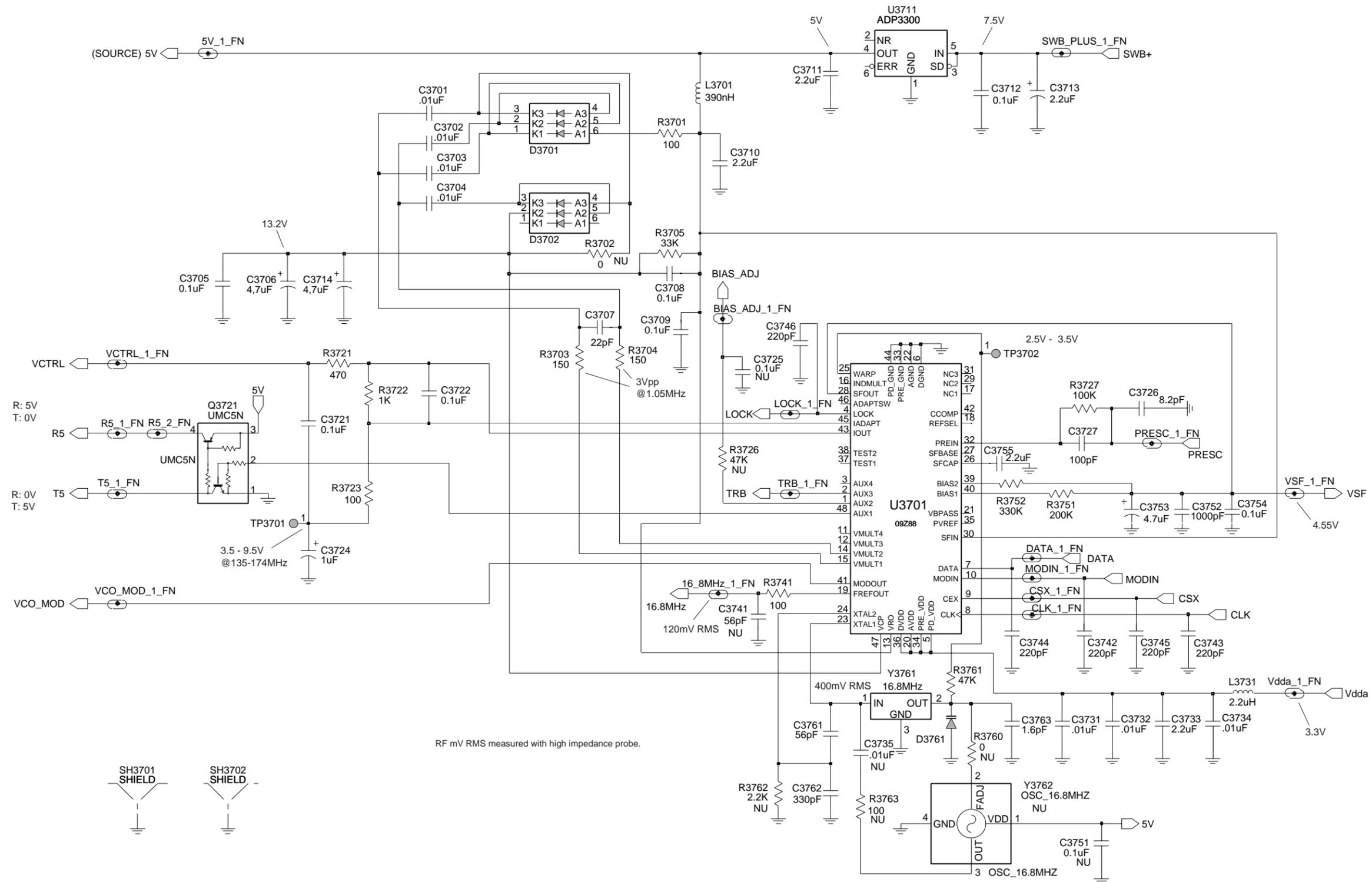


Figure 4-22: VHF Synthesizer Schematic Diagram

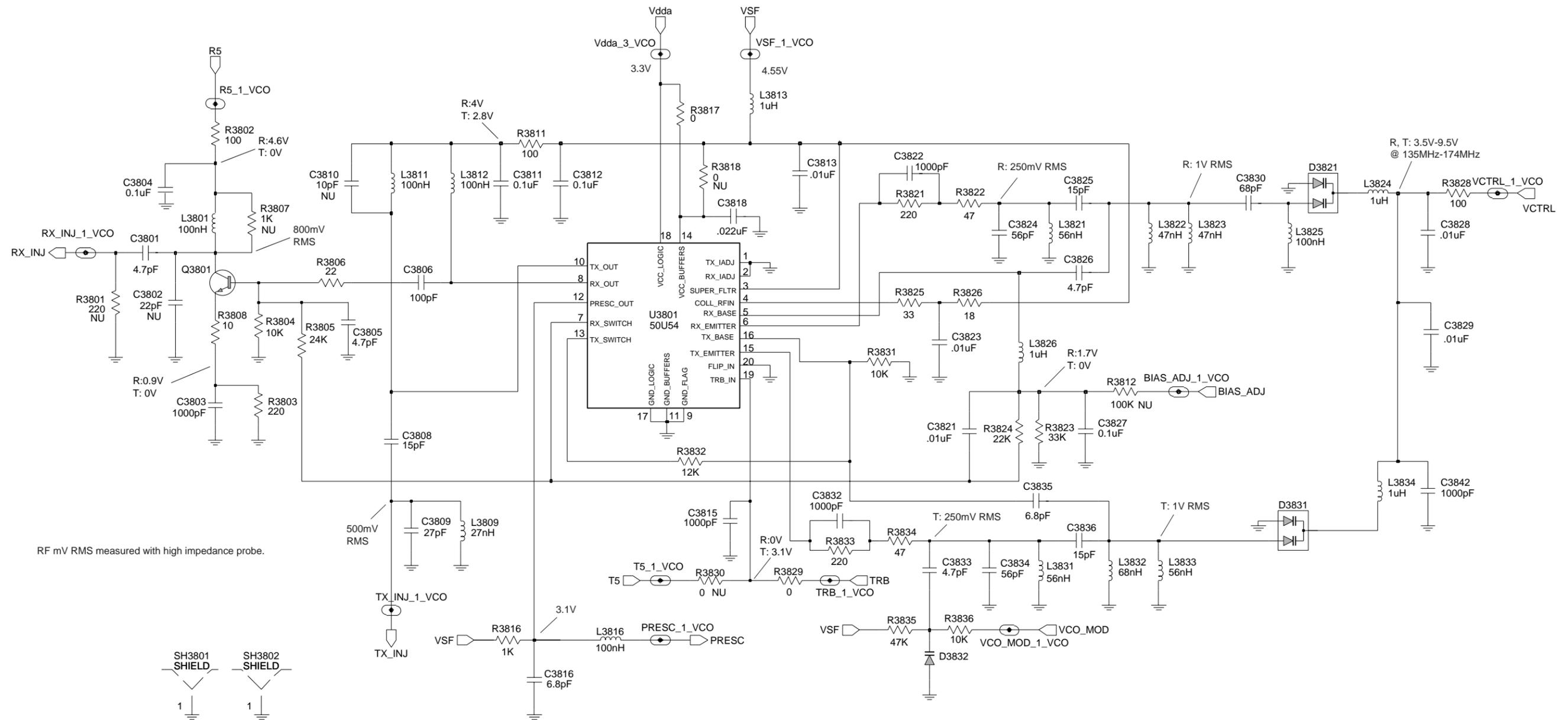


Figure 4-23: VHF Voltage Controlled Oscillator Schematic Diagram

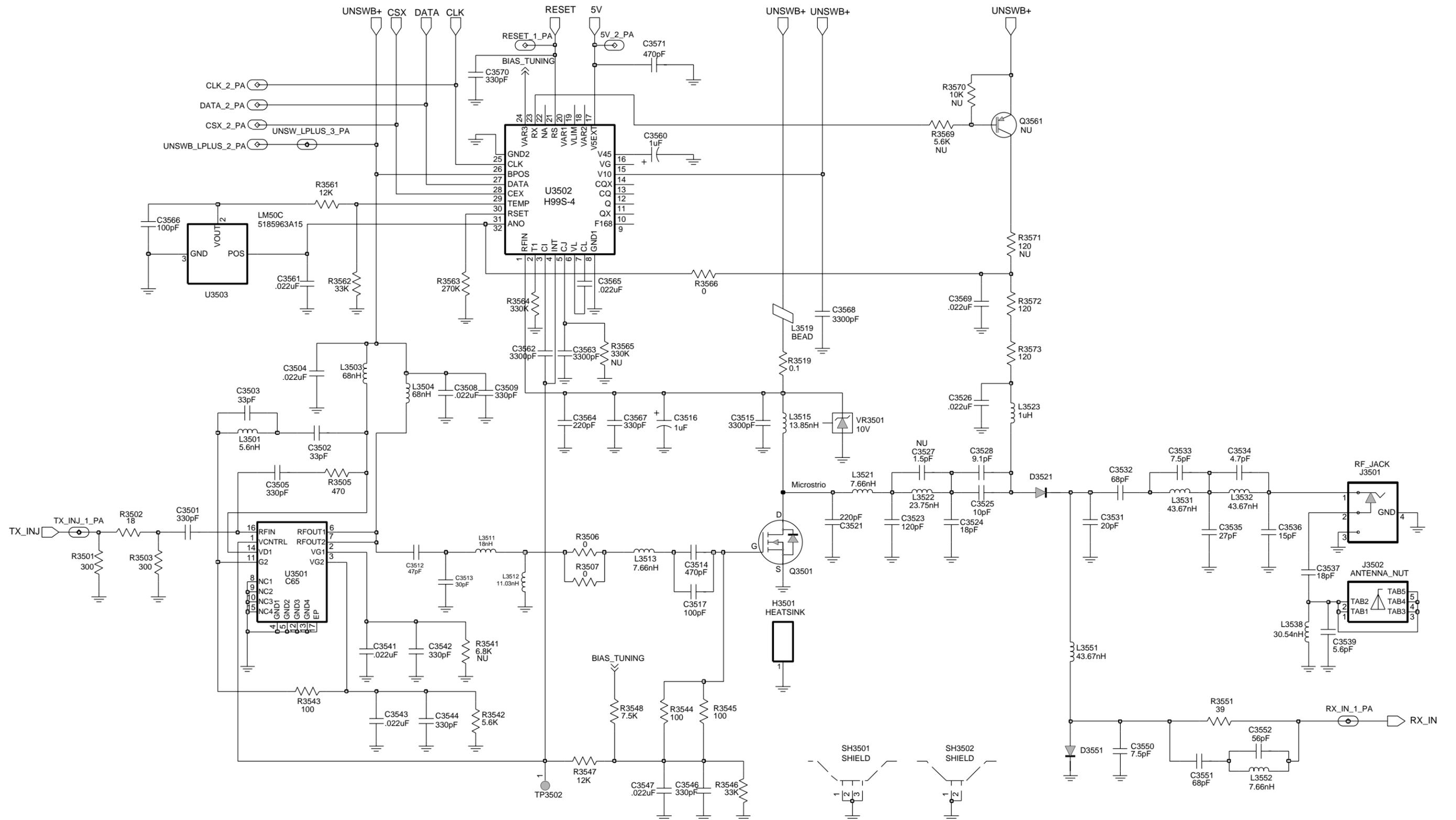


Figure 4-24: VHF Transmitter Schematic Diagram