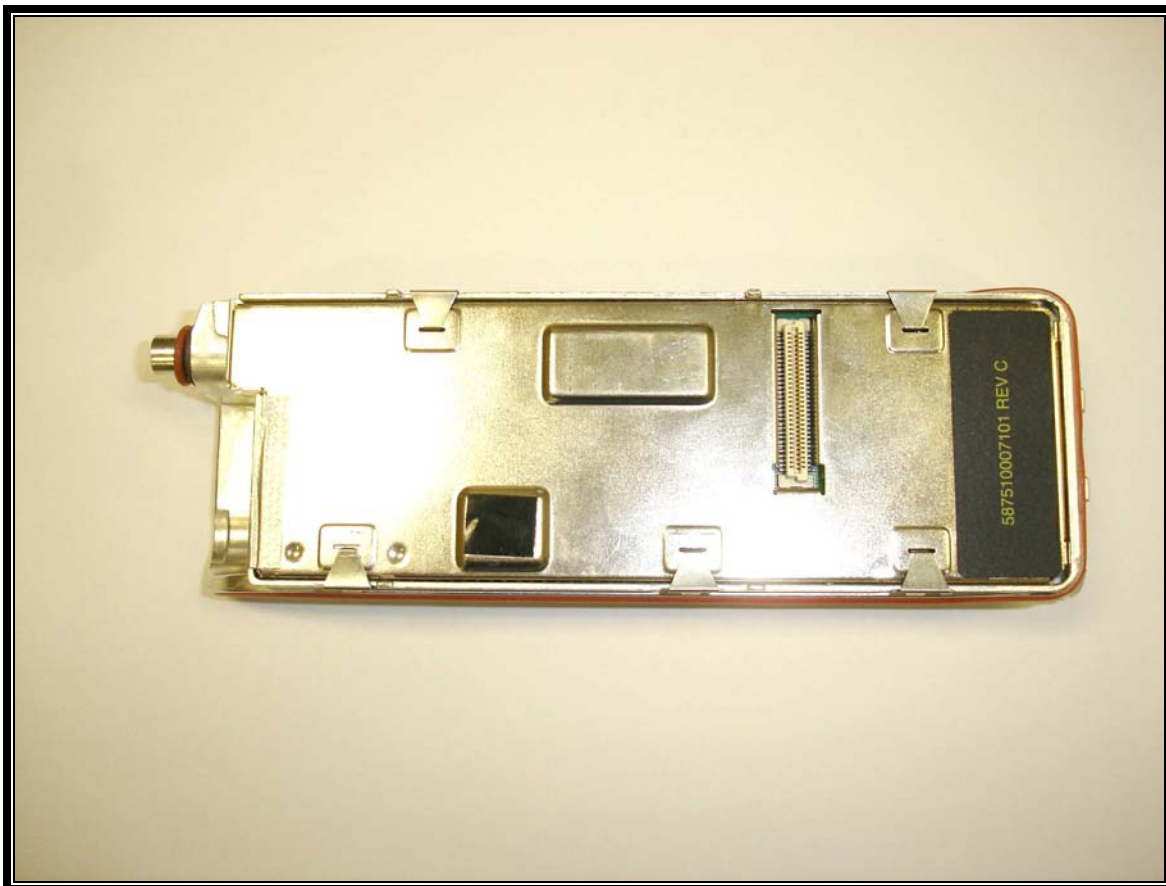
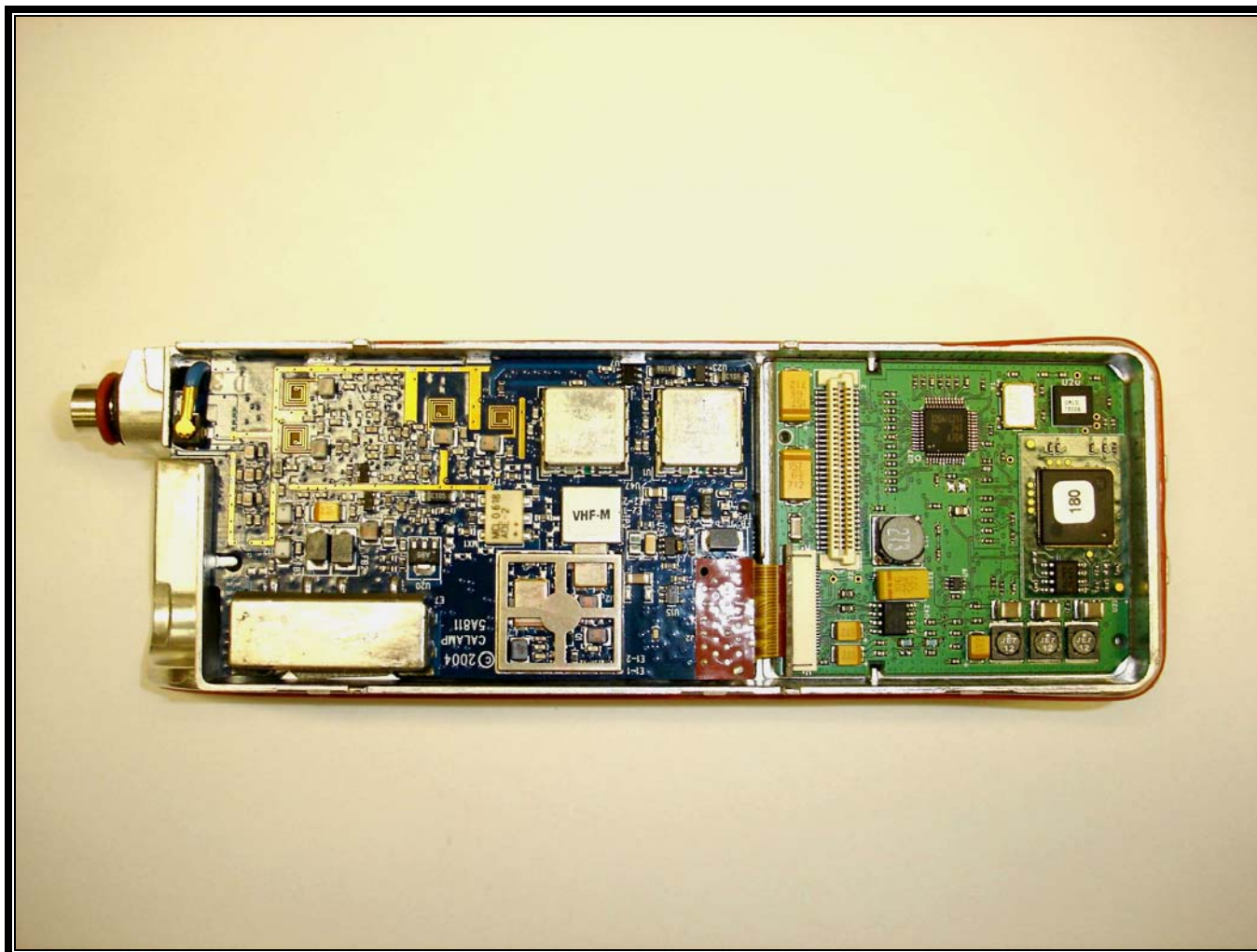


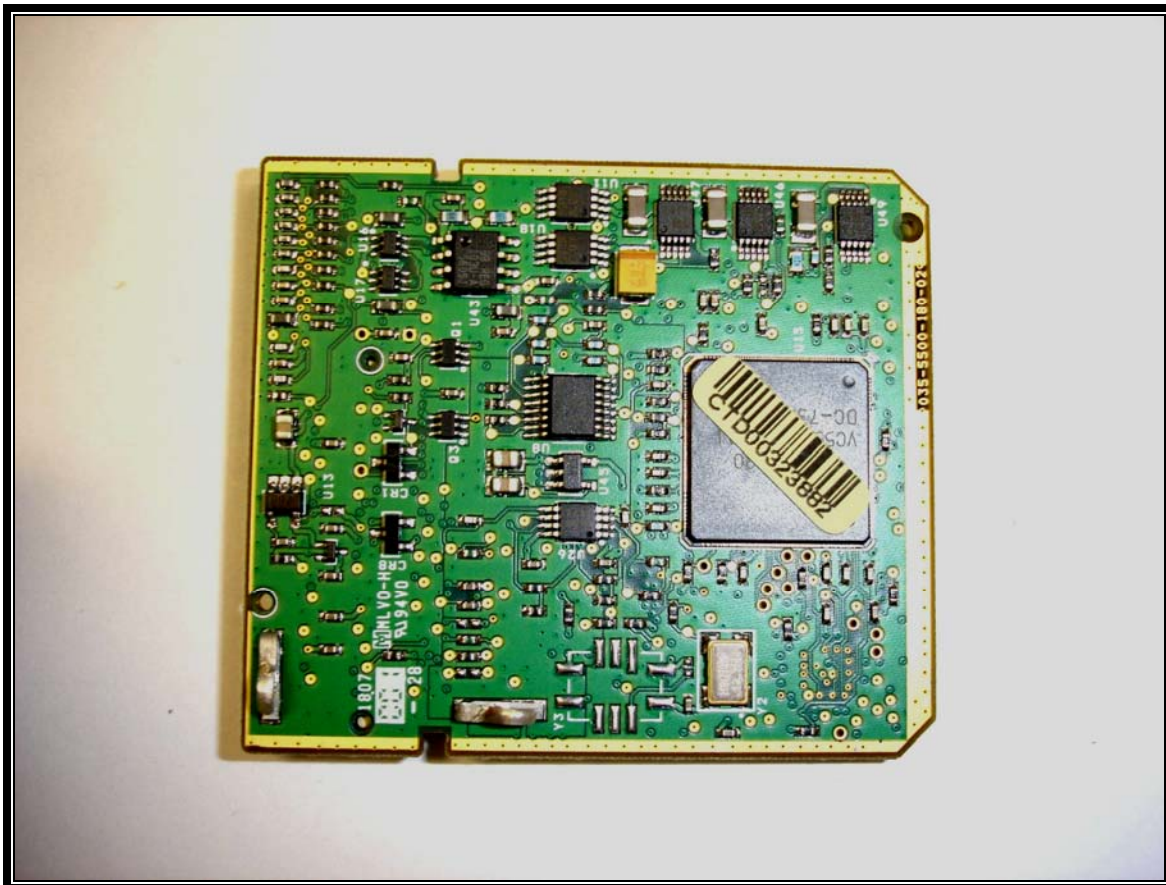
**Appendix O: Internal Photographs**



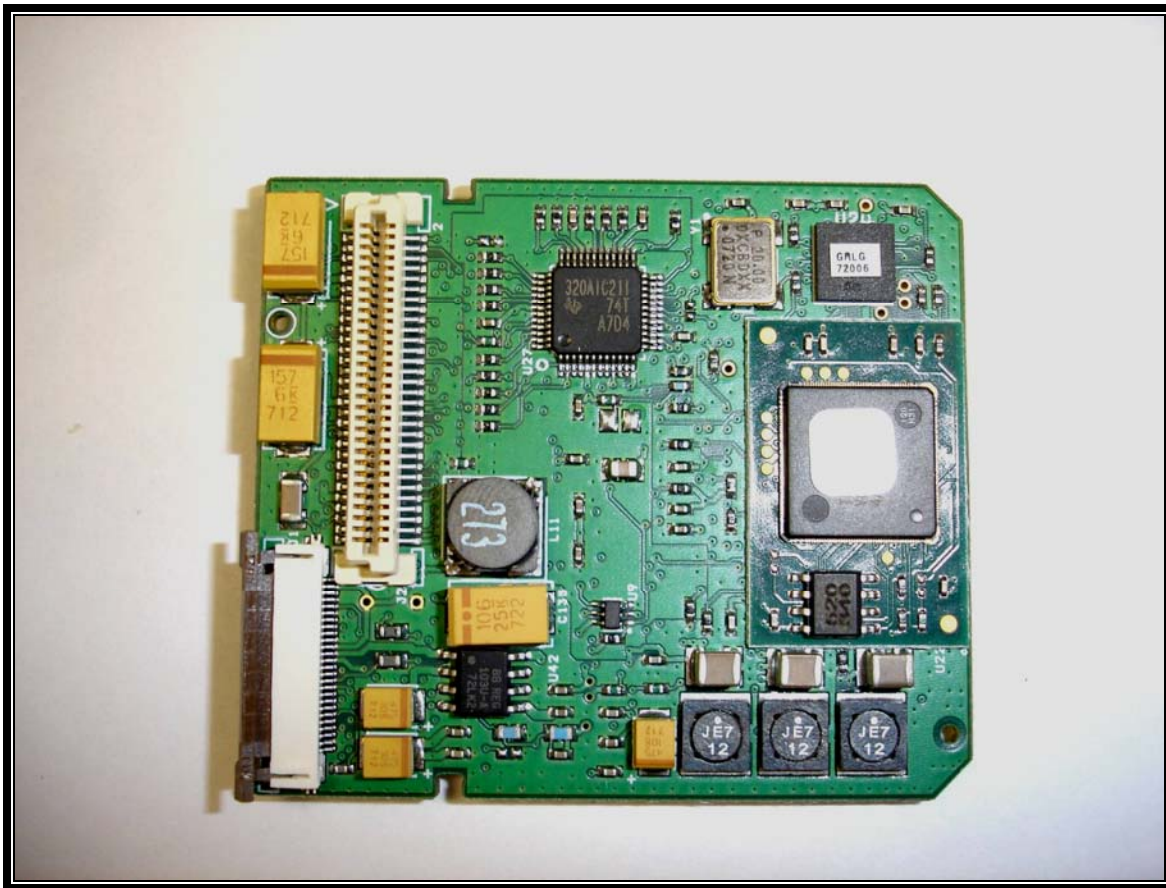
**Photograph 10: Chassis with Shields**



**Photograph 11: Chassis without Shields**



Photograph 12: Logic Board Bottom

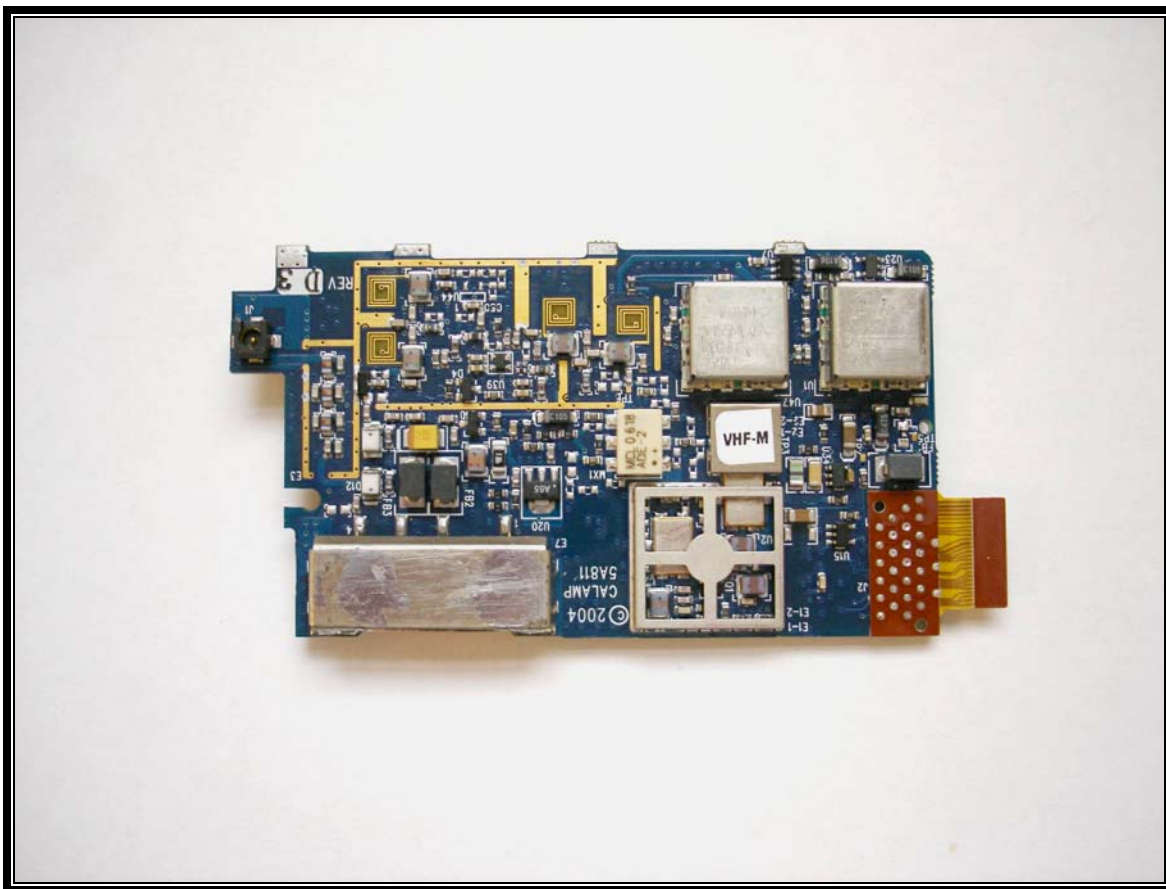


**Photograph 13: Logic Board Top**

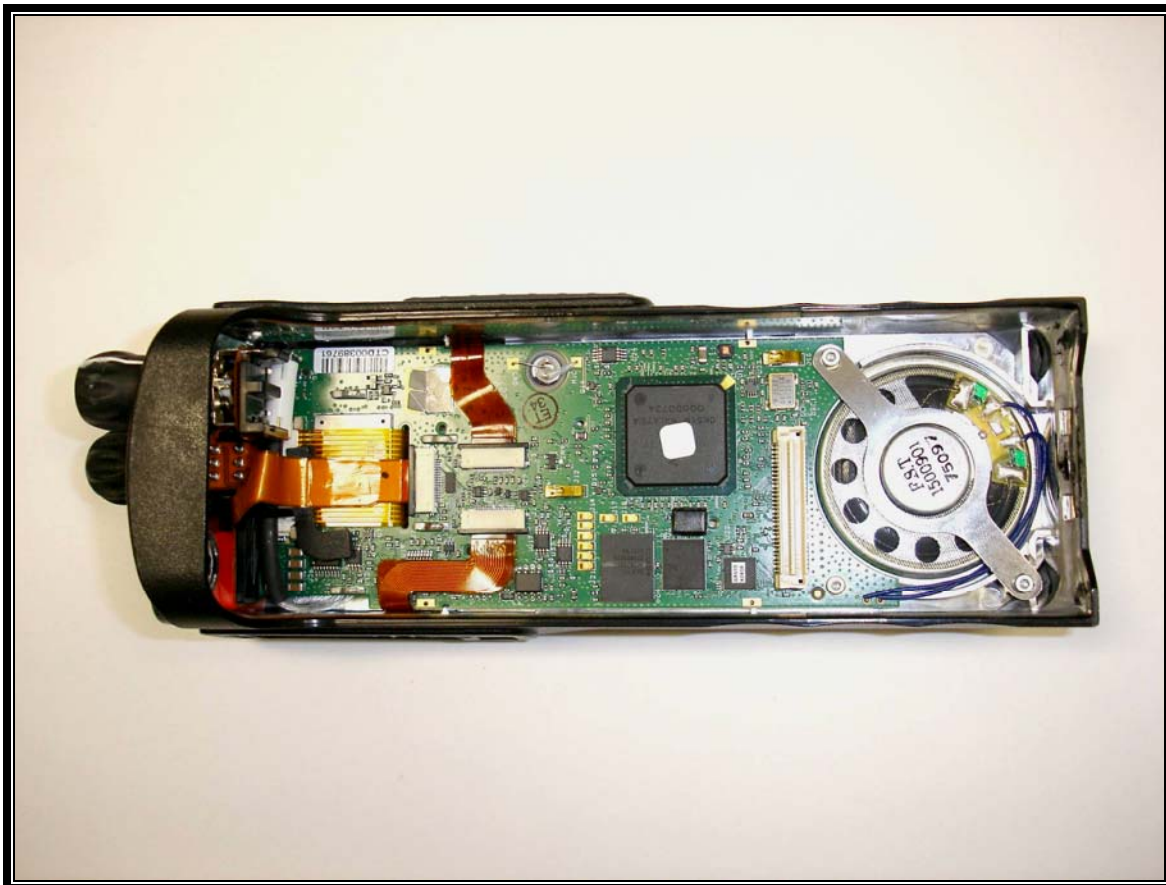




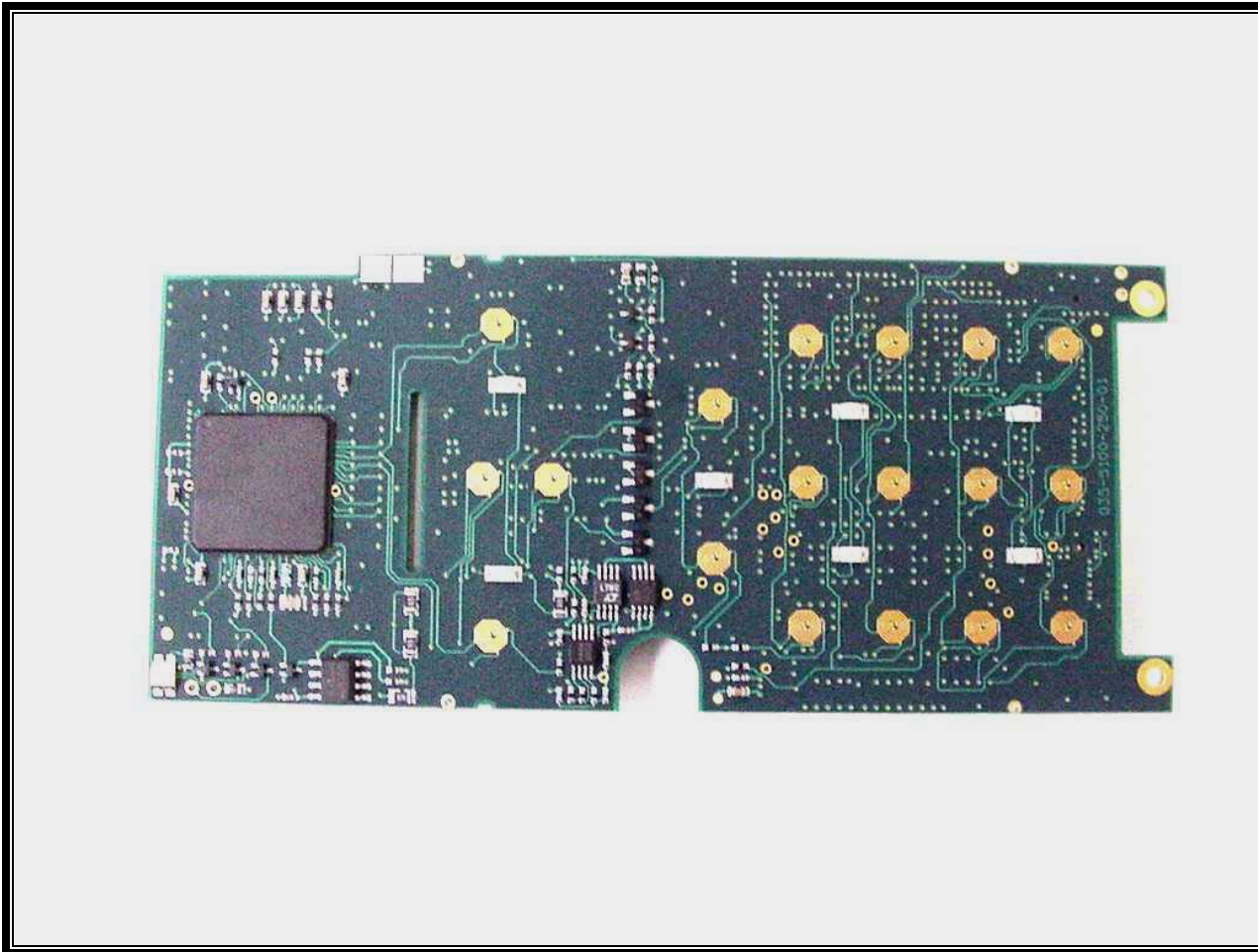
Photograph 14: RF Board Bottom



**Photograph 15: RF Board Top**



**Photograph 16: User Interface Board in Front Cover**



**Photograph 17: User Interface Board Front**