

Exhibit 14 MEASUREMENT OF OCCUPIED BANDWIDTH

SECTION 2.1049 Measurement Of Occupied Bandwidth

Occupied bandwidth measurements were performed for the channel configurations pertinent to full bandwidth A through C Block operation of the *UMTS-CDMA 9341 RRH 40W 1900 MHz System* / FCC ID: AS5ONEBTS-18. This documents the typical performance of the *UMTS-CDMA 9341 RRH 40W 1900 MHz System* / while operating with one through seven CDMA carriers. All power adjustments were performed via the FTR1900 under software control. The measurement of the 1 and 7 carrier 40 W total power cases are described below.

The occupied bandwidth of the *UMTS-CDMA 9341 RRH 40W 1900 MHz System* / FCC ID: AS5ONEBTS-18 was measured using a Rohde & Schwarz FSEM-30 Spectrum Analyzer, a PC based instrumentation controller using TILE™ software and calibrated RF attenuation and coupled signal path. The RF power level was measured and adjusted via the test setup in Figure 14A. The set RF output from the transmitter was reduced by calibrated broadband attenuators to amplitudes usable by the spectrum analyzer and power meter. The attenuation factors are reflected in the displayed values of the charts. The typical occupied bandwidth measurement displays the signal adjusted to the -16.2 dBc level corresponding to the corrected RF power level for a 30 kHz resolution bandwidth (RBW). This set-point was performed as follows:

For each test the power calibration was individually verified at the transmitter antenna connection (J4) with a power meter by using the test setup depicted in Figure 14A. The power calibration was performed to calibrate the setting power meter measurement as a reference for both the measured 30 kHz Occupied Bandwidth signal at the -16.2 dBc line and a 3 MHz RBW measurement against the “Top of Mask” limit. The “Top of Mask” limit corresponds to a single carrier signal at the specified power level which results in 40 W total power for the number of carriers being tested as measured with an RBW of ≥ 1.25 MHz. Since at the transmitter J4 output there may be multiple CDMA carriers, the measurement is made with an RBW setting of 3 MHz which is greater than the individual 1.25 MHz signal bandwidth. These power calibration measurements were performed along with each Occupied Bandwidth measurement. The signals measured at RBW's of 3 MHz and 30 kHz were corrected for path loss and were plotted against the mask limit. As part of the calibration between the power meter measurement and the test analyzer, software was used to place the 3 MHz RBW signal either at the “Top of Mask” for single carriers or at the carrier power calibration line for multi-carrier signals. The carrier as measured with 3 MHz and 30 kHz RBW were corrected with the same attenuation factors. The two measurements are co-plotted on the same graph. A typical single carrier example is shown in Figure 14B which depicts a single carrier (825 F Block) inside the mask for F Block.

The test procedure above, calibrates the carrier power to the “Top of Mask” and accurately places the 30 kHz RBW measured carrier at the -16.2 dBc reference line. All of the plots are presented with a sufficiently wide frequency span for the specific signals or Block of interest. This allows for ease of comparison of the multi-carrier performance. This data was electronically recorded using the TILE™ software and electronically placed in the Occupied Bandwidth Data Sheets. These sheets contain data for “Left Edge of Block”, and “Right Edge of Block” for each PCS frequency Block. Since the 5 MHz Blocks are too narrow for applications with more than 3 carriers, 8 carrier applications data was collected for the combined DB, BE, FC and CG Blocks.

Block Organization and Tests Performed

The FLEXENT PCS Modular Cell 4.0B product line allows the use of transmit filters with bandwidths of 5 MHz to as wide as 65 MHz. The use of EDPD provides the spurious control which allows the use of wide bandwidth filters such as a 60 MHz classic PCS Band, shifted 60MHz for blocks A4 through G or a 65 MHz Full PCS Band filters. These wideband filters provide for the least spurious reduction at “edge of block” and “edge of band” and thus represent the most difficult compliance configuration. The filters do not provide for any spurious reduction at the internal block edges inside the band. The testing of the product documented herein was performed with 60 MHz and shifted 60 MHz PCS band filters. This test configuration are the most difficult for compliance demonstration.

Exhibit 14 *continued*

The demonstration of compliance for the one to 7 carrier RRH Transceiver configurations were performed for operation in all PCS Blocks. The presented data for this Class II change demonstrates the one to 7 carrier 40 W configurations compliance.

In order to adequately evaluate performance the worst case modulation factors of 2G Voice (vs. 3G1X or 3G1X-EV-DO) were used from the governing documents. Thus, the applied signal, from a *UMTS-CDMA 9341 RRH 40W 1900 MHz System*, met the recommended characteristics per "**Table 6.5.2-1 Base Station Test Model, Nominal**" from **3GPP2 C.S0010-C v2.0, 24 February 2006**, Recommended Minimum Performance Standards for cdma2000 Spread Spectrum Base Stations, as defined below in table 14.1.

Type	Number of Channels	Fraction of Power (Linear)	Fraction of Power (dB)	Comments
Forward Pilot	1	0.2000	-7.0	Code channel W_0^{64}
Sync	1	0.0471	-13.3	Code channel W_{32}^{64} ; always 1/8 rate
Paging	1	0.1882	-7.3	Code channel W_1^{64} ; full rate only
Traffic	M = 37	$0.5647/M$ = 0.015262	-2.48 - 10 log(M) = -18.1620	Variable Walsh assignments, full rate only

TABLE 14.1 Base Station Test Model, Nominal for Main Path

Type	Number of Channels	Fraction of Power (Linear)	Fraction of Power (dB)	Comments
Transmit Diversity Pilot	1	0.2000	-7.0	Code channel W_{16}^{128}
Traffic	M = 37	$0.5647/M$ = 0.015262	-2.48 - 10 log(M) = -18.1620	Variable Walsh code channel assignments, full rate only

TABLE 14.2 Base Station Test Model, Nominal for Transmit Diversity Path

The FCC limits contained in **47CFR 24.238 1-Oct-2007** were followed along with the minimum standard presented in **3GPP2 C.S0010-C v2.0, 24 February 2006**. Where combinational measurements of 3G1x-EV-DO are made along with the 2 GV configuration above the applied signal were based upon the 3GPP2 TSG-C.S0032-1 titled "Recommended Minimum Performance Standards for CDMA2000 High Rate Packet Data Access Network ". Section 3.1.2.4 Limitations on Emissions. This standard covers the emissions situation except that we use the maximum 25 MAC full traffic configuration as the standard.

Measurement Offset

The spectrum analysis output plots shows the peak of the CDMA channel signal 16.19 dB below the Mask reference / "zero dBc line" of the spectrum analyzer for the following reason: For the CDMA system there is no carrier without modulation. Since the CDMA signal is broadband and 1.25 MHz wide, all measurements performed at narrower resolution bandwidths need be adjusted for the reduction in signal energy. The following relationship was used to provide the correct level for an unmodulated carrier vs. the broadband modulated signal.

$$10 \cdot \log (\text{Resolution Bandwidth} / \text{Transmit Bandwidth}) = \text{Signal Offset} \quad (1)$$

For the peak of the 1.25 MHz CDMA signal measured with a RBW of 30 kHz the signal offset is:

$$\text{Signal Offset} = 10 \cdot \log (30 \text{ kHz} / 1.25 \text{ MHz}) = -16.19 \text{ dB}$$

Limits which are specified as appropriate at a given RBW can be measured and evaluated at other RBW's if the limit is adjusted per equation (1)

Exhibit 14 *continued***Require Levels**

The minimum standard presented in 3GPP2 C.S0010-C v2.0, 24 February 2006 was followed for Suppression inside the Licensee's Frequency Block(s)

Signals that are within the base station transmit band of 1930.000 to 1990.000 MHz and are within the specific block(s) allocated to the operator's system, the total conducted spurious emissions in any 30 kHz band greater than 885 kHz from the CDMA channel center frequency shall not exceed a level of -45 dBc....

The Limit in 47 CFR 24.238(a)(b) for emissions in the 1 MHz band immediately outside and adjacent to a licensee's frequency block is:

Emissions ≤ 1 MHz outside the Block *when measured with a RBW of 1%* of the emissions Bandwidth shall be attenuated by :

$$-\{43+10\log(\text{mean power output in watts})\} = -13 \text{ dBm}$$

The Limit in 47 CFR 24.238(a) for emissions outside a licensee's frequency block is:

Emissions > 1 MHz outside the Block, *when measured with a RBW of 1 MHz*, shall be attenuated by :

$$-\{43+10\log(\text{mean power output in watts})\} = -13 \text{ dBm.}$$

Measurement at a Resolution Bandwidth of 30 kHz is based on our experience with 47 CFR 24.238 and lacking other guidance.

Adjusted Levels

The following levels apply when measurement of the above limits are performed with an RBW of 30 kHz. Measurement at a Resolution Bandwidth of 30 kHz is based on our experience with 47 CFR 24.238 and lacking other guidance.

1. On any frequency removed from the carrier center frequency by greater than 885 kHz up to 1.25 MHz - at least 45 decibels below the carrier; and
2. On any frequency removed from the carrier center frequency by greater than 1.25 MHz to 2.25 MHz the level shall not exceed -9.2 dBm when measured in a 30 kHz resolution bandwidth (Note 2 below).
For a single carrier at 40 Watts Total the required level is -9.2 dBm
For multiple carriers at 40 Watts Total the required level is -9.2 dBm
3. From the edge of the Block to the 10th harmonic of the carrier at least

$$-\{43+10\log(\text{mean power output in watts})\} \text{ dBm.}$$

For single and multiple 1.25 MHz bandwidth carriers at 40 Watts Total... the required level is -28.2 dBm as measured with a 30 kHz resolution bandwidth (see Note 3). This is equal to -13 dBm measured with a 1 MHz resolution bandwidth.

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Note 2: The -9.2 dBm level was computed as follows: The limit is specified as

$$-\{43+10\log(\text{mean power output in watts})\} \text{ dB} = -13 \text{ dBm}$$

When measured in a resolution bandwidth not less than 1% of the signal bandwidth.

Since the carrier is a 1.25 MHz bandwidth signal, the limit is adjusted to

$$-13 + 10\text{LOG}(30\text{kHz}/12.5 \text{ kHz}) \text{ dBm} = -9.2 \text{ dBm}; \text{ which given a } 46.02 \text{ dBm carrier (40W) equals } -55.22 \text{ dBc}$$

Note 3: The -28.2 dBm level is computed from -13 dBm measured with a 1 MHz resolution bandwidth adjusted by :

$$-13 + 10\text{LOG}(30\text{kHz}/1.0 \text{ MHz}) \text{ dBm} = -28.2 \text{ dBm}; \text{ which given a } 46.02 \text{ dBm carrier (40W) equals } -74.22 \text{ dBc}$$

Exhibit 14 *continued***Mask Description for a Single Carrier in a 40 Watts Total Power application.**

The Mask limits are identical for the left and right side of the PCS Blocks and are as follows:

Figure 14B shows the Mask limit for PCS channel 25 which is the left block edge for Block A and shows limits levels identical for the band edge of the PCS band. The Spectrum Analyzer reference level is set above the Signal Reference to allow for the necessary dynamic range of a CDMA carrier presentation. The top of a typical 46.02 dBm single carrier CDMA signal viewed at a resolution bandwidth of 30 kHz is shown at the 29.82 dBm/ -16.2 dBc line. This line is based on equation 1, and the ratio of the 1.25 MHz bandwidth and the 30 kHz resolution bandwidth of the spectrum analyzer. The vertical line from a to b (i.e. a-b) is at 885 kHz from the center of channel 25 (i.e. f_c), per 3GPP2 TSG-C.S0010-C-v2.0. The horizontal line b-c is 45 dB below the 46.02 dBm/ 0 dBc reference level. The vertical line c-d is at 1.25 MHz from the center of the channel. The placement of line d-e is derived from evaluation of the signal and 12.5 kHz (1%) resolution bandwidth, using the suggested value in section 24.238 of the rules. The ratio of 30 kHz to 12.5 kHz in equation (1) gives 3.8 dB. Adjusting the tolerance line to reflect this difference puts the -13 dBm limit line at -9.2 dBm or -55.22 dBc below the top of mask reference line. The vertical line, e-f is at 2.25 MHz from the center of channel 25. The horizontal line f-g is drawn at -74.22 dBc below the 0 dBc / 46.02 dBm reference because the rules require a 1 MHz resolution bandwidth for measurements 1 MHz or greater outside the PCS band. Again, equation (1) and the ratio of 1 MHz to 1.25 MHz provides this value. The same logic was used in determining the other block and band edge tolerances.

Mask Description for Multiple Carriers at 40 Watts Total Power.

The mask for multiple carriers with a fixed total power adjusts top of mask level and the -45 dBc level along with the width of the carrier portion of the mask. For the example given above...with multiple carriers there would be no adjustments made to the “Left Edge of Block” requirements. The specified “Right Edge Limit” is treated as an expansion of the non Block edge corner **bb** to be the required + 885 kHz from the center of the “right most” channel. The “Right Edge of Block” limits were derived consistently. Table 14.3 below provides the appropriate levels for the mask break points.

Total Power	Number of carriers	Power per Carrier	Power per Carrier	-45 dBc Lines b-c and bb-cc	1st MHz outside of Block	> 1 MHz Outside of Block	Power Calibration Value	-16.2 dBc Reference line
Watts	n	Watts	dBm	dBm	dBm	dBm	dBm	dBm
40	1	40.00	46.02	1.02	-9.20	-28.23	49.82	29.82
40	2	20.00	43.01	-1.99	-9.20	-28.23	46.81	26.81
40	3	13.33	41.25	-3.75	-9.20	-28.23	45.05	25.05
40	4	10.00	40.00	-5.00	-9.20	-28.23	43.80	23.80
40	5	8.00	39.03	-5.97	-9.20	-28.23	42.83	22.83
40	6	6.67	38.24	-6.76	-9.20	-28.23	42.04	22.04
40	7	5.71	37.57	-7.43	-9.20	-28.23	41.37	21.37

TABLE 14.3 Mask Values for Various Carrier Configurations

Trace Description and Power Calibration

Figure 14B shows the single carrier channel 25 CDMA signal measured with two different resolution bandwidths. The additional upper magenta trace displays the signal as measured with a resolution bandwidth of 3 MHz. The black trace is the same signal as measured with a 30 kHz resolution bandwidth and is the appropriate trace for compliance evaluation against the mask. The wider resolution bandwidth of the magenta trace allows for a true power calibration of the measured signal against the top of mask and calibration of the analyzer to the more accurate power meter. The top of the mask is appropriate for a single carrier power calibration as it represents the true power level of a single carrier as measured with a power meter. For a two carrier signal the total power is 3 dB higher. For a three carrier signal the total power is 4.771 dB higher. The power calibration trace would be appropriately displayed if measured with a resolution bandwidth greater than signal bandwidth of 3.75 MHz. Spectrum analyzers, however, typically have a maximum bandwidth of 3 MHz and therefore a correction factor is necessary to account for the difference. The bandwidth correction factor for the 3 carrier signal (3.75 MHz) measured with a 3 MHz bandwidth is:

$$-0.969 \text{ dB} = 10 \cdot \text{LOG}(3/3.75)$$

The power calibration line is appropriately adjusted above the top of mask by:

$$4.771 \text{ dB} + 10 * \text{LOG}(3/3.75) \text{ dB} = + 3.800 \text{ dB}$$

Exhibit 14 *continued*

The Power Calibration level for 3 or more carriers equals:

$$\text{Single Carrier Power level (dBm)} + 4.77 + 10 * \text{LOG}(3/3.75) = \text{Single Carrier Power level (dBm)} + 3.80 \text{ dB}$$

$$46.02 \text{ dBm} + 3.80 \text{ dB} = 49.82 \text{ dBm}$$

The power calibration value for 1~7 carrier configurations is listed in Table 14.3 above. These values are depicted on the occupied bandwidth charts as the dashed magenta Power Calibration Line h-hh on each chart and as shown in the example chart in Figure 14C.

Measurement of the One To Seven Carrier Configuration

All of the tolerance lines for the output are referenced to the top of the Occupied Bandwidth mask, which is defined in Table 14.3 above. For all measurements of the *UMTS-CDMA 9341 RRH 40W 1900 MHz System Occupied Bandwidth*, the output power was set to the 40 W total power level using a broadband RF power meter. The Top of Mask value is appropriately the value in Table 14.3 for the specific number of carriers. When the magenta trace is adjusted to the power calibration line the 30 kHz signal trace will be correctly set to the -16.2 dBc reference line. Comparison of the 30 kHz measurement to the Mask is then appropriate and accurate.

In order to depict the tolerance lines that are required by Sec 24.238 of the FCC Rules and 3GPP2 C.S0010-C v2.0, all measurements were made with a resolution bandwidth of 30 kHz and the limits were adjusted using equation (1). A sample detector was employed using minimum of 25 sweeps averaging per trace.

Exhibit 14 continued

TABLE 14.4 PCS Occupied Bandwidth Compliance Tabulation

PCS - Block	PCS -Channels	Number of carriers	Power per Carrier, W/c	Total Power Watts	Results Occupied Bandwidth
1 Carrier Configuration					
A	25	1	40	40	Compliant
A	275	1	40	40	Compliant
D + B	325 & 375	1	40	40	Compliant
B	425	1	40	40	Compliant
B	675	1	40	40	Compliant
E	725 & 775	1	40	40	Compliant
F	825 & 875	1	40	40	Compliant
C	925	1	40	40	Compliant
C	1175	1	40	40	Compliant
2 Carrier Configuration					
A through C	Left and Right side of Block	2	20	40	Compliant
3 Carrier Configuration					
A through C	Left and Right side of Block	3	13.33	40	Compliant
4 Carrier Configuration					
A through C	Left and Right side of Block	4	10	40	Compliant
5 Carrier Configuration					
A through C	Left and Right side of Block	5	8	40	Compliant
6 Carrier Configuration					
A through C	Left and Right side of Block	6	6.66	40	Compliant
7 Carrier Configuration					
A	25 - 175	7	5.71	40	Compliant
A	125-275	7	5.71	40	Compliant
D + B	325-475	7	5.71	40	Compliant
B	425 - 575	7	5.71	40	Compliant
B	525-675	7	5.71	40	Compliant
B + E	625 - 775	7	5.71	40	Compliant
F + C	825 - 975	7	5.71	40	Compliant
C	925-1075	7	5.71	40	Compliant
C	1025-1175	7	5.71	40	Compliant

Results

The Block designation, PCS channels, frequencies and Measured RF Power are tabulated on each plot. The transmitter output signals are plotted for each frequency/ channel of interest. Plots are provided for Left Edge and Right Edge of each PCS Block evaluated. These frequencies were chosen to show the occupied bandwidth in the channels in each of the PCS Blocks in which this product can be operated, in compliance with Section 24.229 and 24.238 (c) of the Commission code. The signal used to show the occupied bandwidth is defined in table 14.1. This is the signal recommended in 3GPP2 C.S0010-C v2.0. The power output level was adjusted to provide the documented value on each chart.

Conclusion

The above data demonstrates that the **UMTS-CDMA 9341 RRH 40W 1900 MHz System / FCC ID: AS5ONEBTS-18**, the subject of this application, complies with Sections 2.1053, 24.238 and 2.1049 of the Rules.

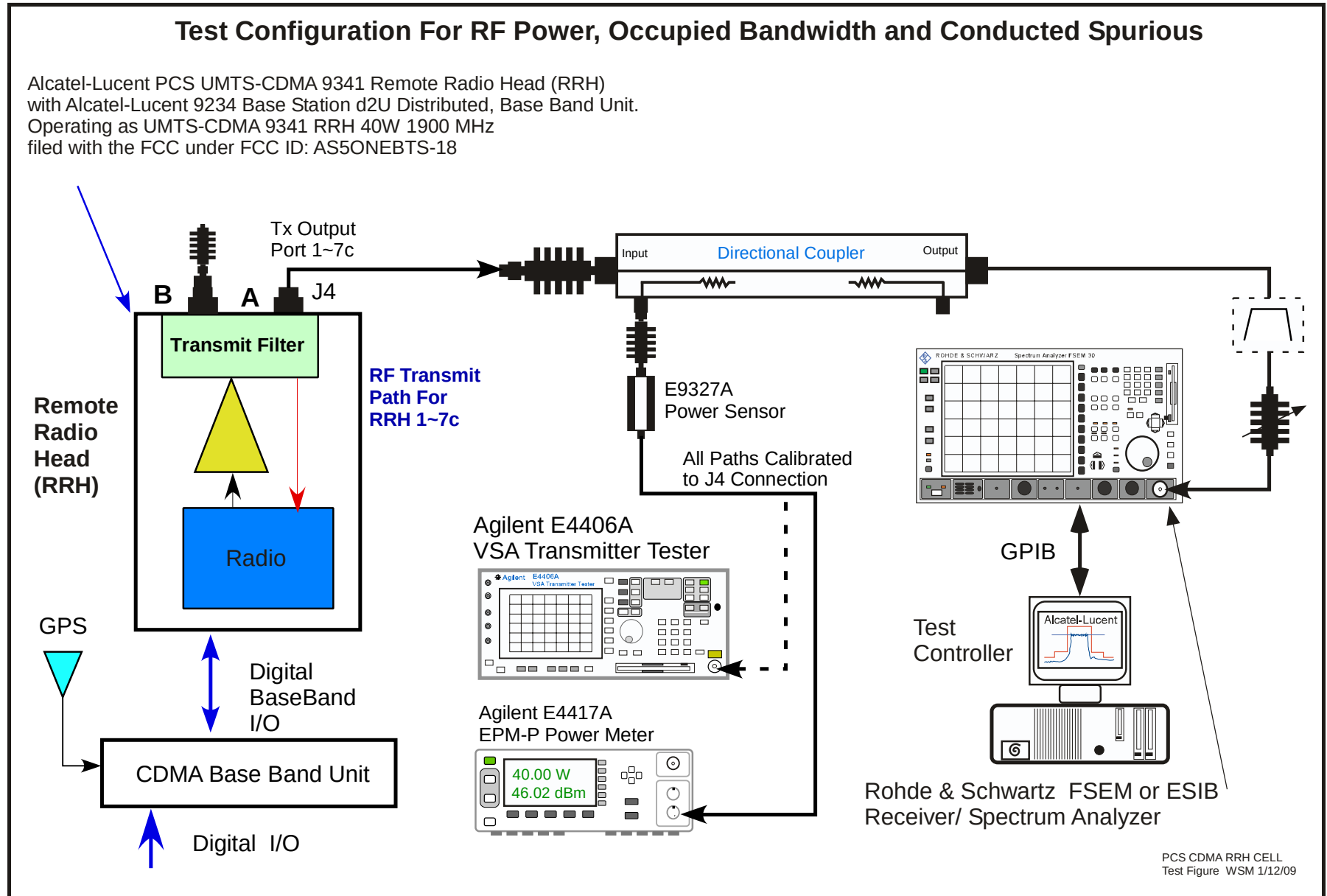
Figure 14A Test Setup for Antenna Port Measurement of Transmit Power, Occupied Bandwidth and Conducted Spurious Emissions

Figure 14B Occupied Bandwidth Power Calibration
(PCS A Block is depicted with a single carrier signal showing use of the Power Calibration Trace)

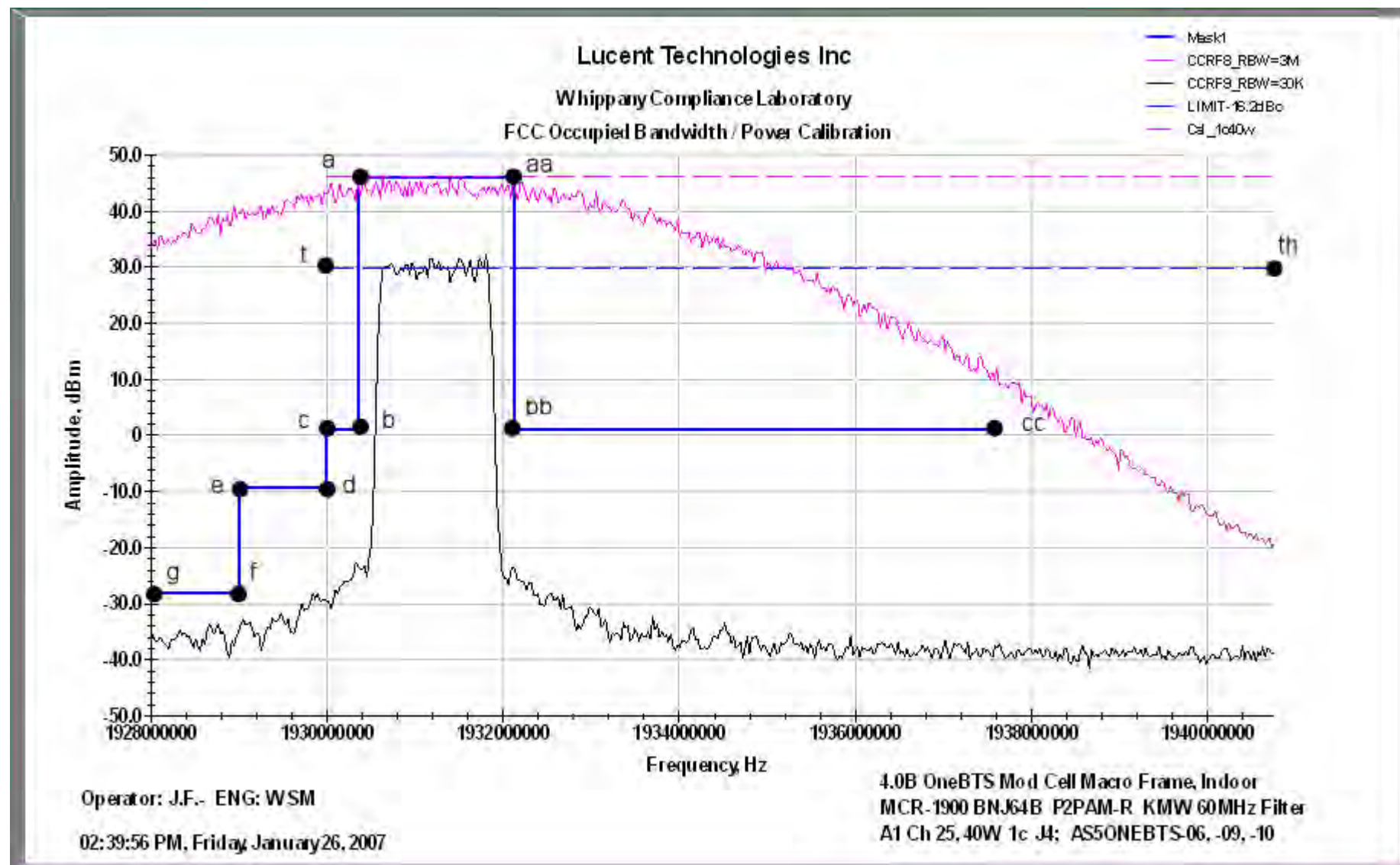
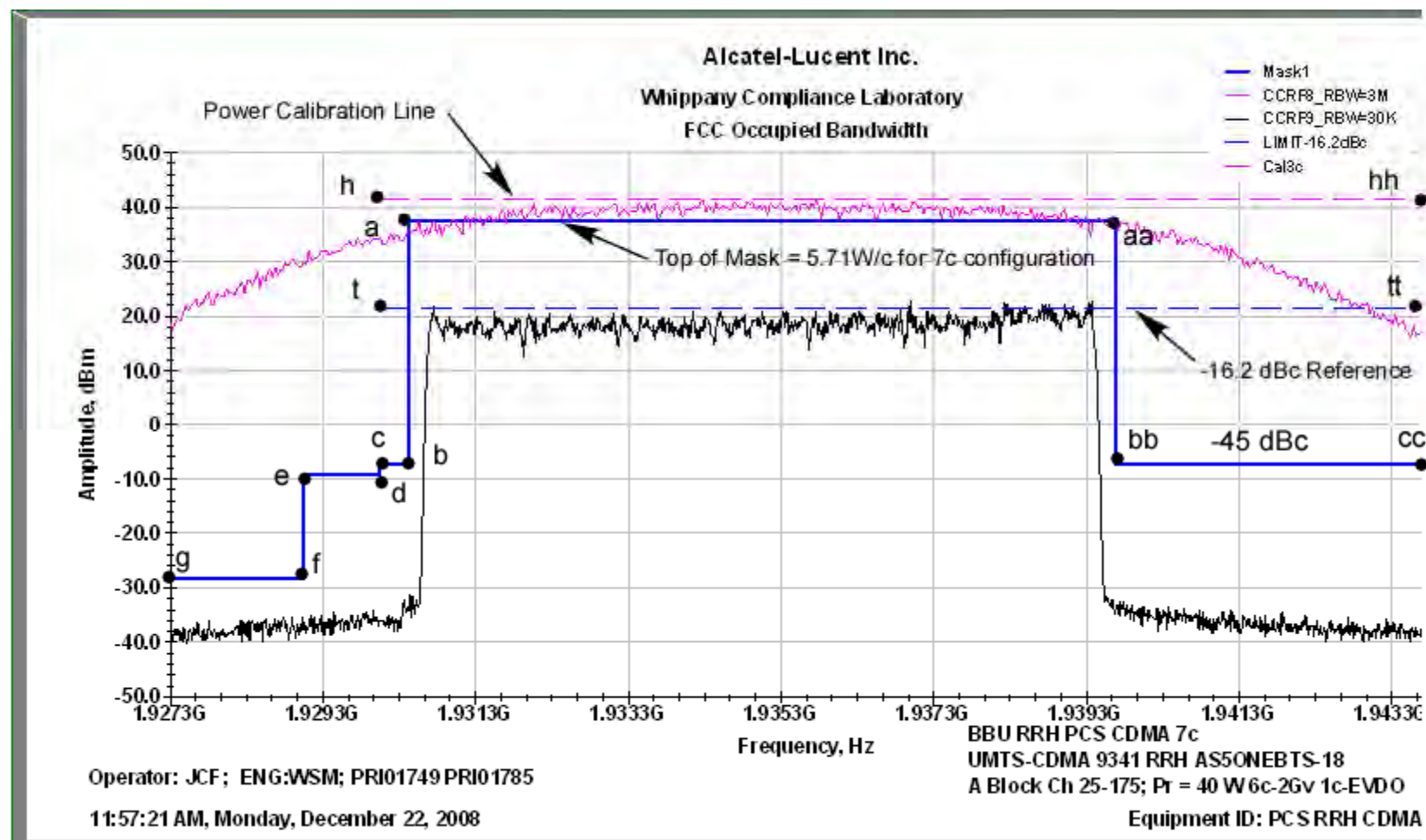


Figure 14C Multi-Carrier Occupied Bandwidth Power Calibration
(PCS A Block is depicted with a seven carrier signal showing use of the Power Calibration Trace)

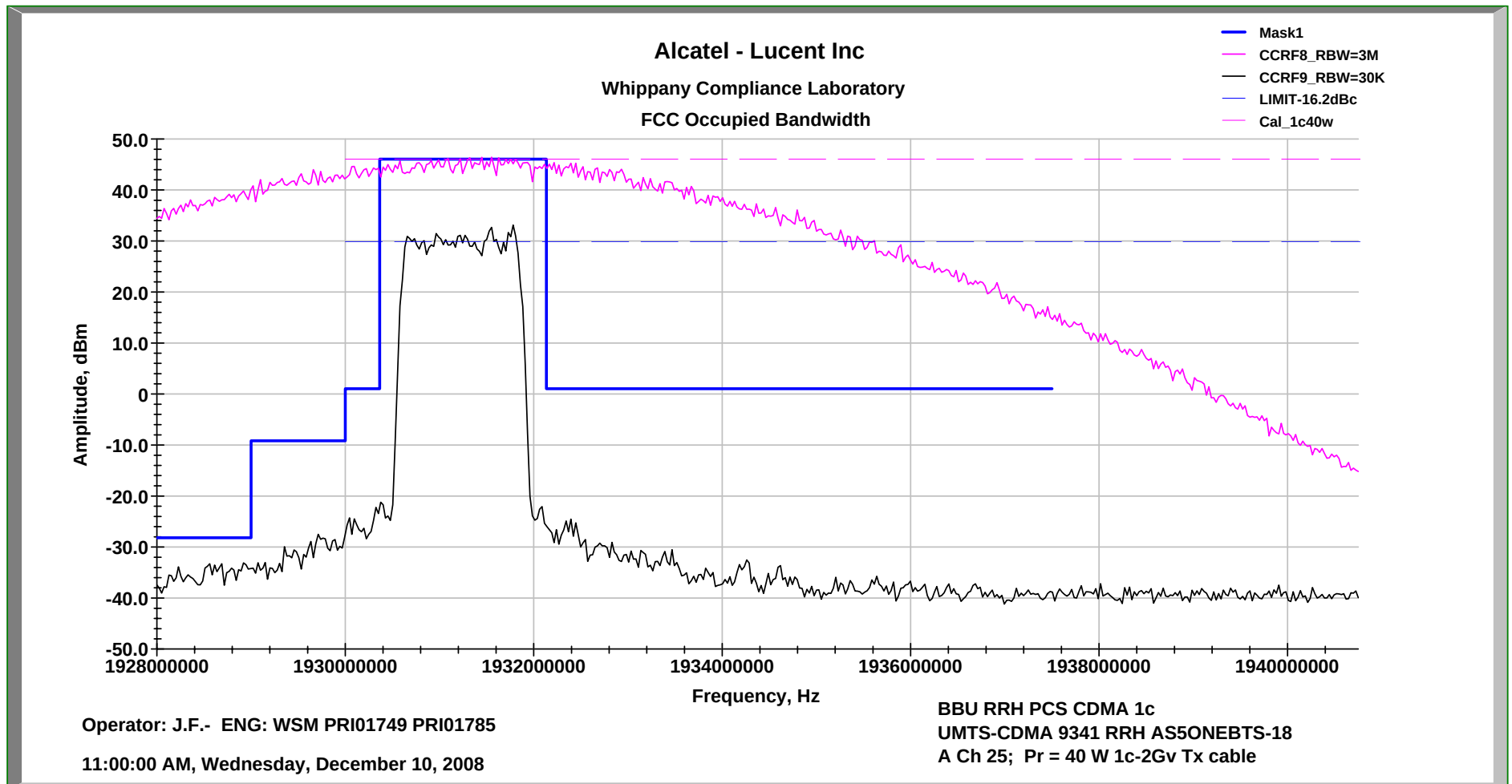


**Transmitter Measurements
of
CDMA Occupied Bandwidth
for
Alcatel-Lucent Inc.
UMTS-CDMA 9341 RRH 40W 1900 MHz System
FCC ID: AS5ONEBTS-18
with
9234 Base Station d2U Distributed Base Band Unit (BBU)
One to Seven Carrier Configurations
CDMA Operation at
40W Total Power**

W. Steve Majkowski NCE
CDMA Certification
Whippany FCC Compliance Laboratory
Alcatel-Lucent.
Lab: 973-386-2135
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**One Carrier Occupied Bandwidth Data
PCS Blocks A, D, B, E, F, and C
Left side of Block, Right side of Block
And
PCS Band Intermodulation**

Occupied Bandwidth PCS Ch A1-25 1c w/EDPD 40W Total

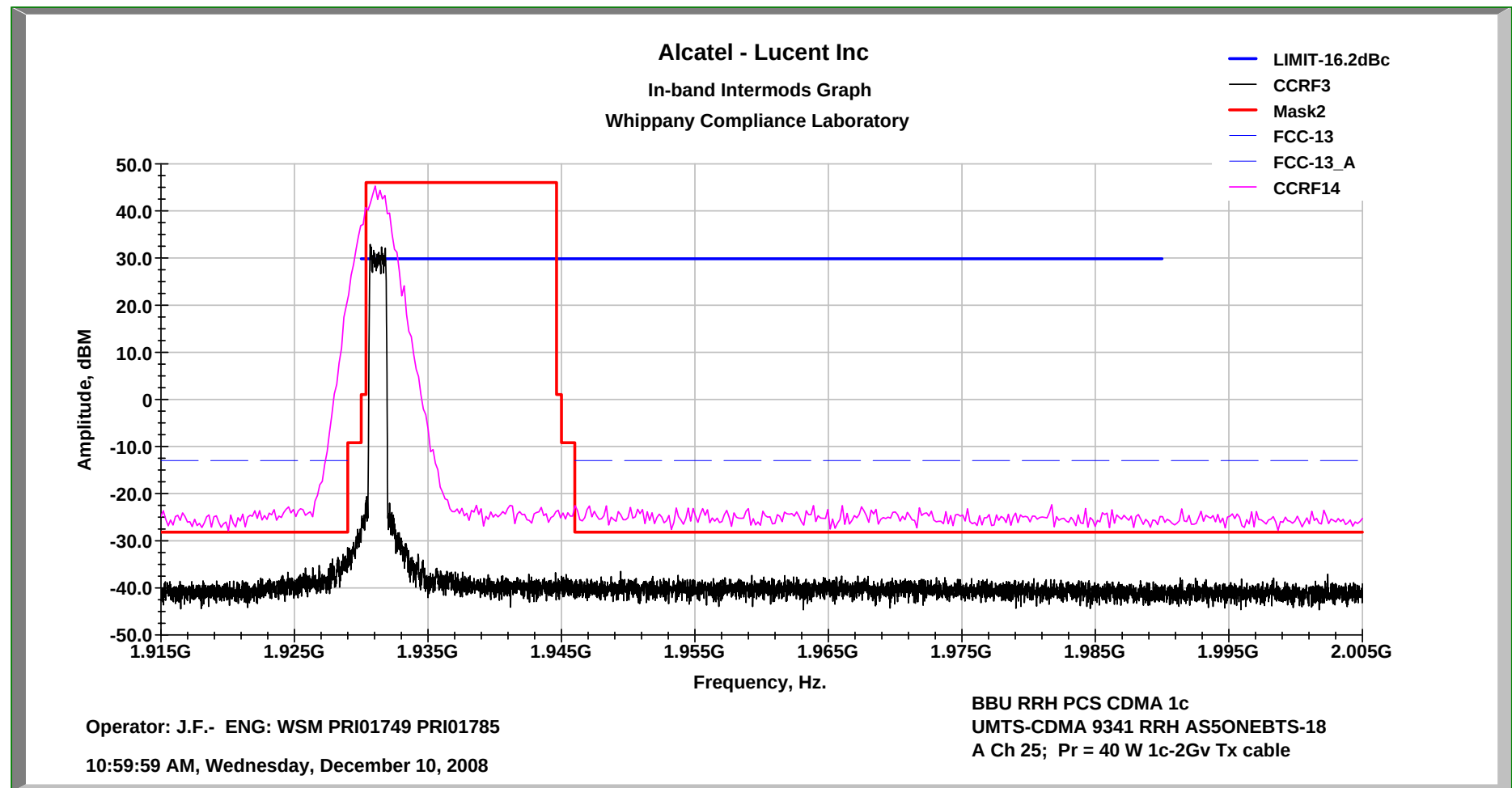


In-band Inter-modulation

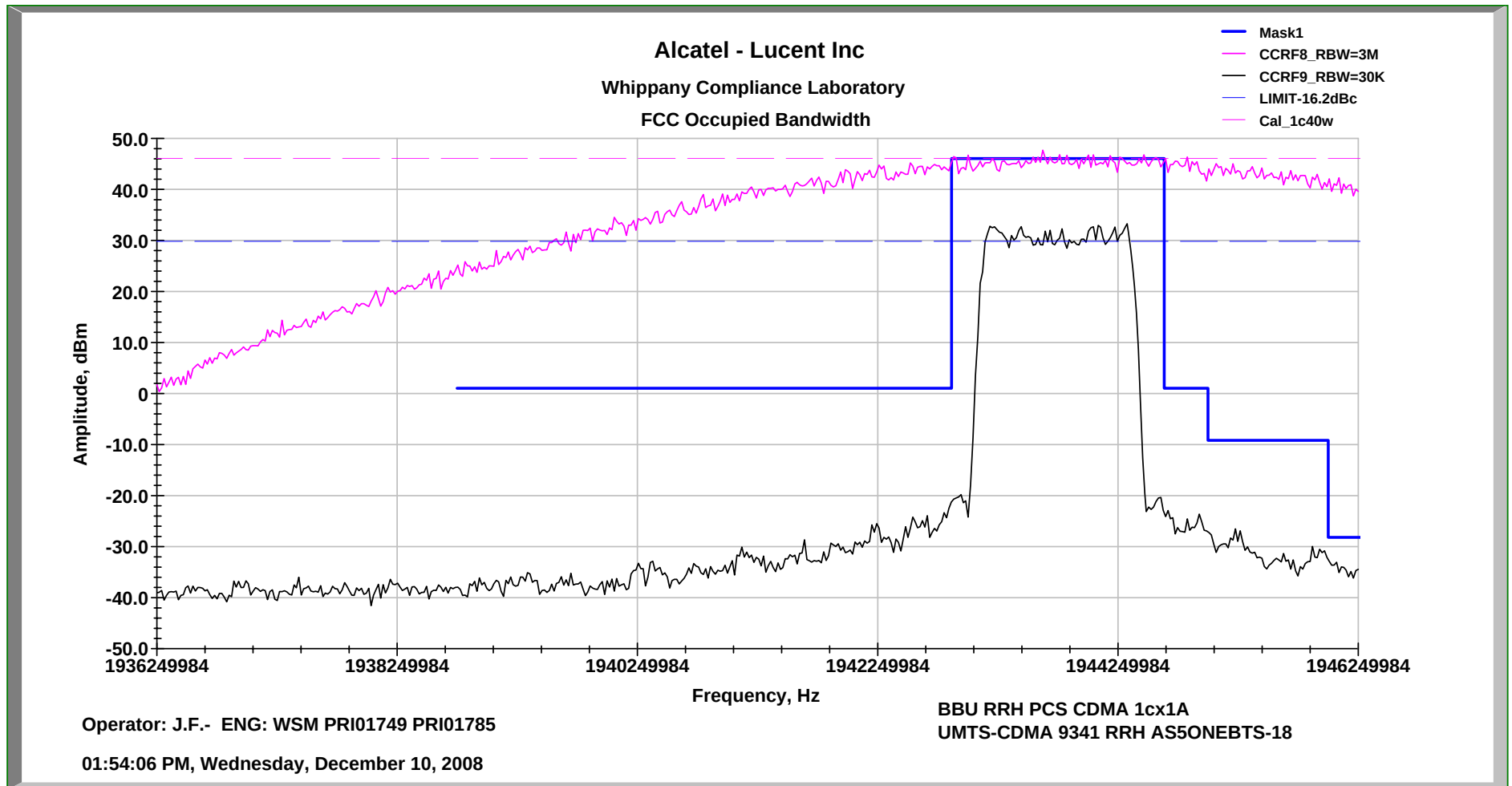
PCS Ch A1-25

1c w/EDPD

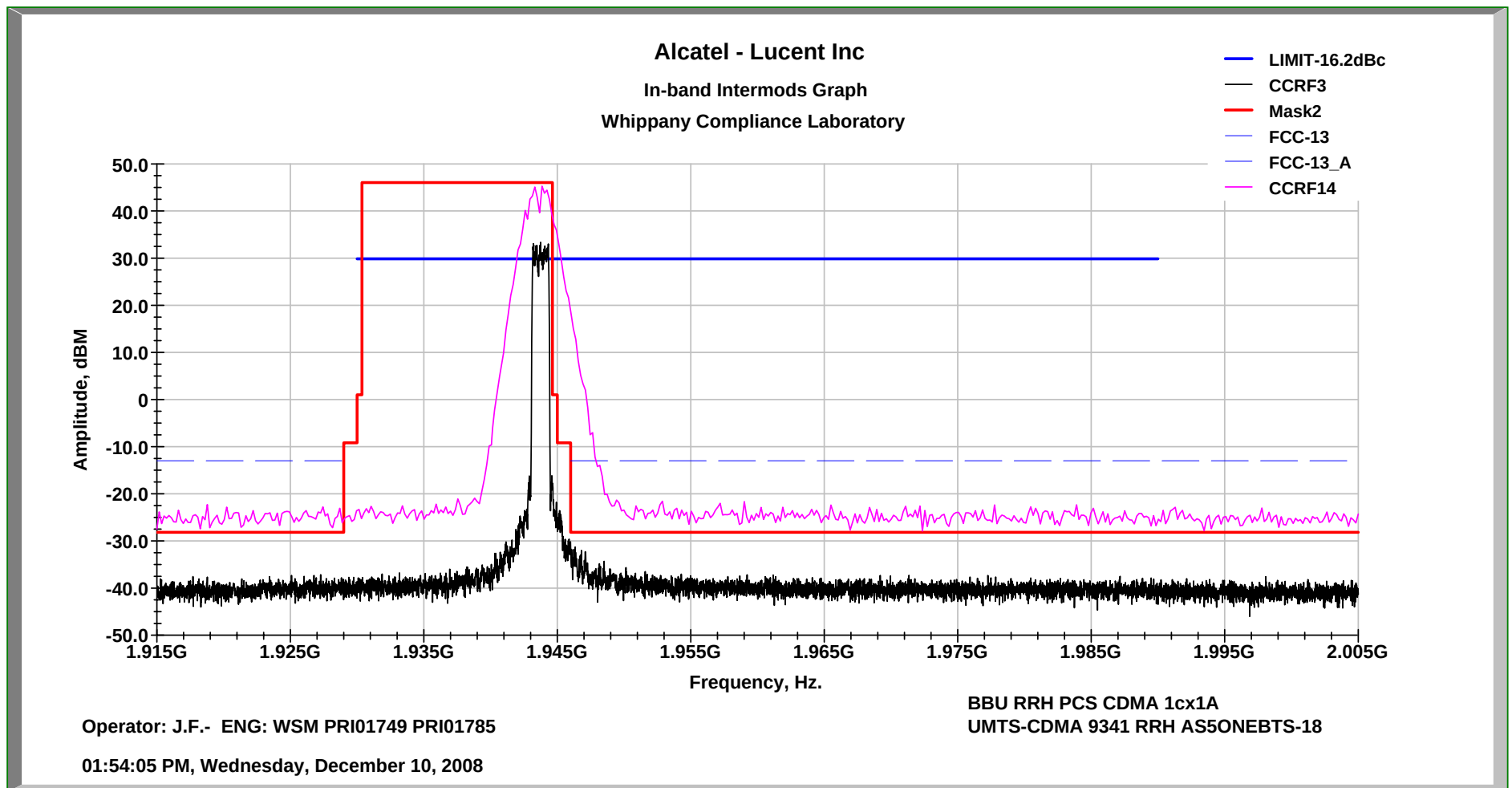
40W Total



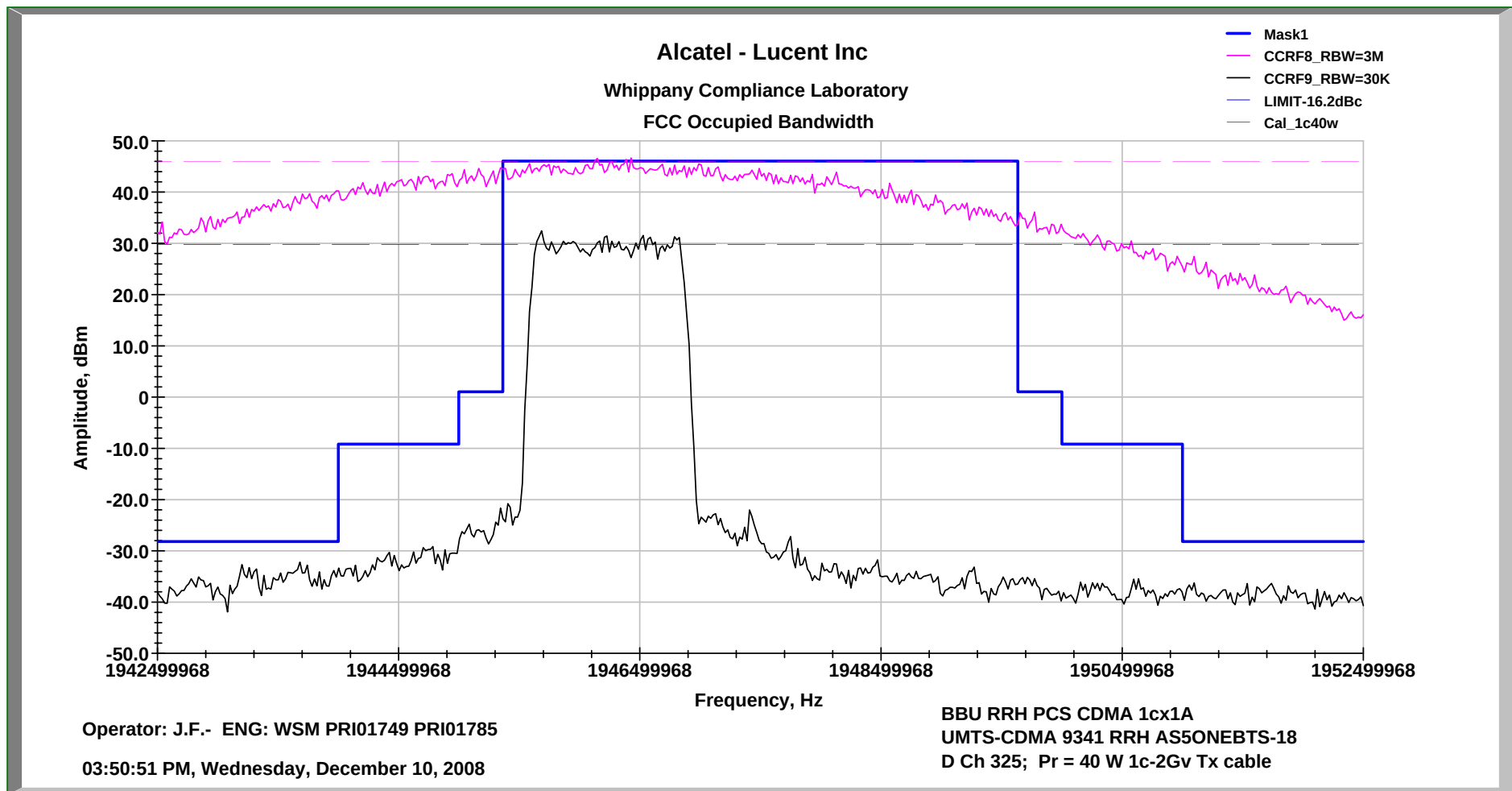
Occupied Bandwidth PCS Ch A5-275 1c w/EDPD 40W Total



In-band Inter-modulation PCS Ch A5-275 1c w/EDPD 40W Total



Occupied Bandwidth PCS Ch D-325 1c w/EDPD 40W Total

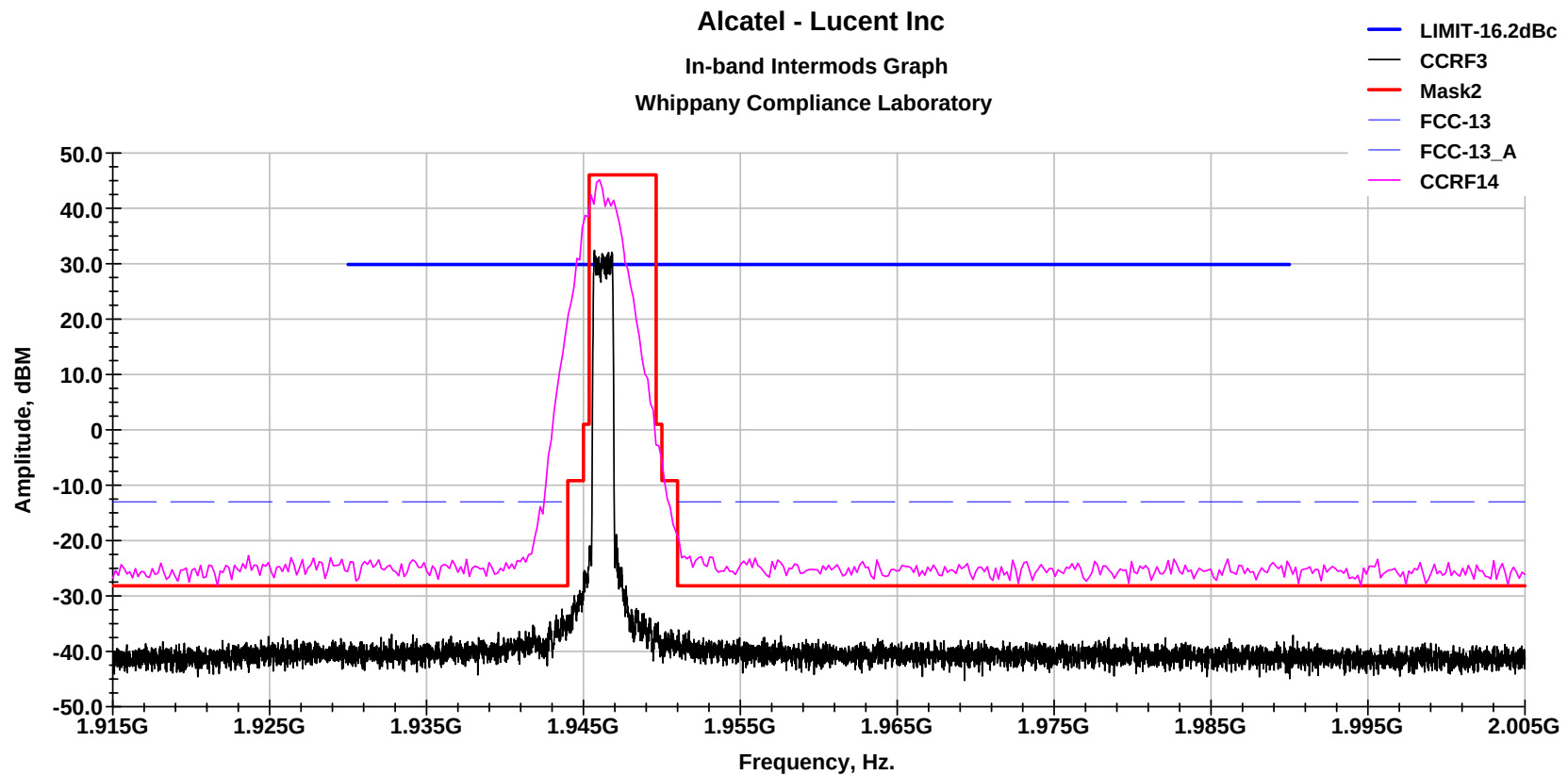


In-band Inter-modulation

PCS Ch D-325

1c w/EDPD

40W Total

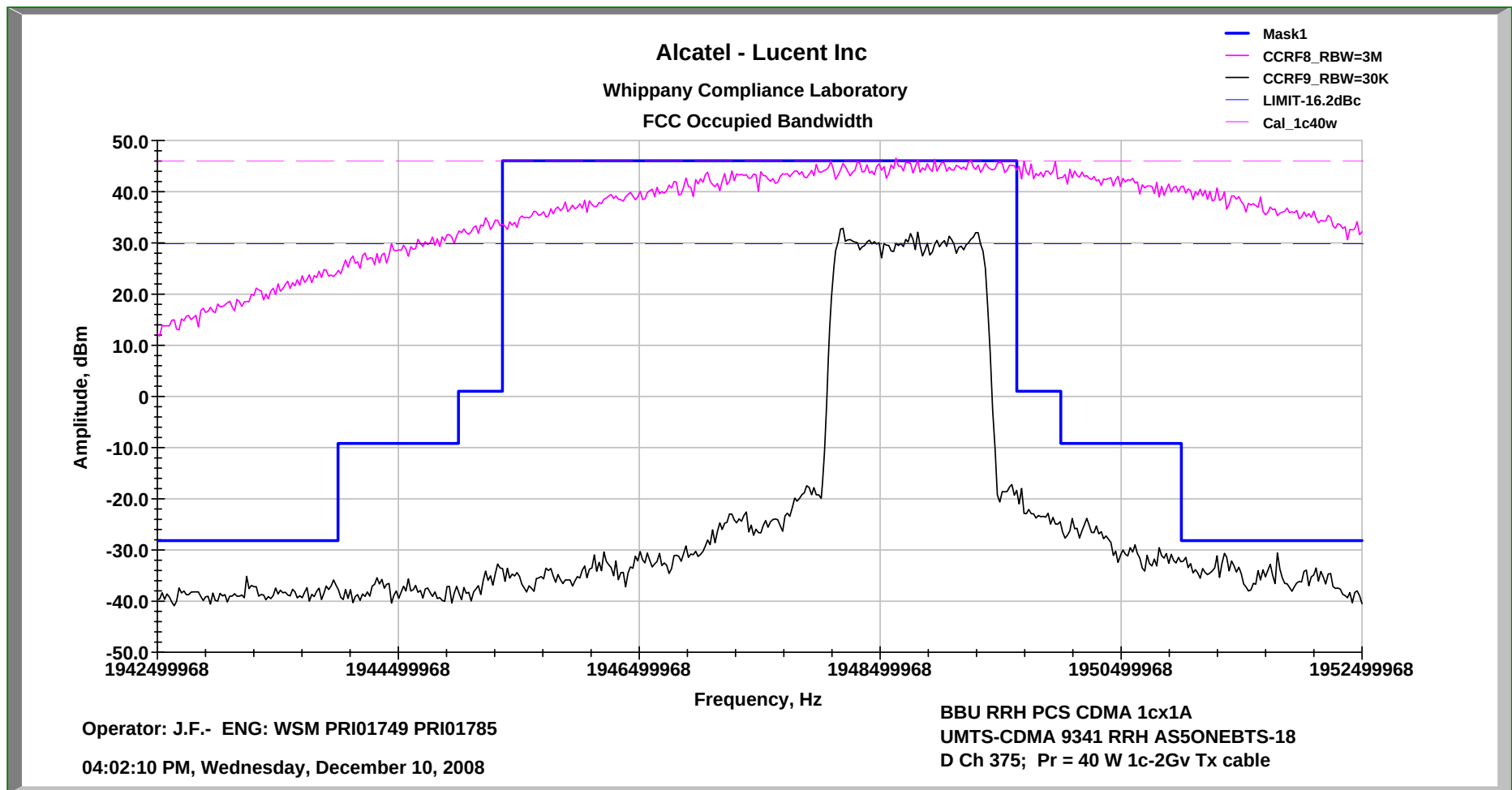


Operator: J.F.- ENG: WSM PRI01749 PRI01785

03:50:50 PM, Wednesday, December 10, 2008

BBU RRH PCS CDMA 1cx1A
UMTS-CDMA 9341 RRH AS5ONEBTS-18
D Ch 325; Pr = 40 W 1c-2Gv Tx cable

Occupied Bandwidth PCS Ch D-375 1c w/EDPD 40W Total

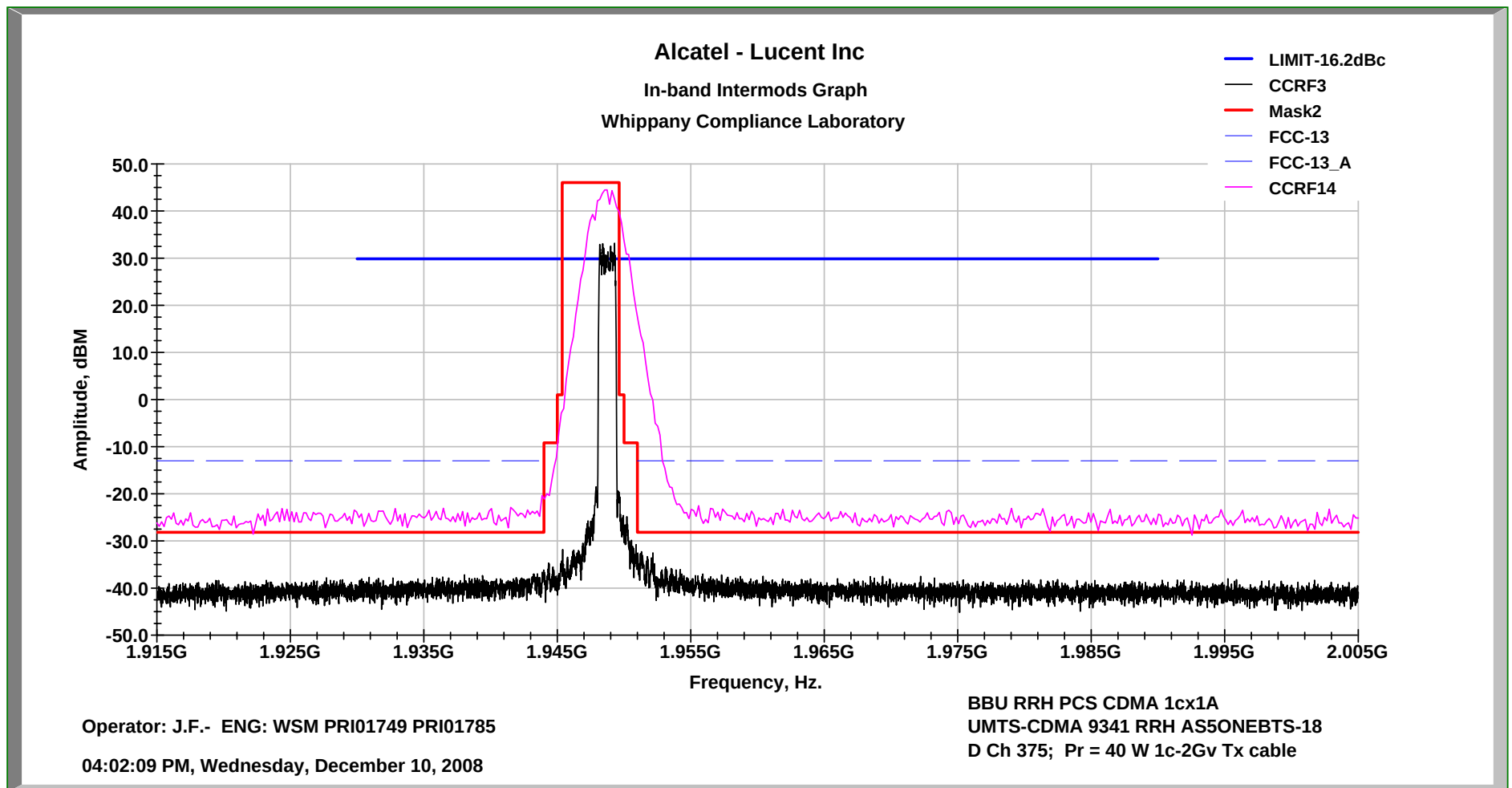


In-band Inter-modulation

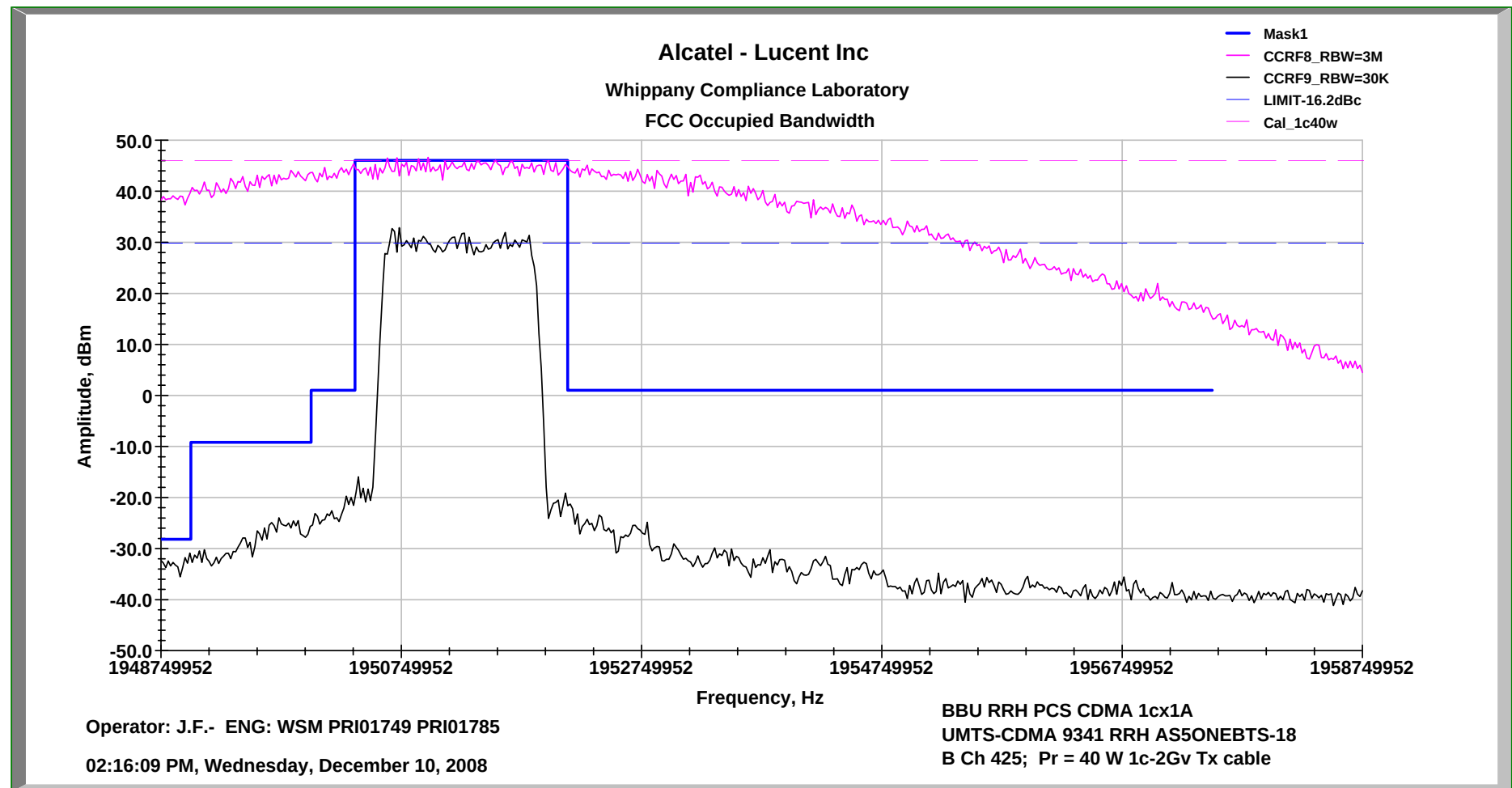
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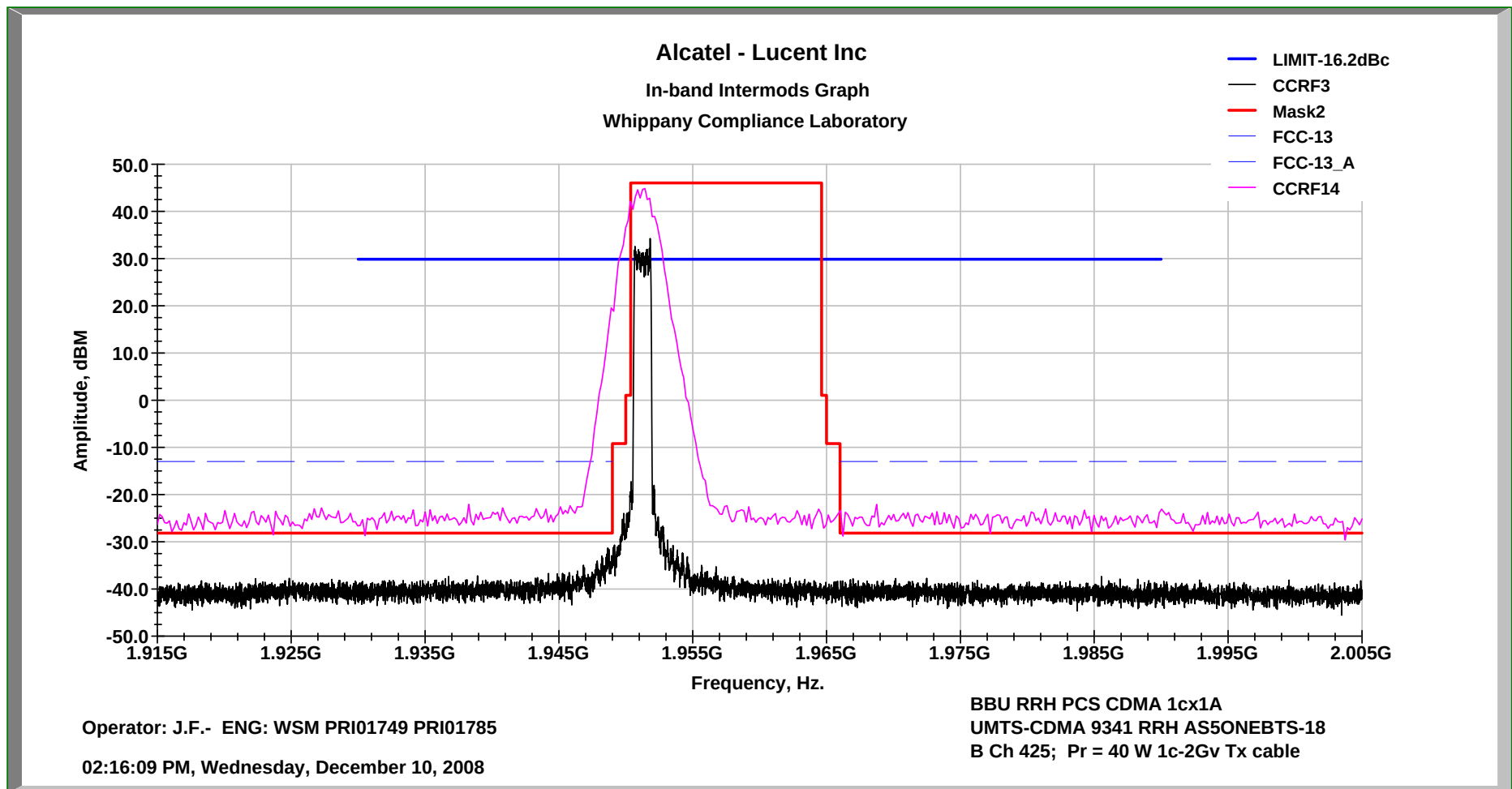
1c w/EDPD

40W Total

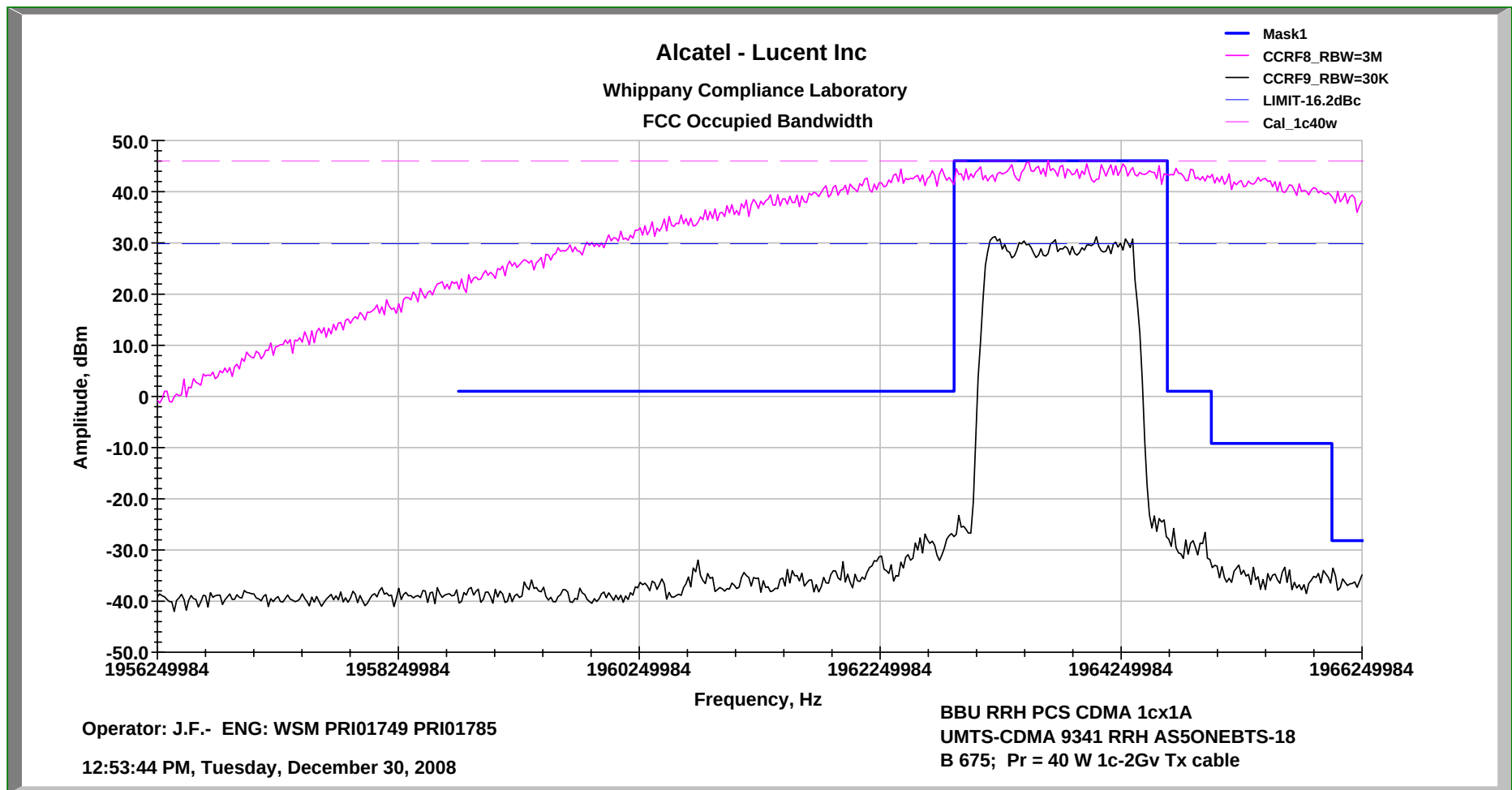


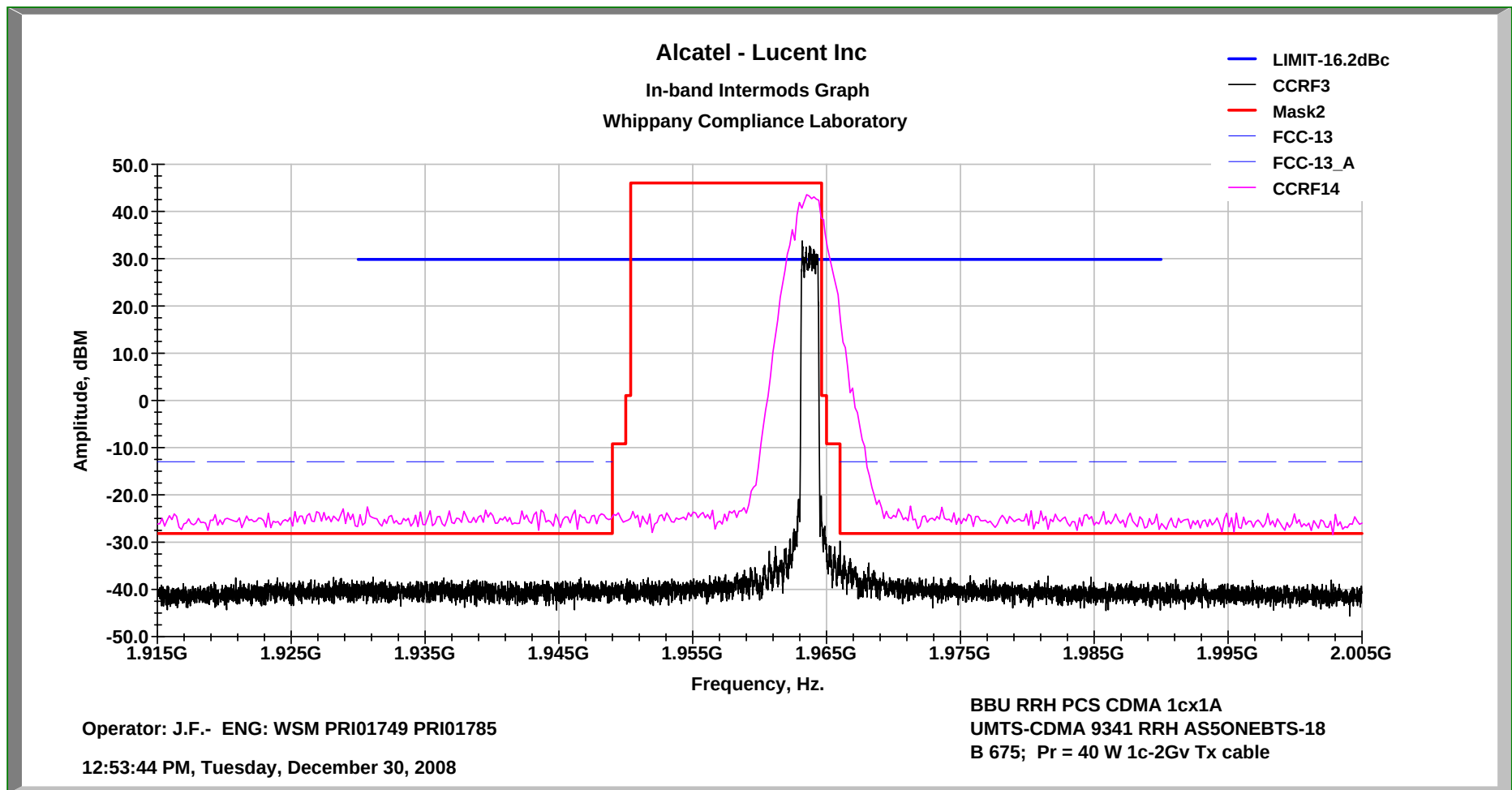
Occupied Bandwidth PCS Ch B1-425 1c w/EDPD 40W Total



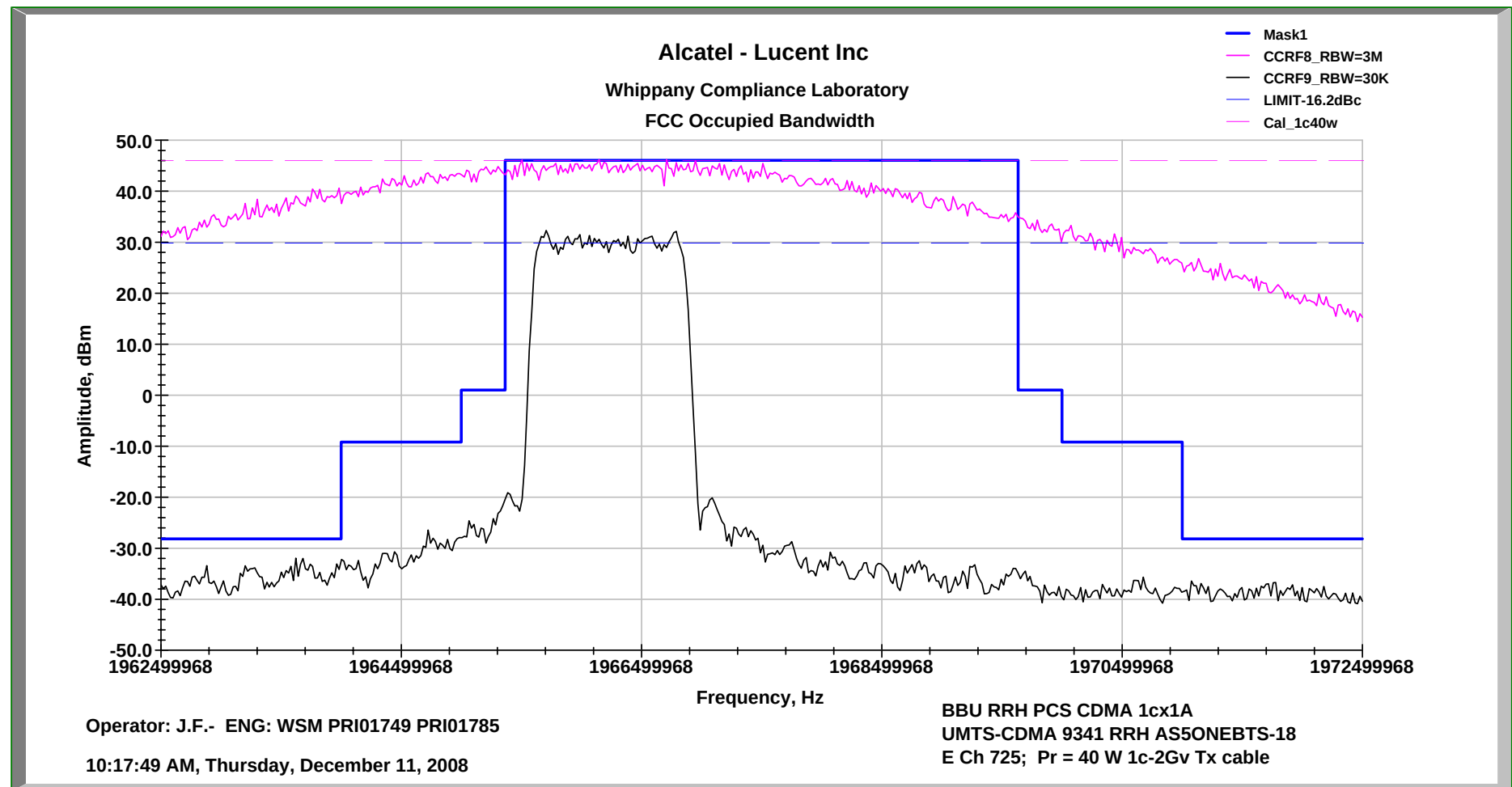
In-band Inter-modulation PCS Ch B1-425 1c w/EDPD 40W Total

Occupied Bandwidth PCS Ch B5-675 1c w/EDPD 40W Total



In-band Inter-modulation PCS Ch B5-675 1c w/EDPD 40W Total

Occupied Bandwidth PCS Ch E-725 1c w/EDPD 40W Total

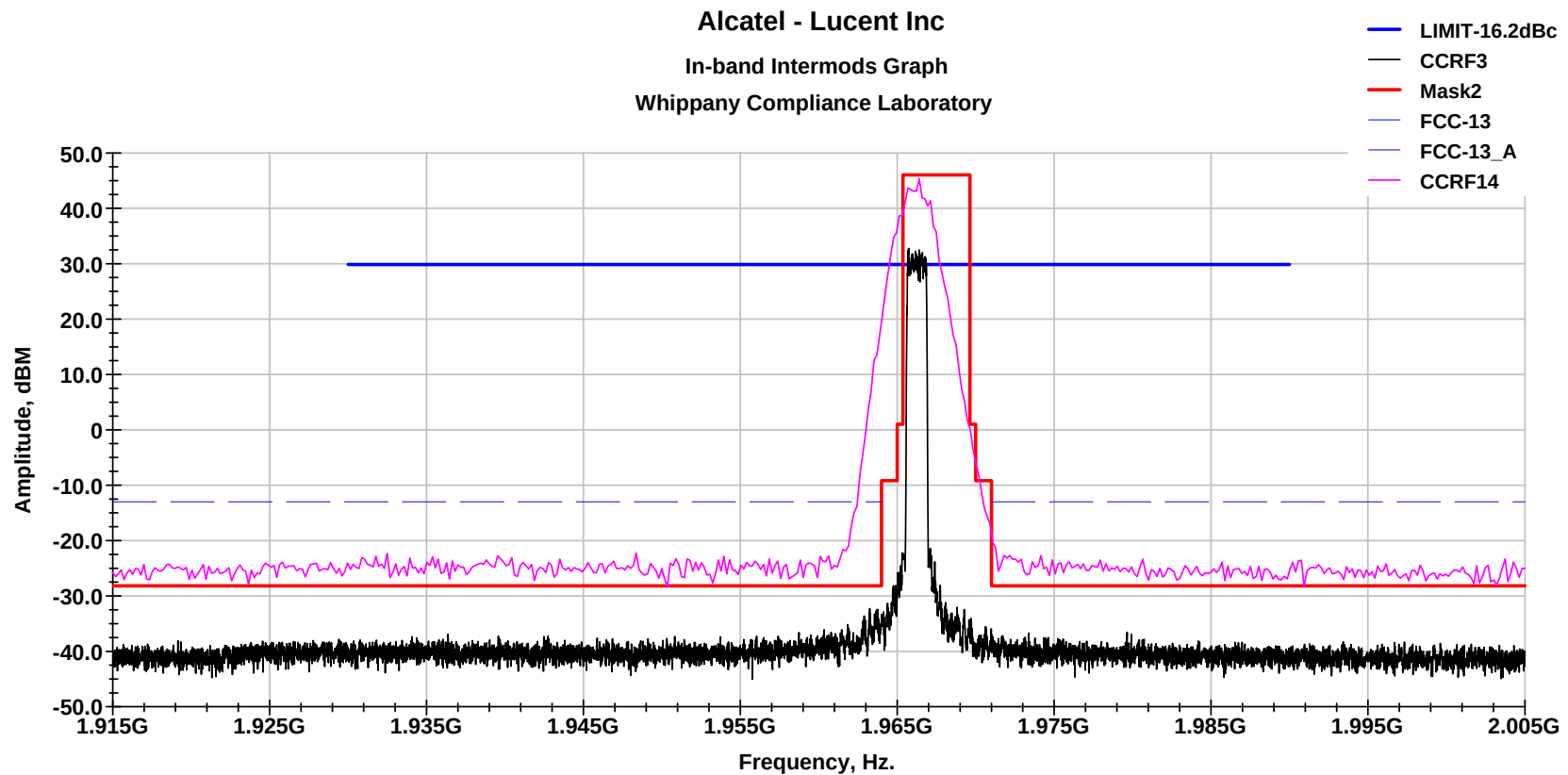


In-band Inter-modulation

PCS Ch E-725

1c w/EDPD

40W Total

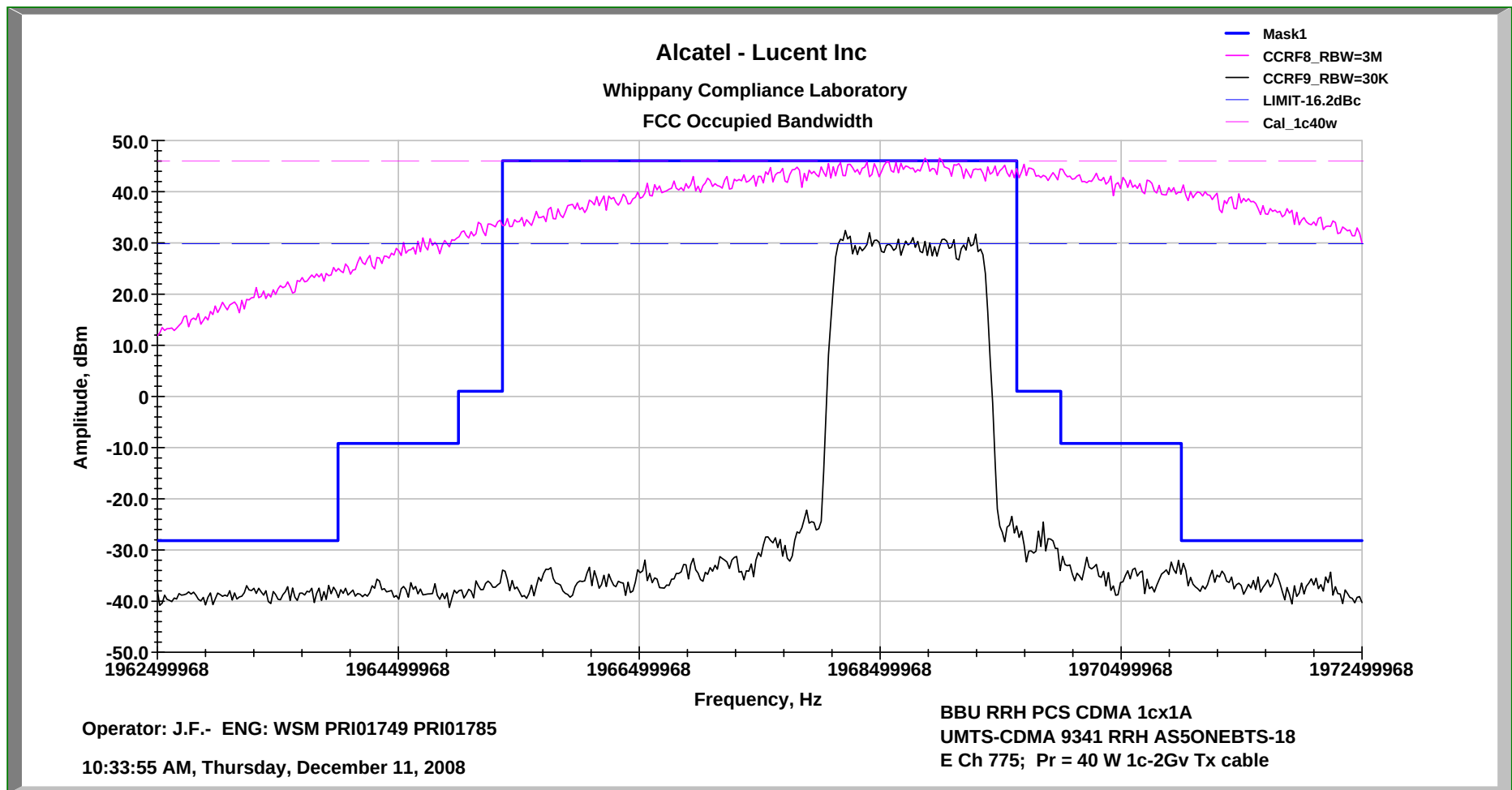


Operator: J.F.- ENG: WSM PRI01749 PRI01785

10:17:49 AM, Thursday, December 11, 2008

BBU RRH PCS CDMA 1cx1A
UMTS-CDMA 9341 RRH AS5ONEBTS-18
E Ch 725; Pr = 40 W 1c-2Gv Tx cable

Occupied Bandwidth PCS Ch E-775 1c w/EDPD 40W Total

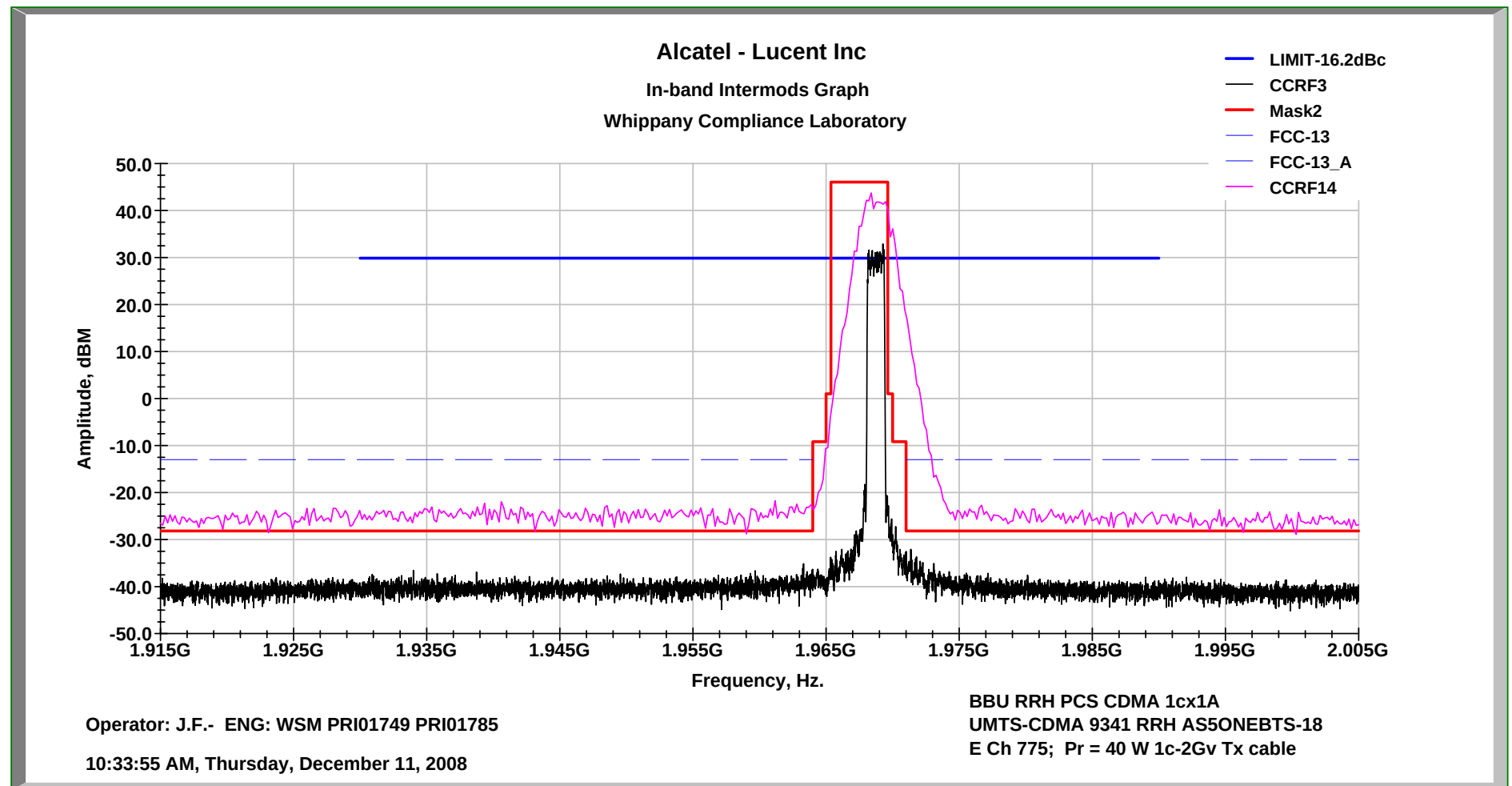


In-band Inter-modulation

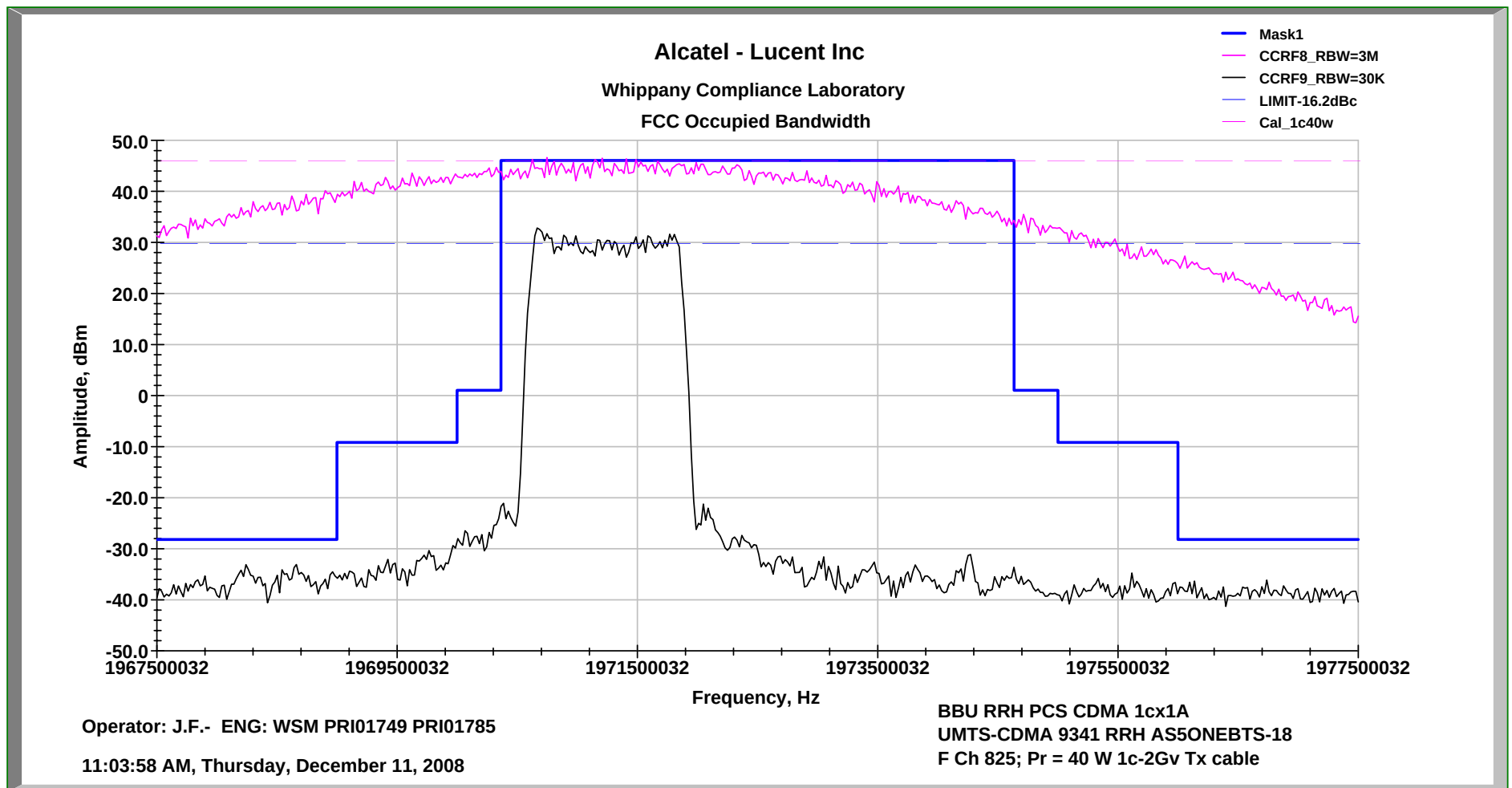
PCS Ch E-775

1c w/EDPD

40W Total



Occupied Bandwidth PCS Ch F-825 1c w/EDPD 40W Total

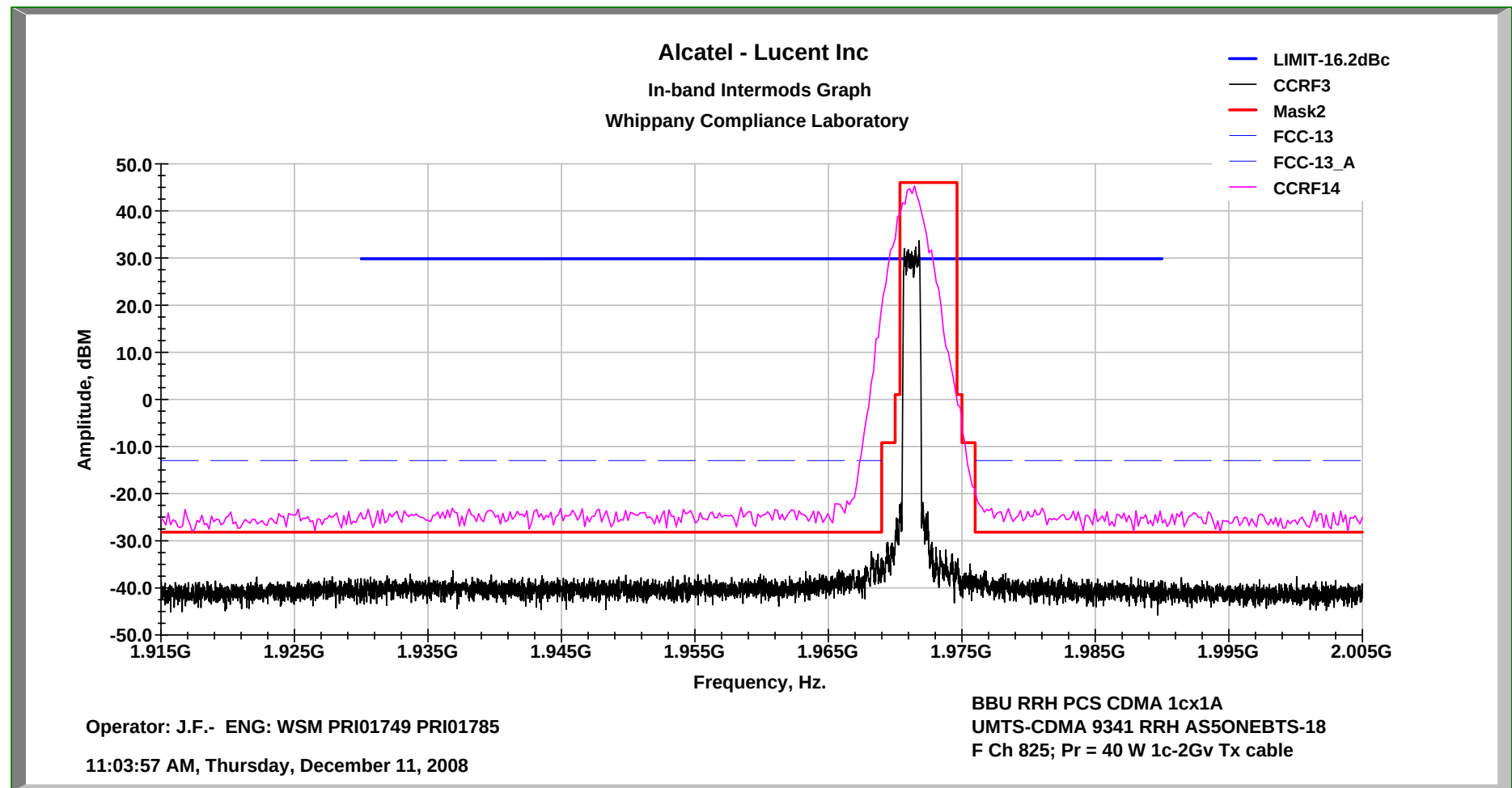


In-band Inter-modulation

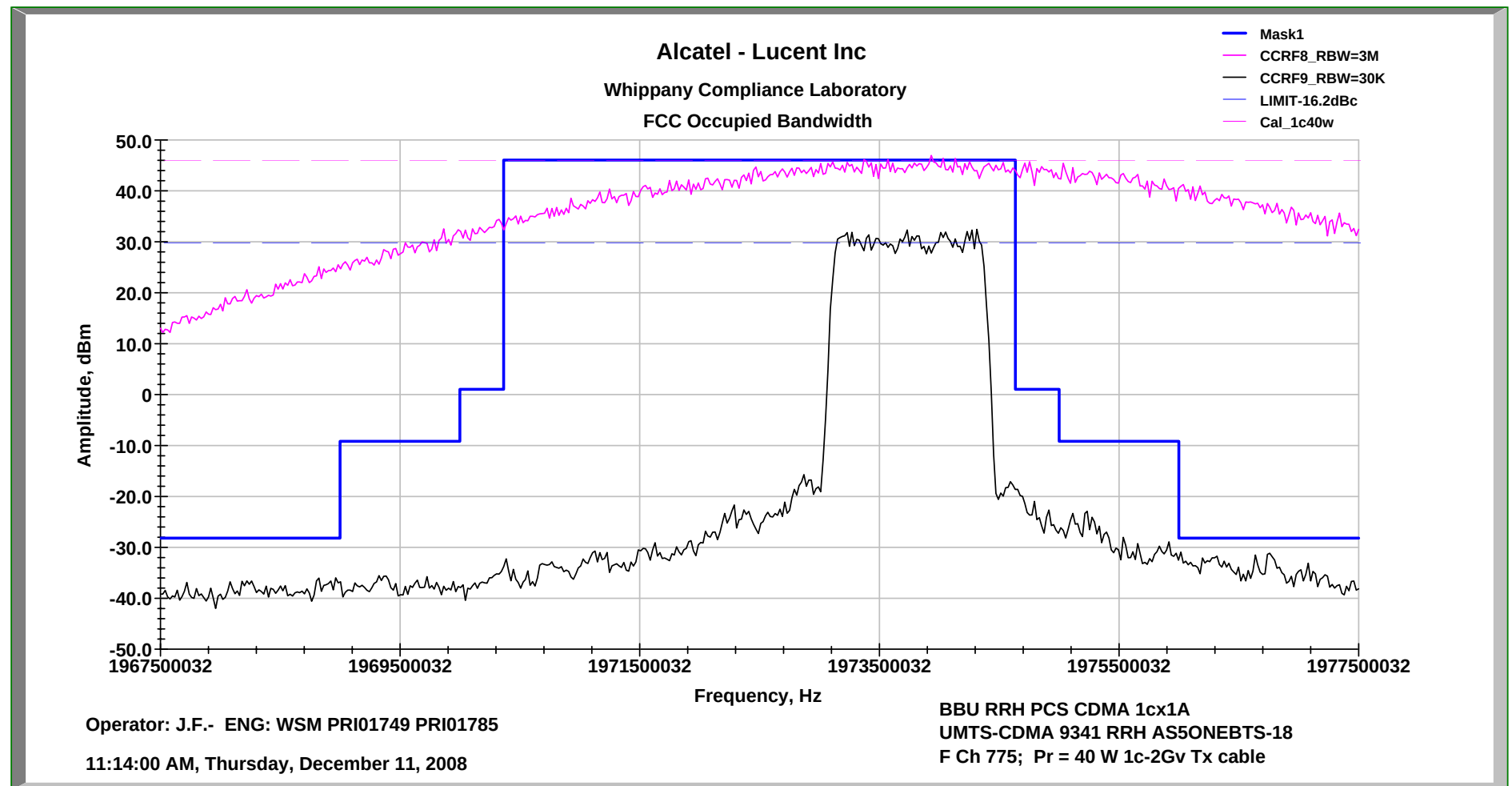
PCS Ch F-825

1c w/EDPD

40W Total



Occupied Bandwidth PCS Ch F-875 1c w/EDPD 40W Total

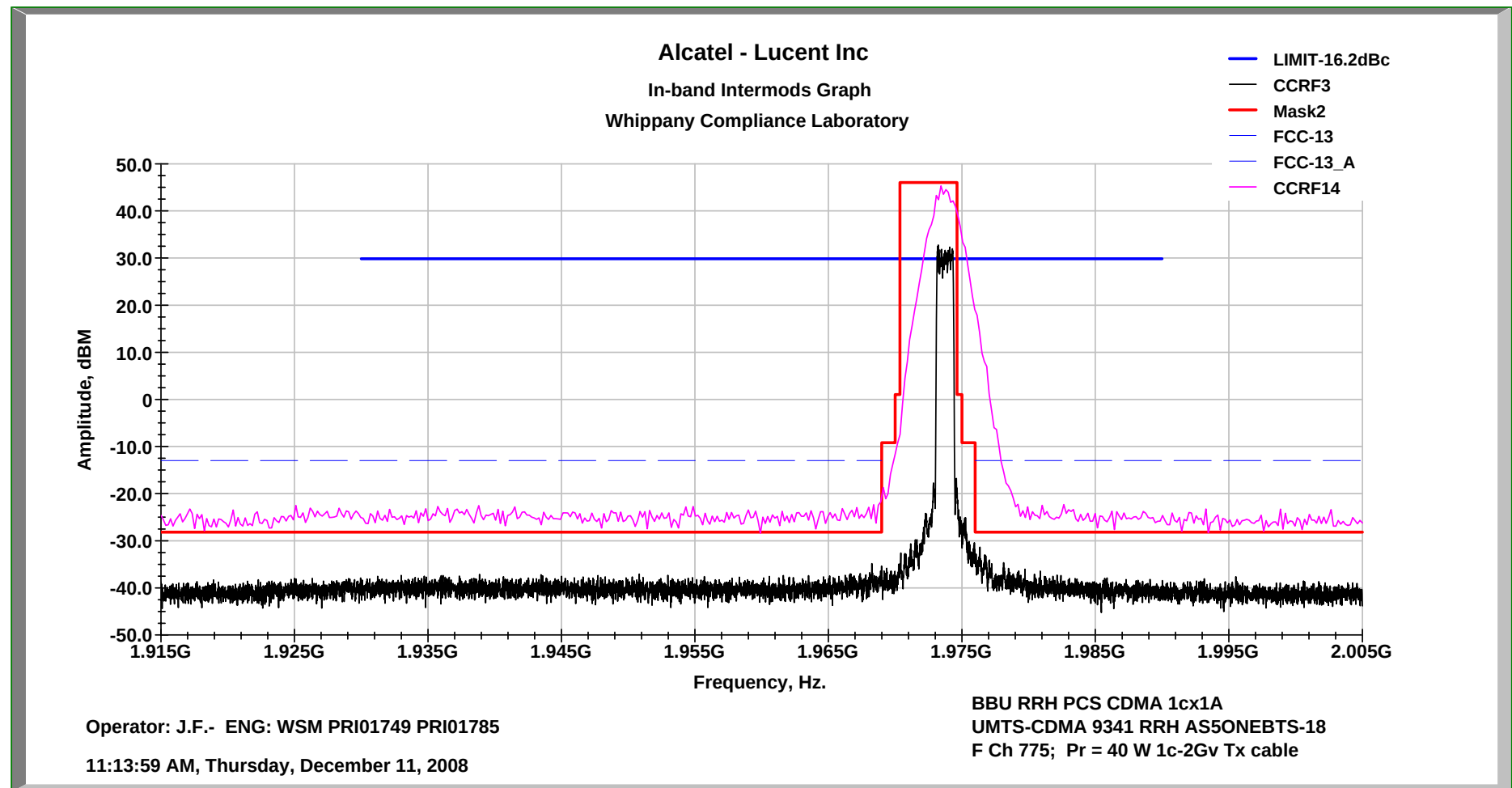


In-band Inter-modulation

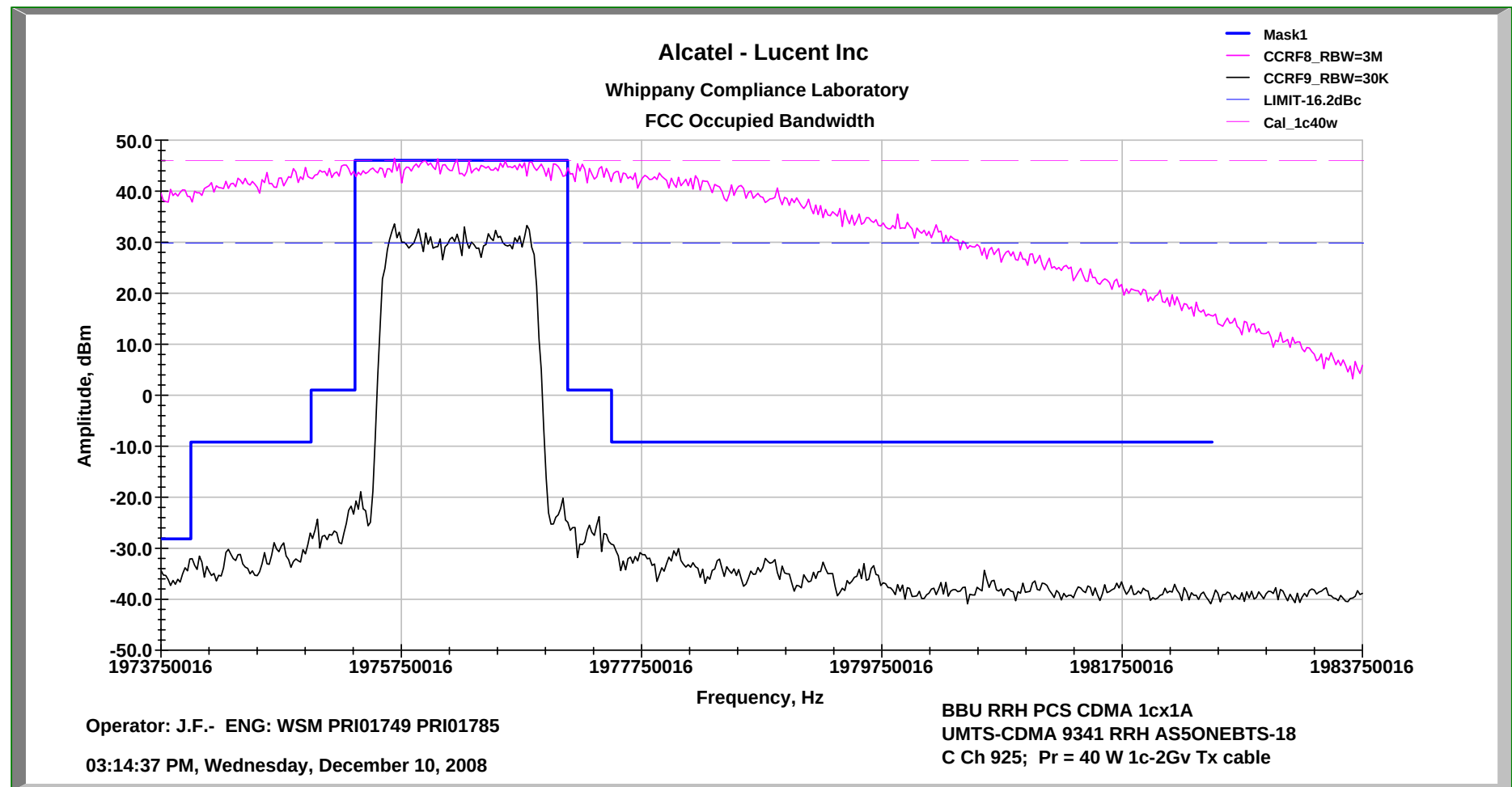
PCS Ch F-875

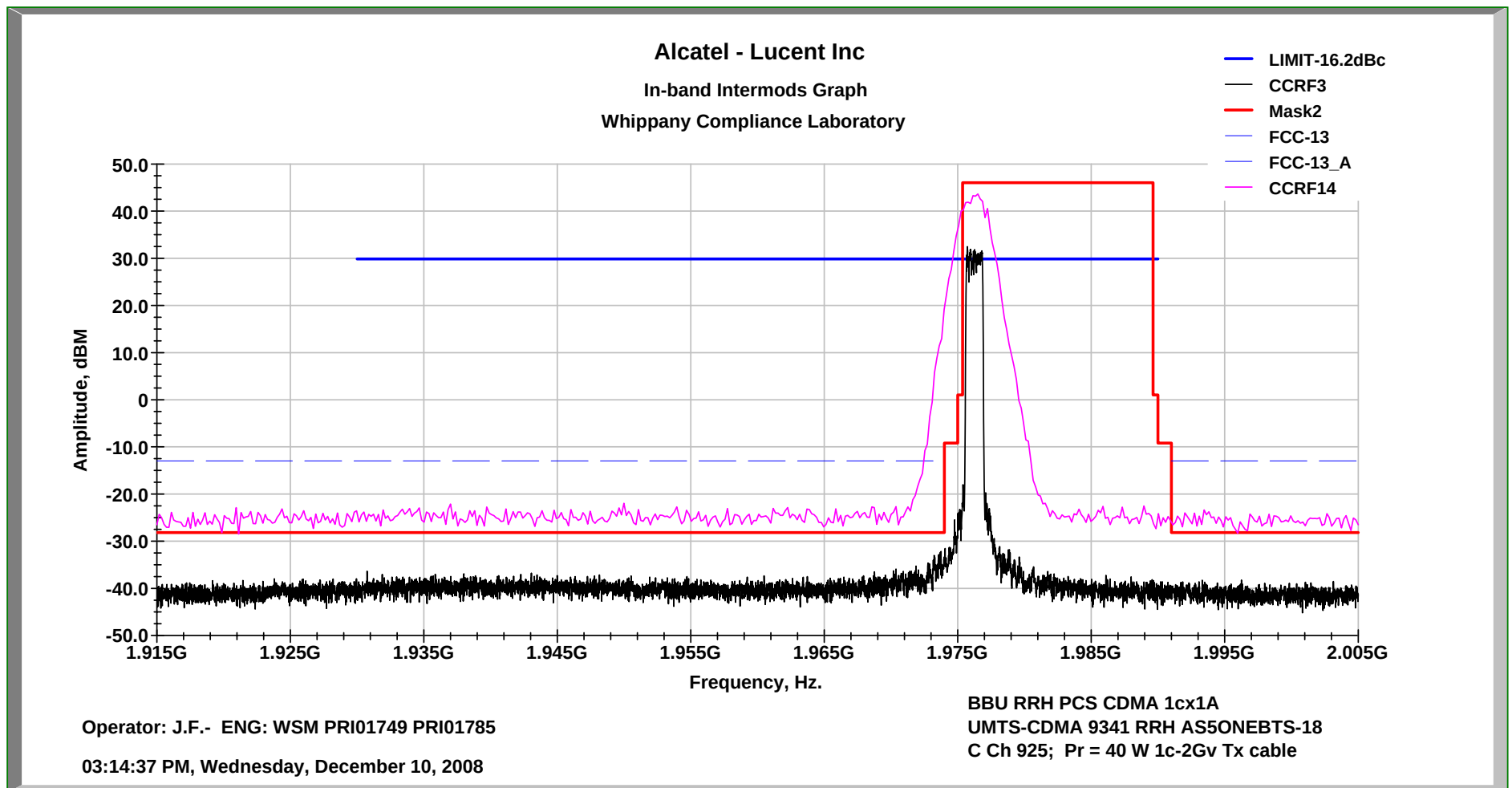
1c w/EDPD

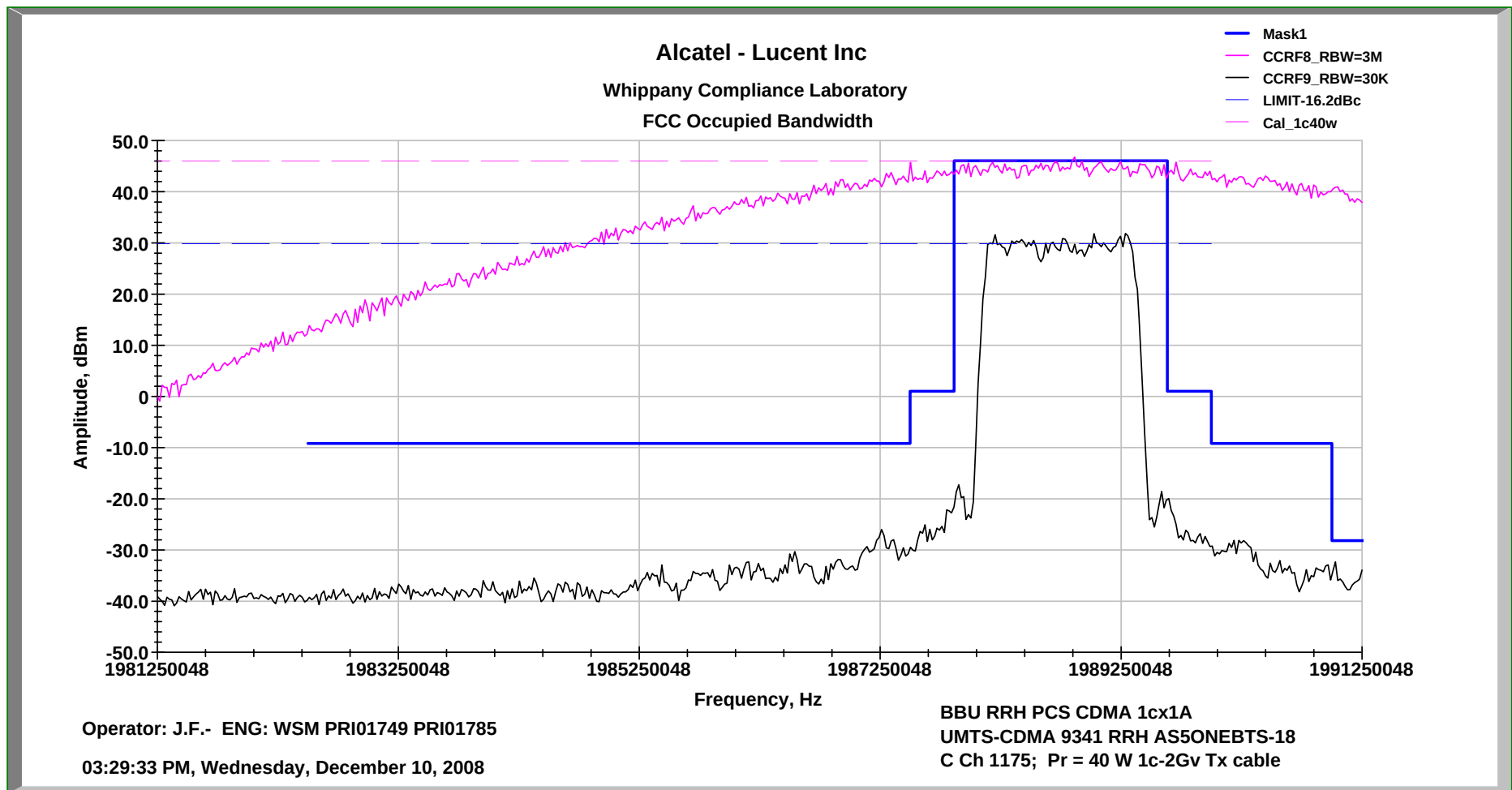
40W Total



Occupied Bandwidth PCS Ch C1-925 1c w/EDPD 40W Total



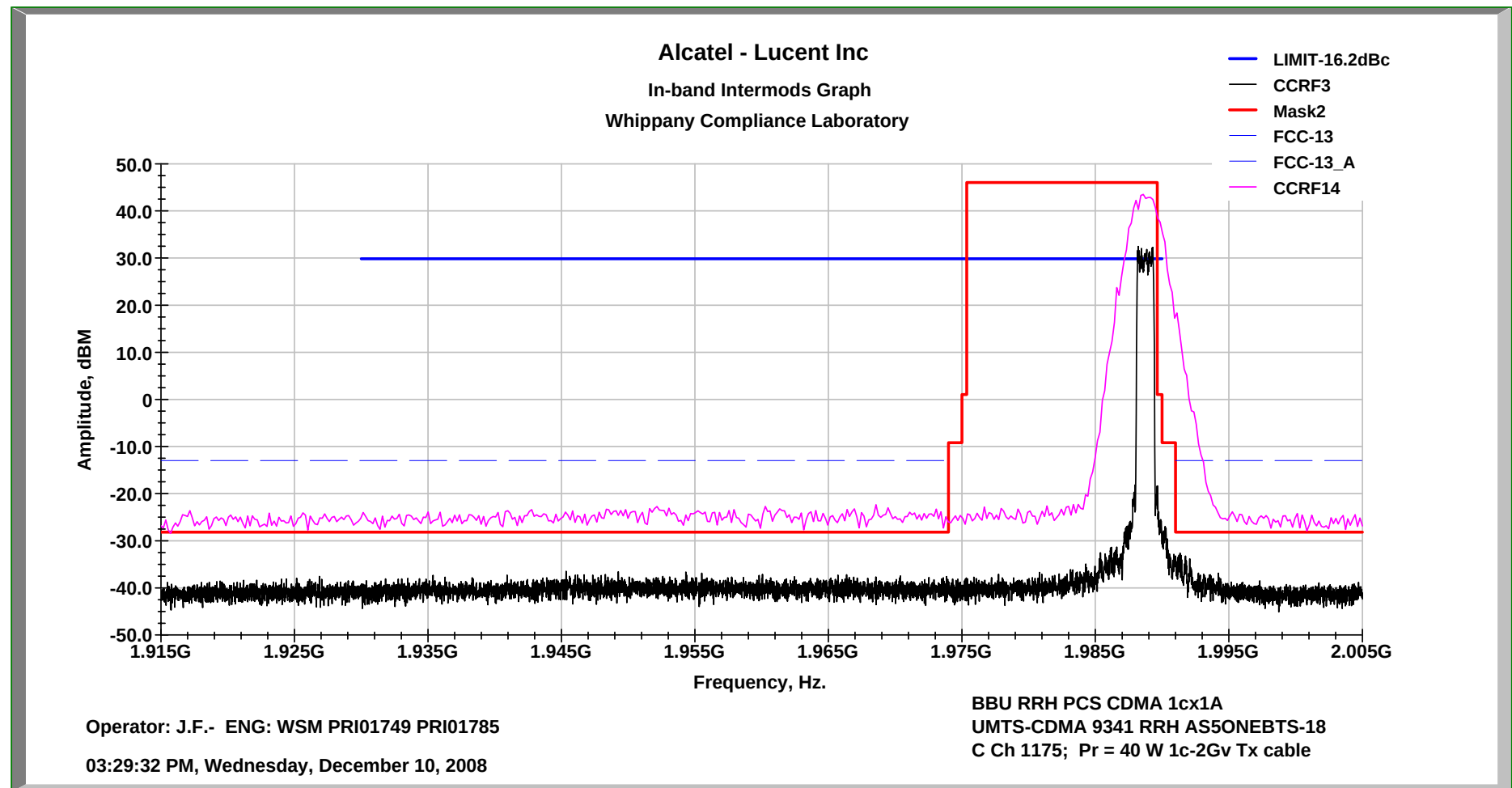
In-band Inter-modulation PCS Ch C1-925 1c w/EDPD 40W Total

Occupied Bandwidth PCS Ch C5-1175 1c w/EDPD 40W Total

In-band Inter-modulation

PCS Ch C5-1175 1c w/EDPD

40W Total



**Two Carrier Occupied Bandwidth Data
PCS Blocks A, D, B, E, F, and C
Left side of Block, Right side of Block
And
PCS Band Intermodulation**

Occupied Bandwidth

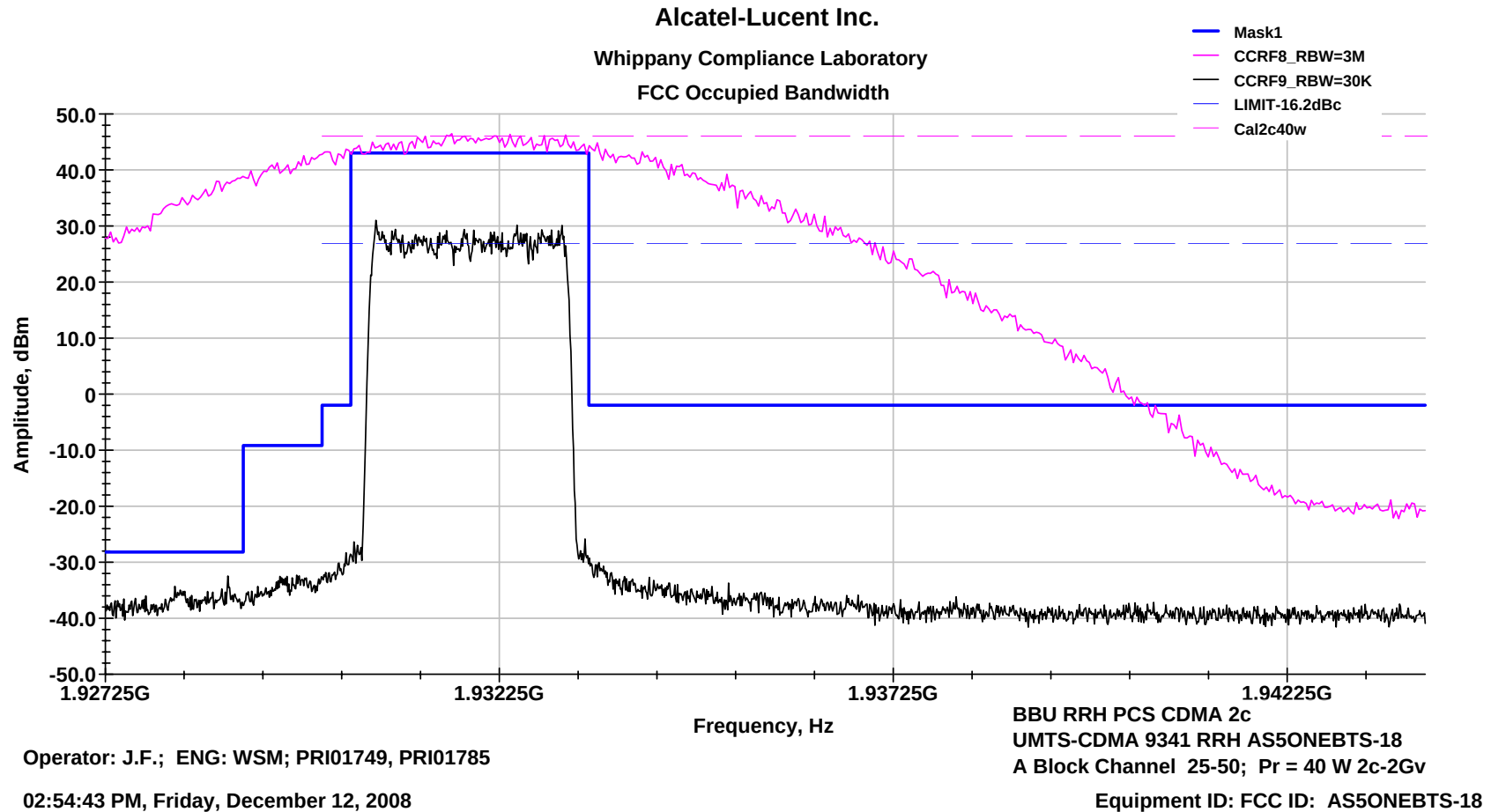
PCS Ch A1-25, 50

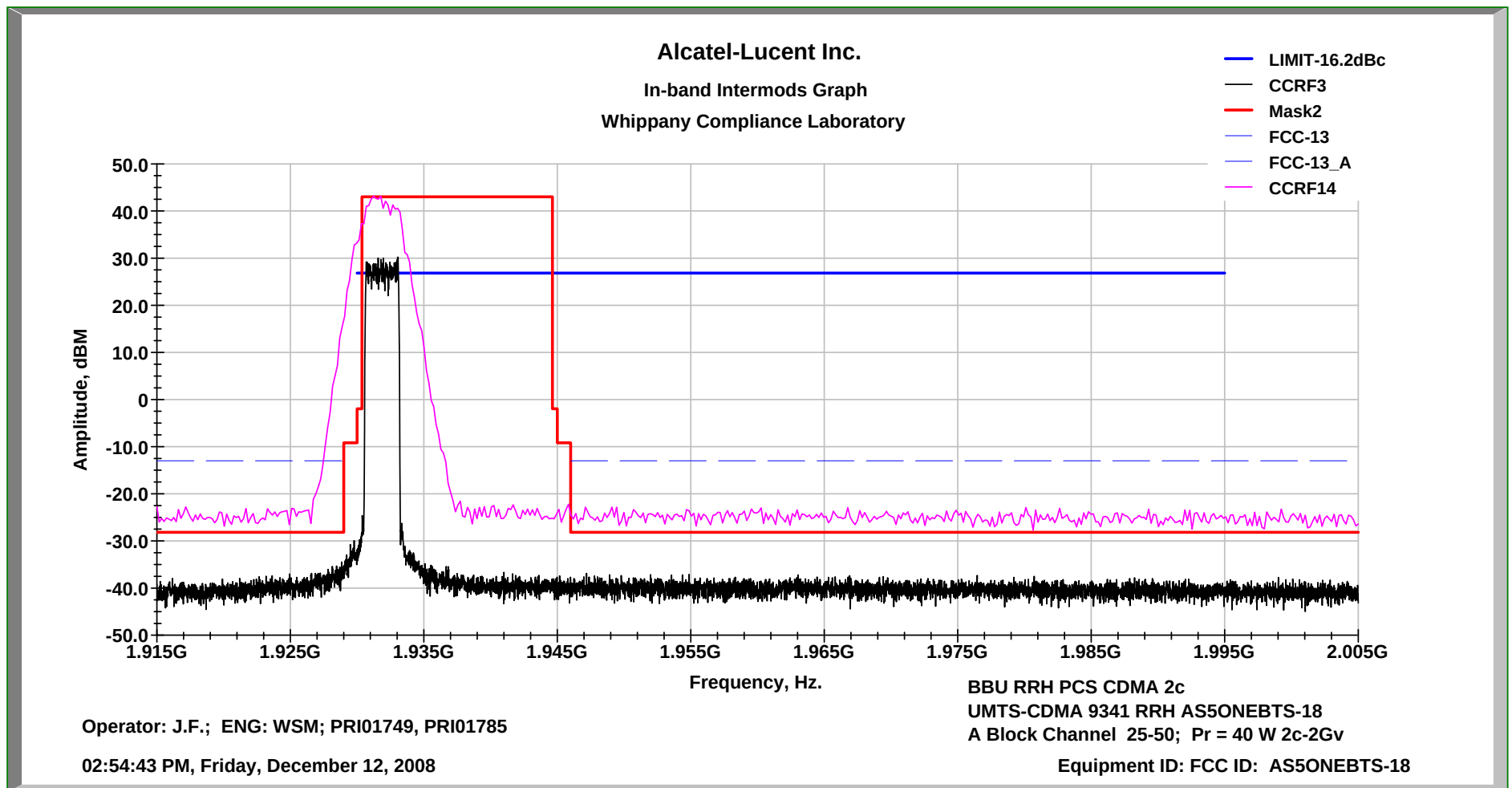
2c

2Gv

w/EDPD

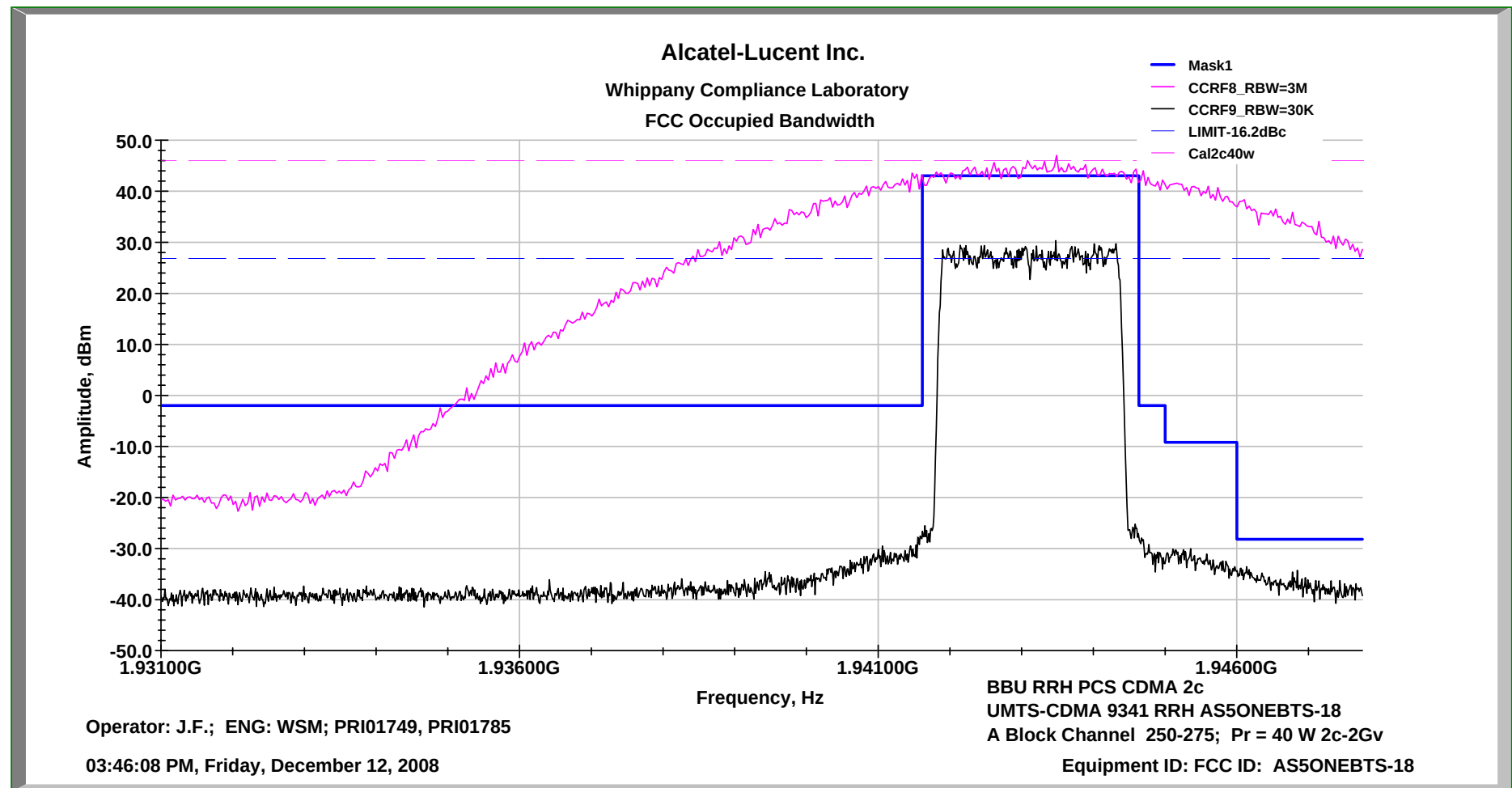
40W Total

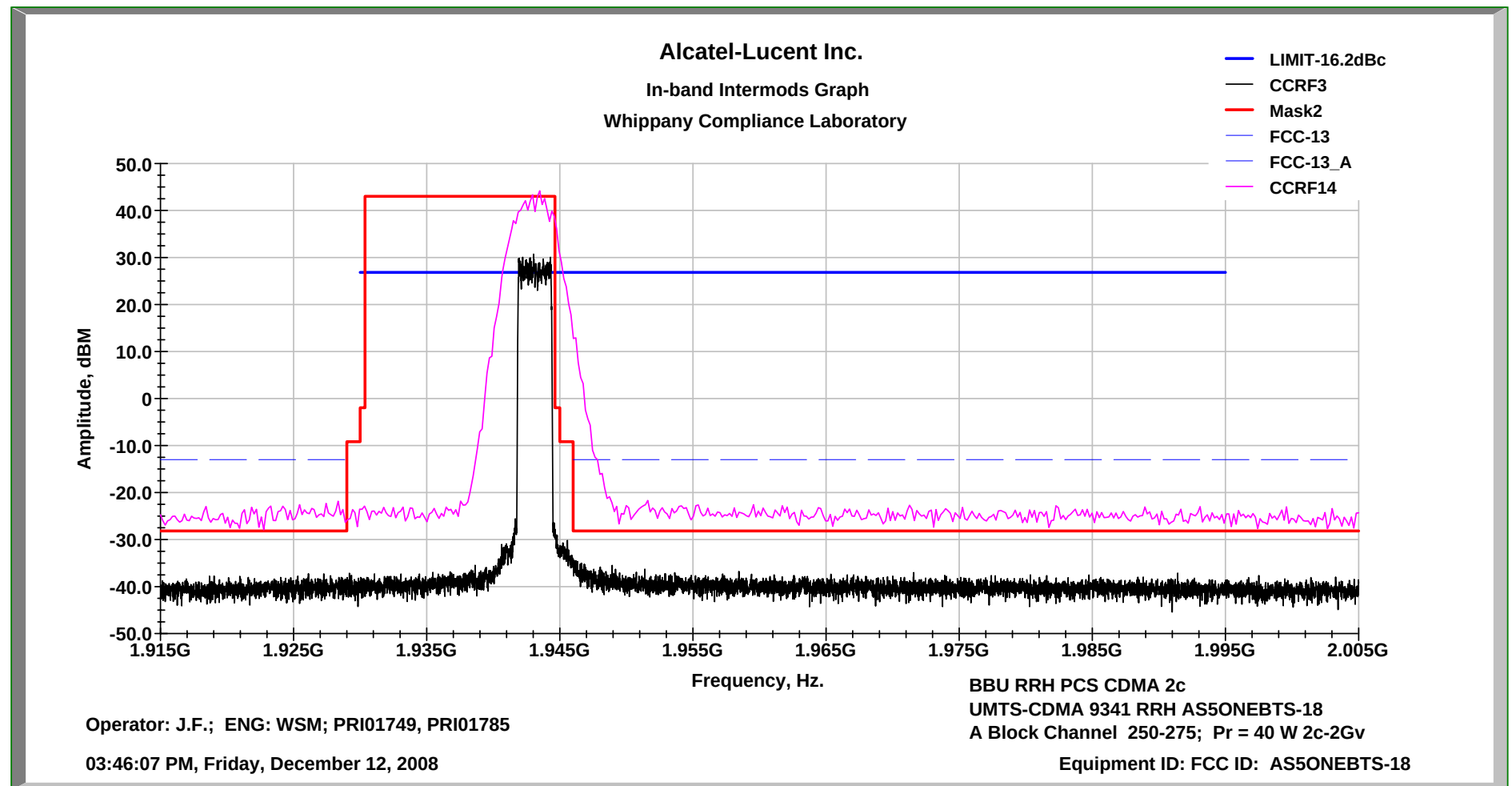


In-band Inter-modulation PCS Ch A1-25, 50 2c 2Gv w/EDPD 40W Total

Occupied Bandwidth

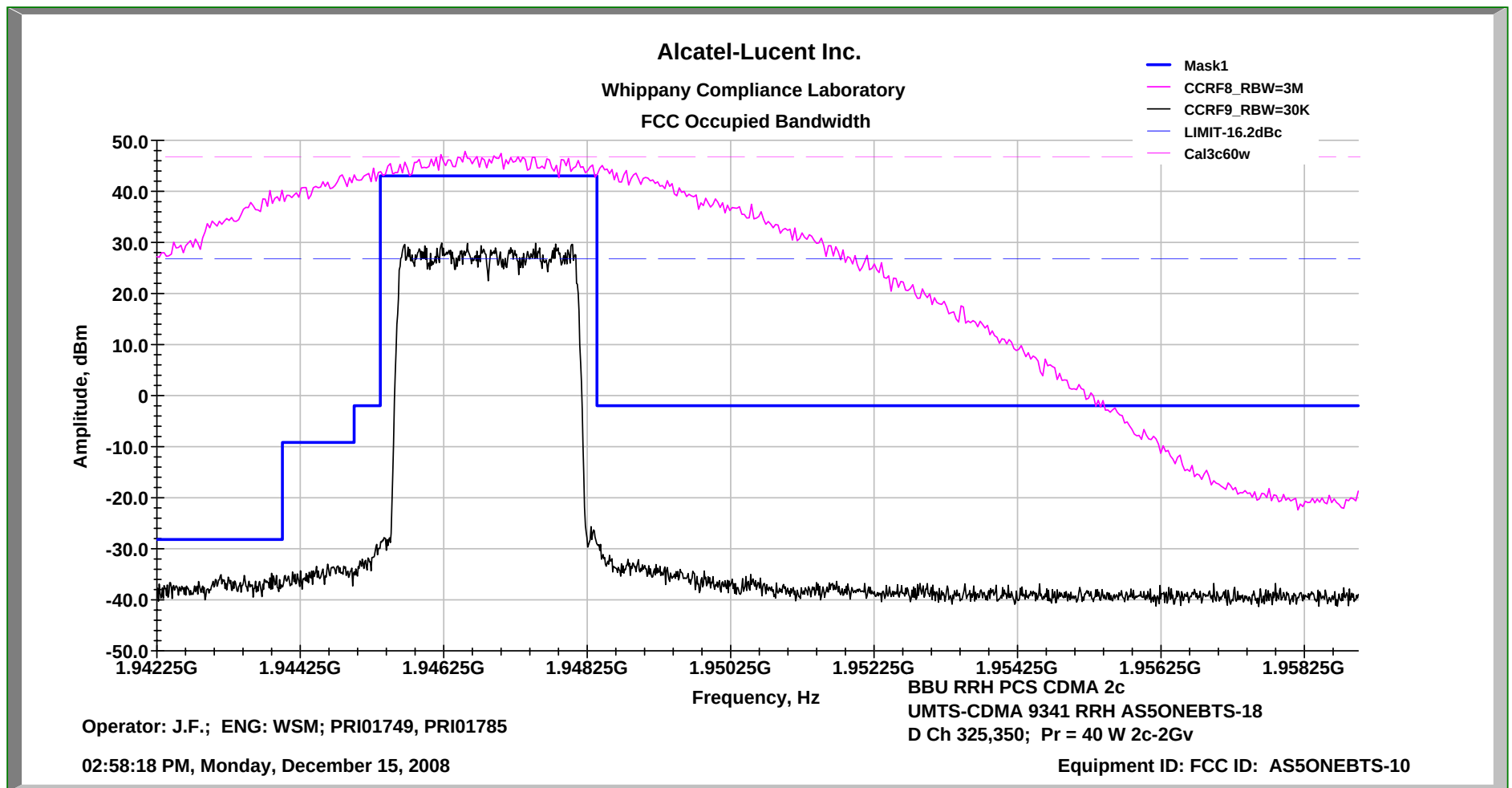
PCS Ch A5-250, 275 2c 2Gv w/EDPD 40W Total

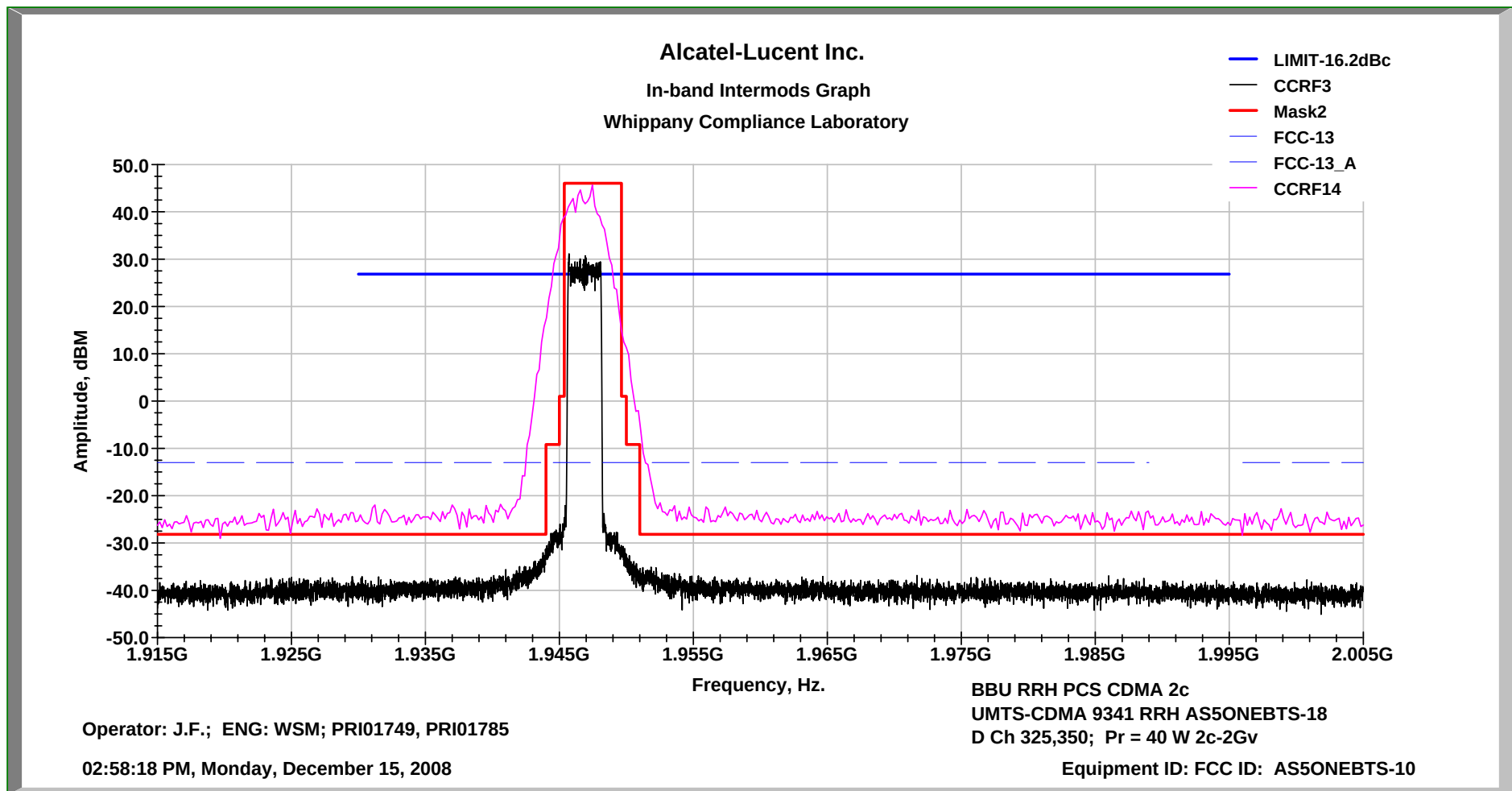


In-band Inter-modulation PCS Ch A5-250, 275 2c 2Gv w/EDPD 40W Total

Occupied Bandwidth

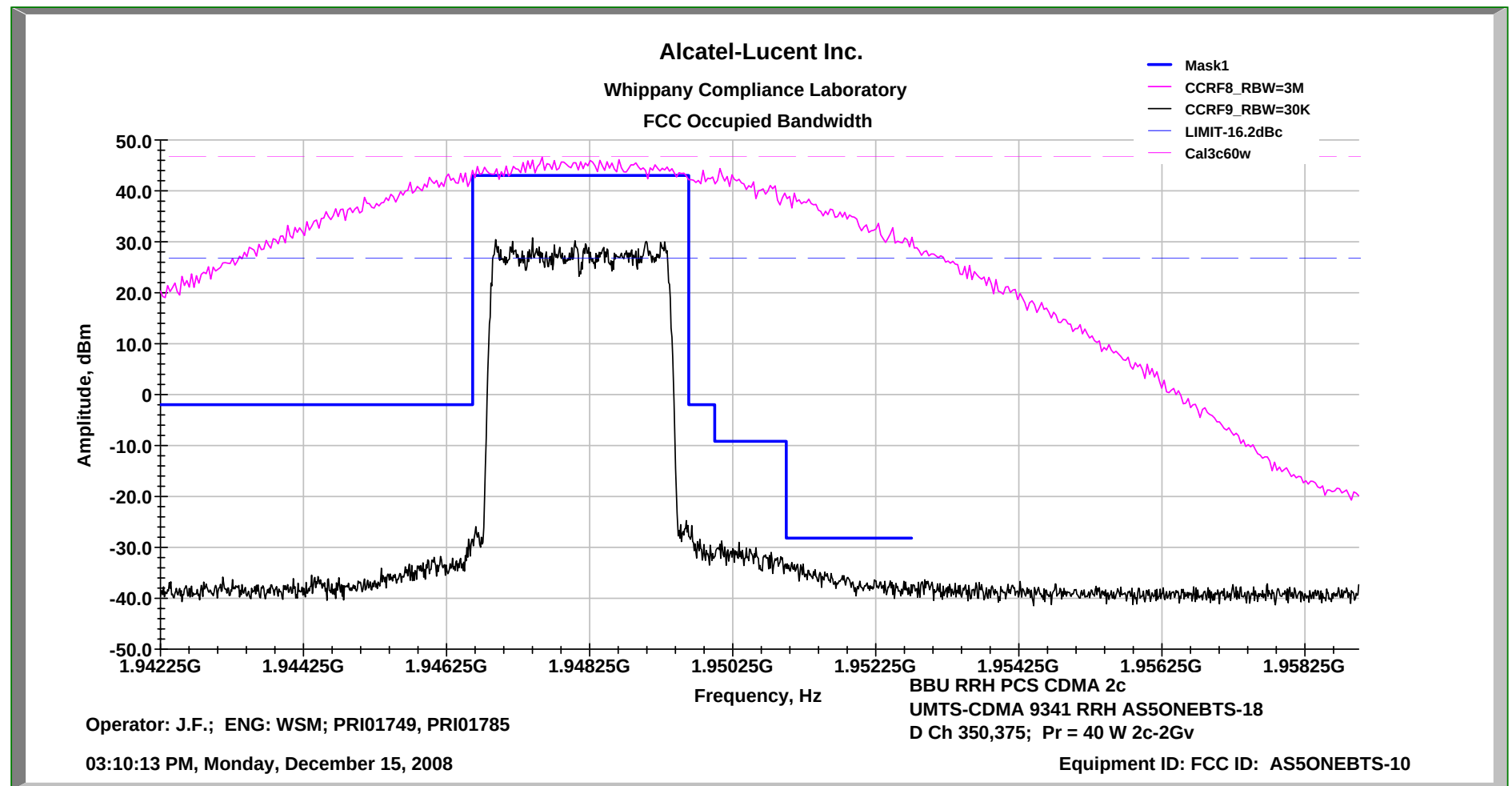
PCS Ch DL-325, 350 2c 2Gv w/EDPD 40W Total

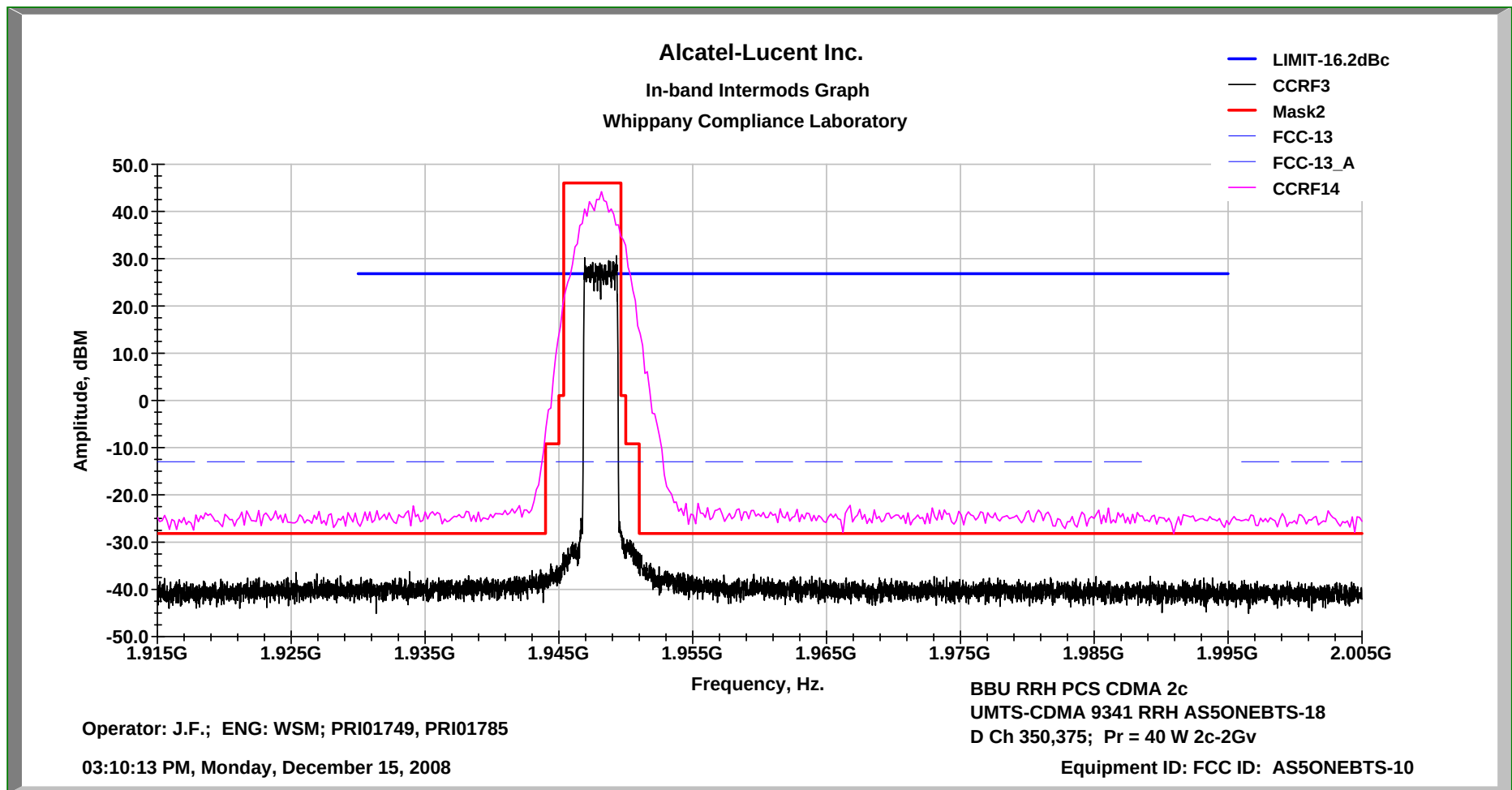


In-band Inter-modulation PCS Ch DL-325, 350 2c 2Gv w/EDPD 40W Total

Occupied Bandwidth

PCS Ch DR-350, 375 2c 2Gv w/EDPD 40W Total



In-band Inter-modulation PCS Ch DR-350, 375 2c 2Gv w/EDPD 40W Total

Occupied Bandwidth

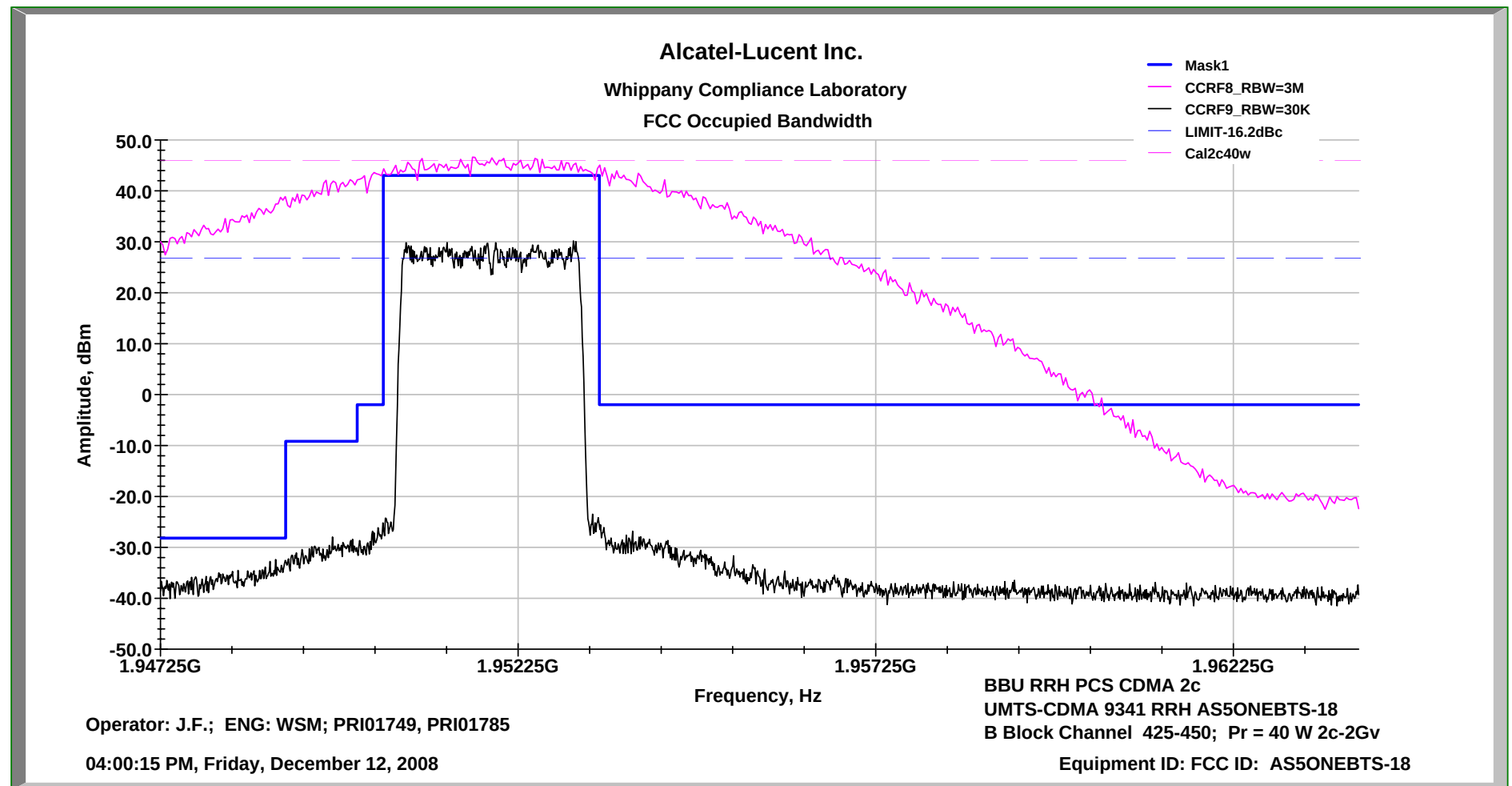
PCS Ch B1-425,450

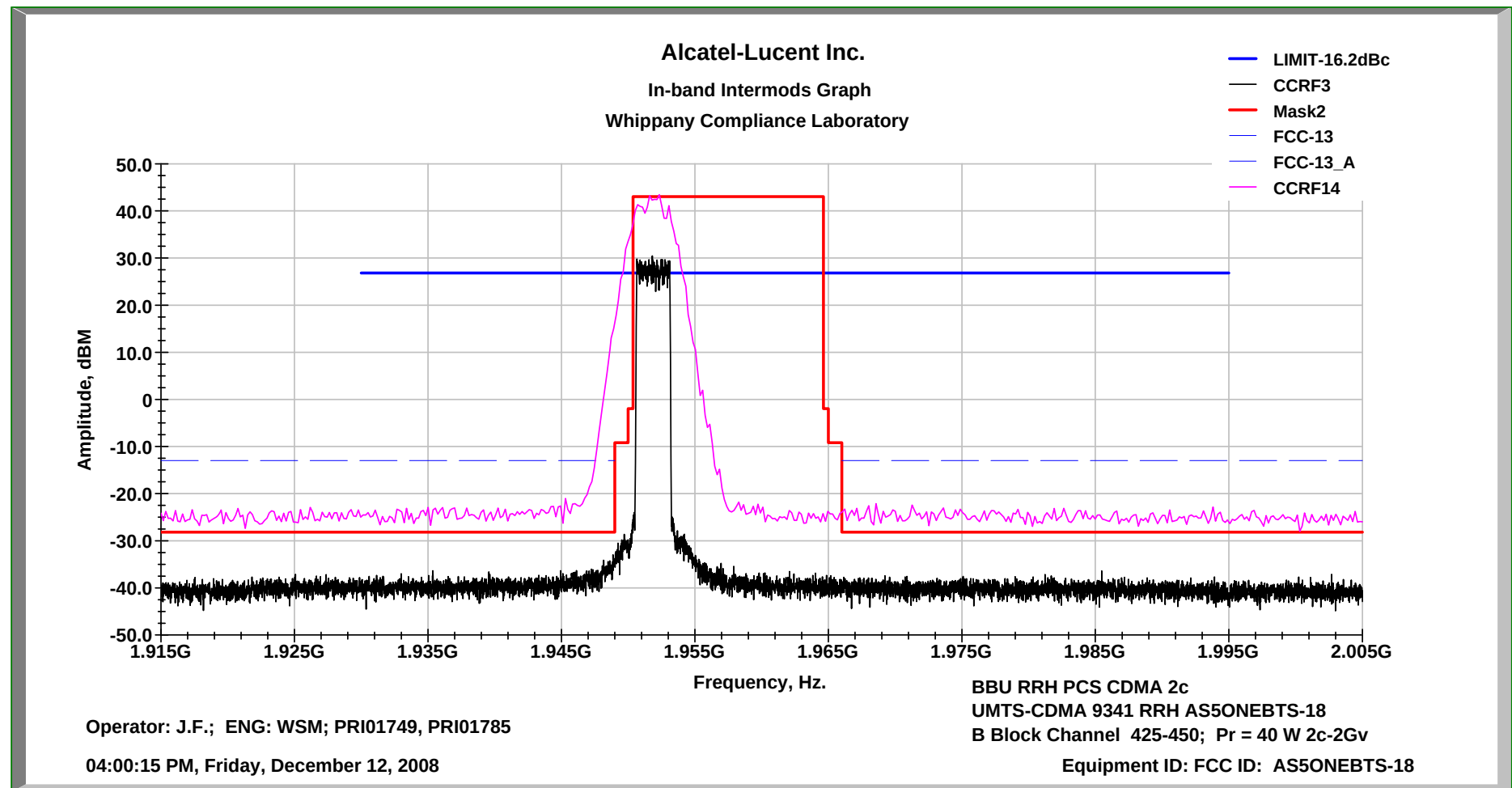
2c

2Gv

w/EDPD

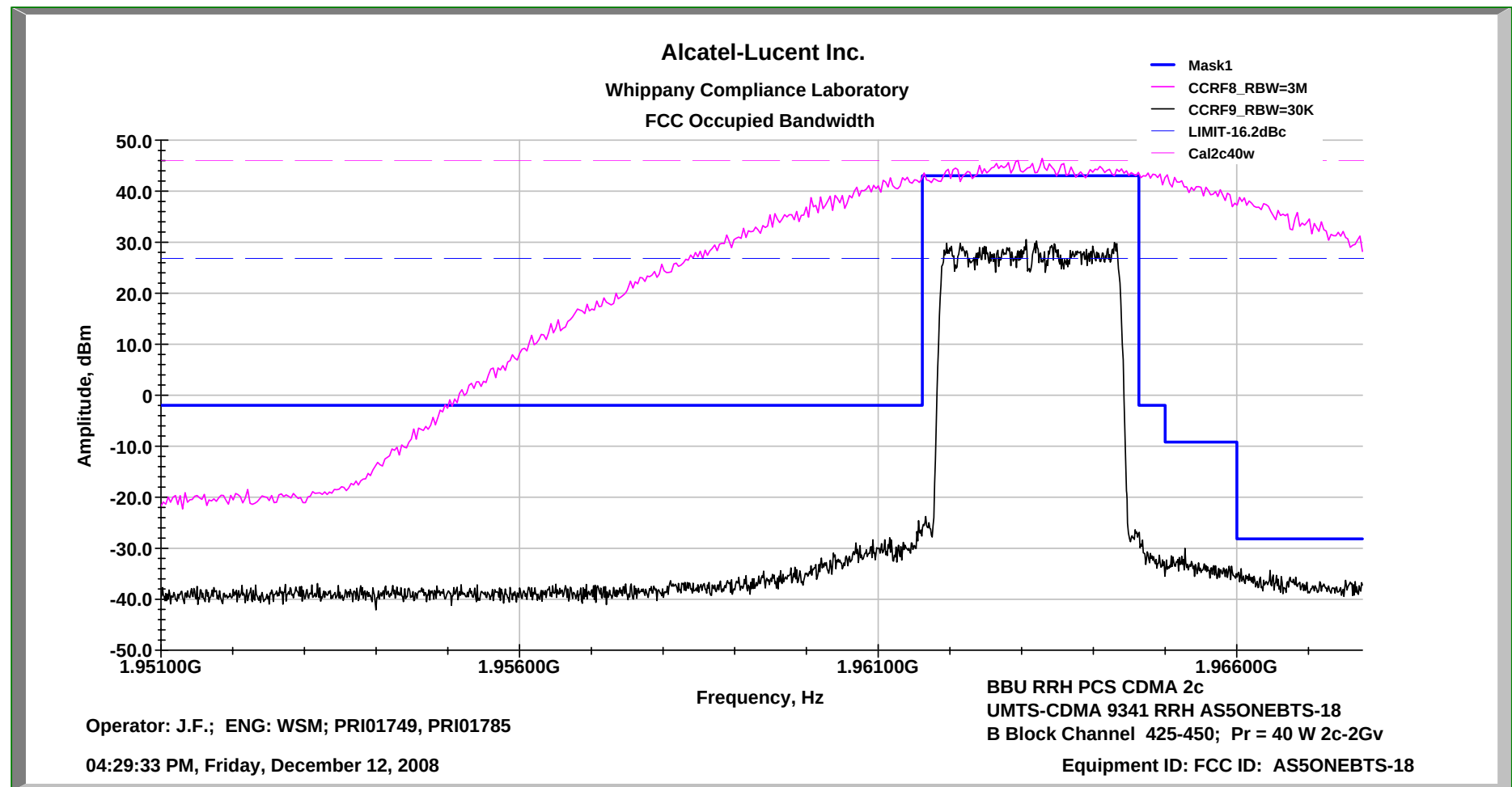
40W Total

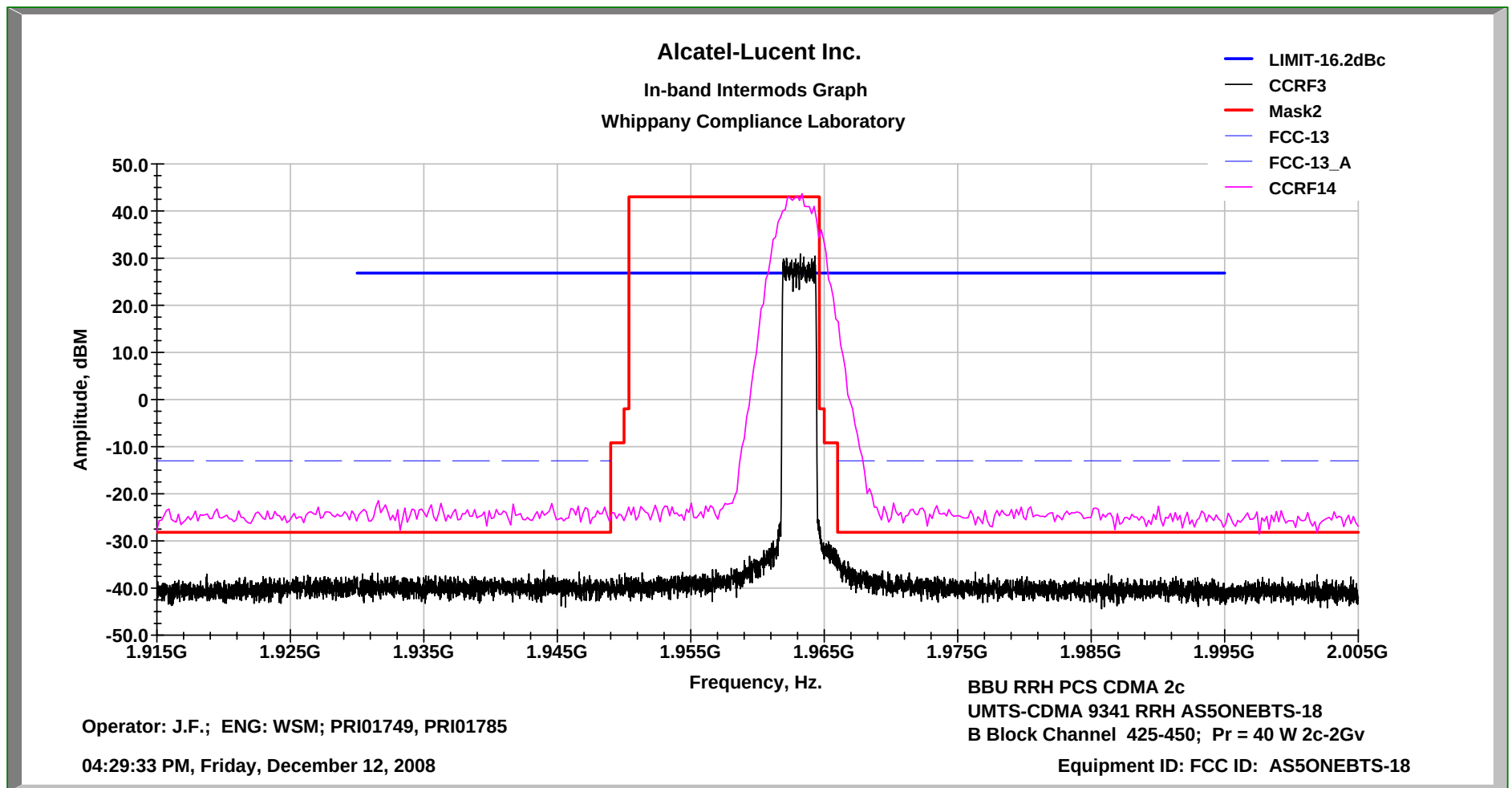


In-band Inter-modulation PCS Ch B1-425,450 2c 2Gv w/EDPD 40W Total

Occupied Bandwidth

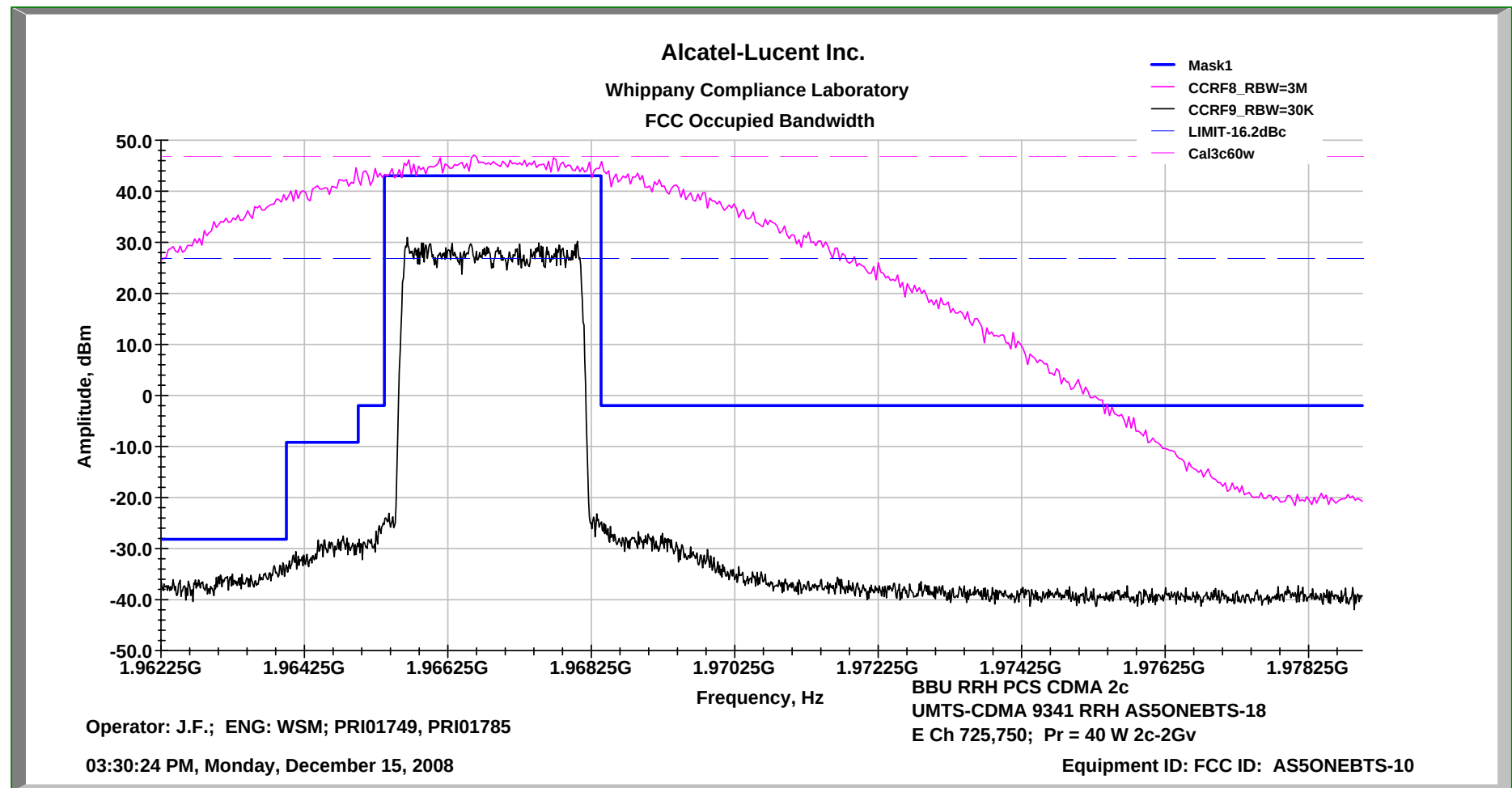
PCS Ch B5-650, 675 2c 2Gv w/EDPD 40W Total

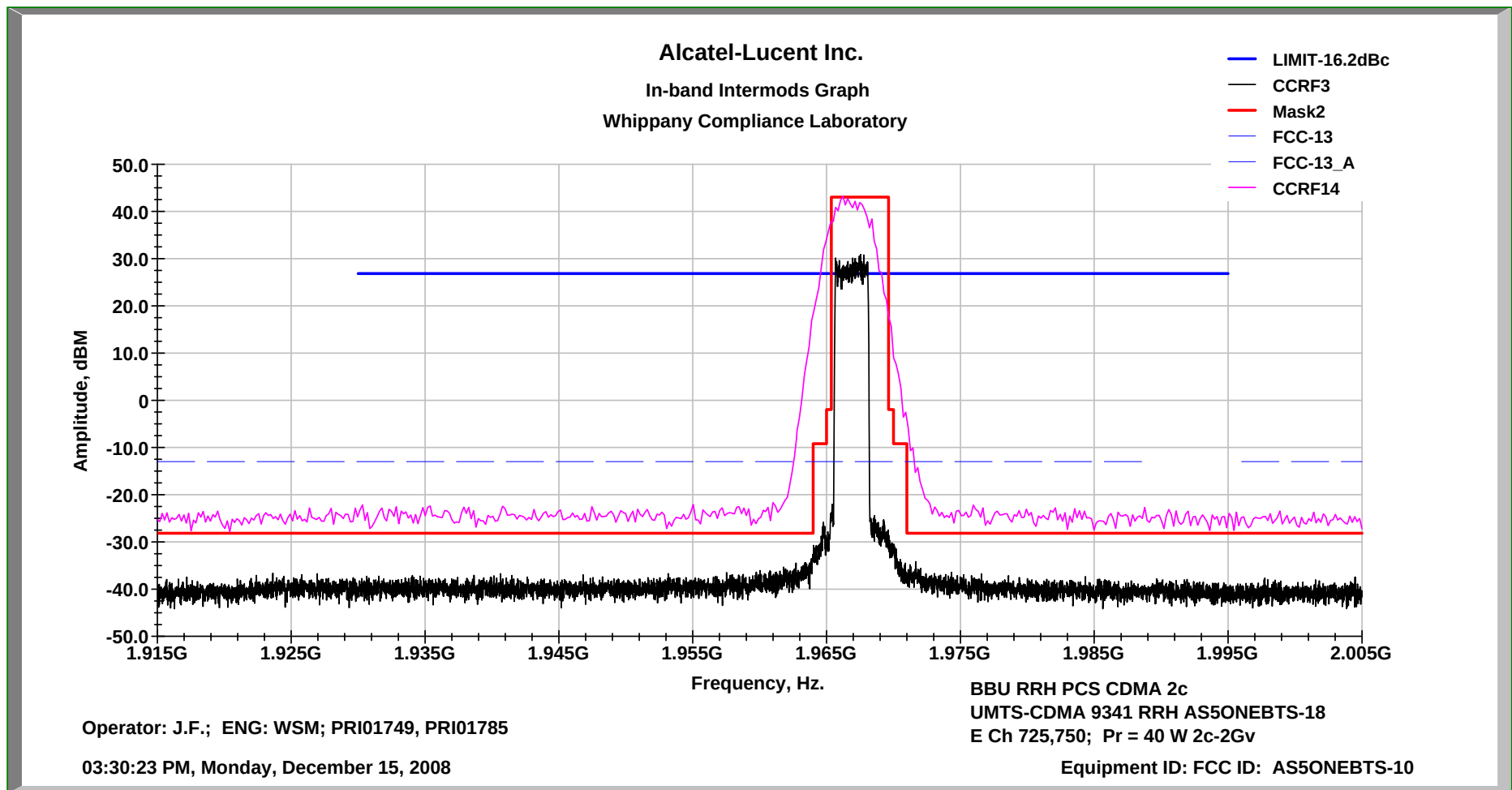


In-band Inter-modulation PCS Ch B5-650, 675 2c 2Gv w/EDPD 40W Total

Occupied Bandwidth

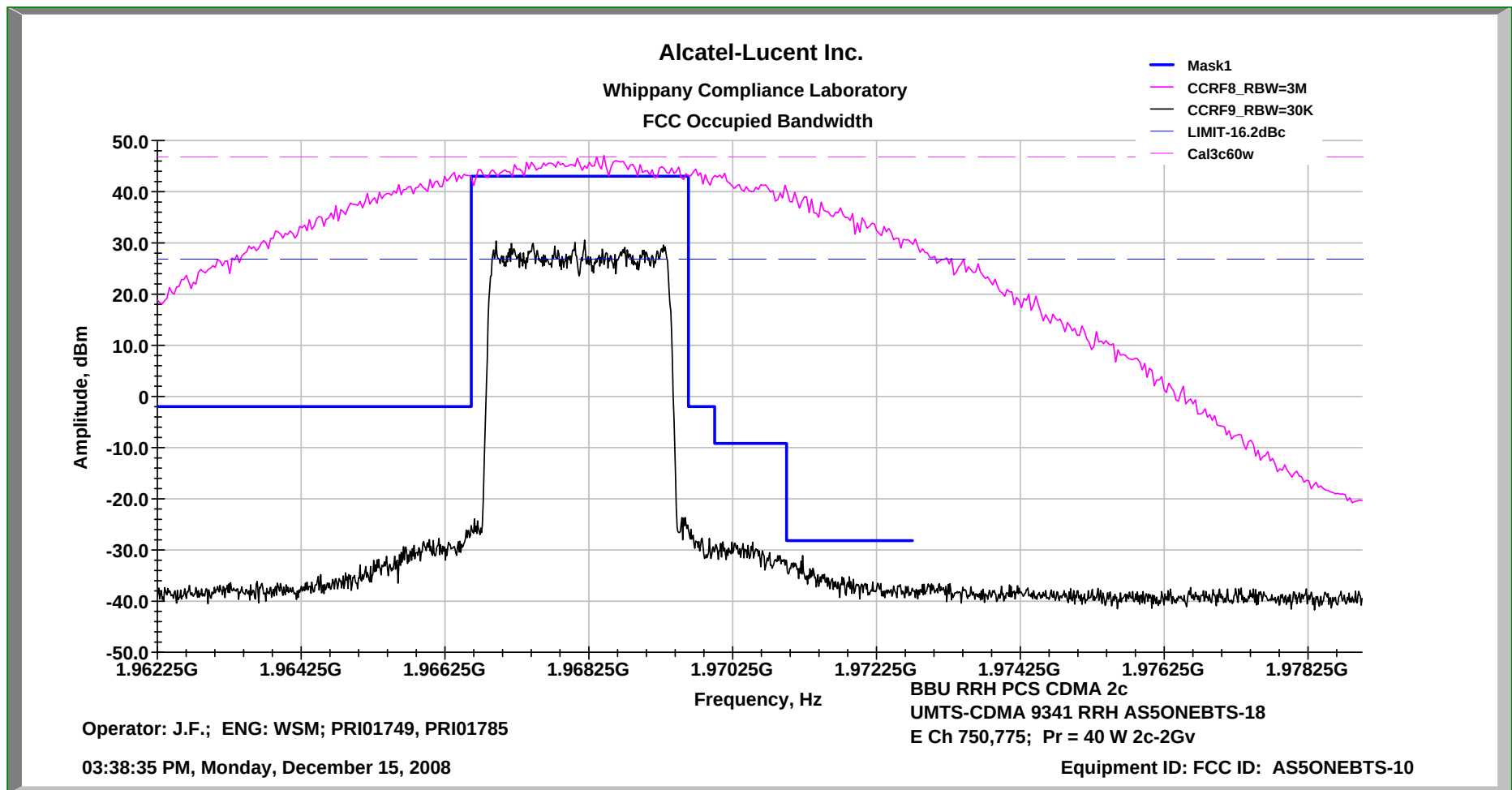
PCS Ch EL-725, 750 2c 2Gv w/EDPD 40W Total

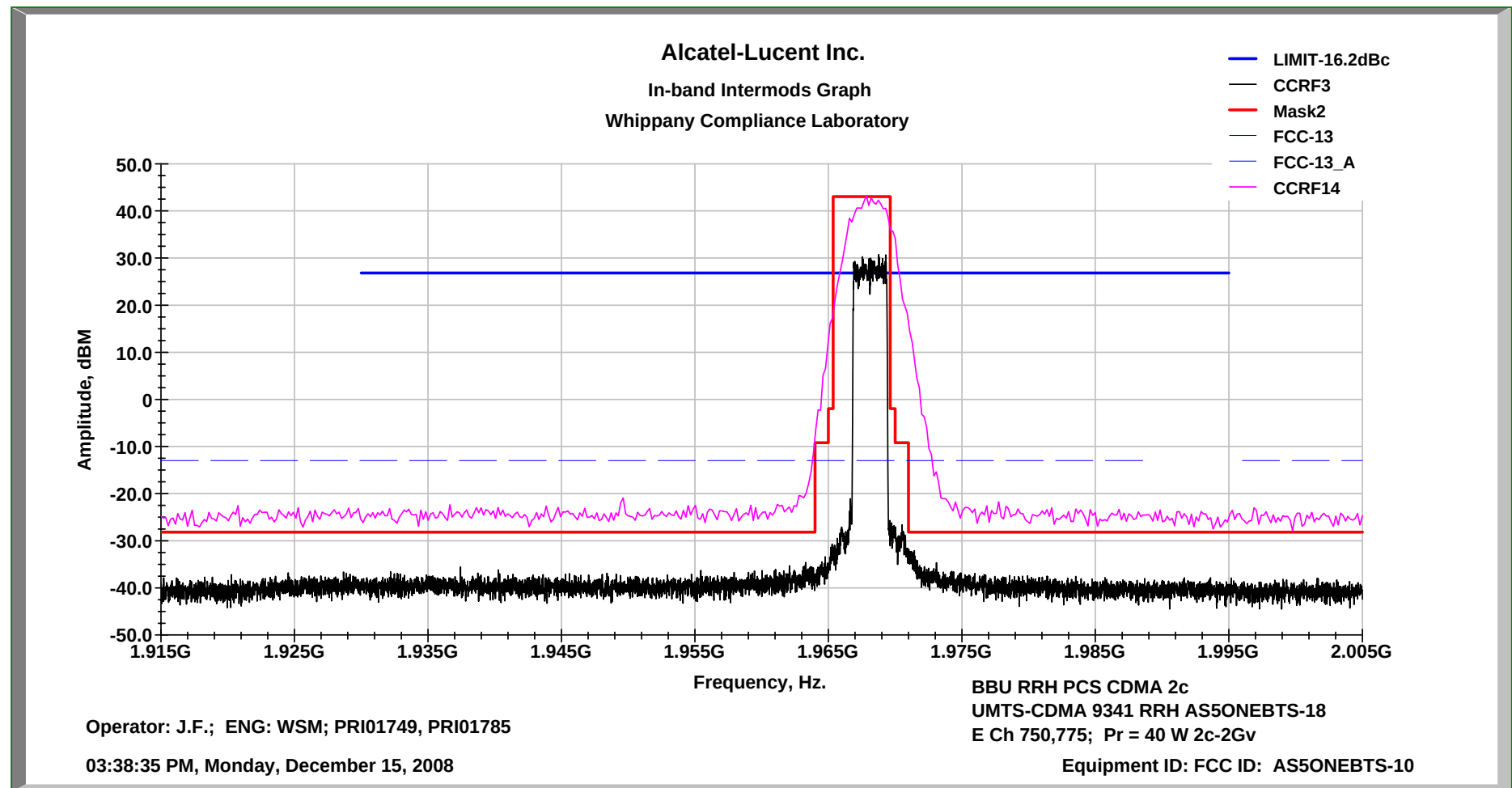


In-band Inter-modulation PCS Ch EL-725, 750 2c 2Gv w/EDPD 40W Total

Occupied Bandwidth

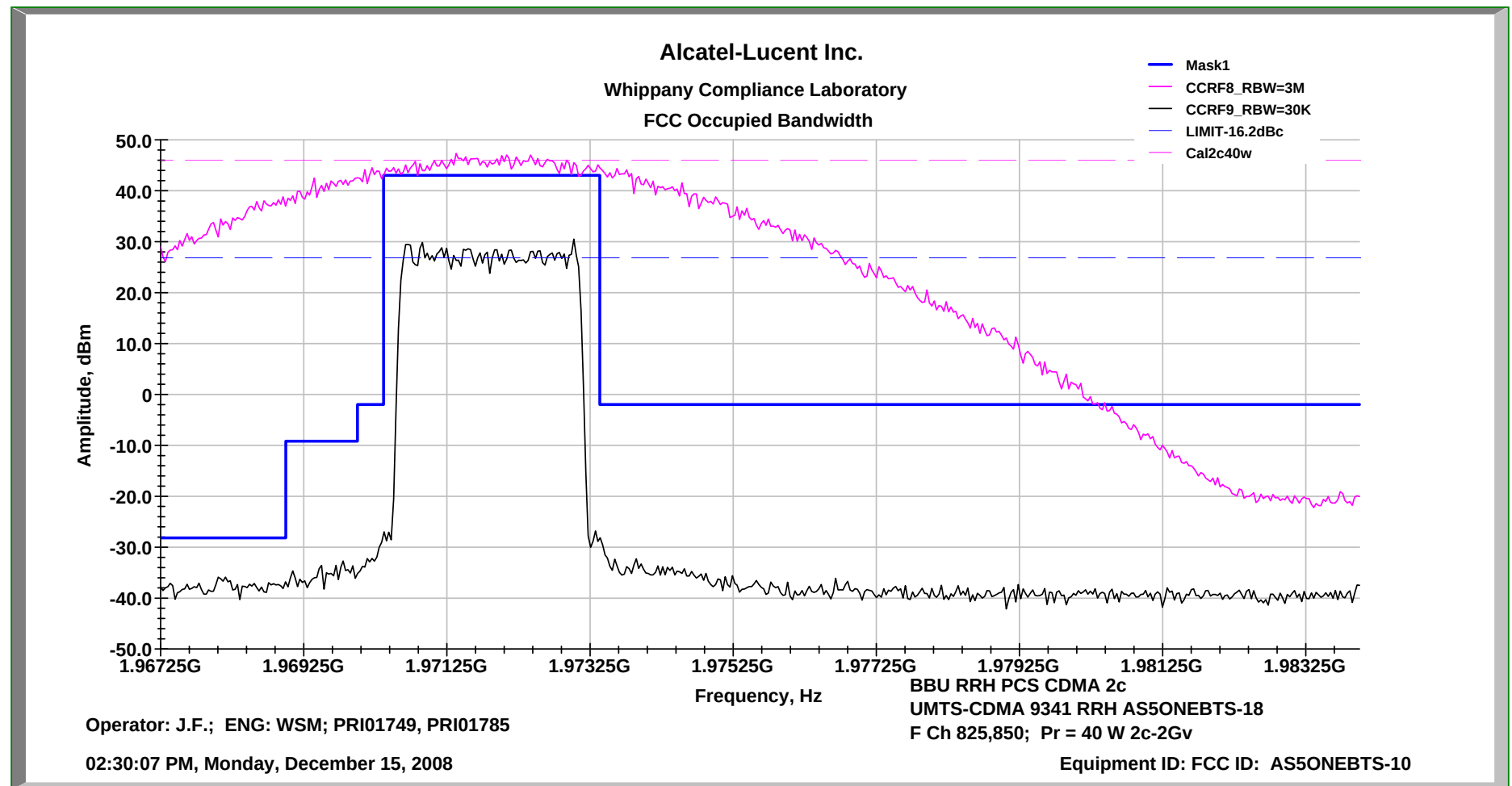
PCS Ch ER-750, 775 2c 2Gv w/EDPD 40W Total

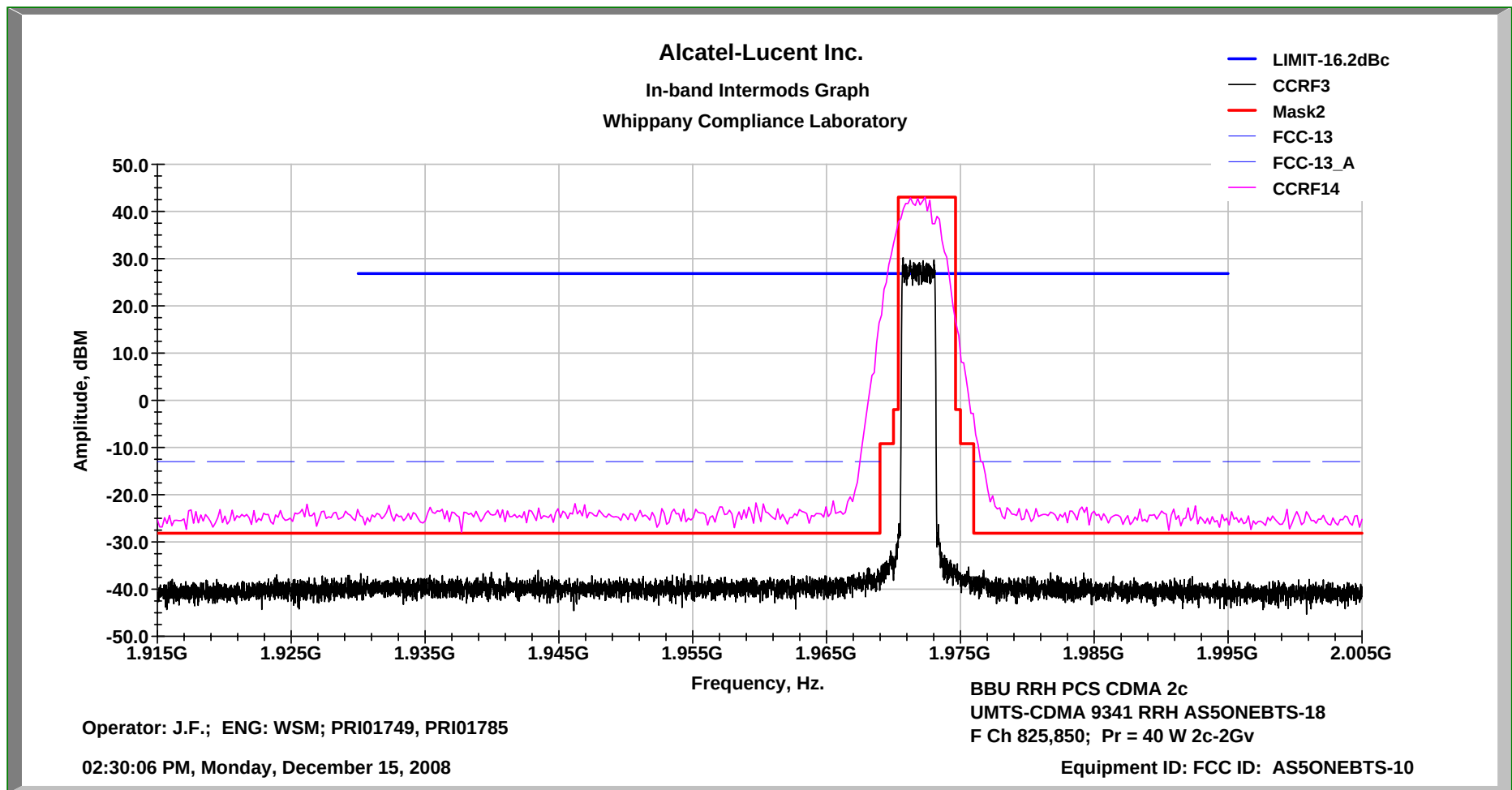


In-band Inter-modulation PCS Ch ER-750, 775 2c 2Gv w/EDPD 40W Total

Occupied Bandwidth

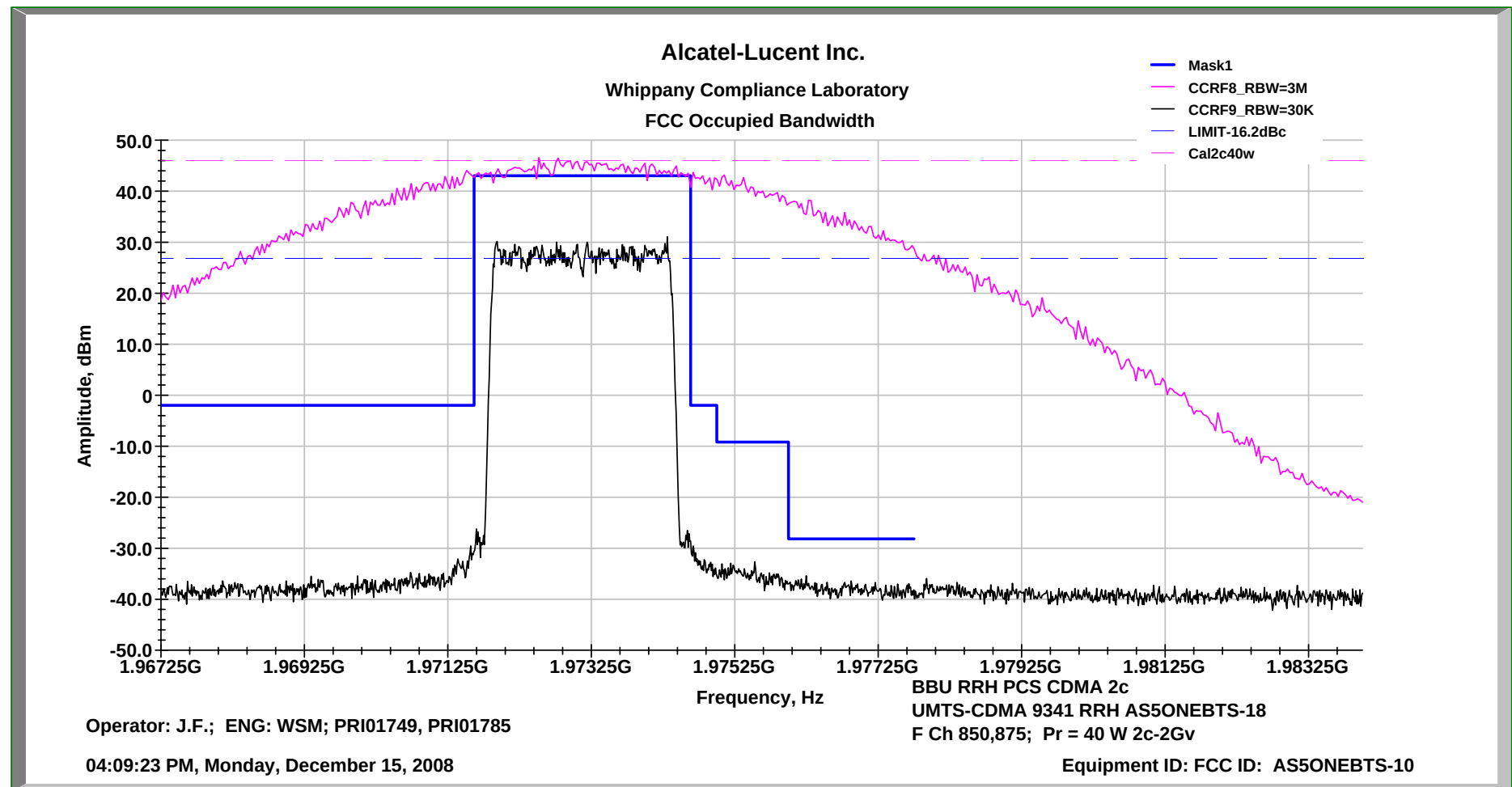
PCS Ch FL-825, 850 2c 2Gv w/EDPD 40W Total

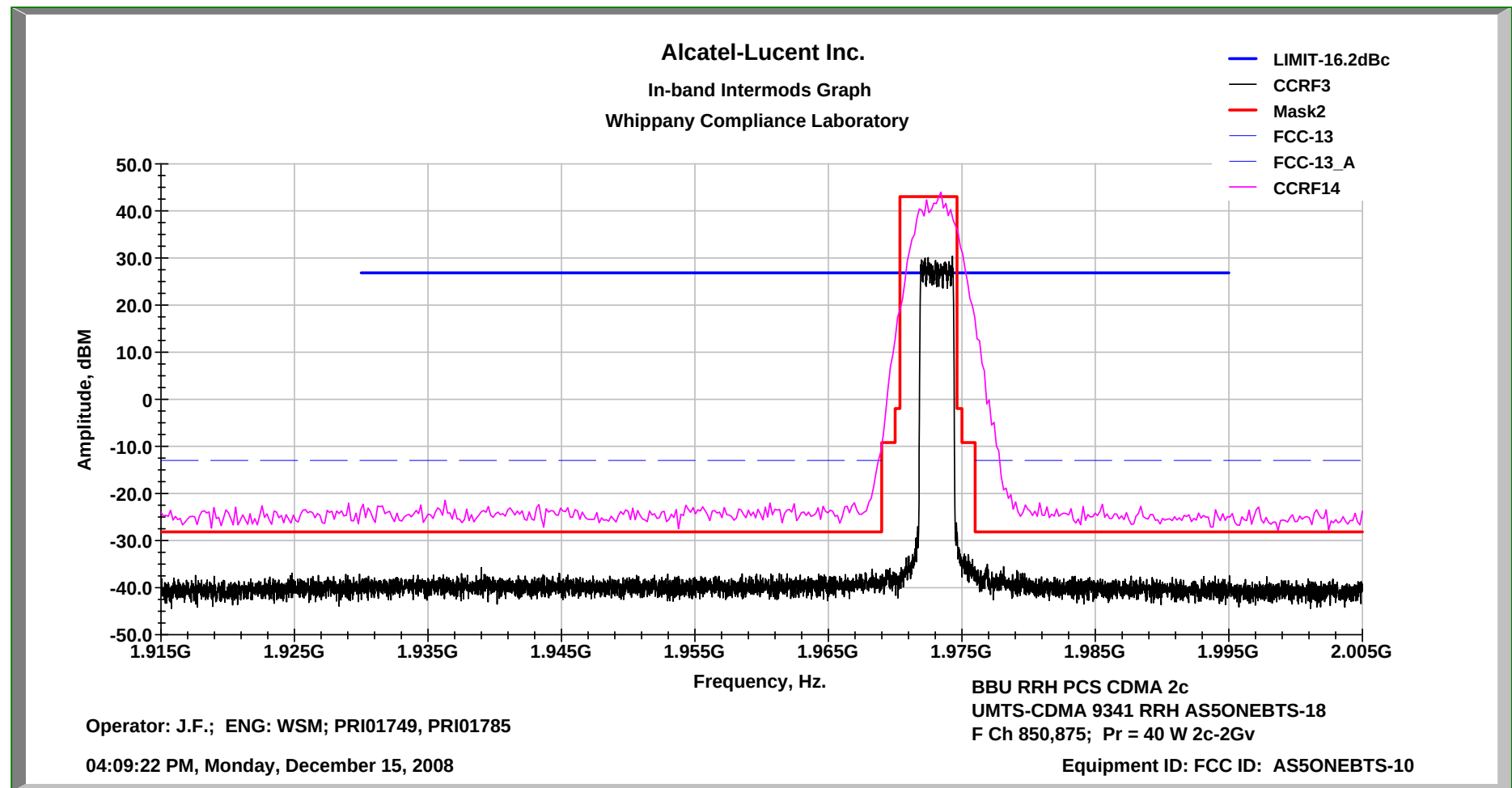


In-band Inter-modulation PCS Ch FL-825, 850 2c 2Gv w/EDPD 40W Total

Occupied Bandwidth

PCS Ch FR-850, 875 2c 2Gv w/EDPD 40W Total



In-band Inter-modulation PCS Ch FR-850, 875 2c 2Gv w/EDPD 40W Total

Occupied Bandwidth

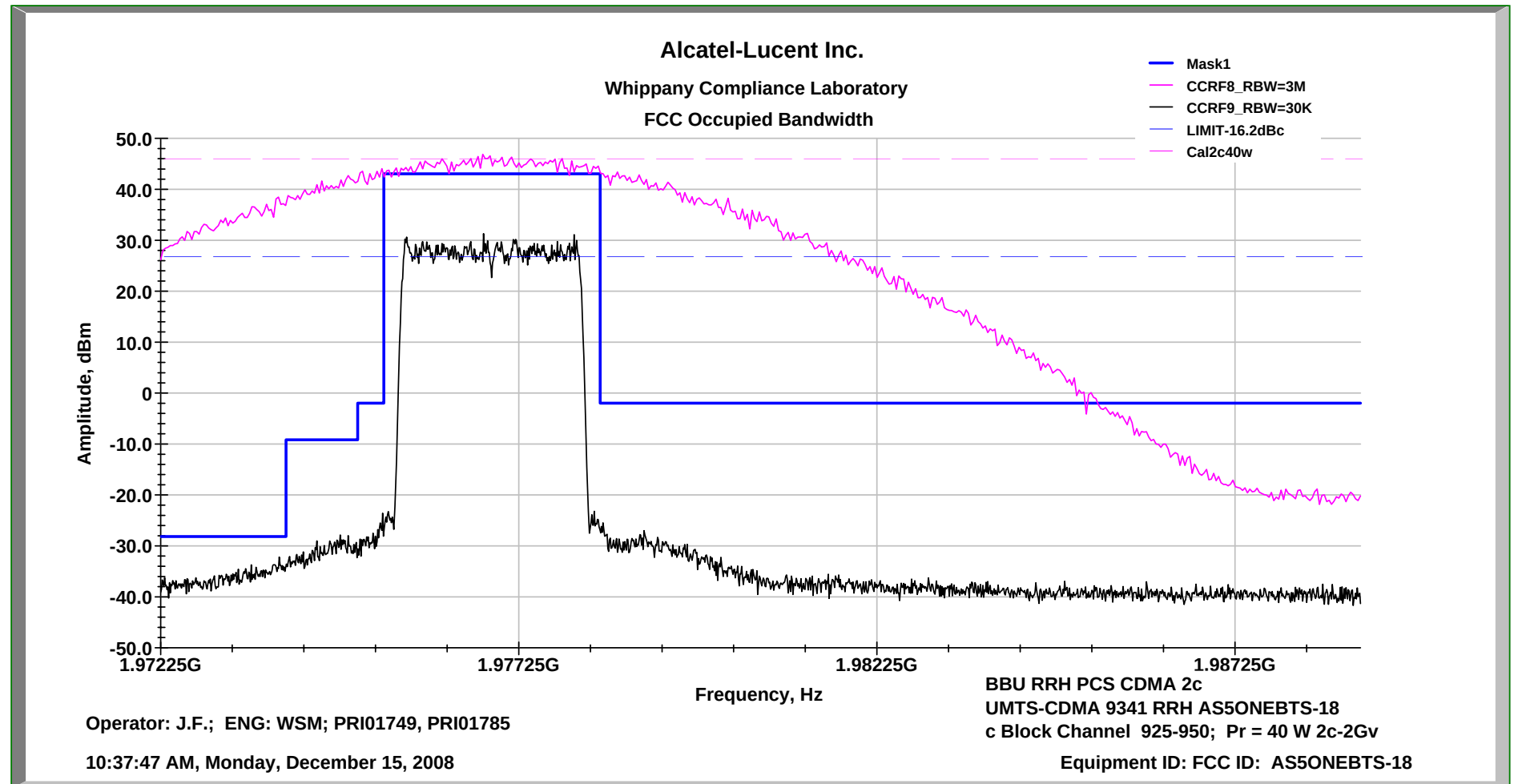
PCS Ch C1-925, 950

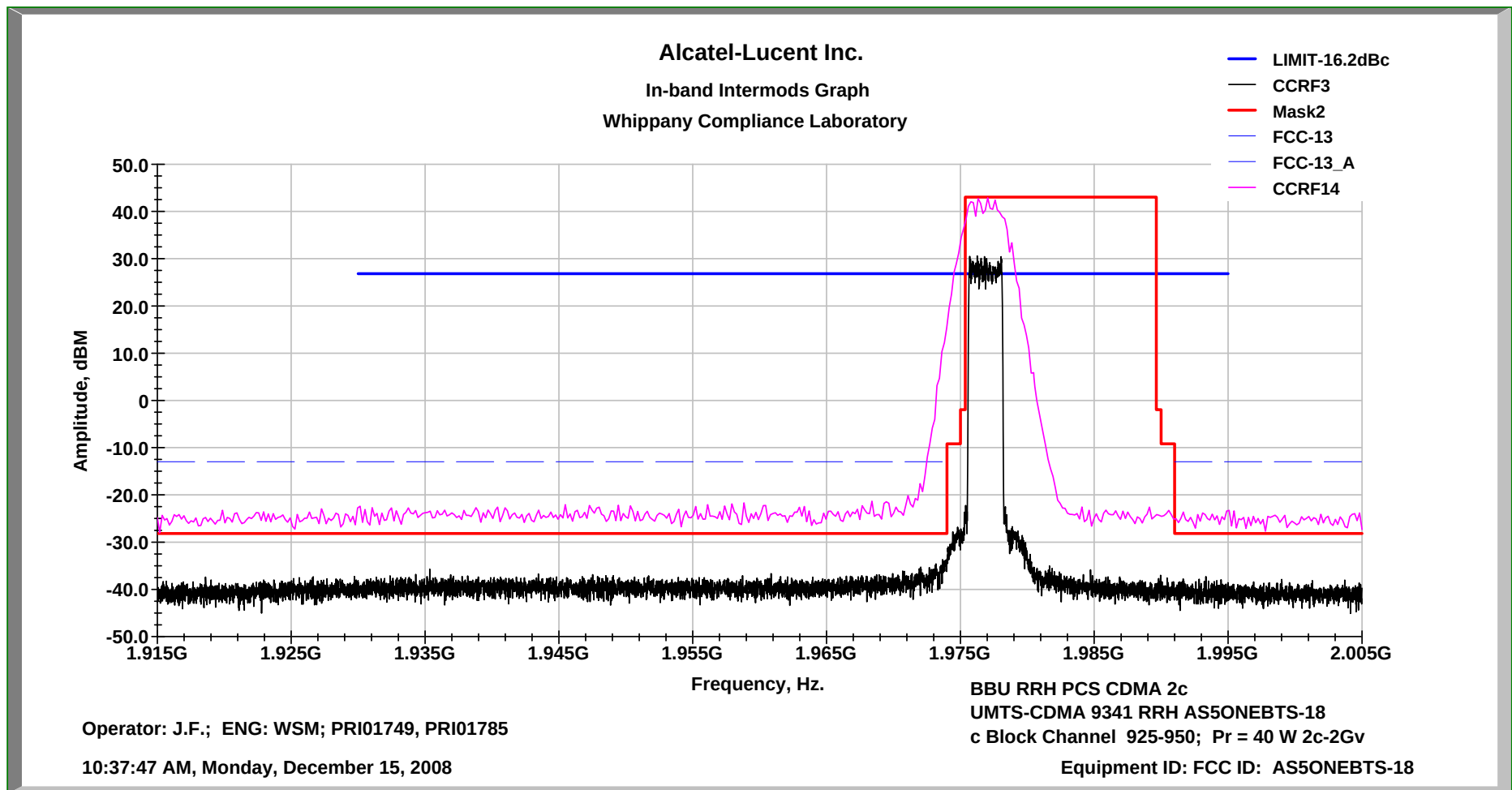
2c

2Gv

w/EDPD

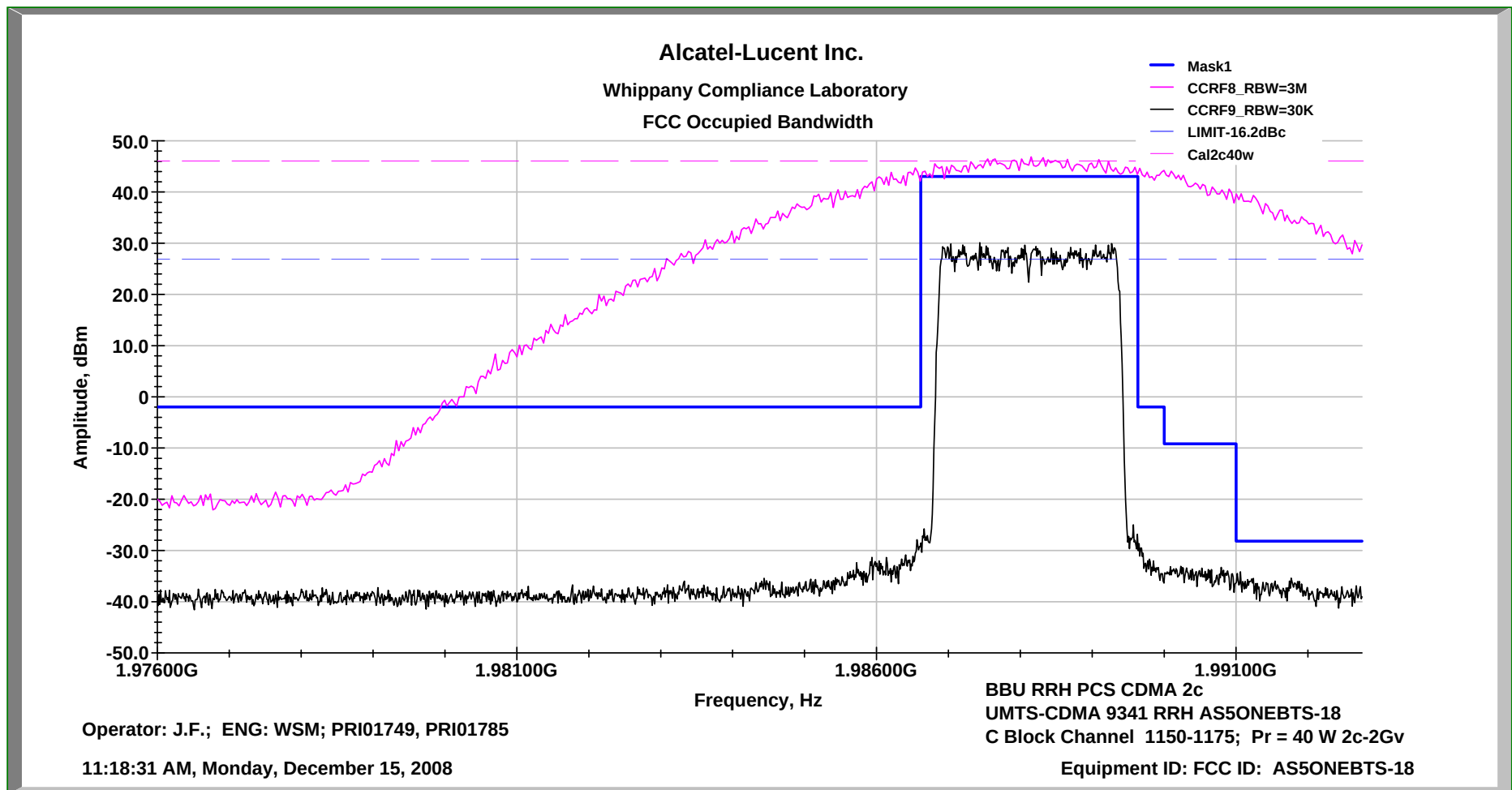
40W Total

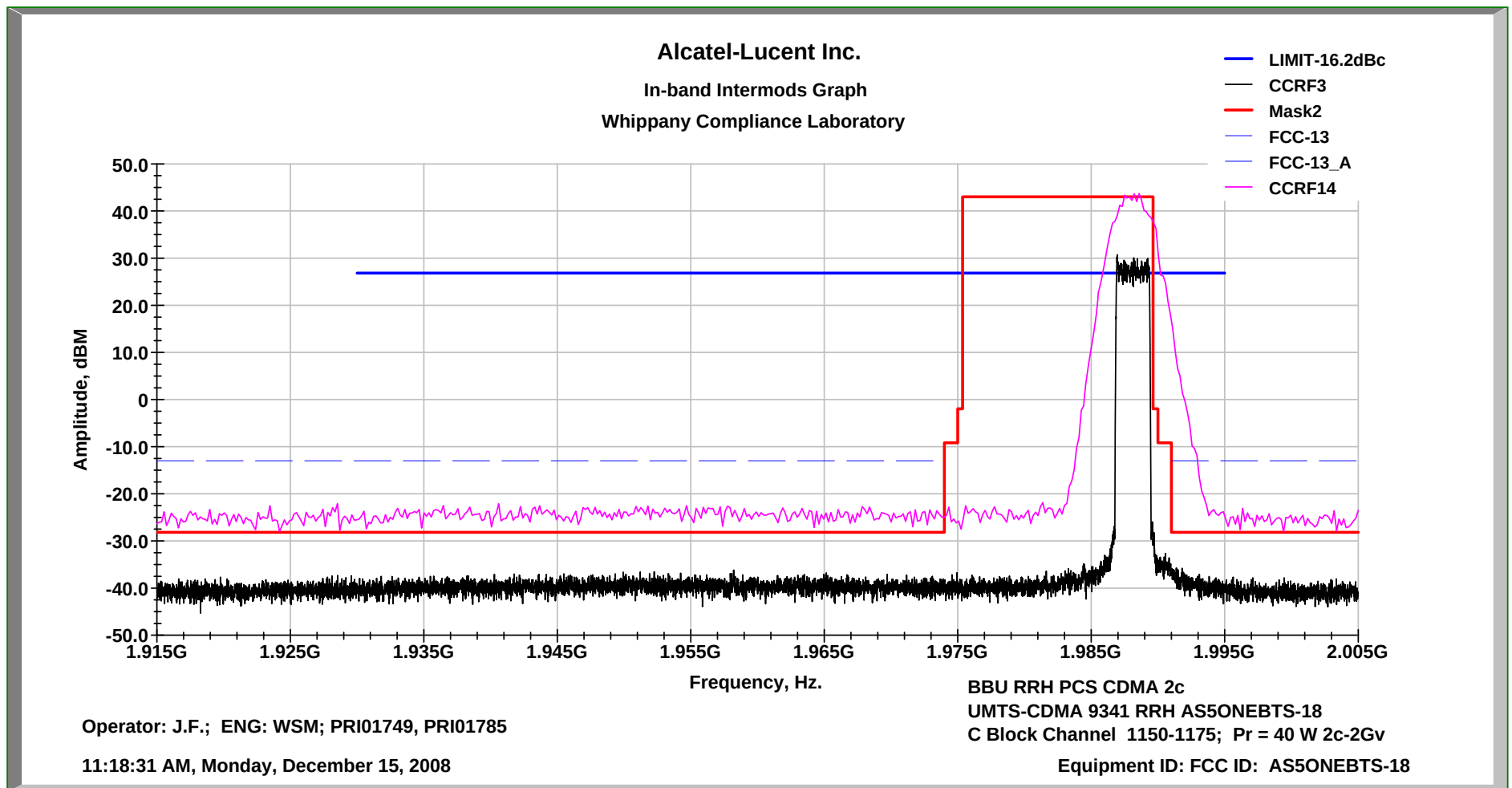


In-band Inter-modulation PCS Ch C1-925, 950 2c 2Gv w/EDPD 40W Total

Occupied Bandwidth

PCS Ch C5-1150, 1175 2c 2Gv w/EDPD 40W Total



In-band Inter-modulation PCS Ch C5-1150, 1175 2c 2Gv w/EDPD 40W Total

**Three Carrier Occupied Bandwidth Data
PCS Blocks A, D, B, E, F, and C
Left side of Block, Right side of Block
And
PCS Band Intermodulation**

Occupied Bandwidth

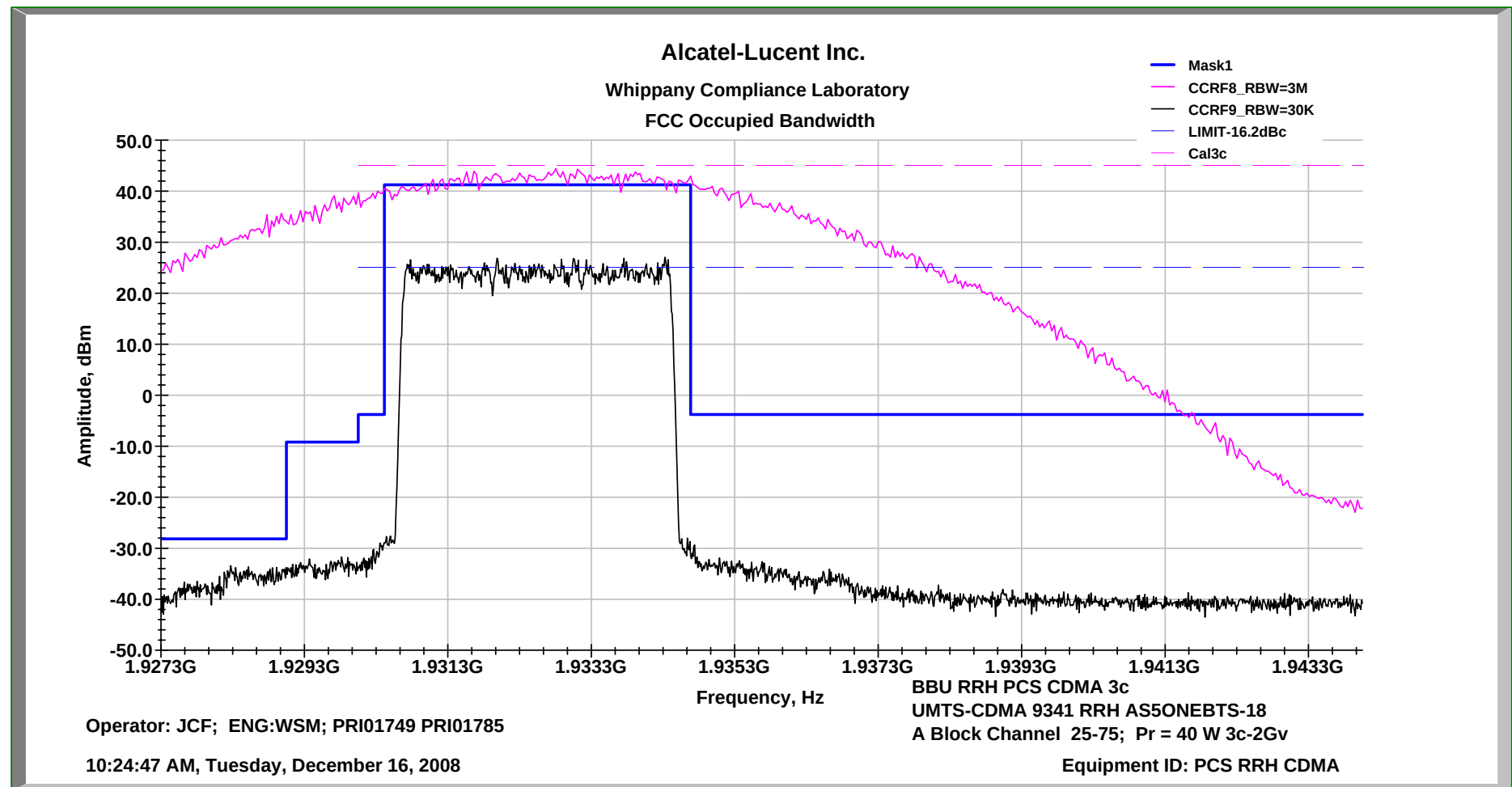
PCS Ch A1 25-75

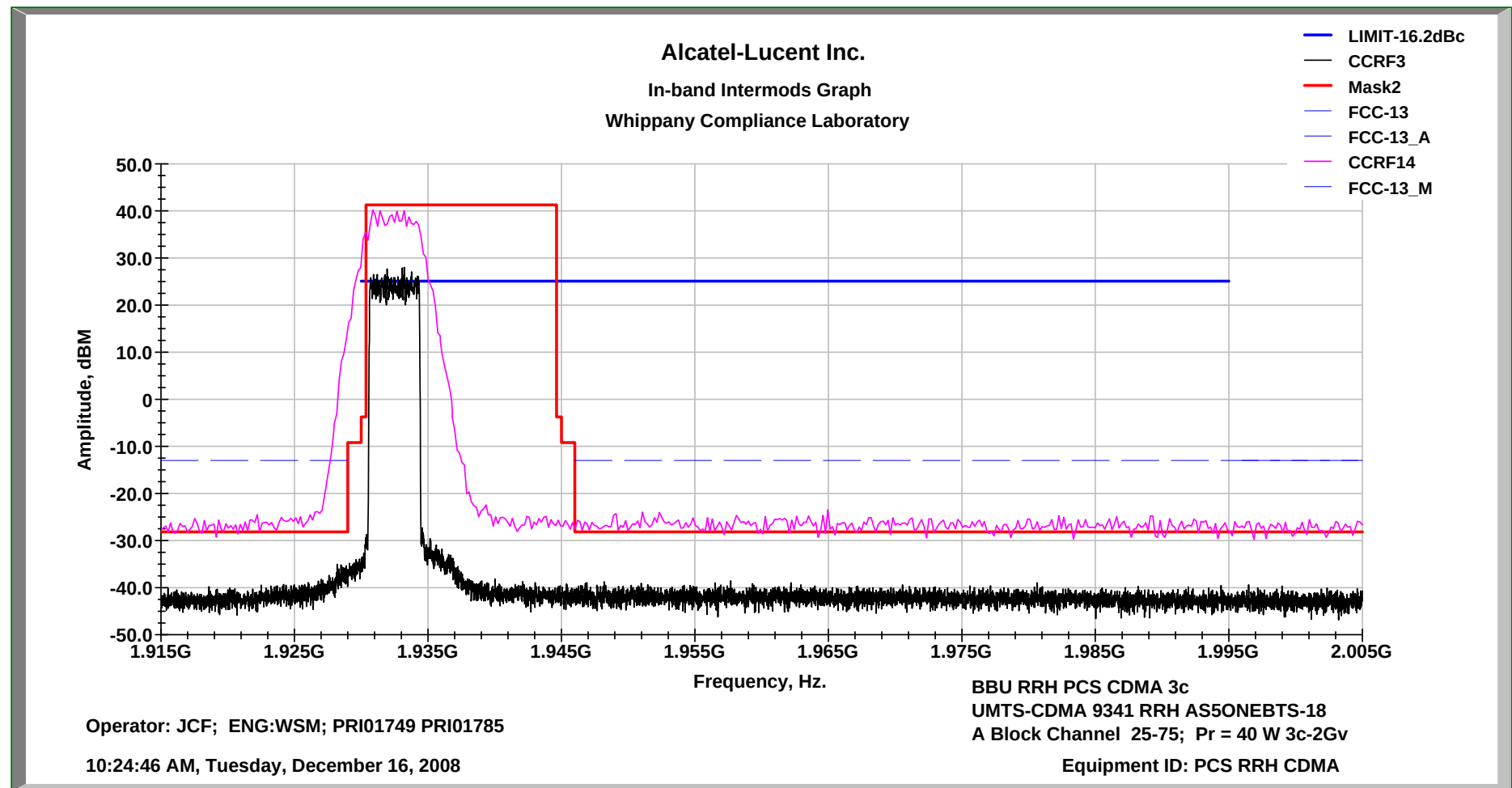
3c

2Gv

w/EDPD

40W Total



In-band Inter-modulation PCS Ch A1 25-75 3c 2Gv w/EDPD 40W Total

Occupied Bandwidth

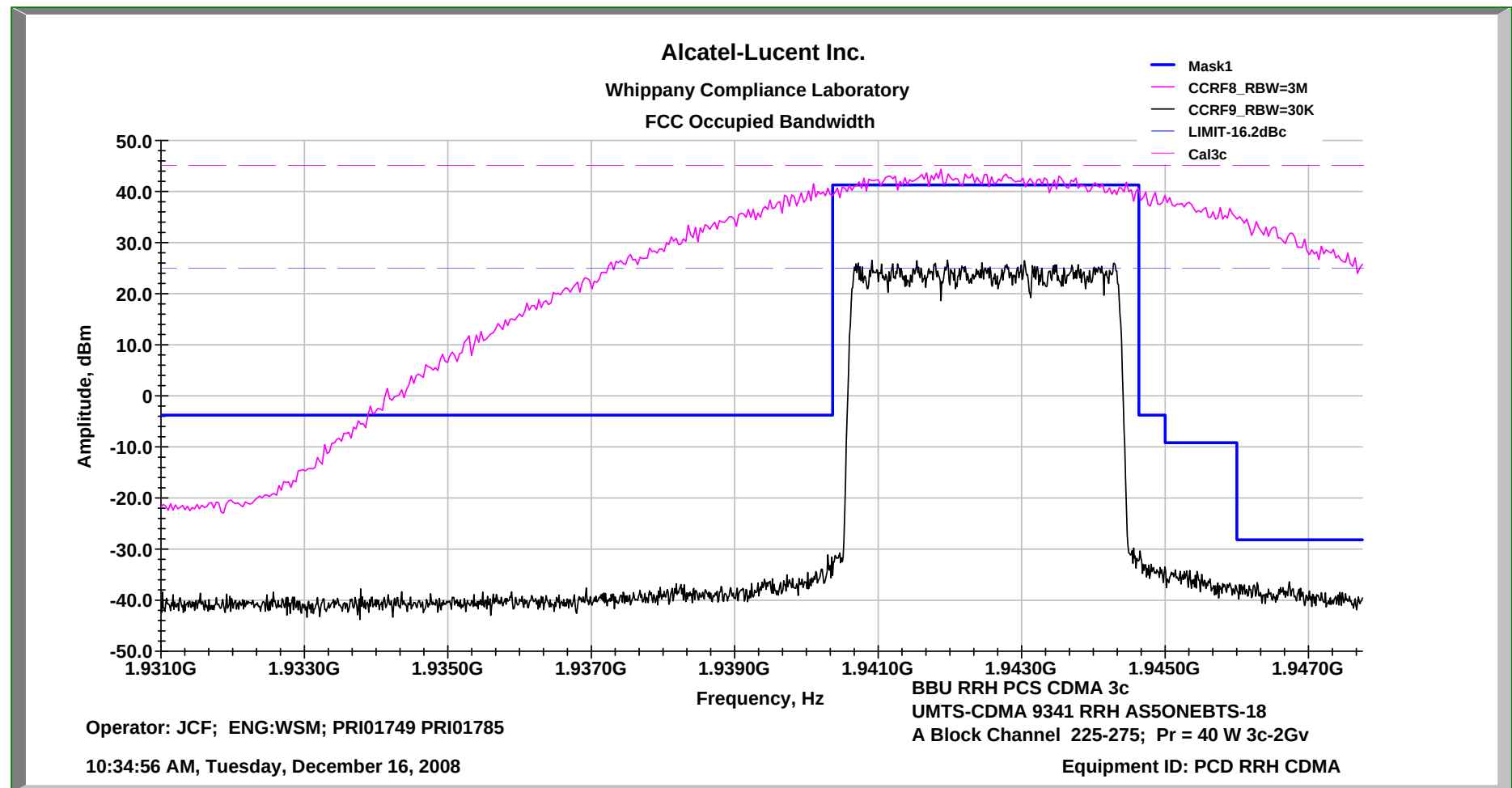
PCS Ch A5 225-275

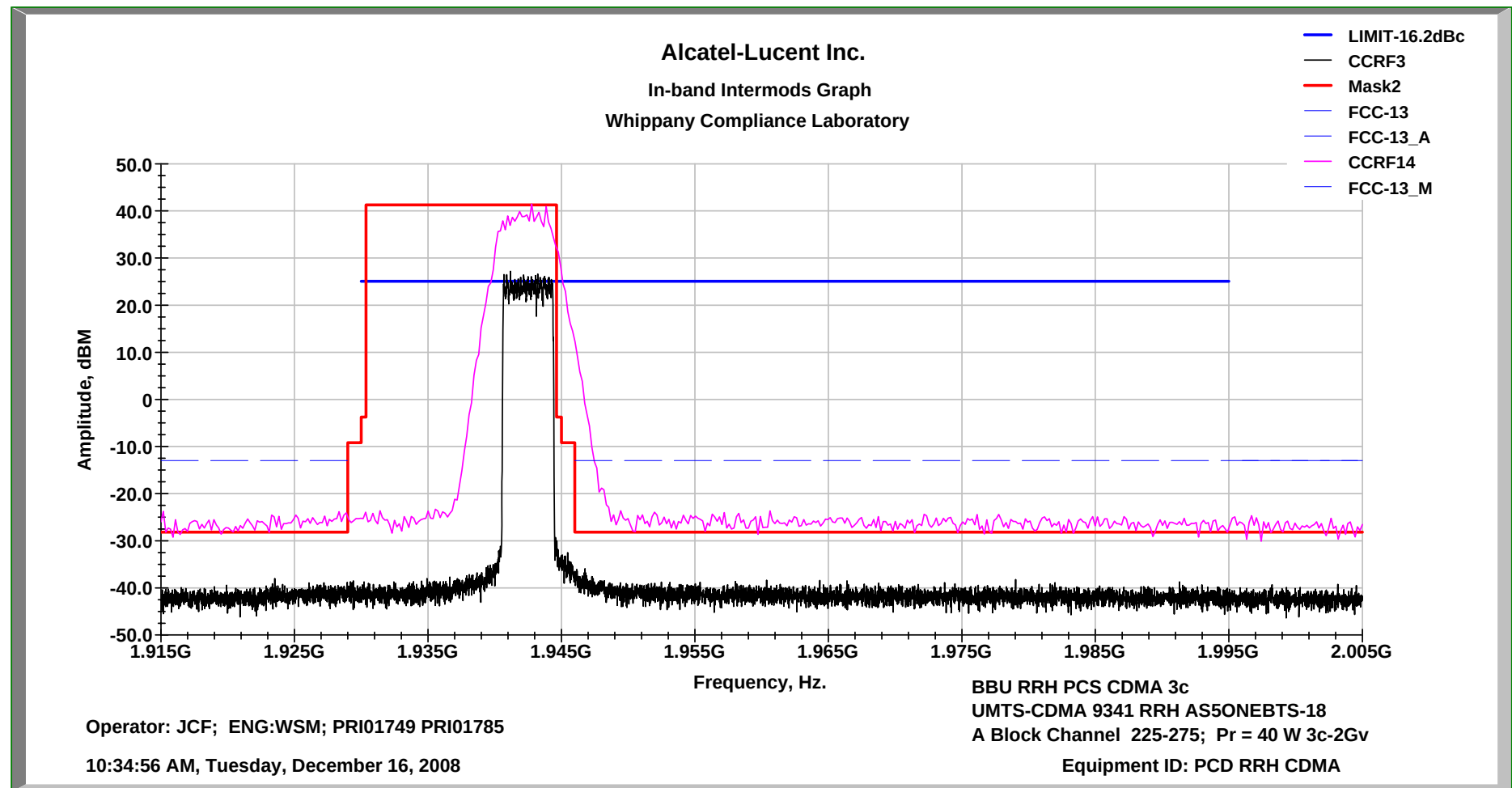
3c

2Gv

w/EDPD

40W Total



In-band Inter-modulation PCS Ch A5 225-275 3c 2Gv w/EDPD 40W Total

Occupied Bandwidth

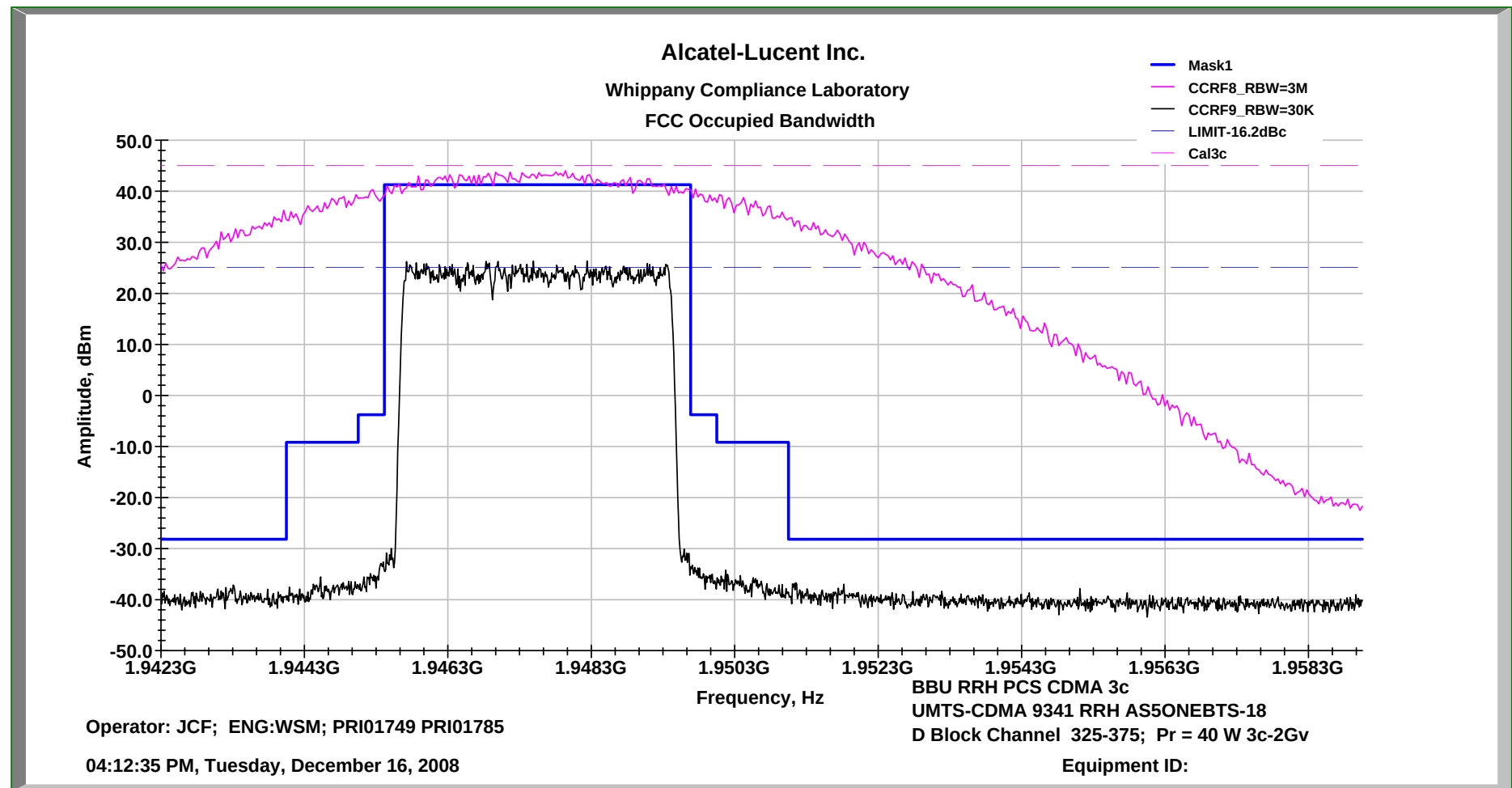
PCS Ch D 325-375

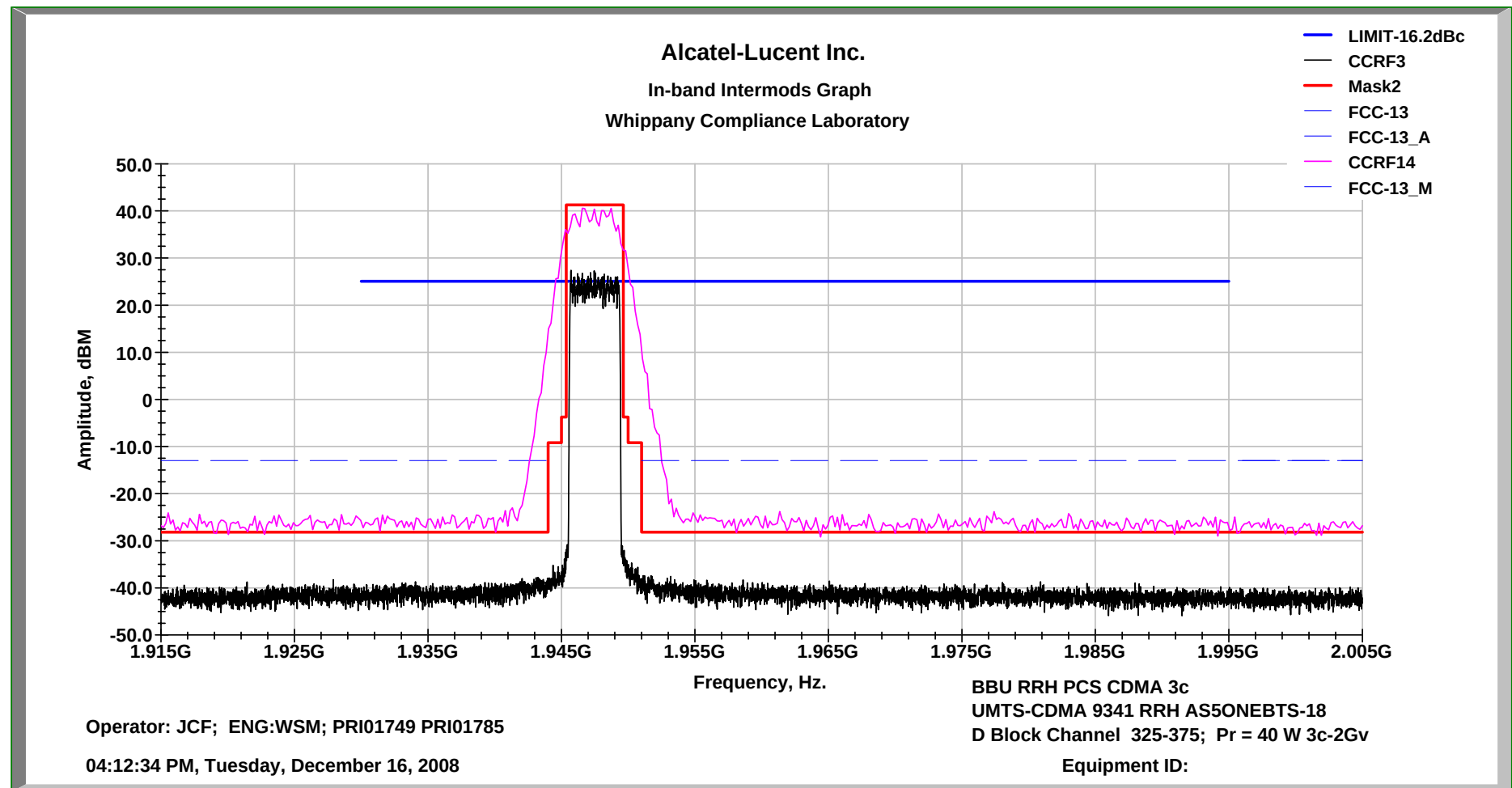
3c

2Gv

w/EDPD

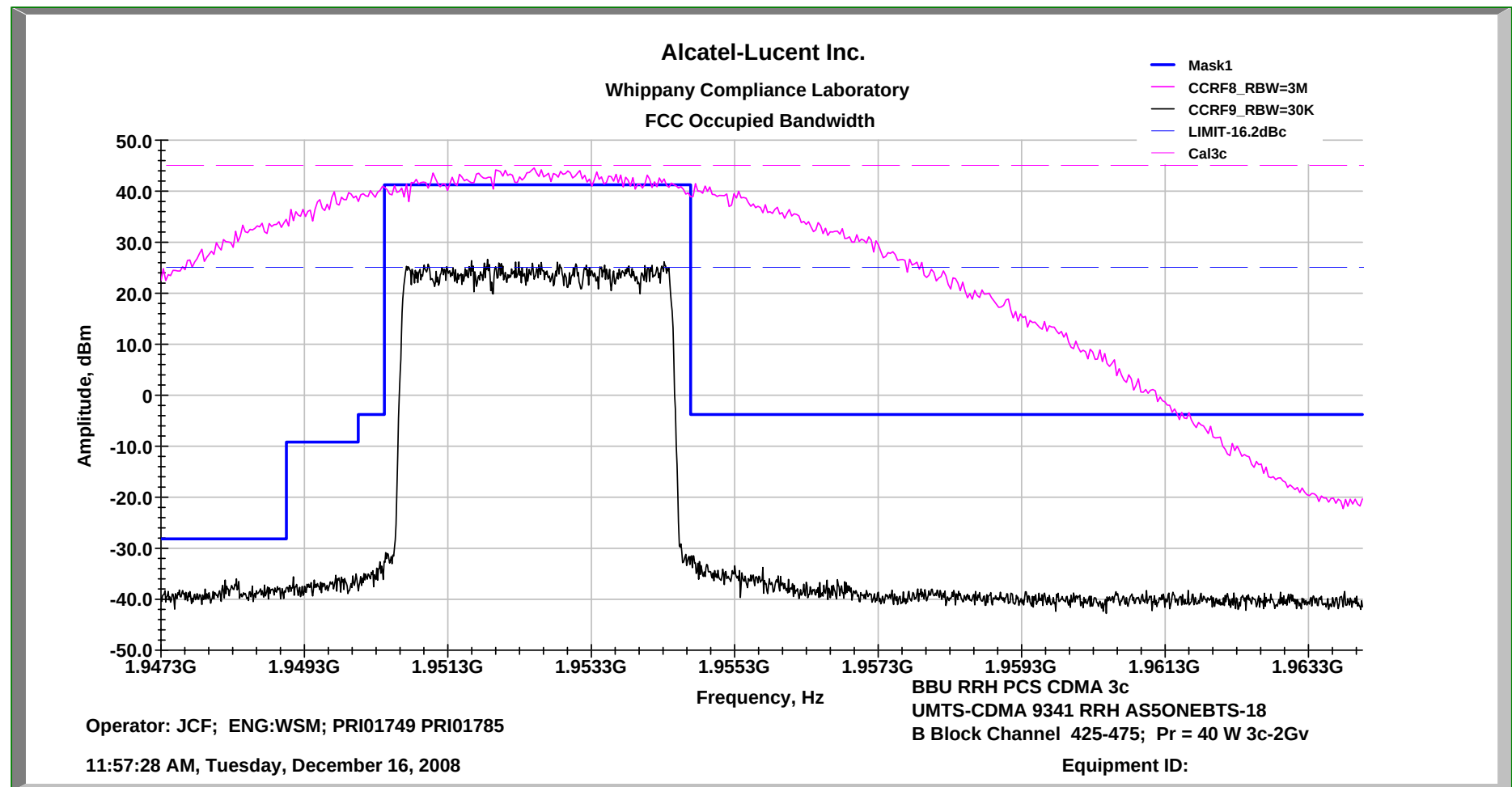
40W Total

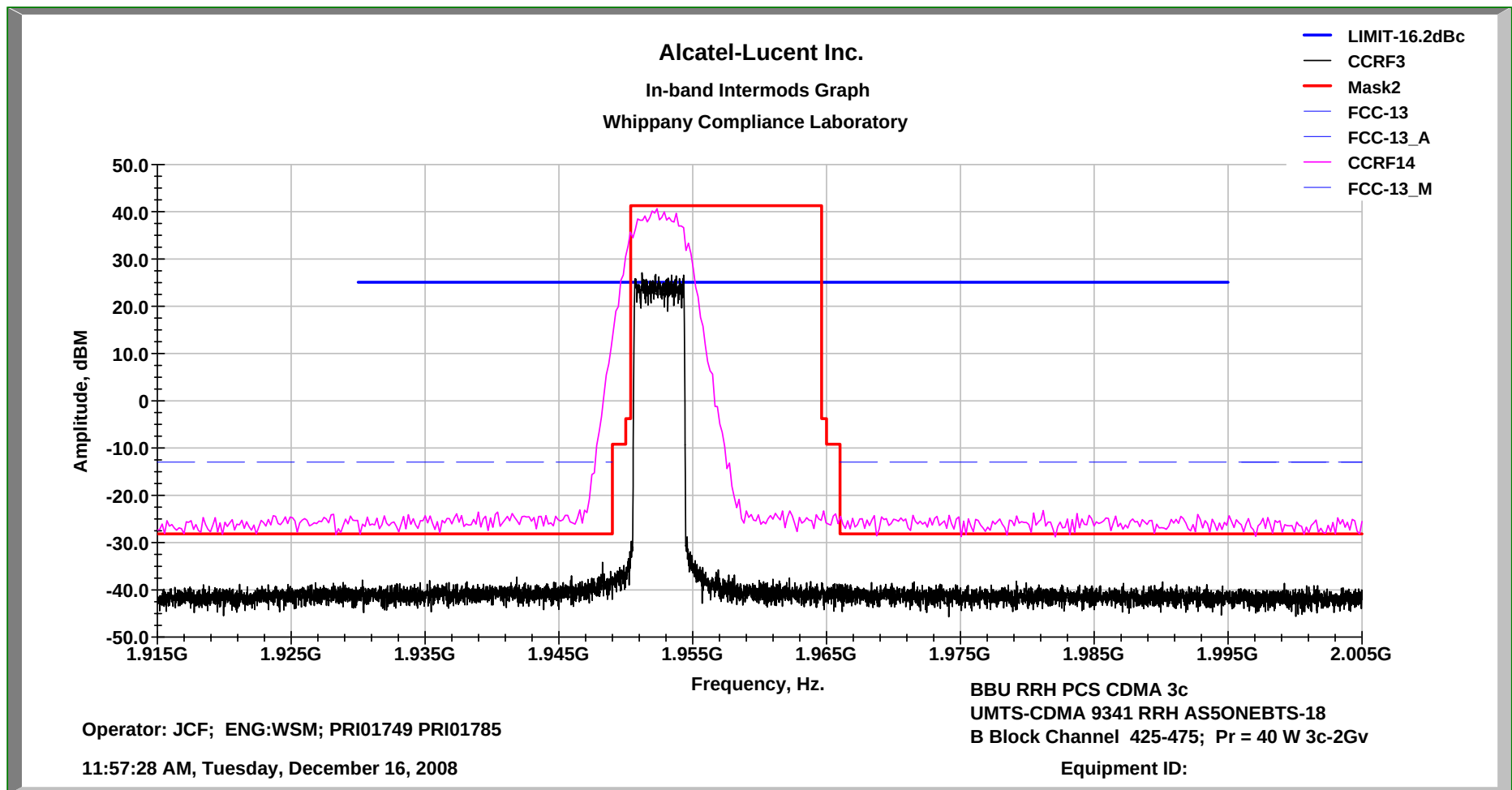


In-band Inter-modulation PCS Ch D 325-375 3c 2Gv w/EDPD 40W Total

Occupied Bandwidth

PCS Ch B1 425 - 475 3c 2Gv w/EDPD 40W Total



In-band Inter-modulation PCS Ch B1 425 - 475 3c 2Gv w/EDPD 40W Total

Occupied Bandwidth

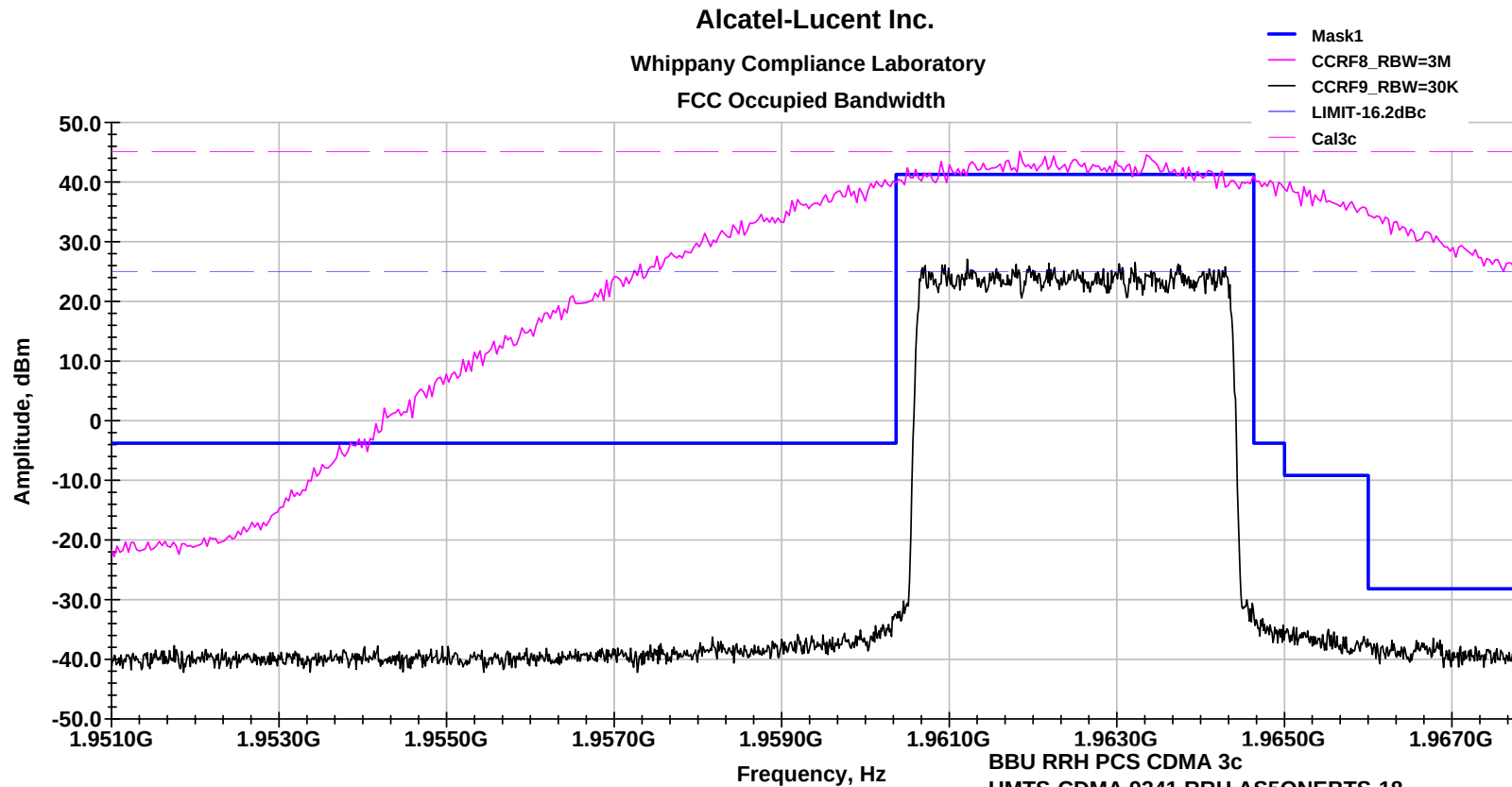
PCS Ch B5 625-675

3c

2Gv

w/EDPD

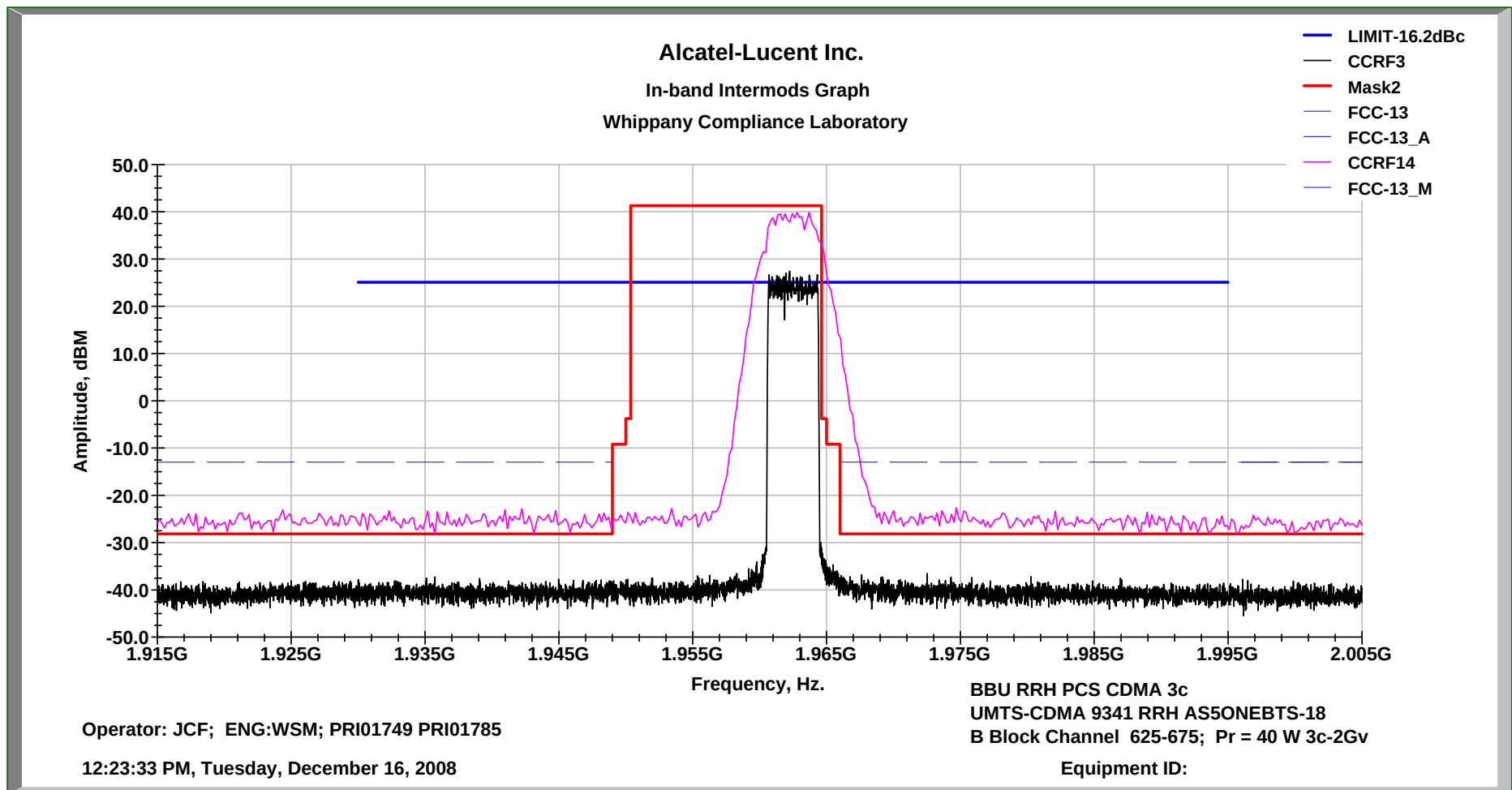
40W Total



Operator: JCF; ENG:WSM; PRI01749 PRI01785

12:23:33 PM, Tuesday, December 16, 2008

Equipment ID:

In-band Inter-modulation PCS Ch B5 625-675 3c 2Gv w/EDPD 40W Total

Occupied Bandwidth

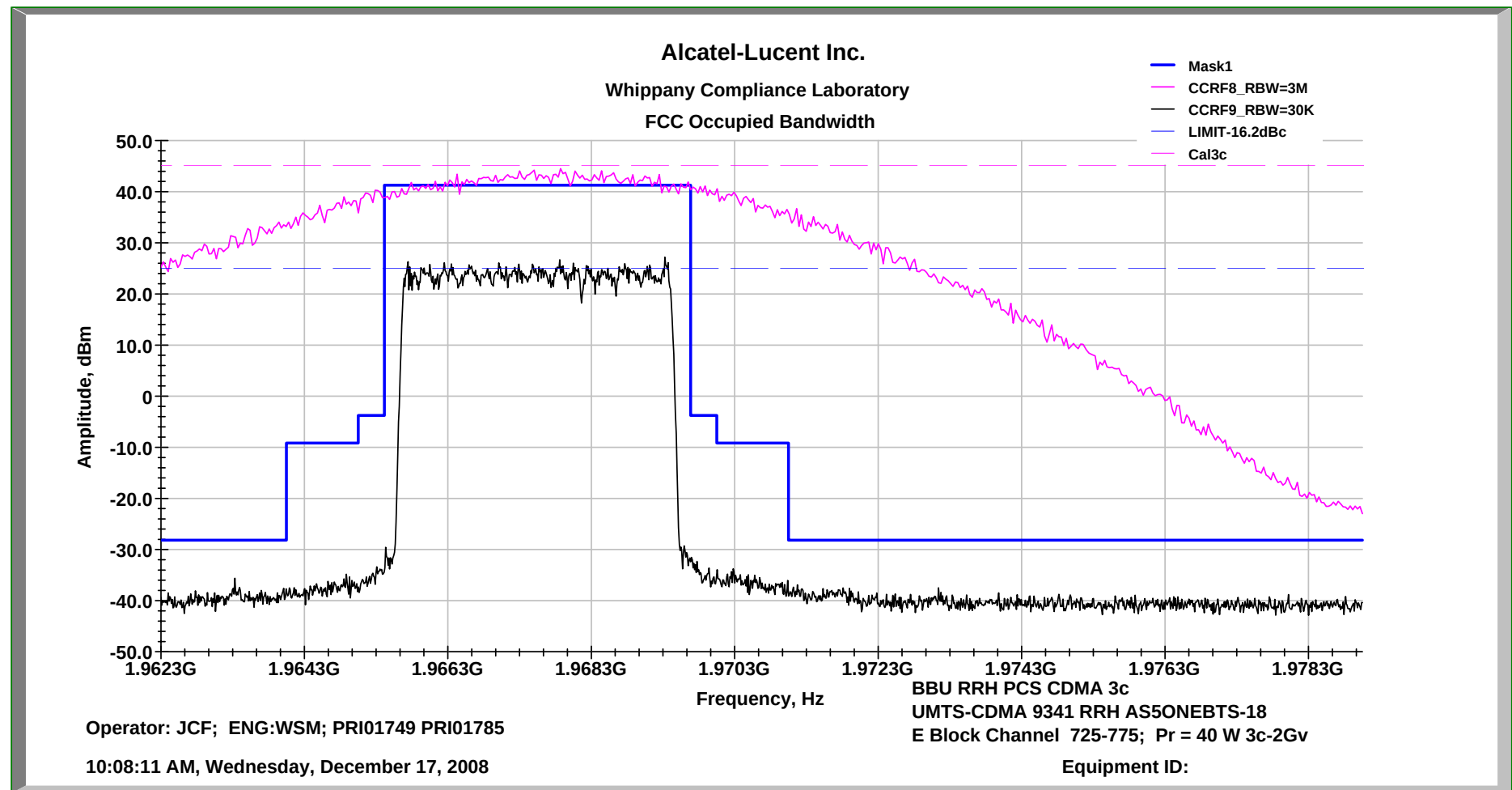
PCS Ch E-725-775

3c

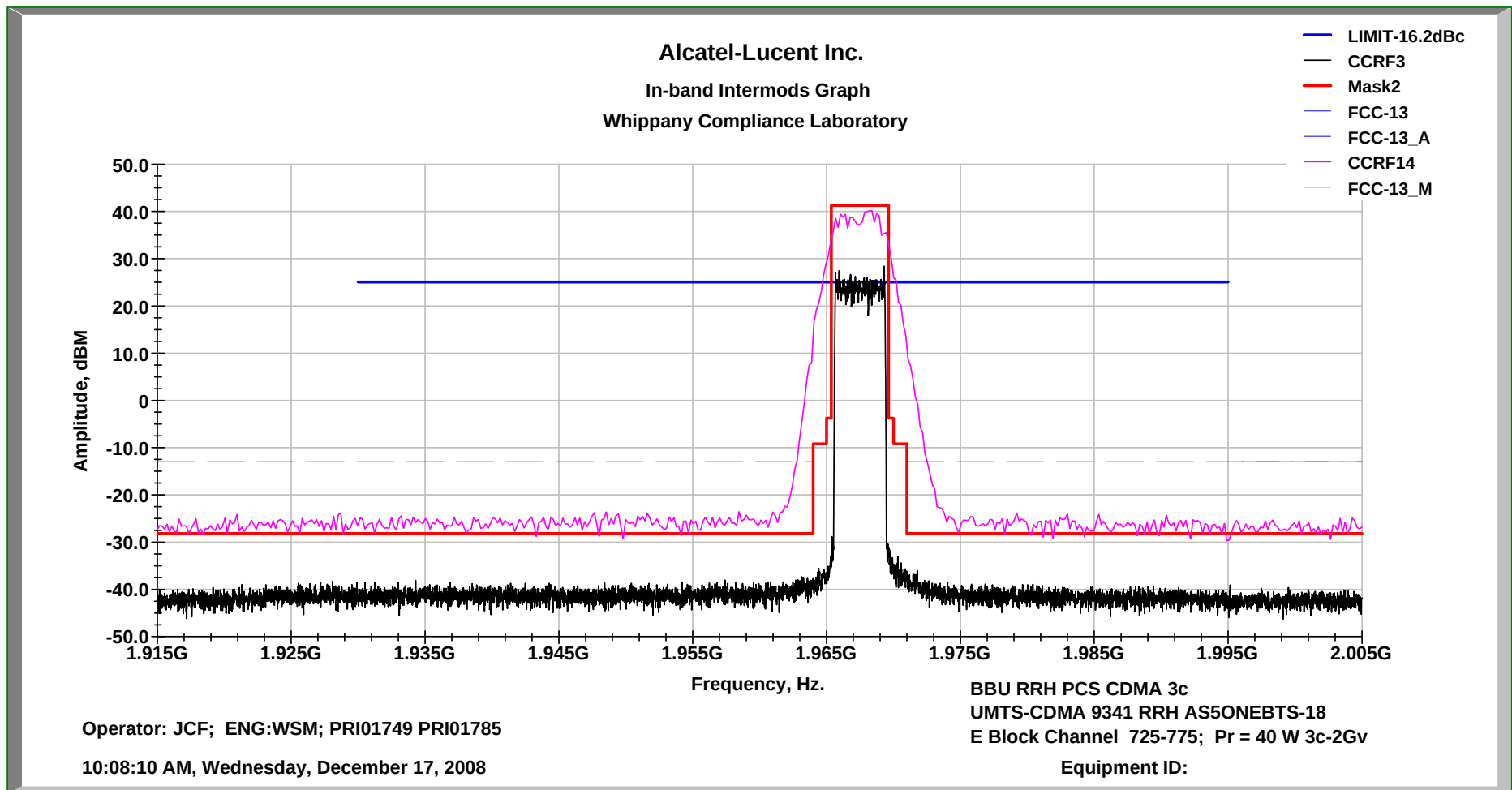
2Gv

w/EDPD

40W Total



In-band Inter-modulation PCS Ch E 725-775 3c 2Gv w/EDPD 40W Total



Occupied Bandwidth

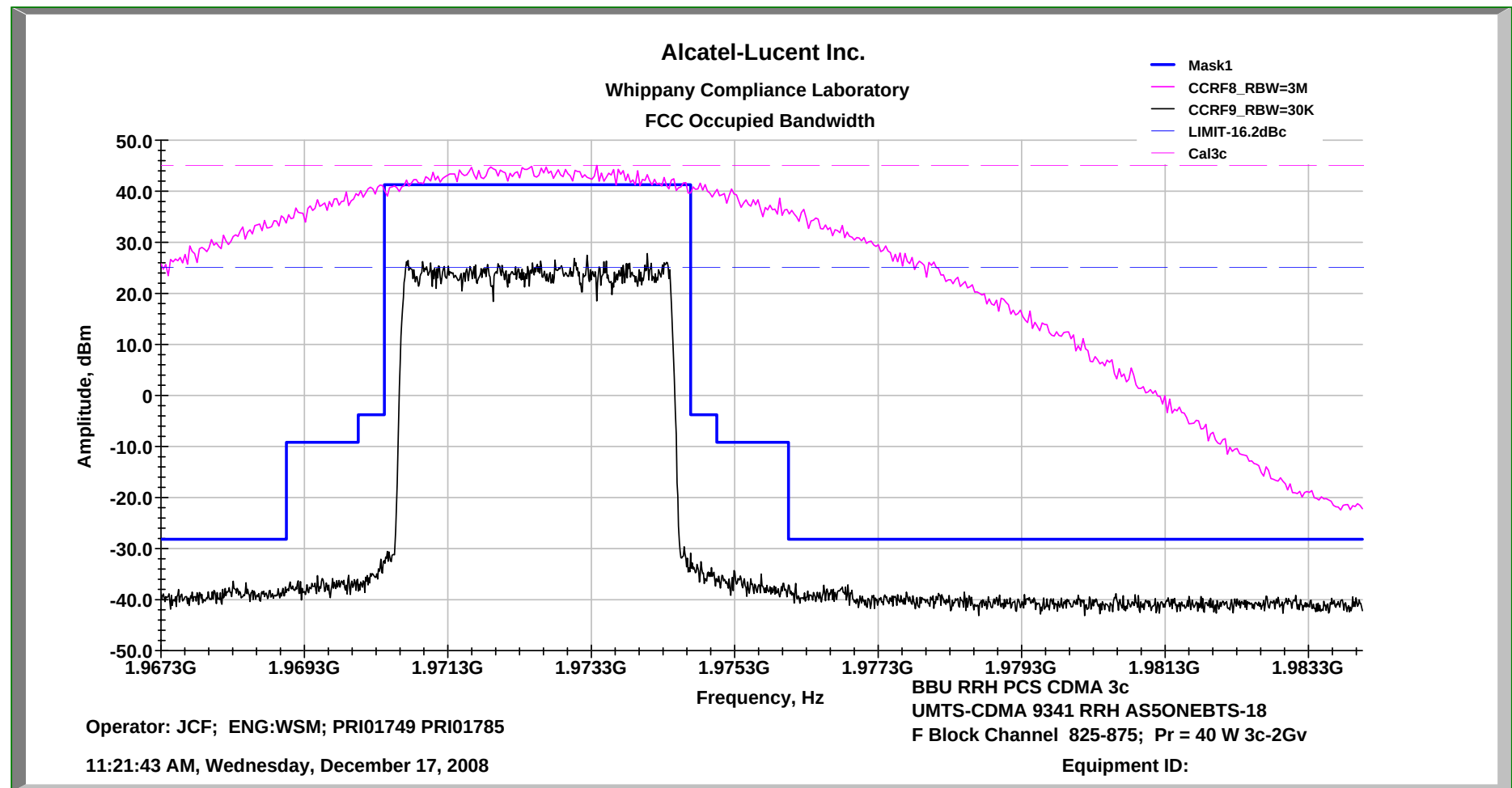
PCS Ch F 825-875

3c

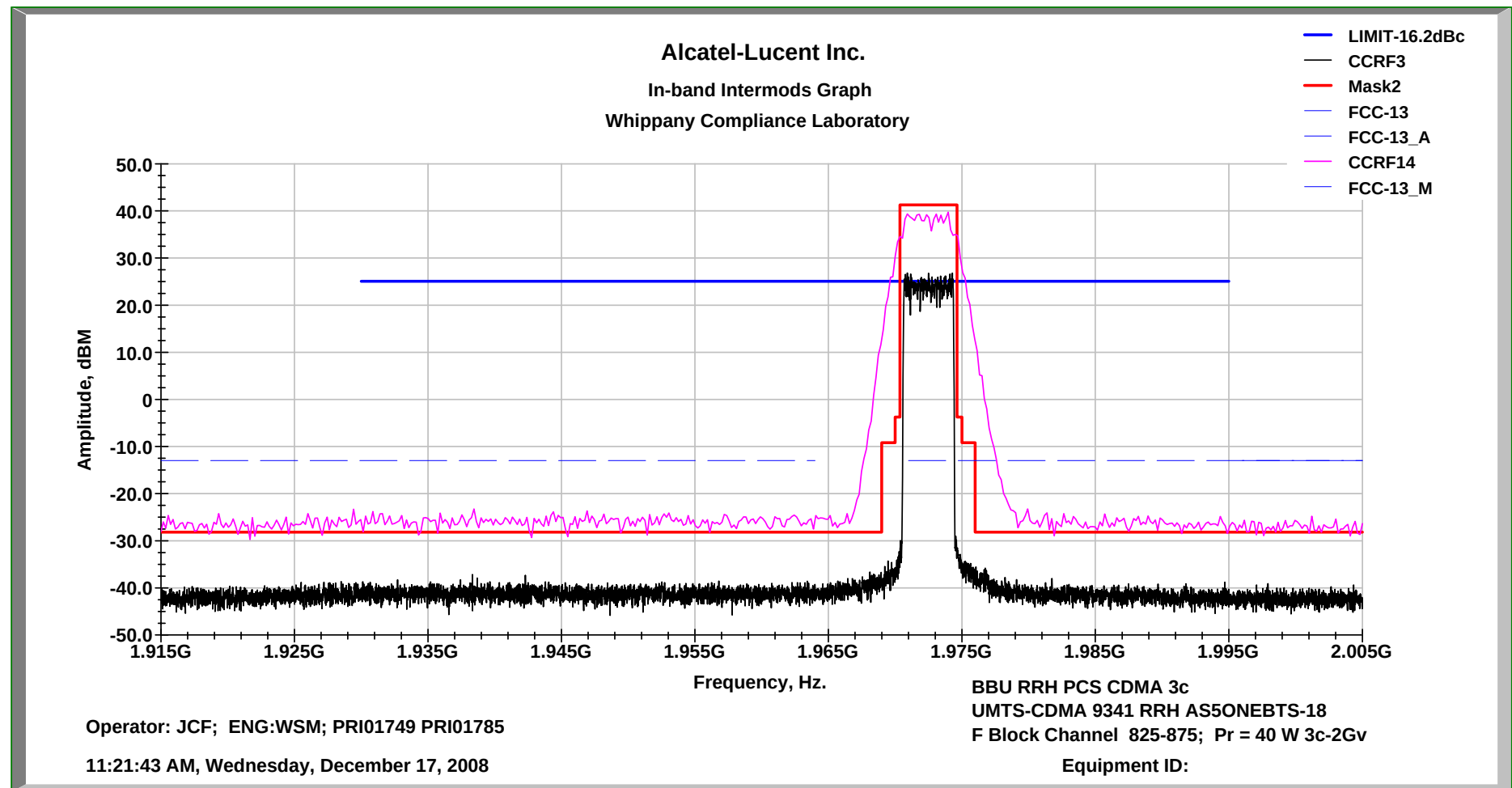
2Gv

w/EDPD

40W Total



In-band Inter-modulation PCS Ch F 825-875 3c 2Gv w/EDPD 40W Total



Occupied Bandwidth

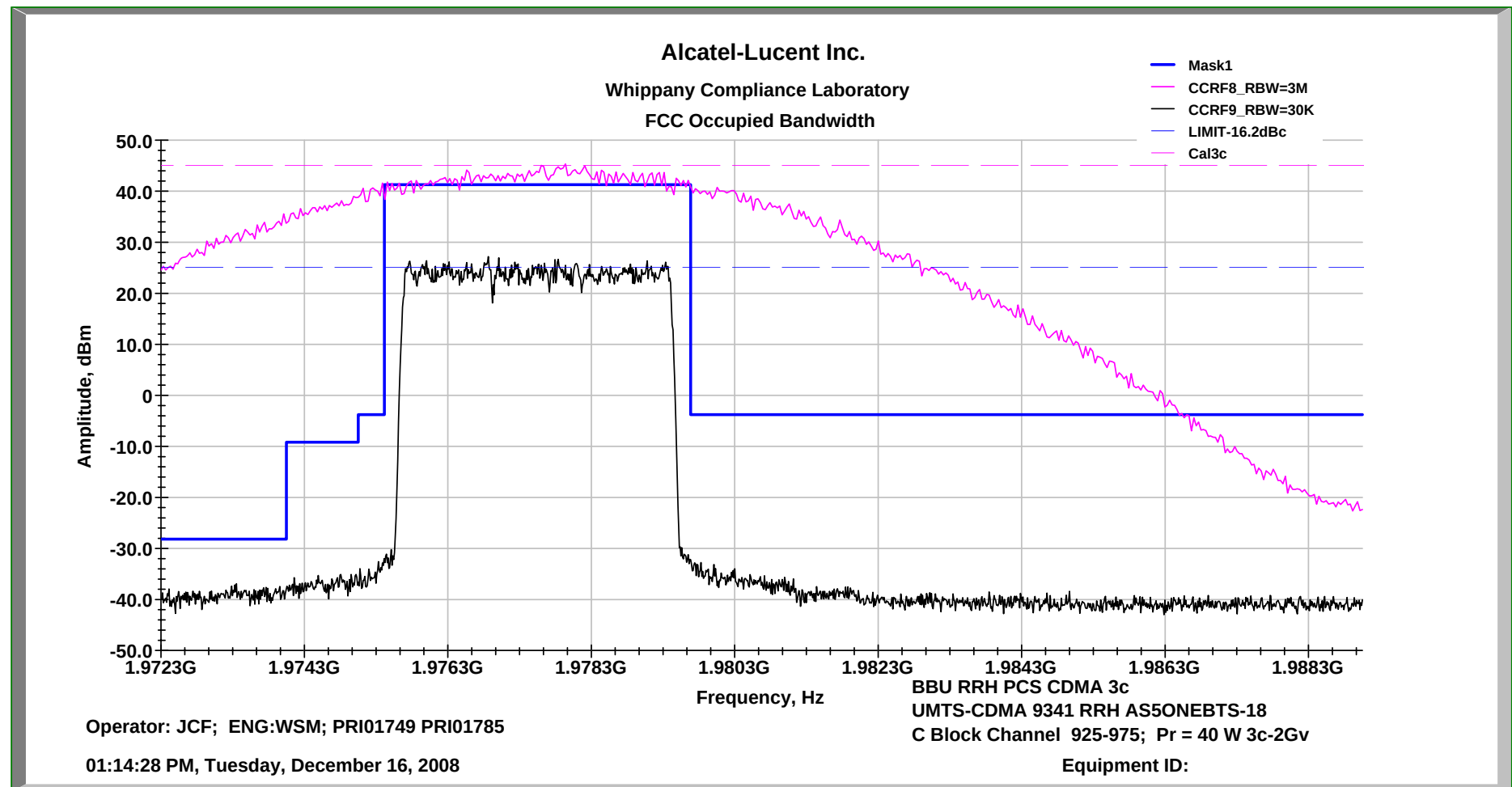
PCS Ch C1 925-975

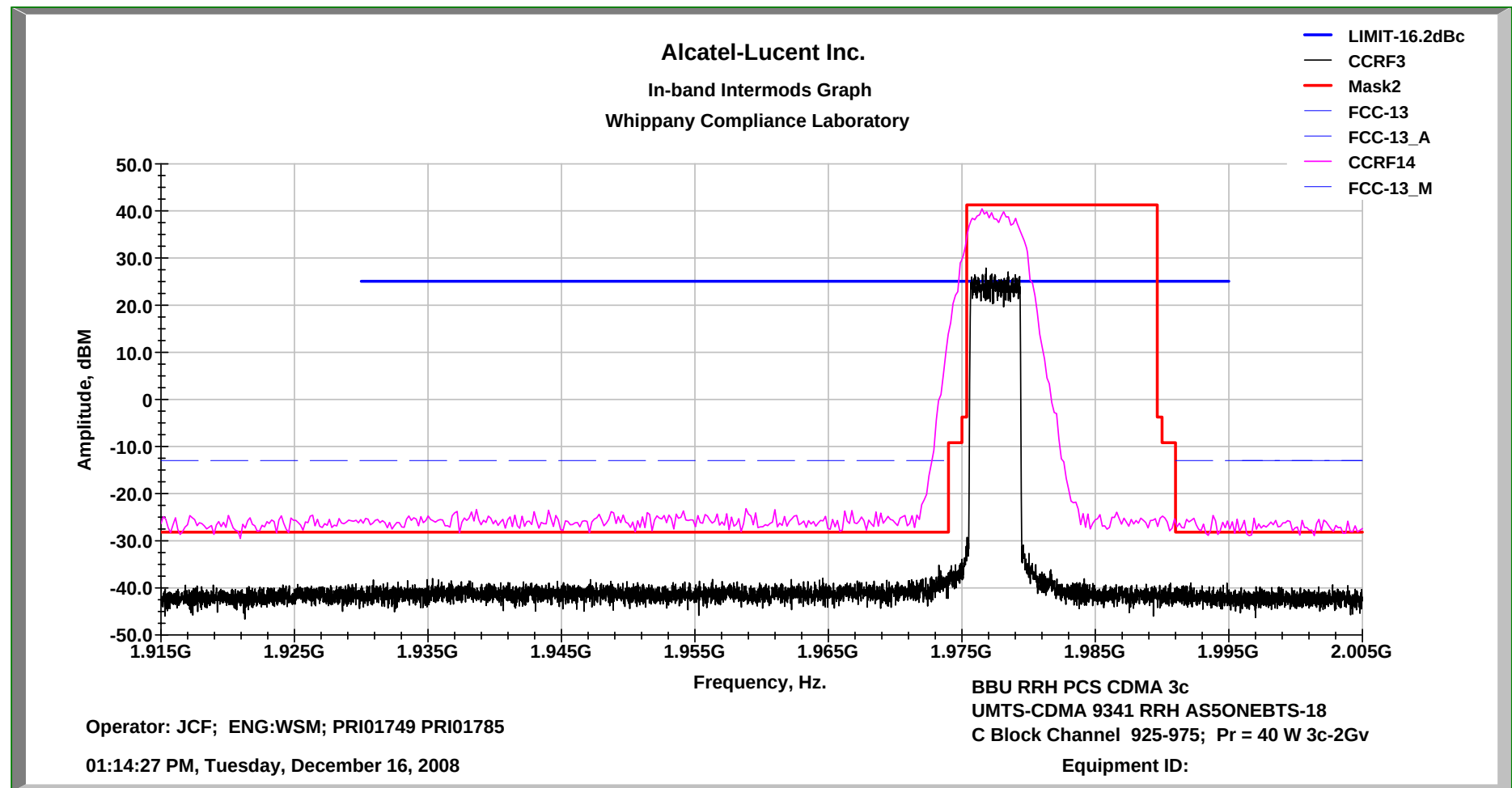
3c

2Gv

w/EDPD

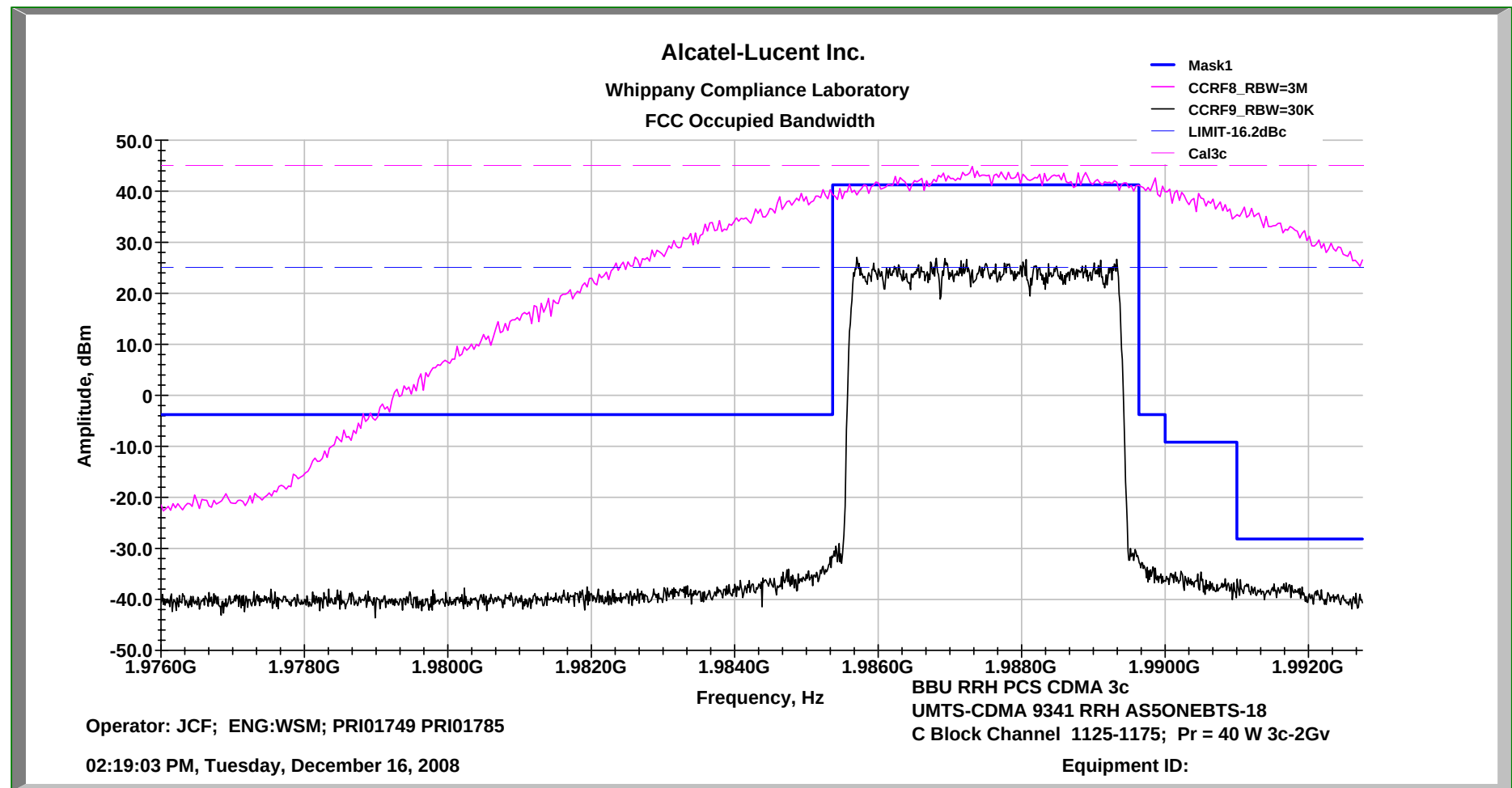
40W Total

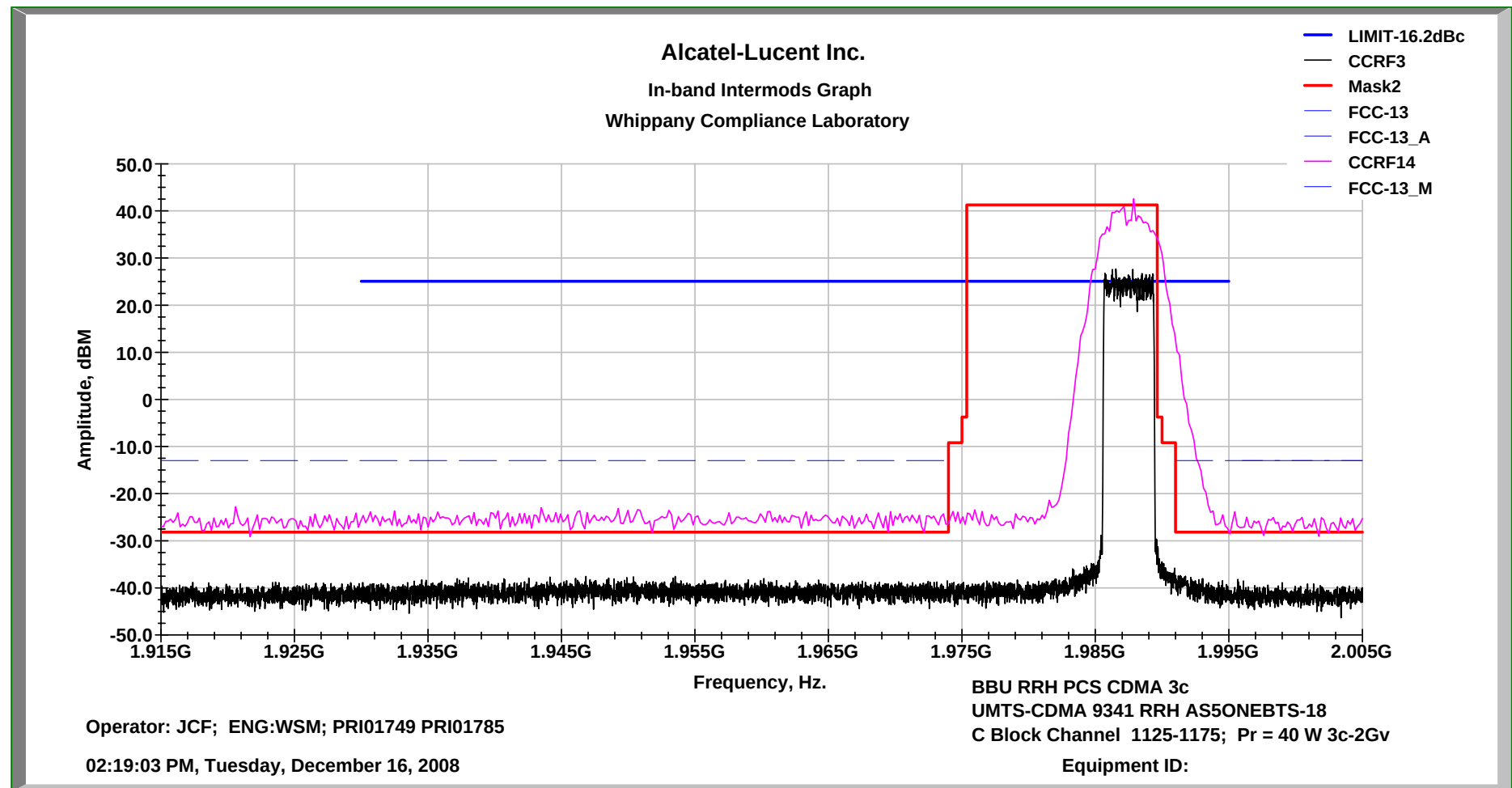


In-band Inter-modulation PCS Ch C1 925-975 3c 2Gv w/EDPD 40W Total

Occupied Bandwidth

PCS Ch C5 1125-1175 3c 2Gv w/EDPD 40W Total



In-band Inter-modulation PCS Ch C5 1125-1175 3c 2Gv w/EDPD 40W Total

**Four Carrier Occupied Bandwidth Data
PCS Blocks A, D, B, E, F, and C
Left side of Block, Right side of Block
And
PCS Band Intermodulation**

Occupied Bandwidth

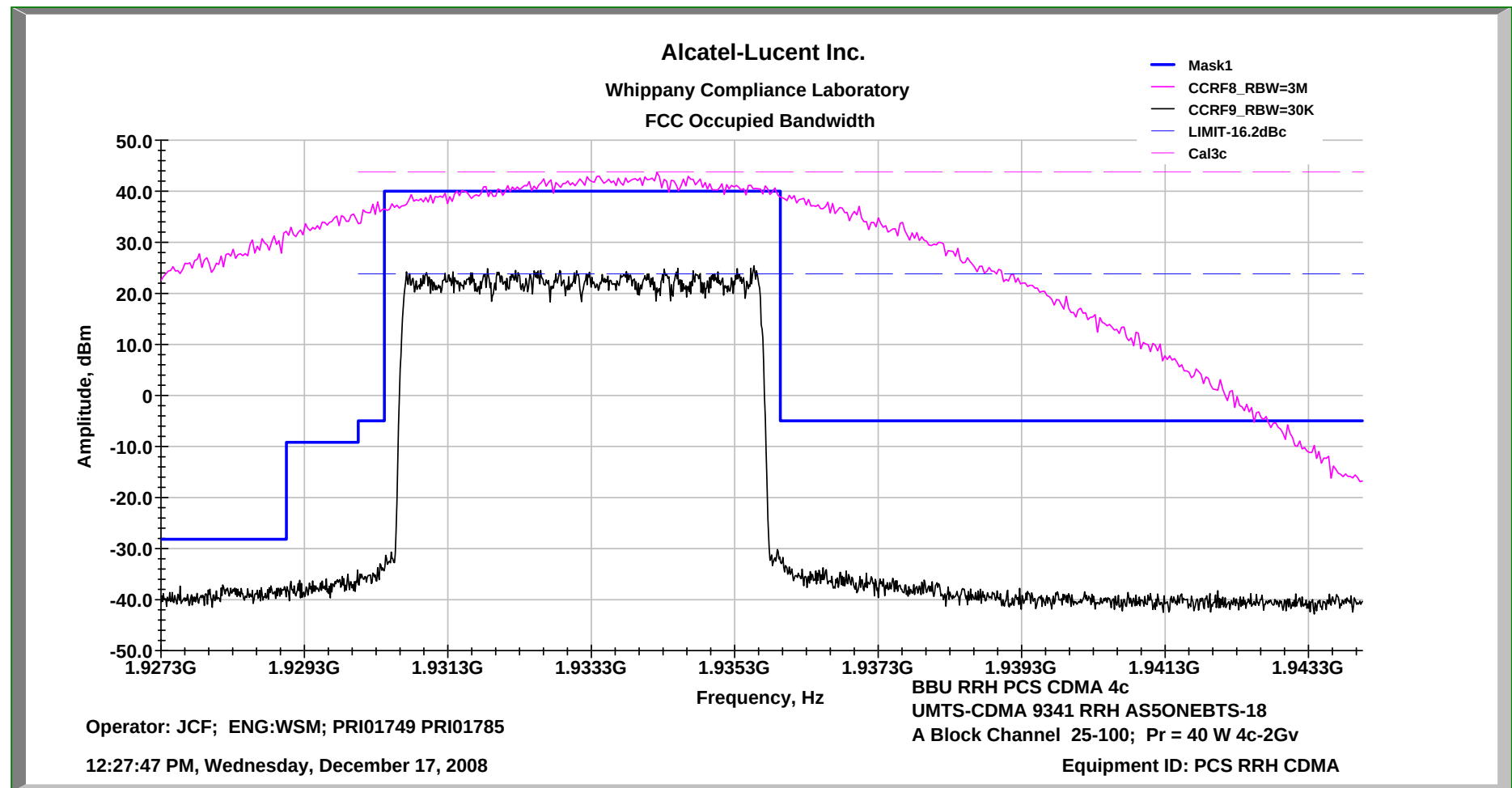
PCS Ch A1 25-100

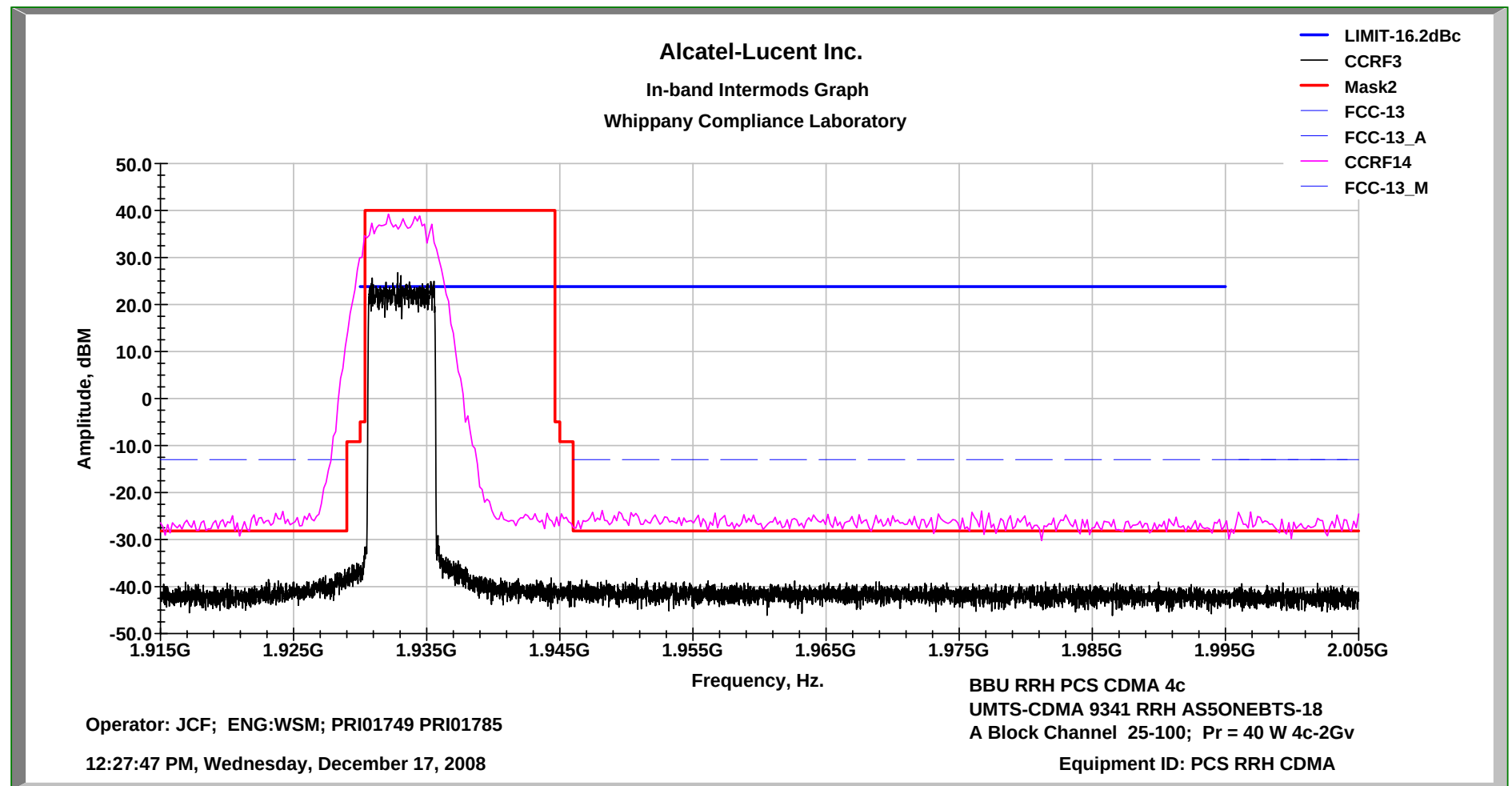
4c

2Gv

w/EDPD

40W Total



In-band Inter-modulation PCS Ch A1 25-100 4c 2Gv w/EDPD 40W Total

Occupied Bandwidth

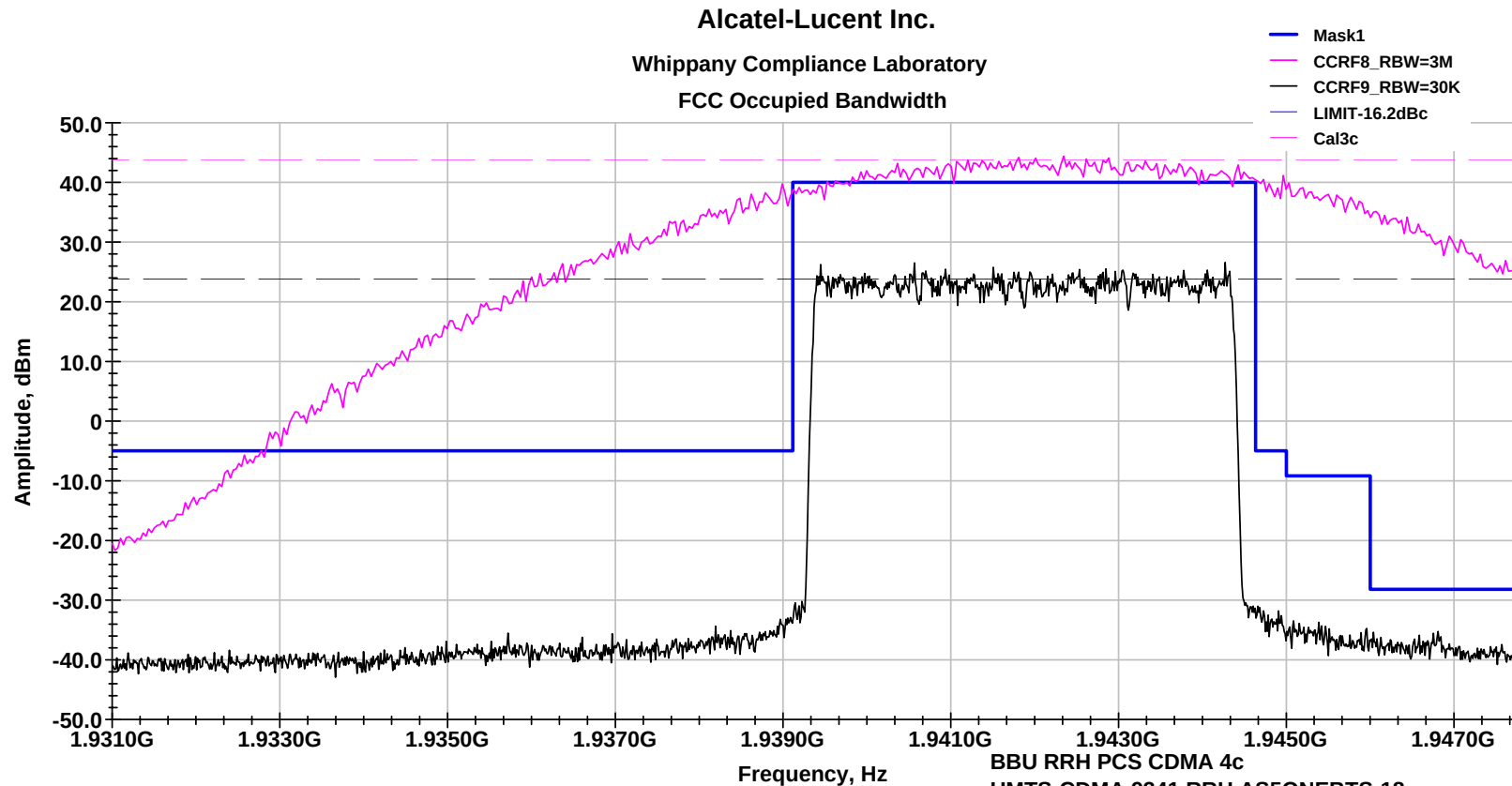
PCS Ch A5 200-275

4c

2Gv

w/EDPD

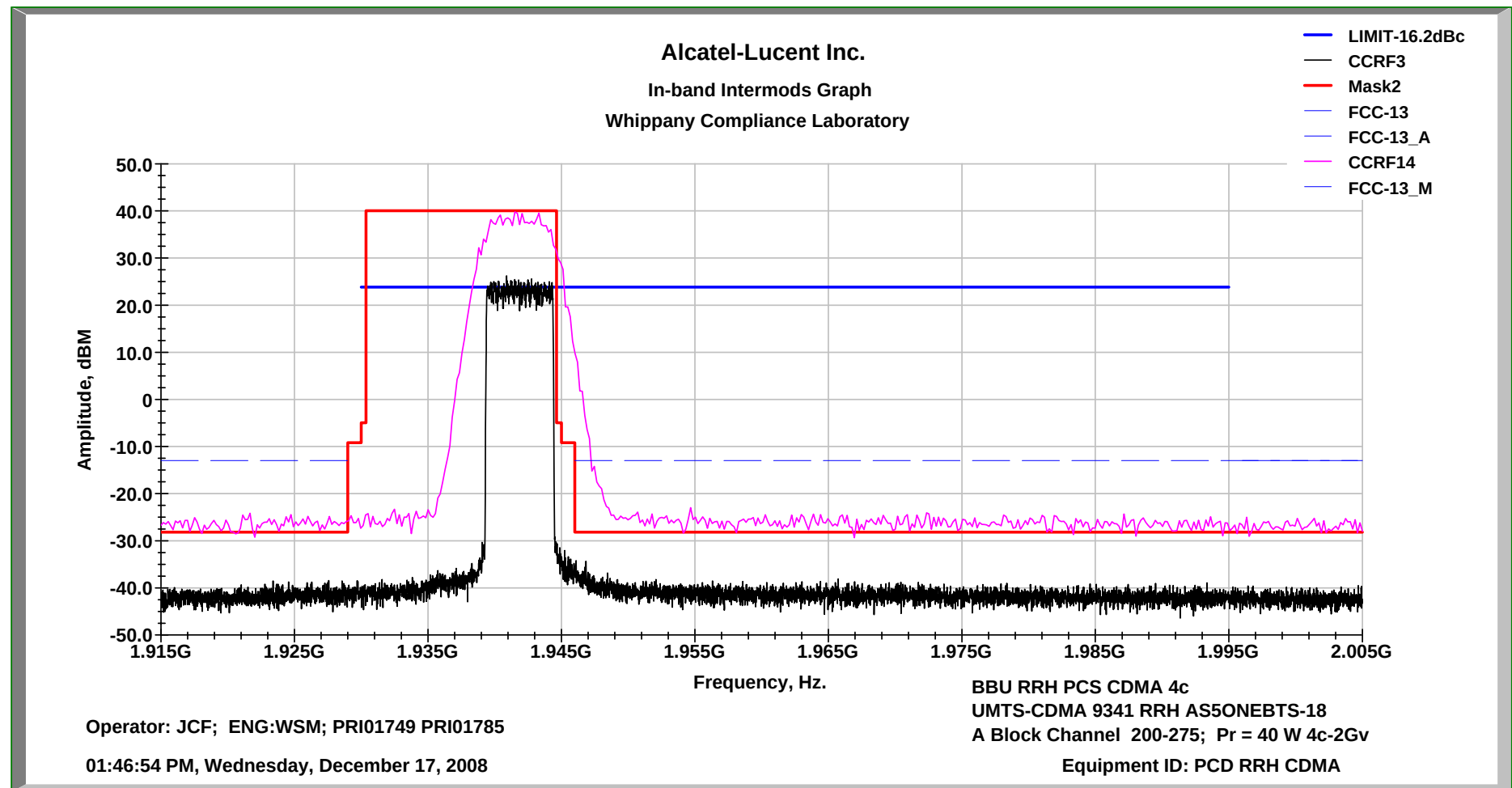
40W Total



Operator: JCF; ENG:WSM; PRI01749 PRI01785

01:46:54 PM, Wednesday, December 17, 2008

Equipment ID: PCD RRH CDMA

In-band Inter-modulation PCS Ch A5 200-275 4c 2Gv w/EDPD 40W Total

Occupied Bandwidth

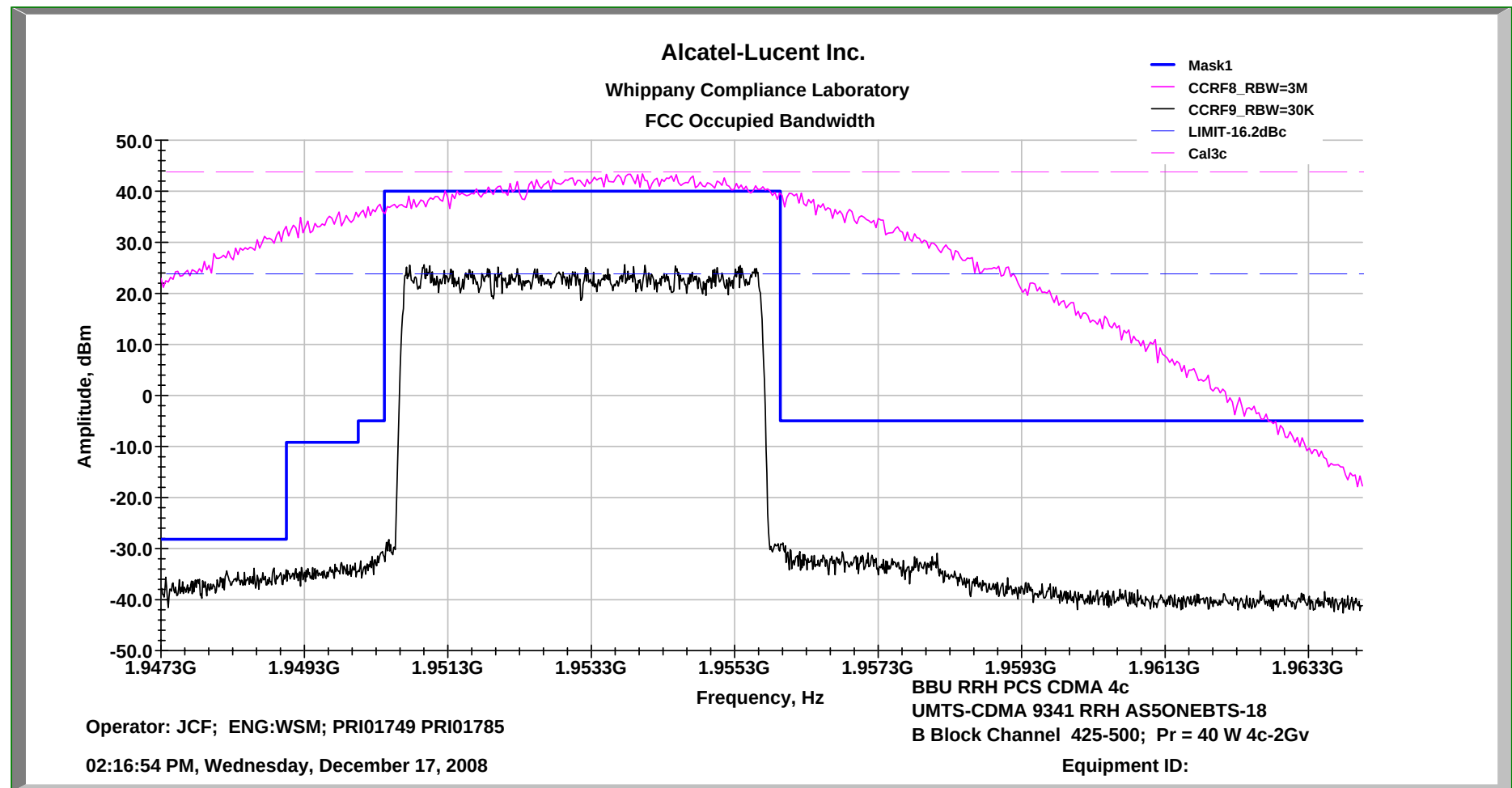
PCS Ch B1 425-500

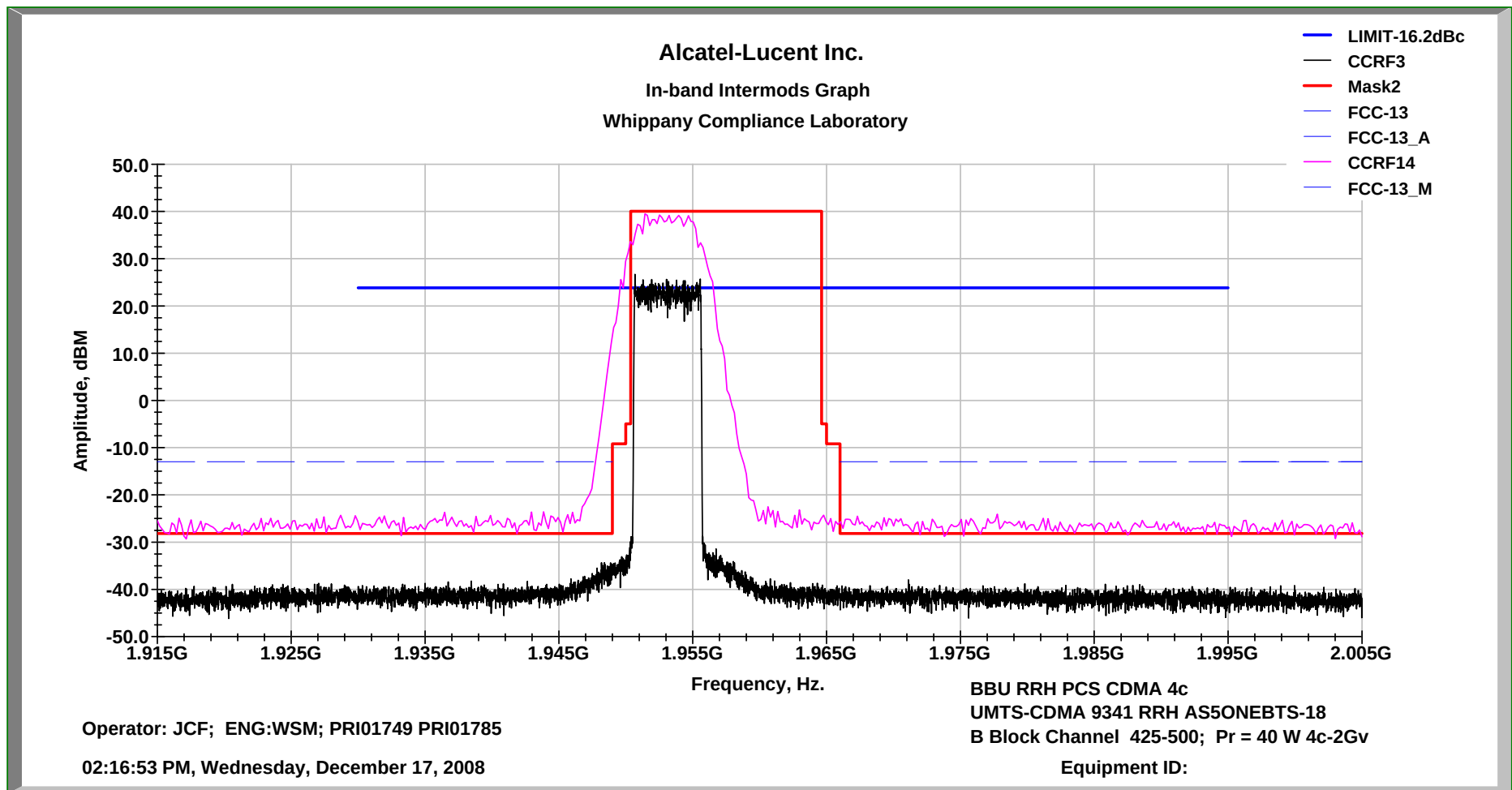
4c

2Gv

w/EDPD

40W Total



In-band Inter-modulation PCS Ch B1 425-500 4c 2Gv w/EDPD 40W Total

Occupied Bandwidth

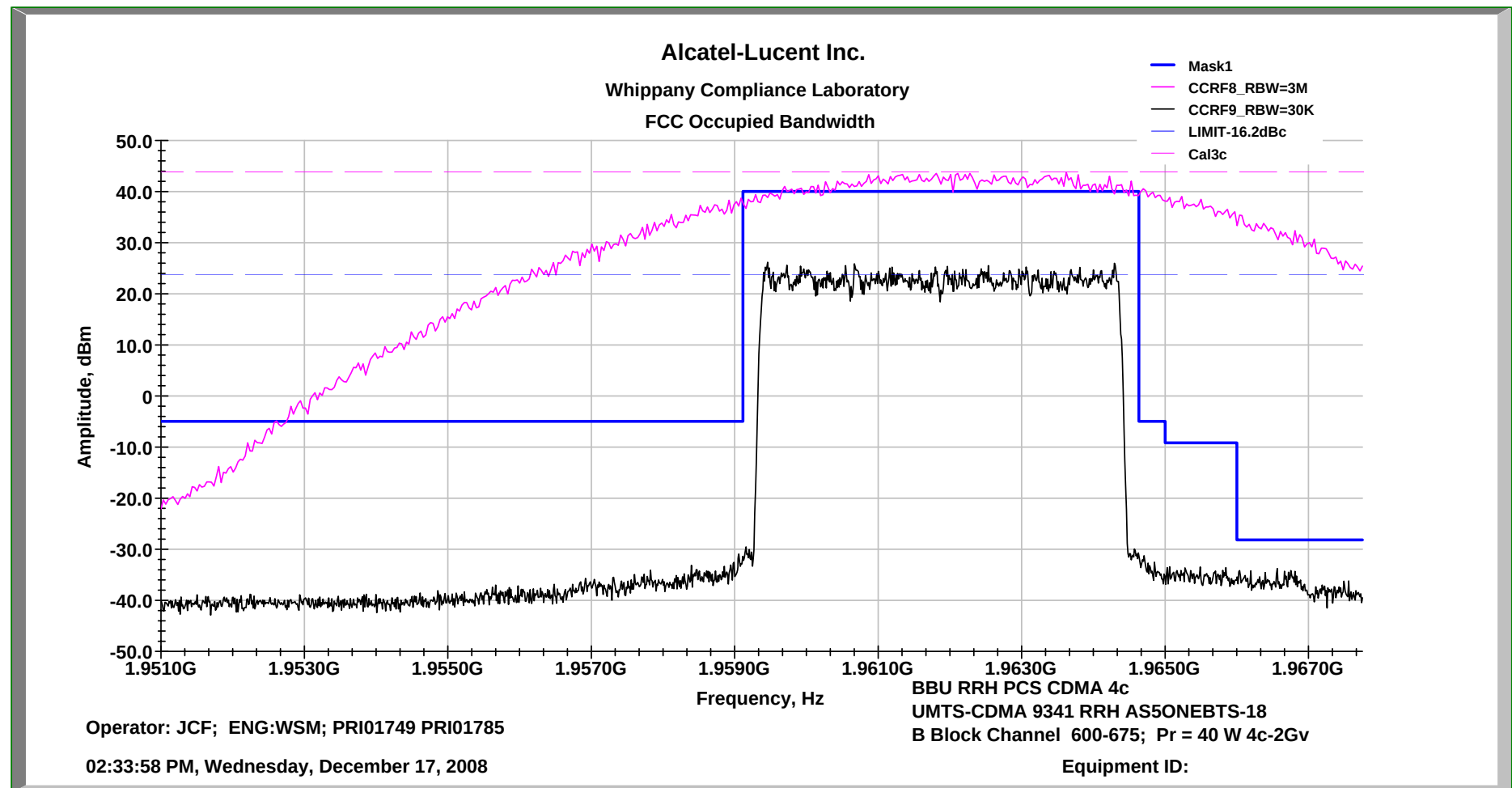
PCS Ch B5 600-675

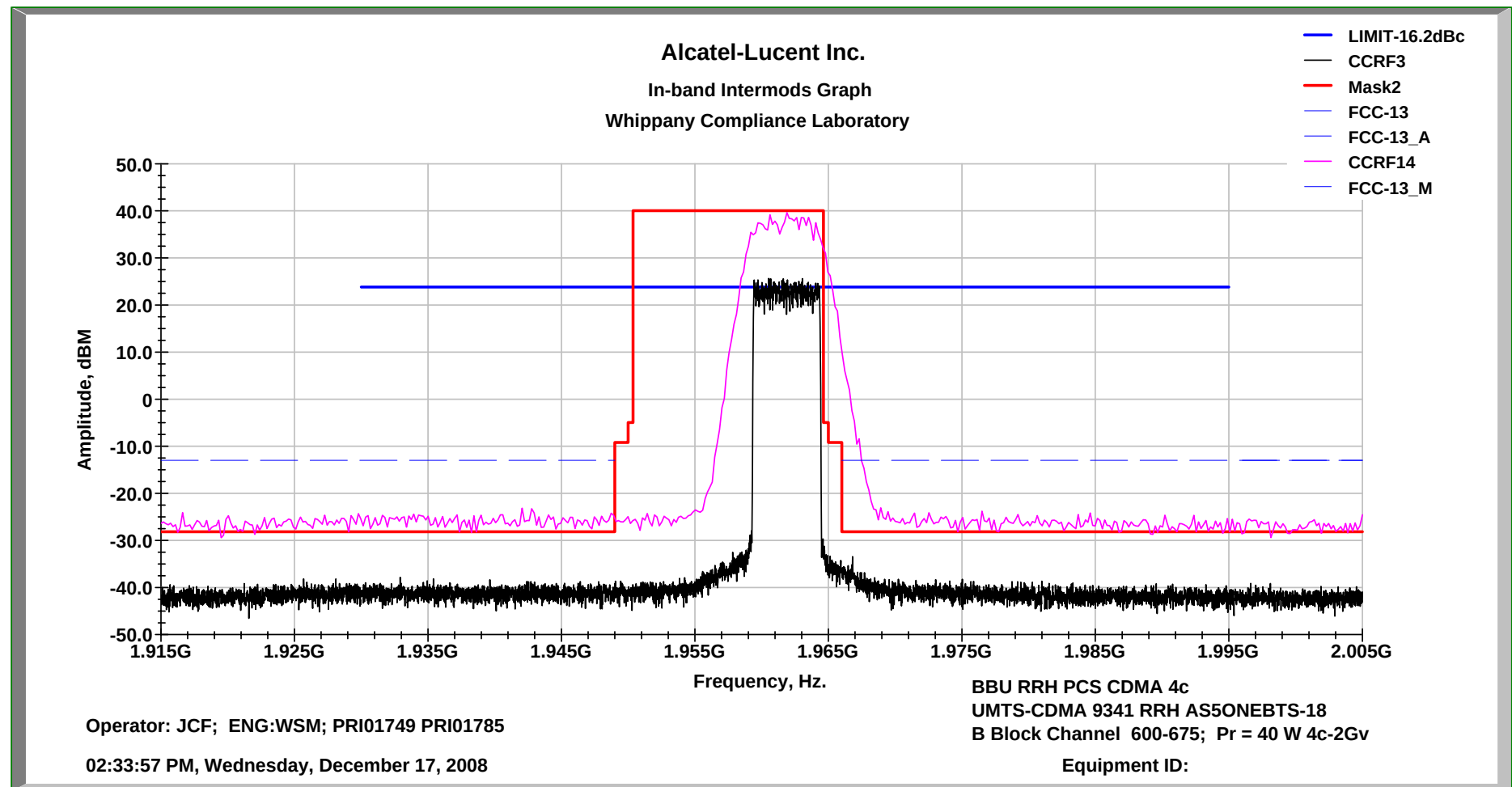
4c

2Gv

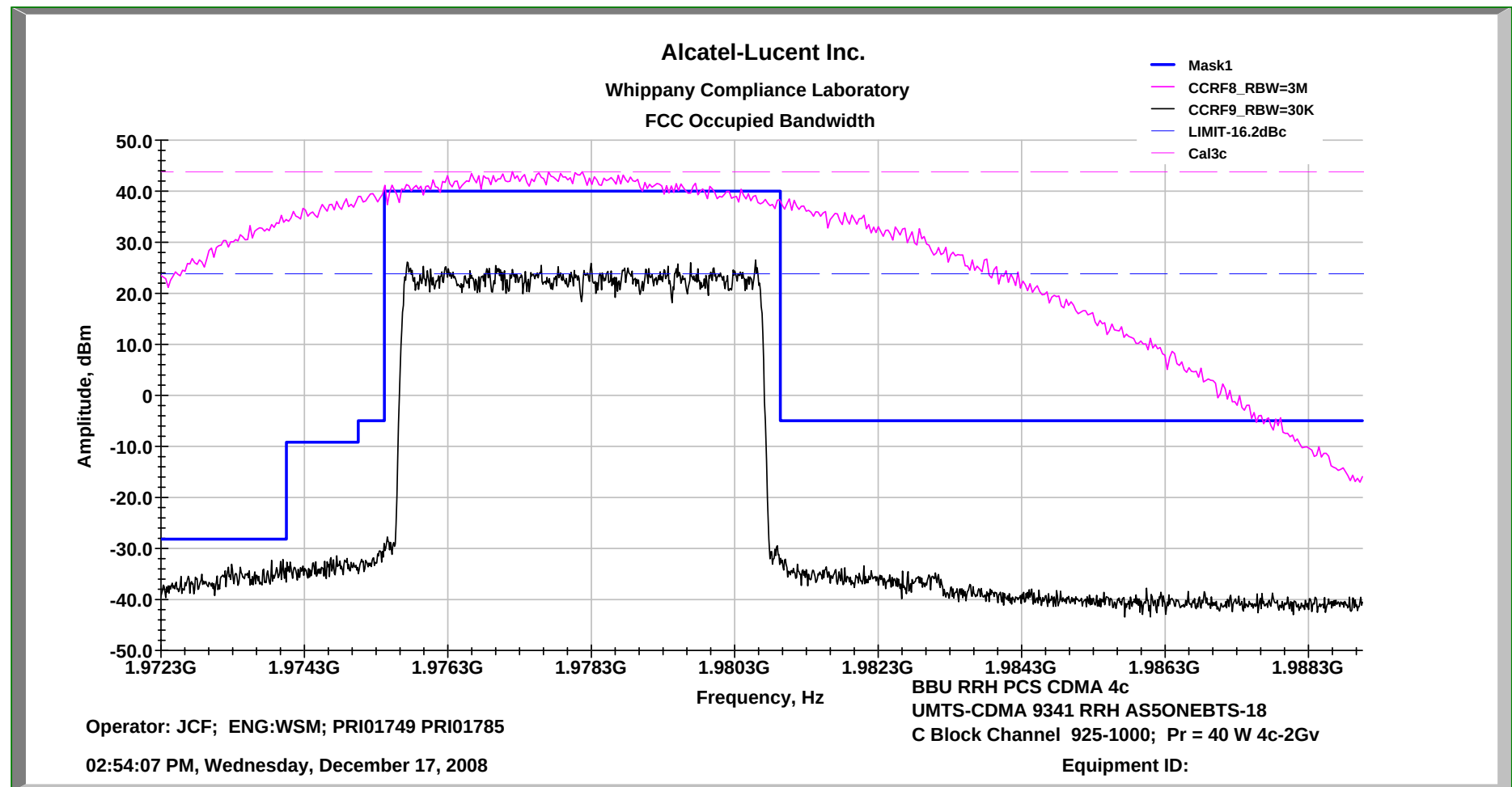
w/EDPD

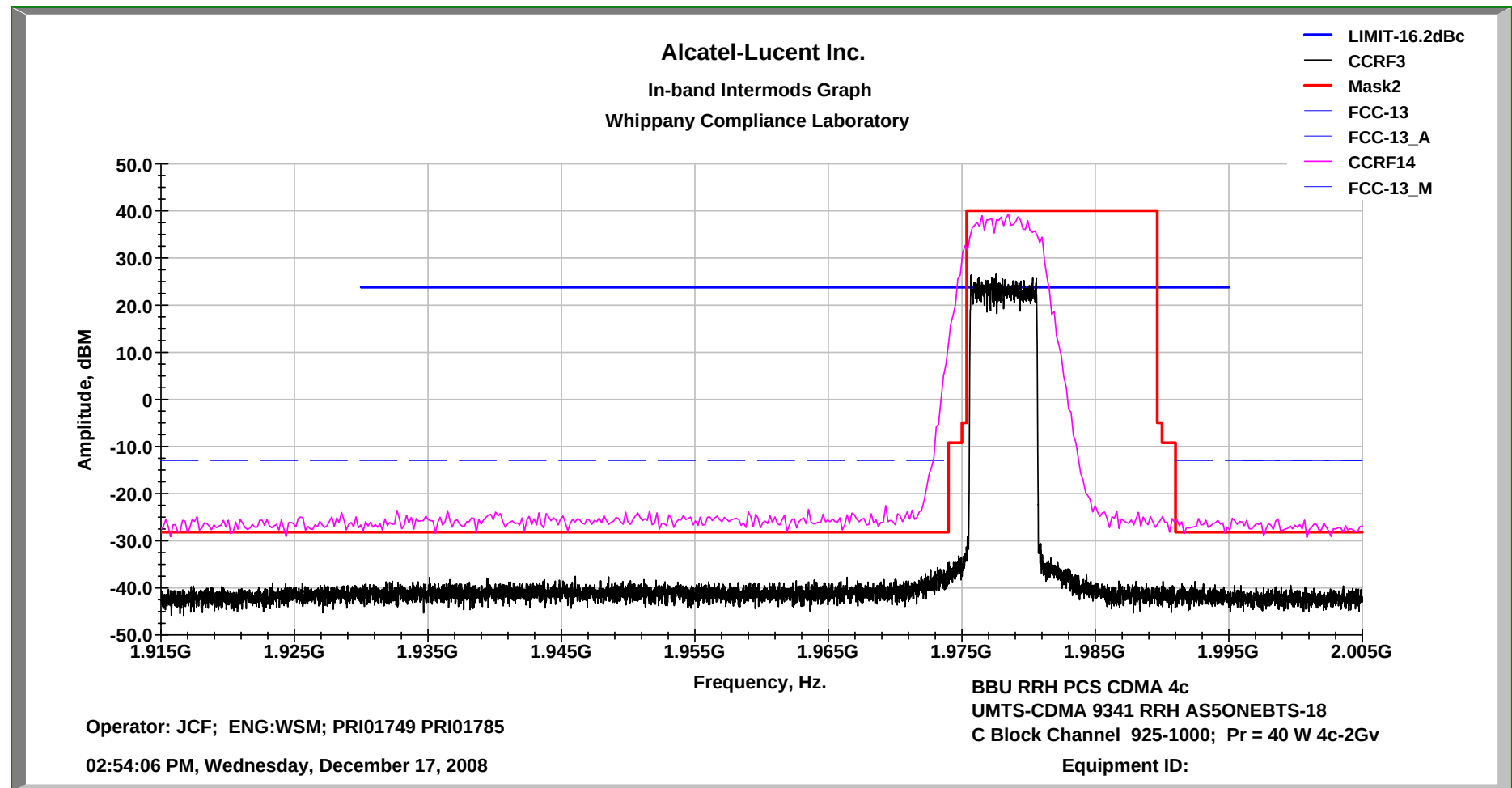
40W Total



In-band Inter-modulation PCS Ch B5 600-675 4c 2Gv w/EDPD 40W Total

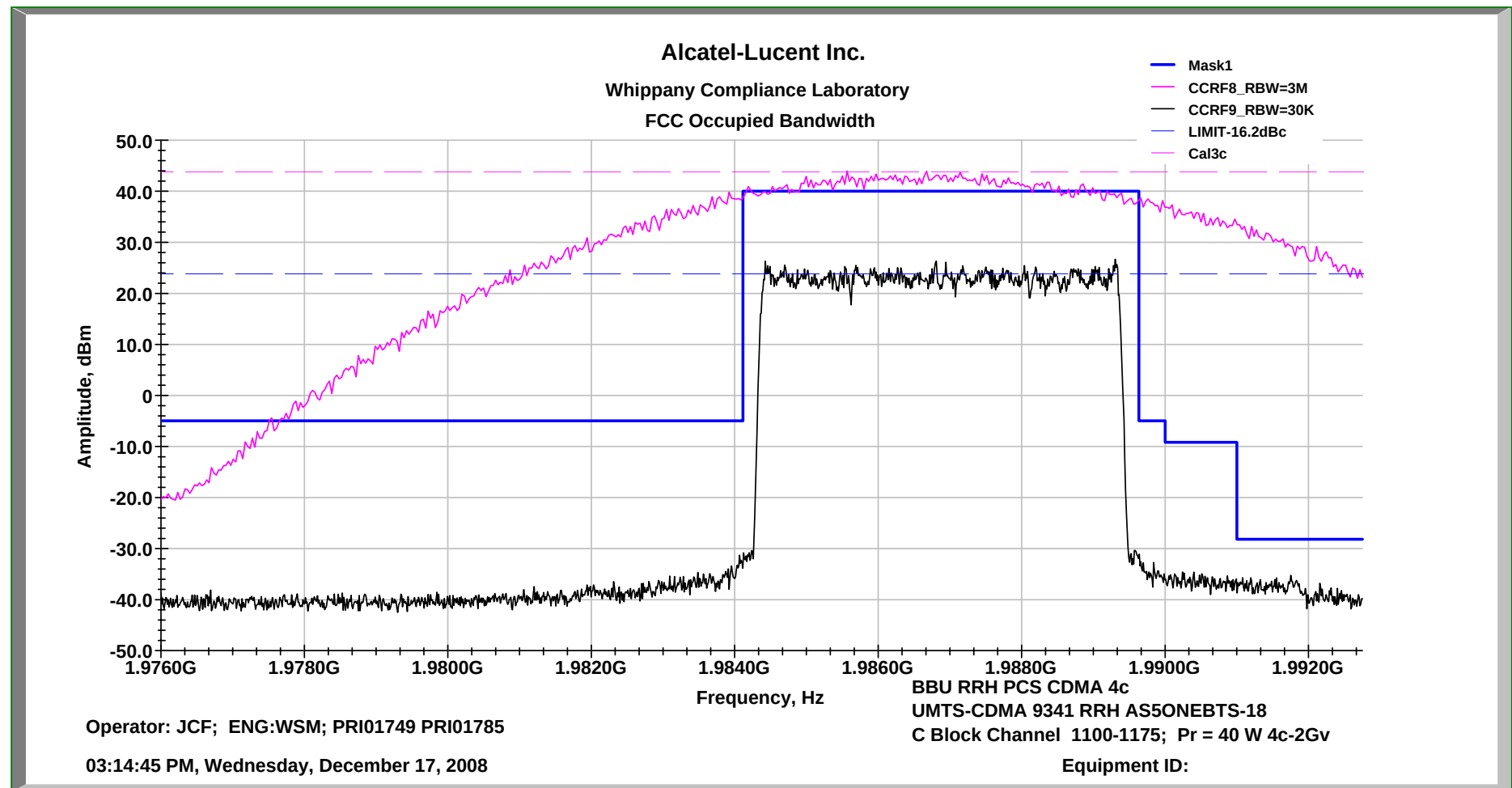
Occupied Bandwidth PCS Ch C1 925-1000 4c 2Gv w/EDPD 40W Total

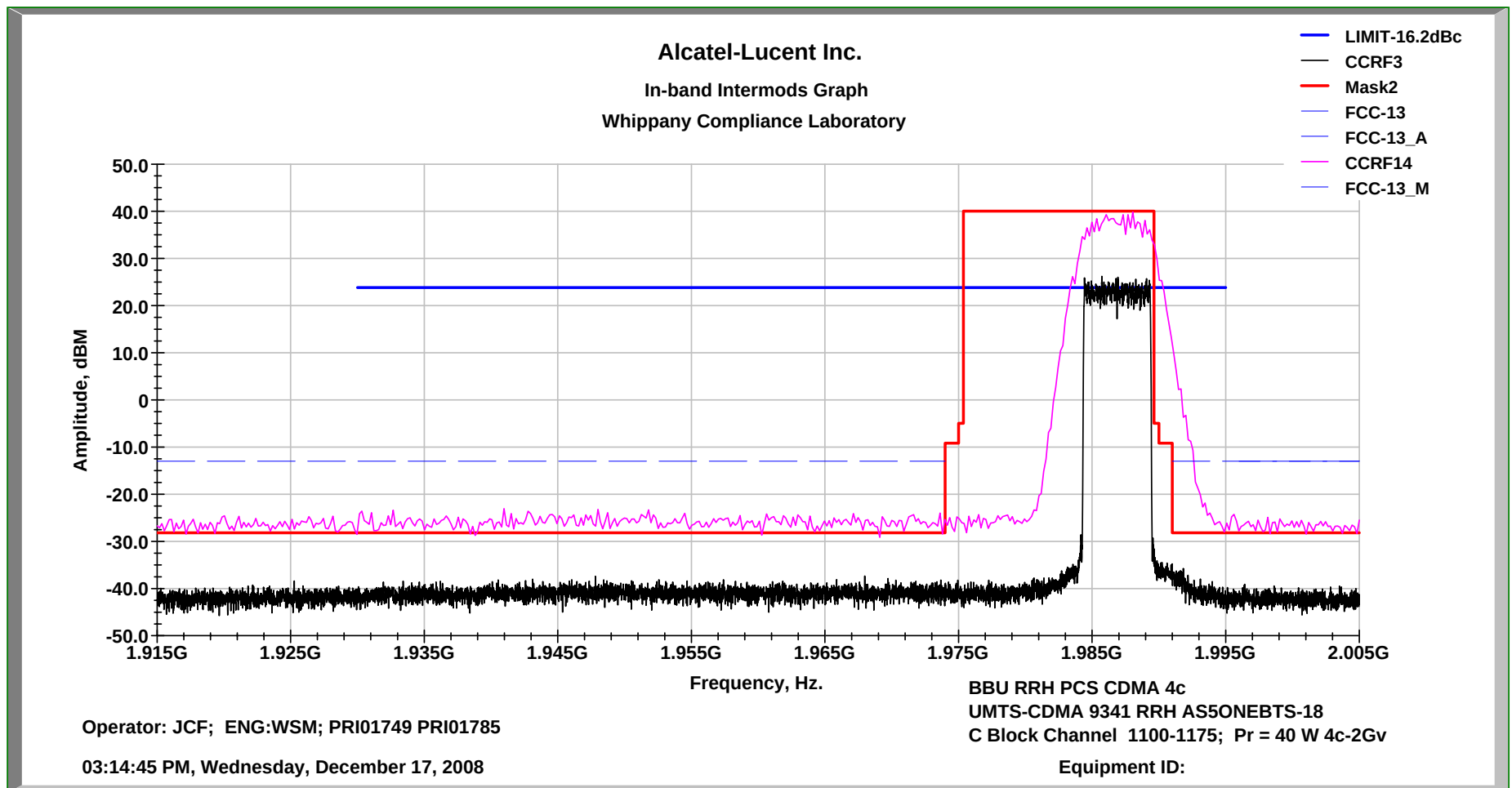


In-band Inter-modulation PCS Ch C1 925-1000 4c 2Gv w/EDPD 40W Total

Occupied Bandwidth

PCS Ch C5-1100-1175 4c 2Gv w/EDPD 40W Total



In-band Inter-modulation PCS Ch C5-1100-1175 4c 2Gv w/EDPD 40W Total

**Five Carrier Occupied Bandwidth Data
PCS Blocks A, D, B, E, F, and C
Left side of Block, Right side of Block
And
PCS Band Intermodulation**

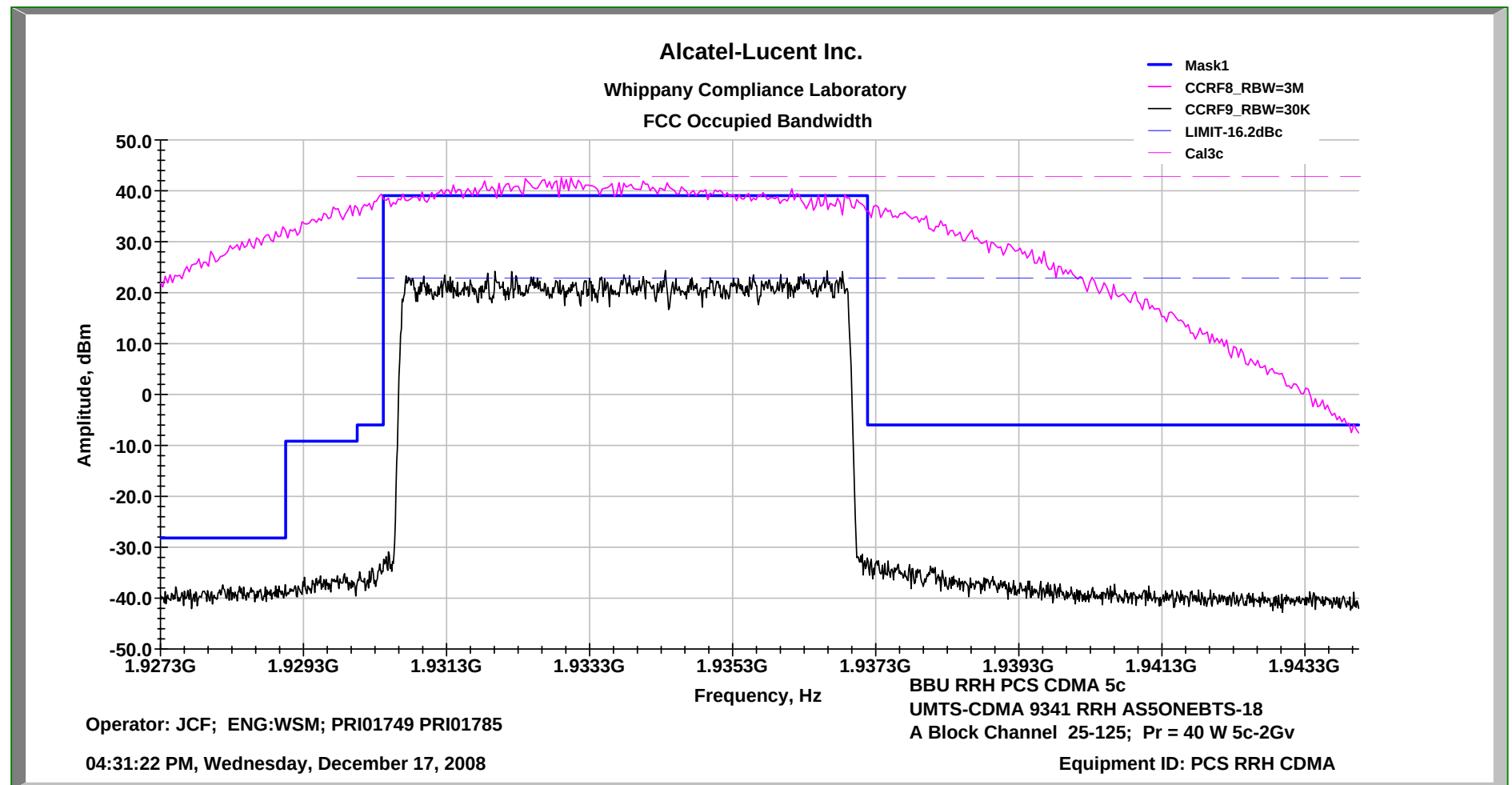
Occupied Bandwidth

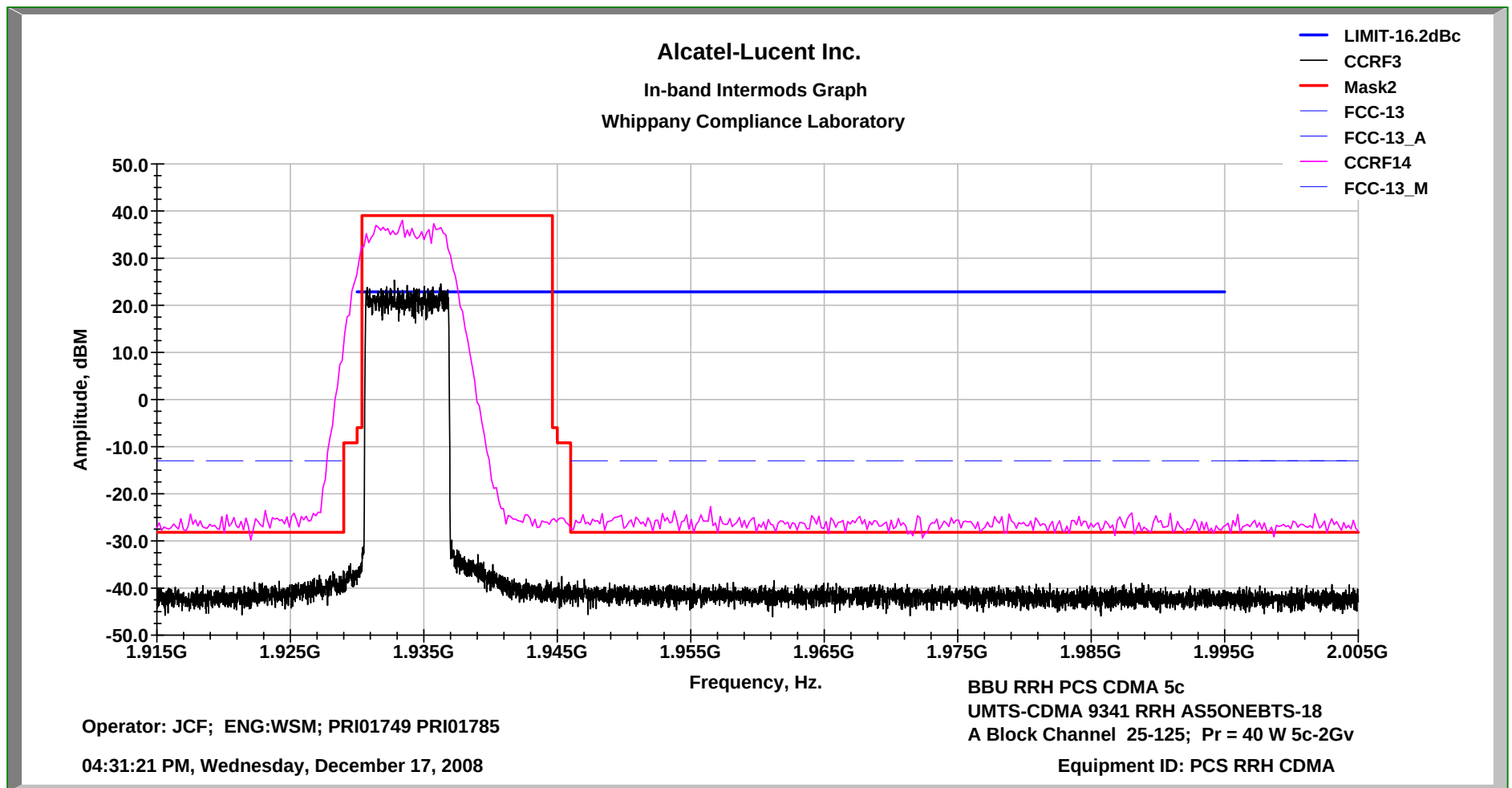
PCS Ch A1 25- 125

5c

2Gv w/EDPD

40W Total



In-band Inter-modulation PCS Ch A1 25- 125 5c 2Gv w/EDPD 40W Total

Occupied Bandwidth

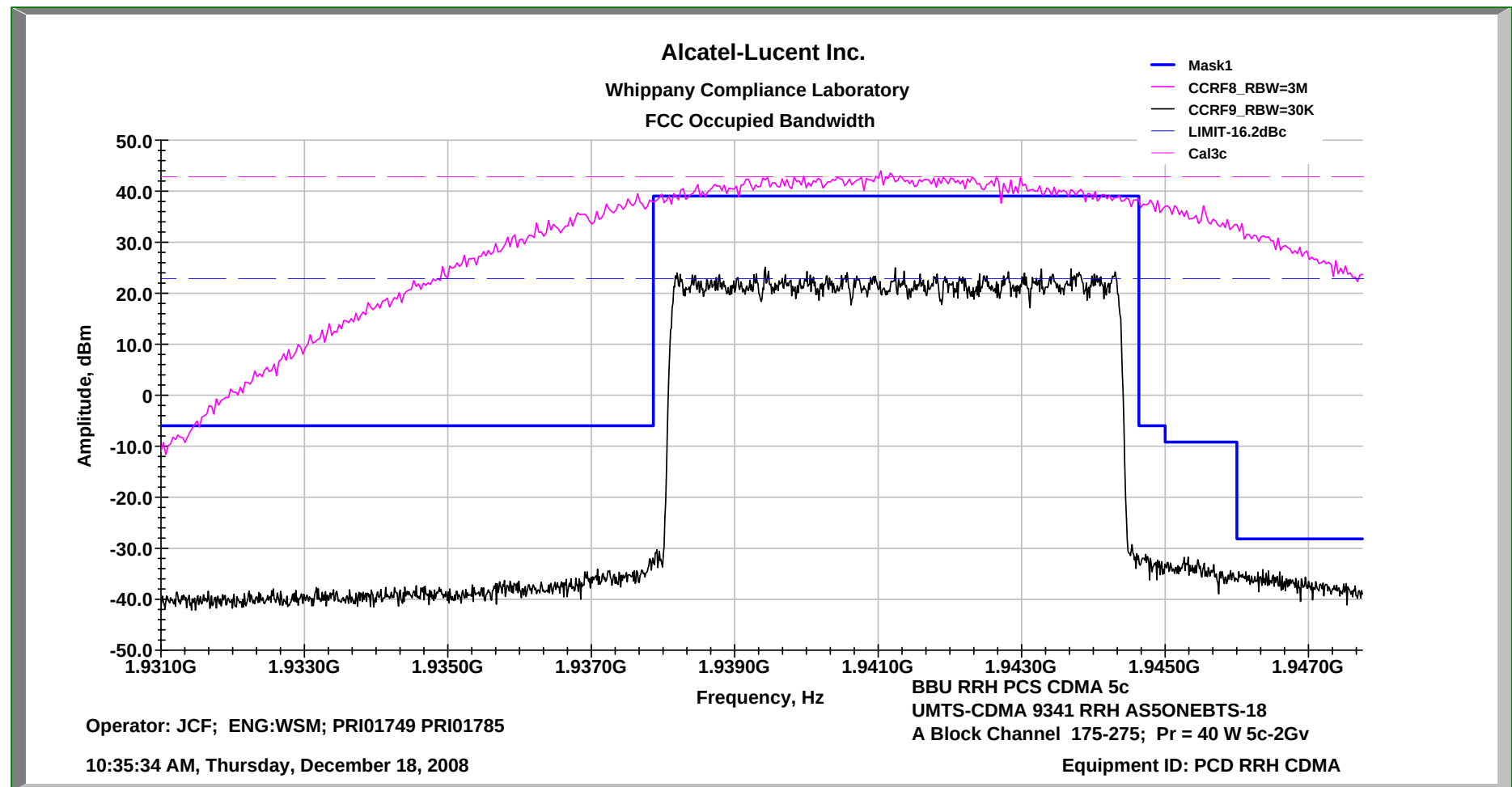
PCS Ch A5 175-275

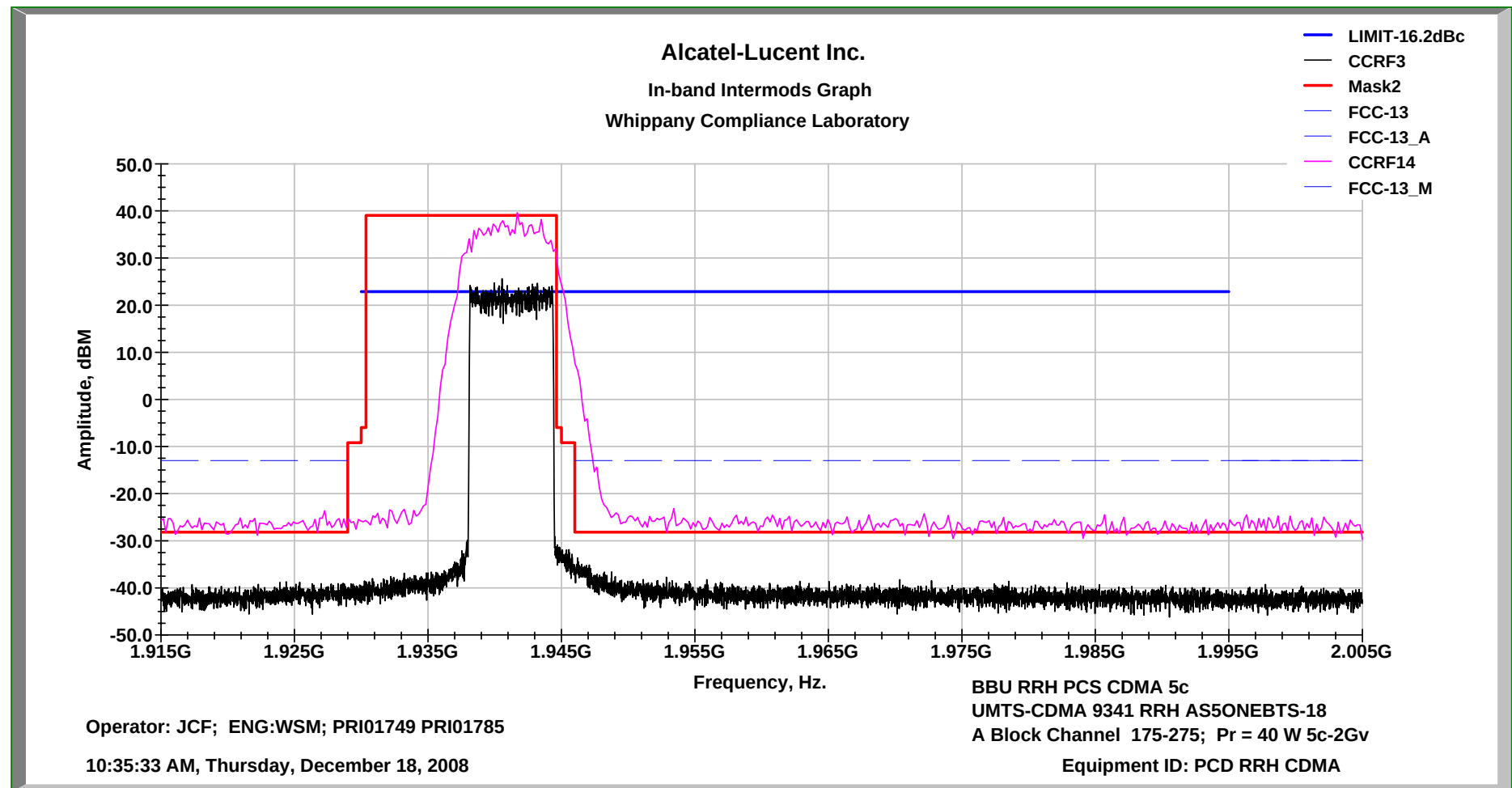
5c

2Gv

w/EDPD

40W Total



In-band Inter-modulation PCS Ch A5 175-275 5c 2Gv w/EDPD 40W Total

Occupied Bandwidth

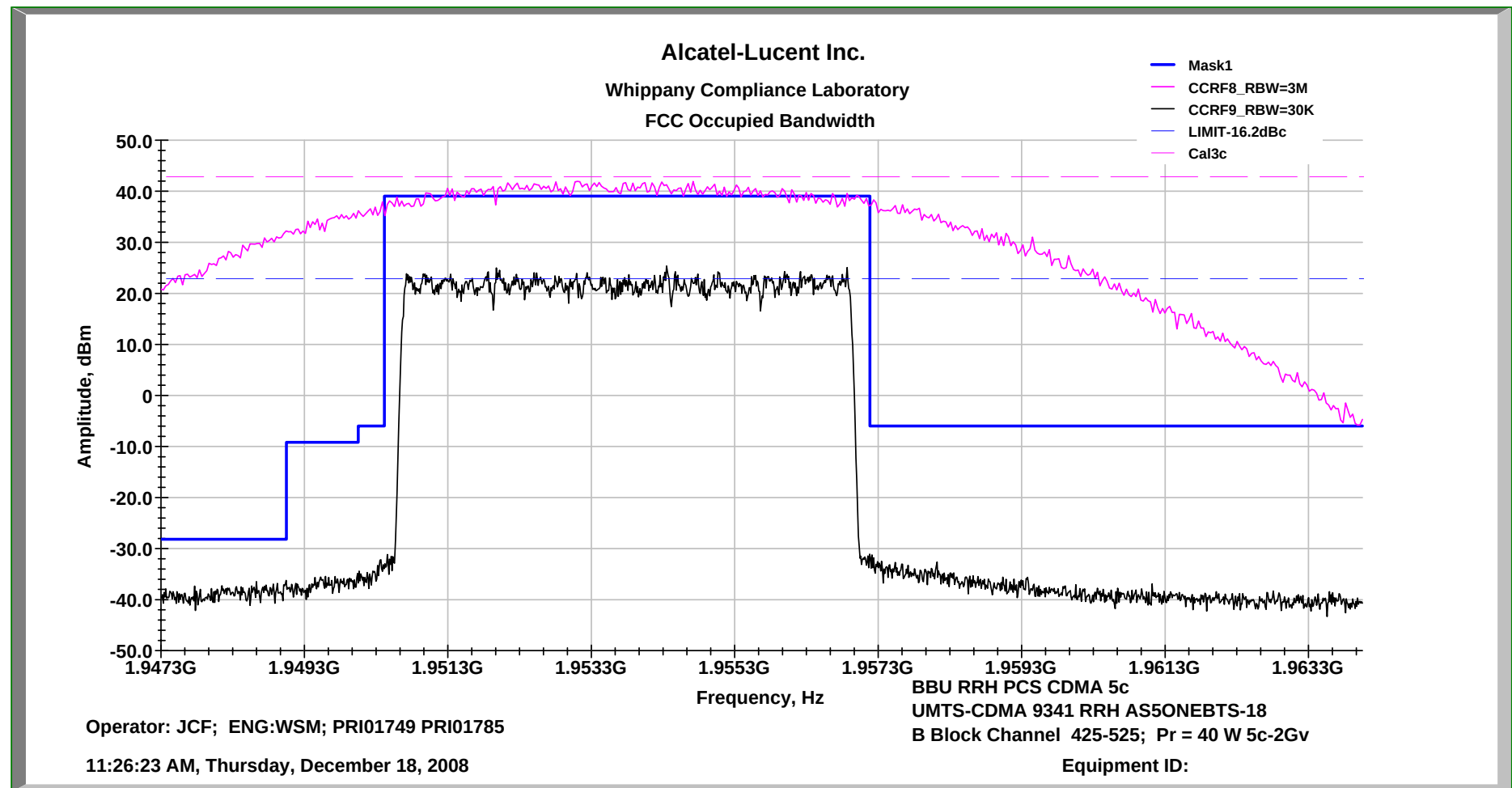
PCS Ch B1 425-525

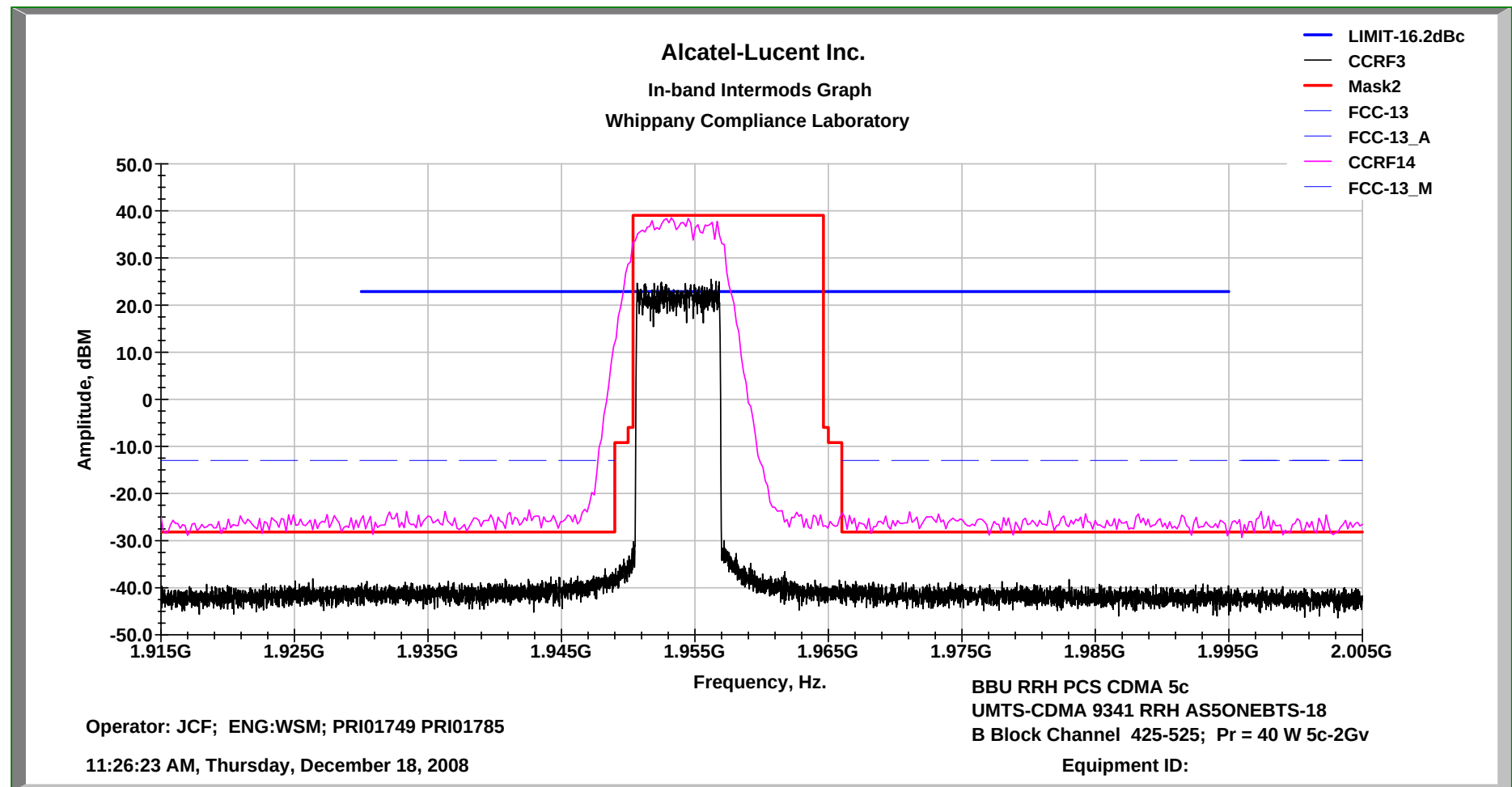
5c

2Gv

w/EDPD

40W Total



In-band Inter-modulation PCS Ch B1 425-525 5c 2Gv w/EDPD 40W Total

Occupied Bandwidth

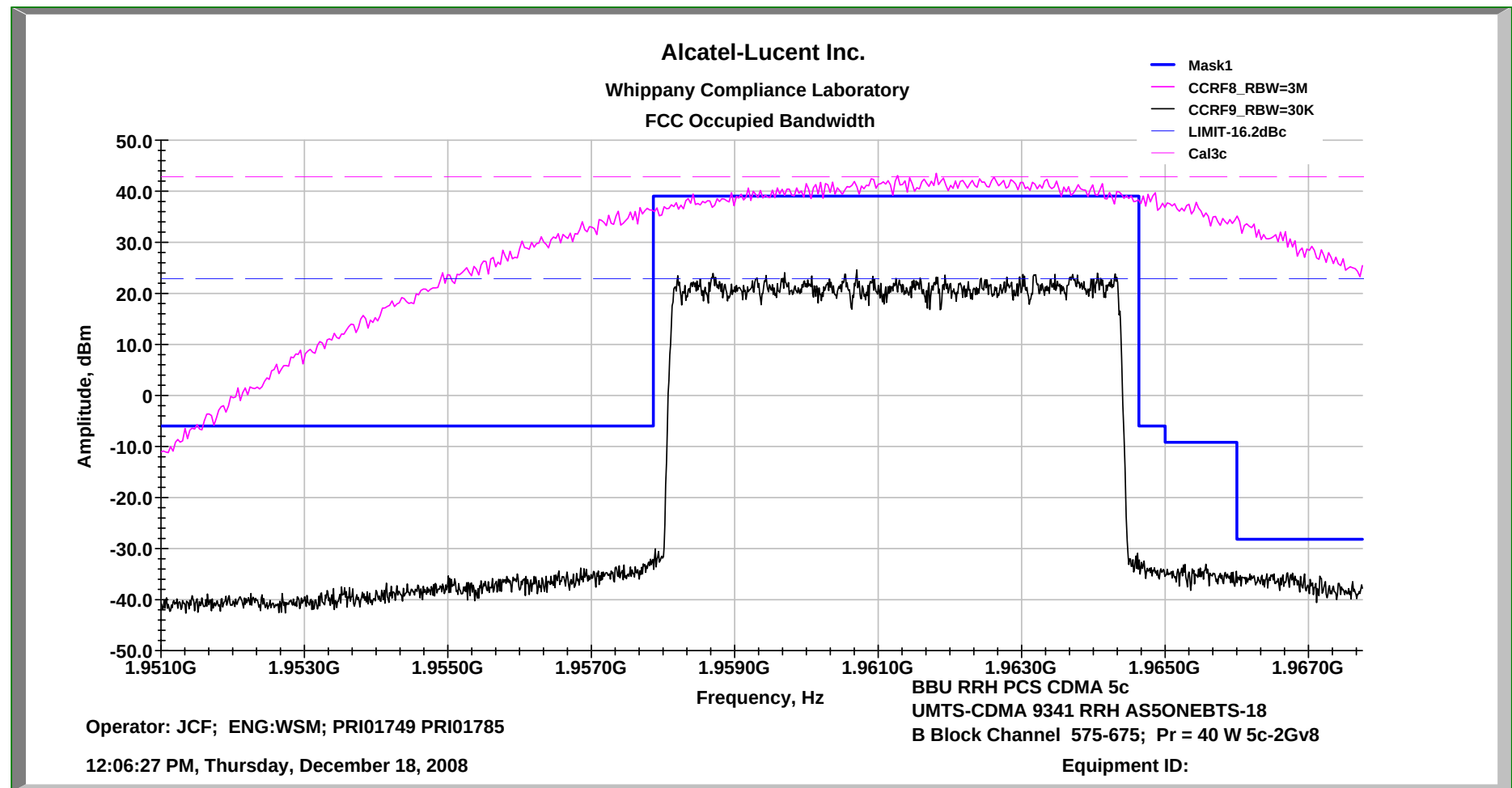
PCS Ch B5 575-675

5c

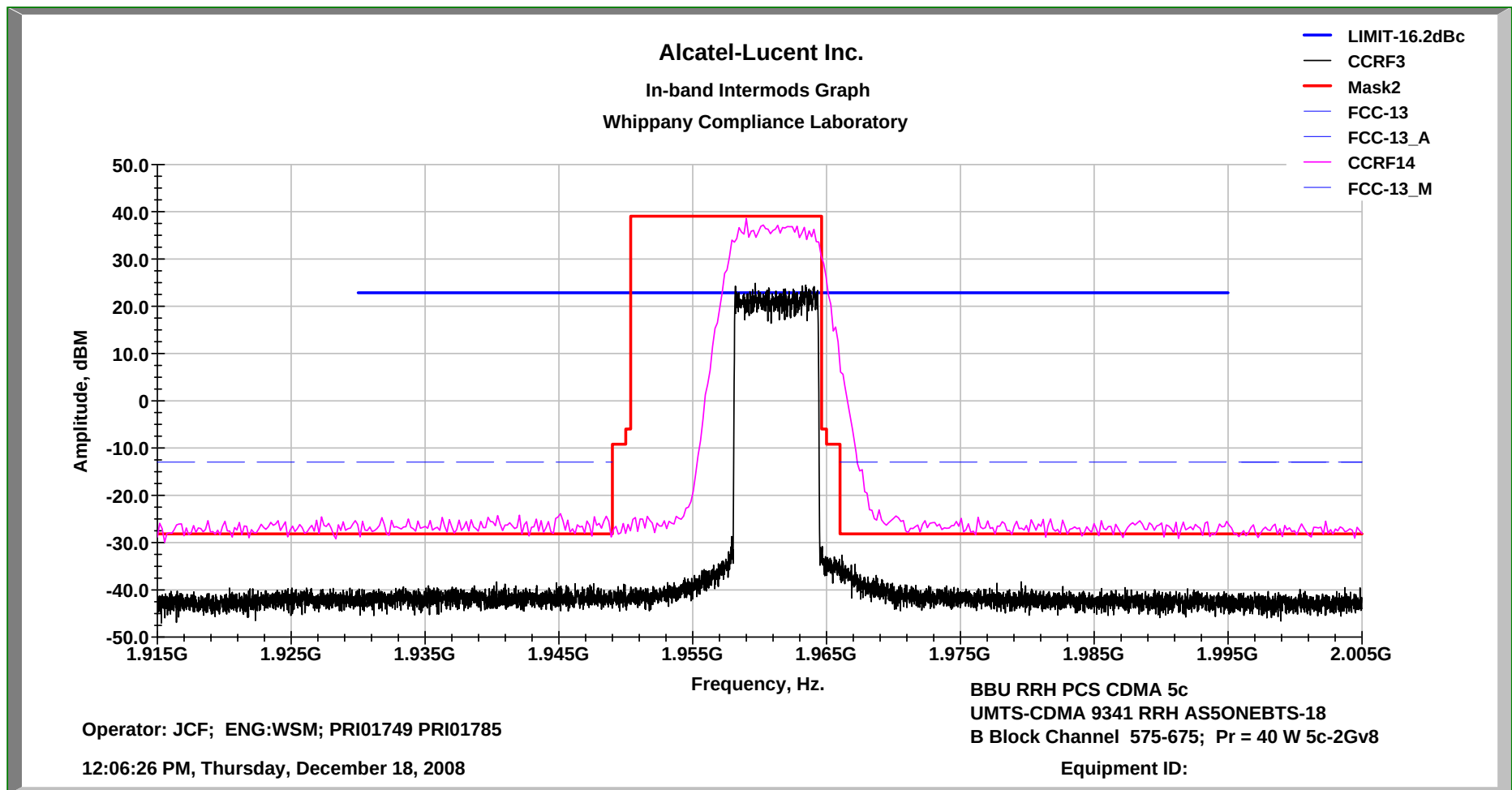
2Gv

w/EDPD

40W Total

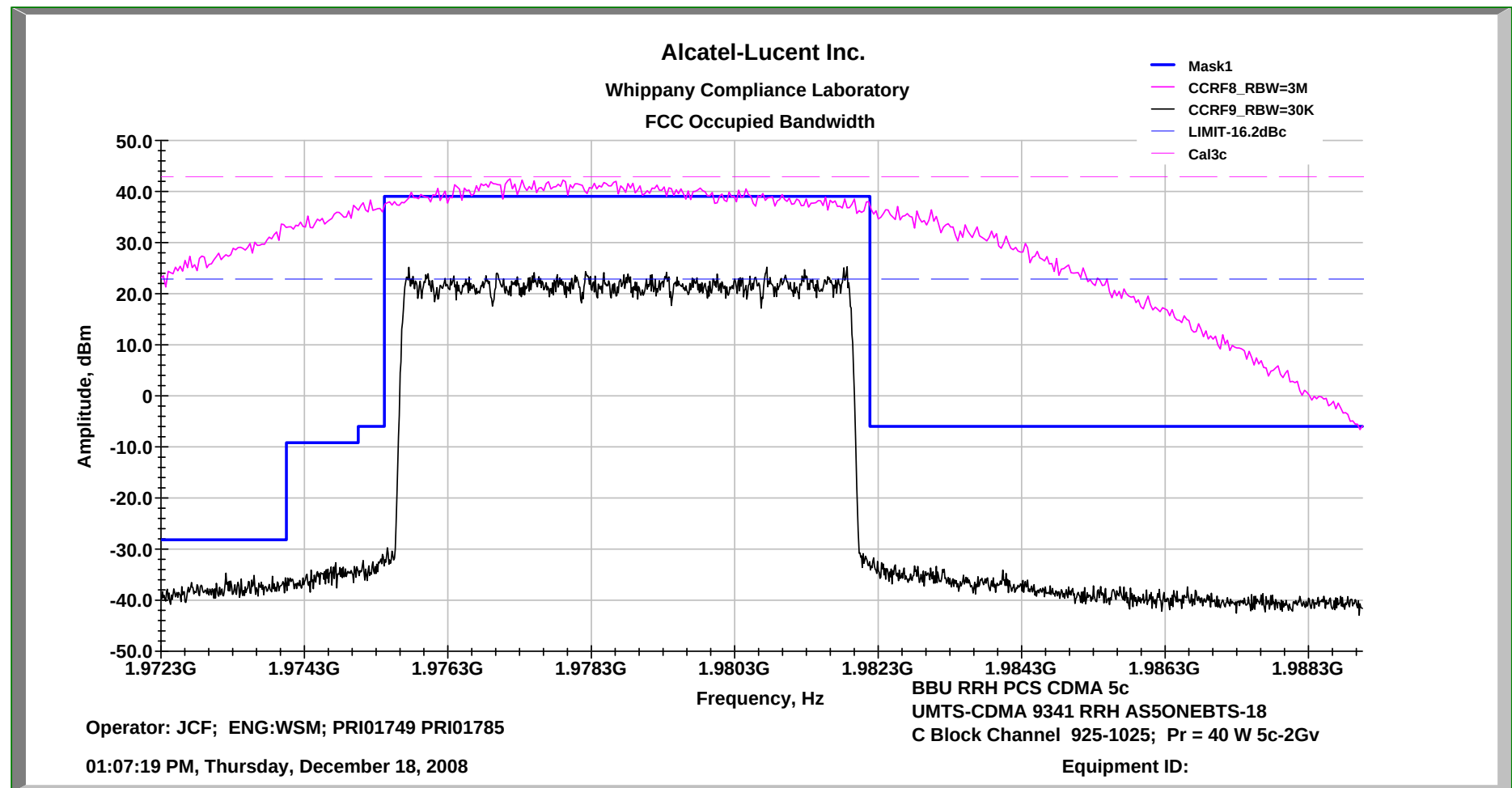


In-band Inter-modulation PCS Ch B5 575-675 5c 2Gv w/EDPD 40W Total

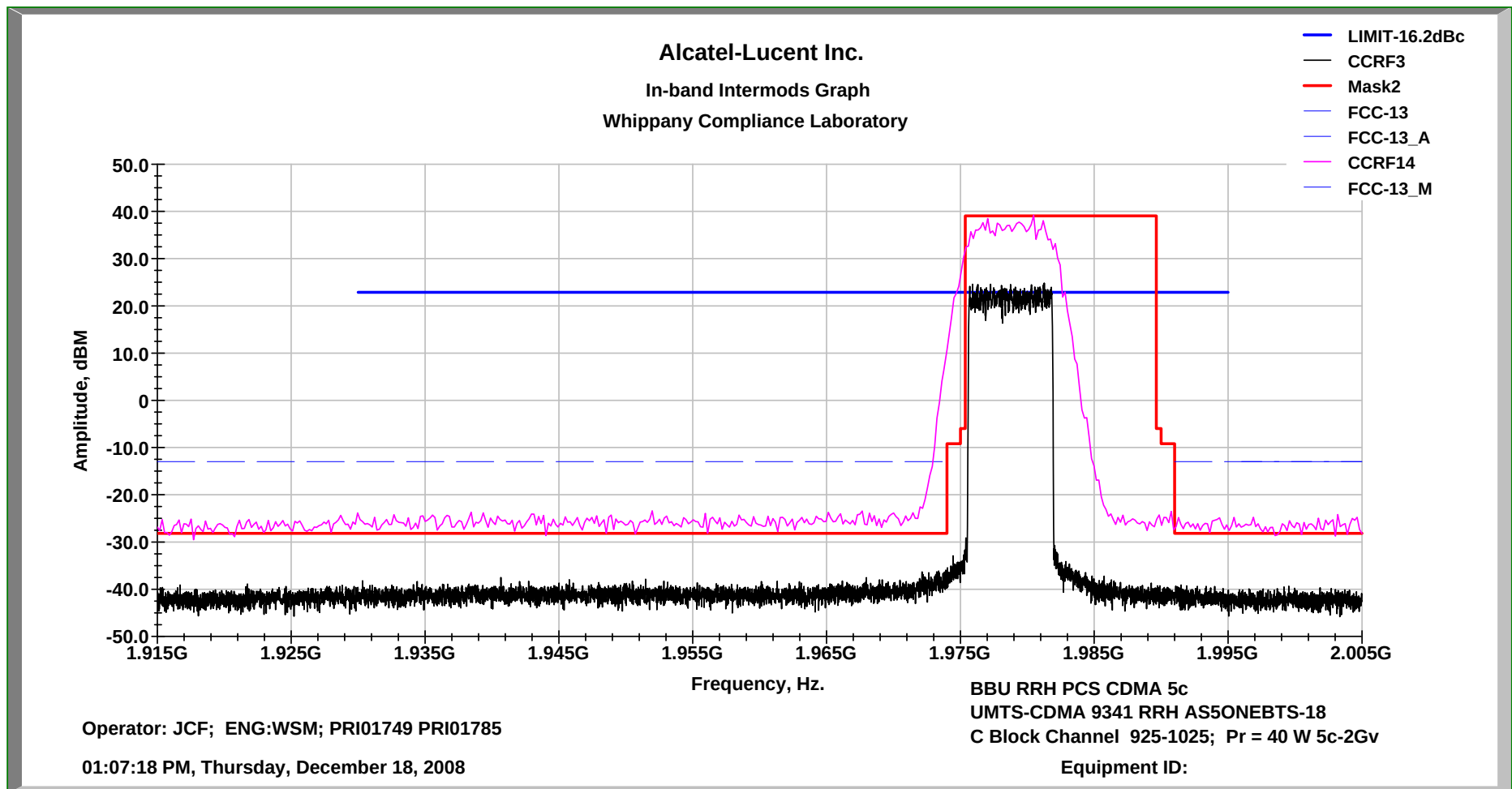


Occupied Bandwidth

PCS Ch C1 925-1025 5c 2Gv w/EDPD 40W Total

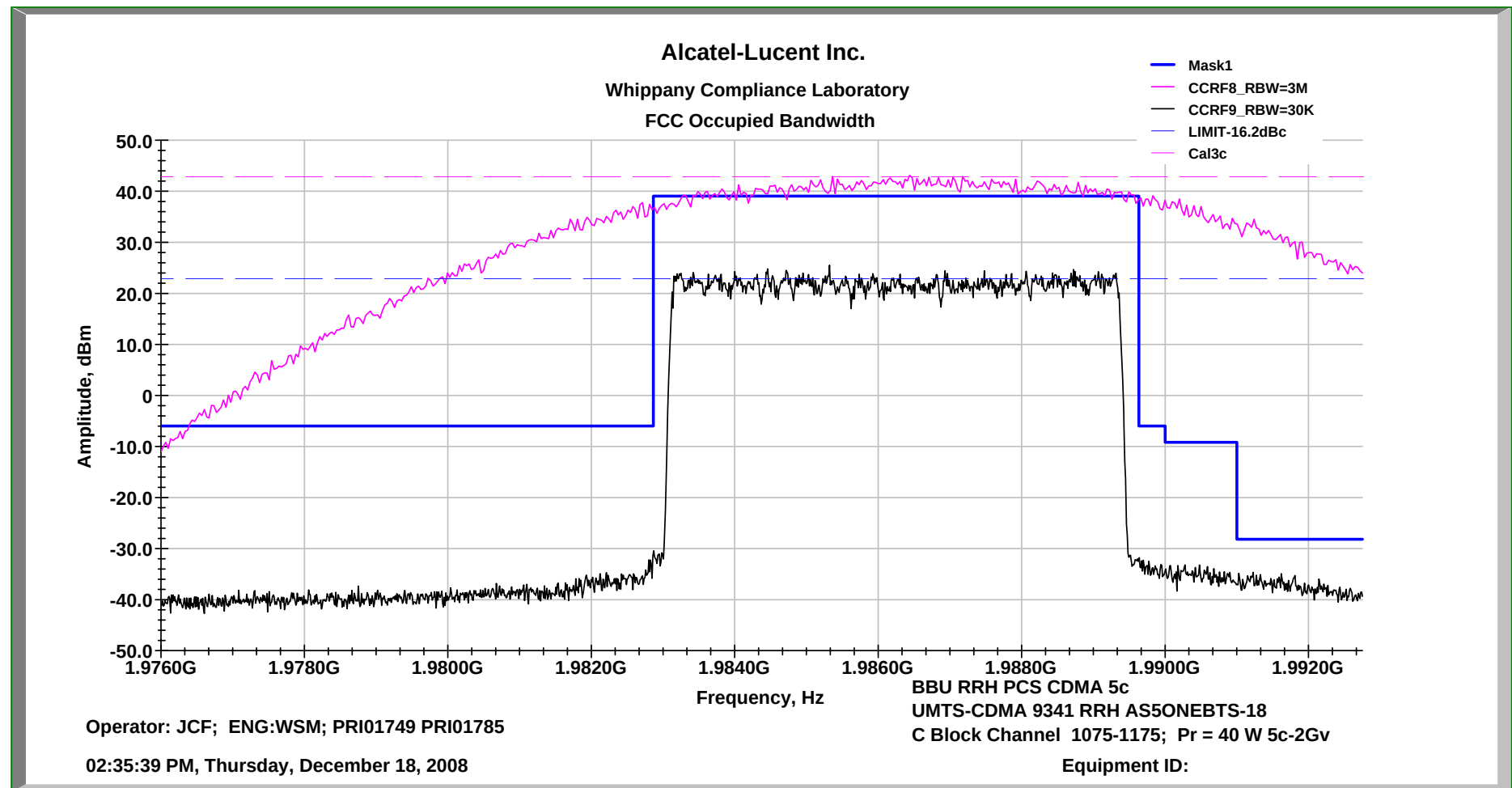


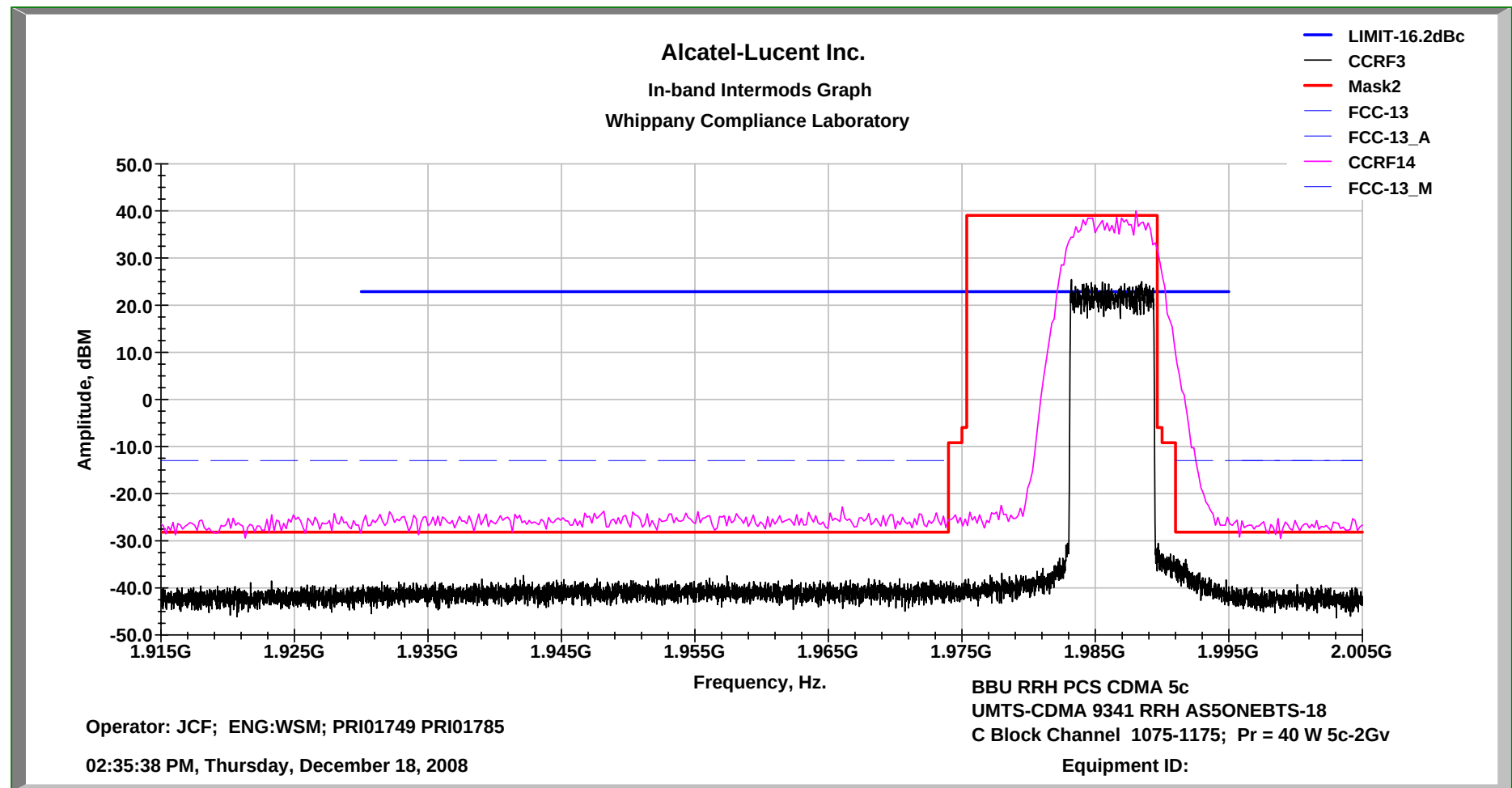
In-band Inter-modulation PCS Ch C1 925-1025 5c 2Gv w/EDPD 40W Total



Occupied Bandwidth

PCS Ch C5 1075-1175 5c 2Gv w/EDPD 40W Total



In-band Inter-modulation PCS Ch C5 1075-1175 5c 2Gv w/EDPD 40W Total

**Six Carrier Occupied Bandwidth Data
PCS Blocks A, D, B, E, F, and C
Left side of Block, Right side of Block
And
PCS Band Intermodulation**

Occupied Bandwidth

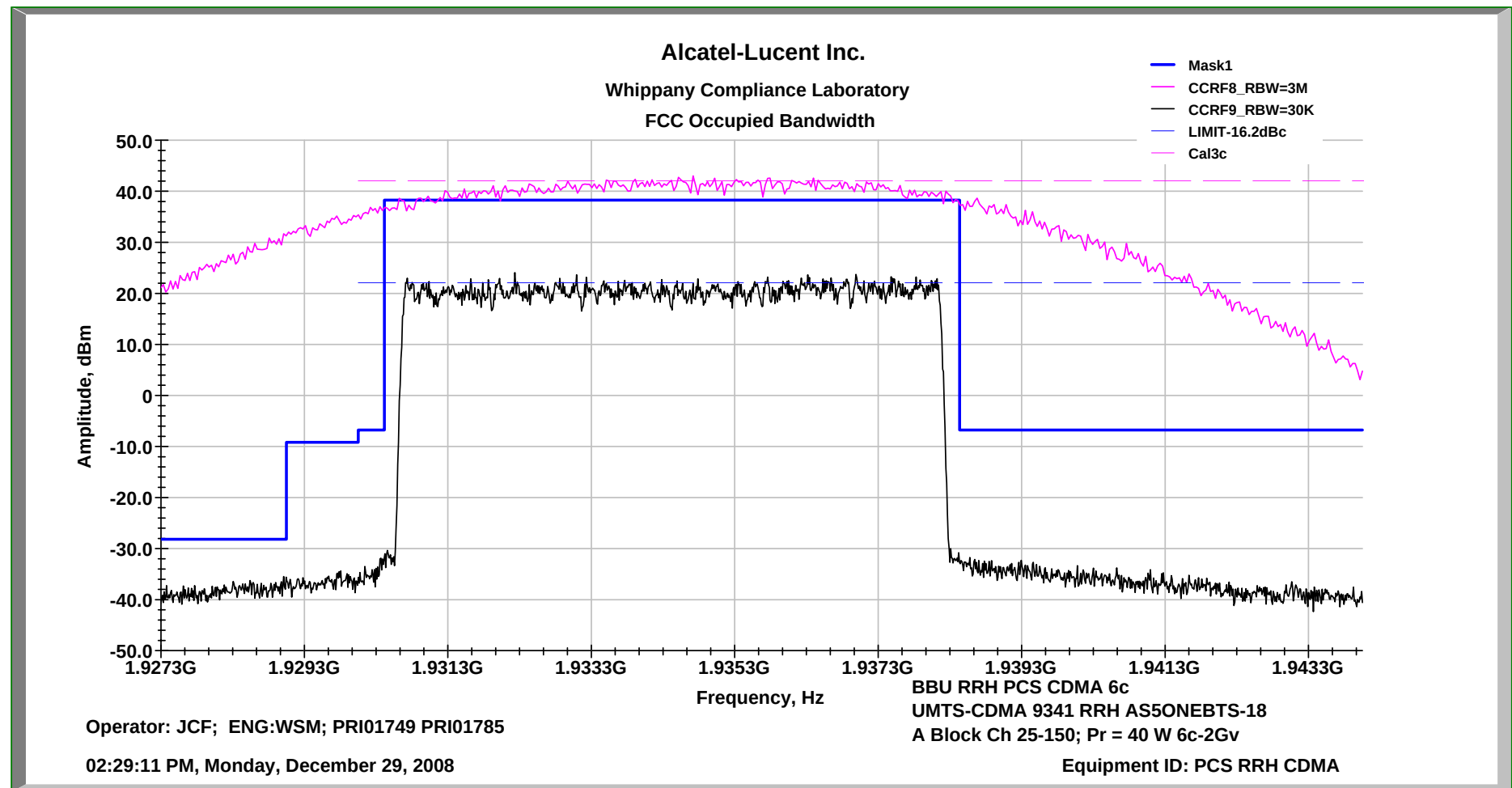
PCS Ch A1 25-150

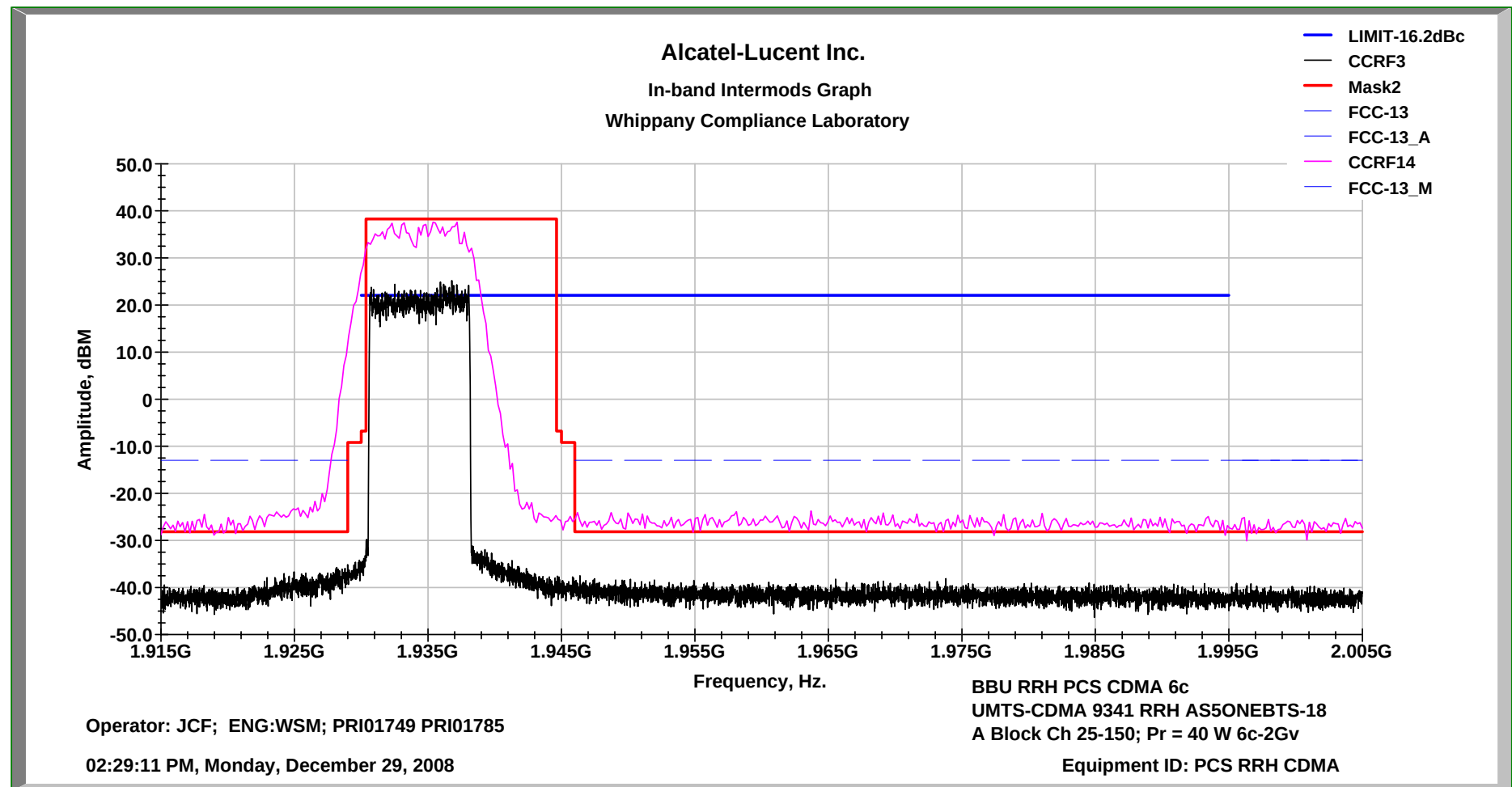
6c

2Gv

w/EDPD

40W Total



In-band Inter-modulation PCS Ch A1 25-150 6c 2Gv w/EDPD 40W Total

Occupied Bandwidth

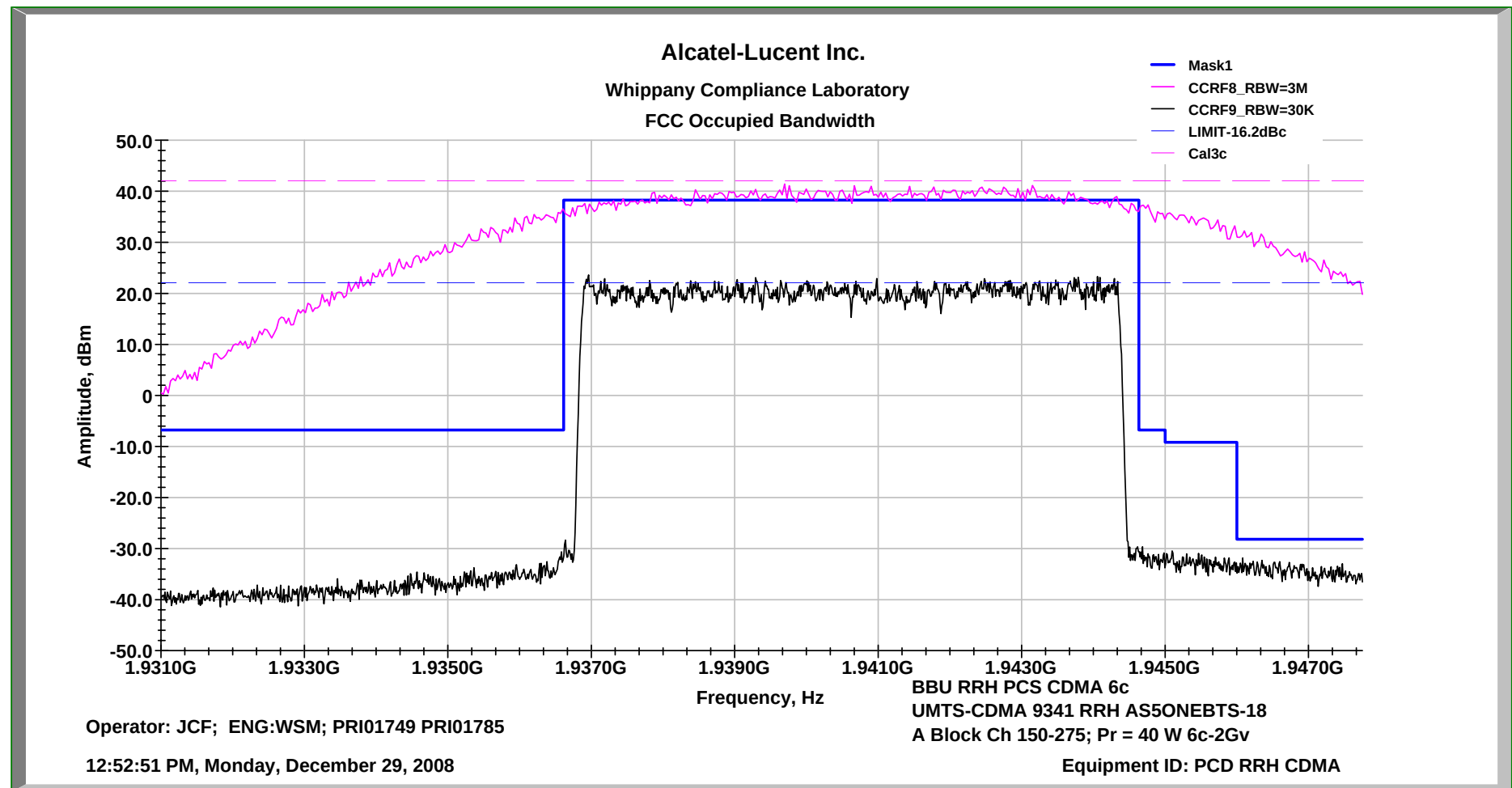
PCS Ch A5 150-275

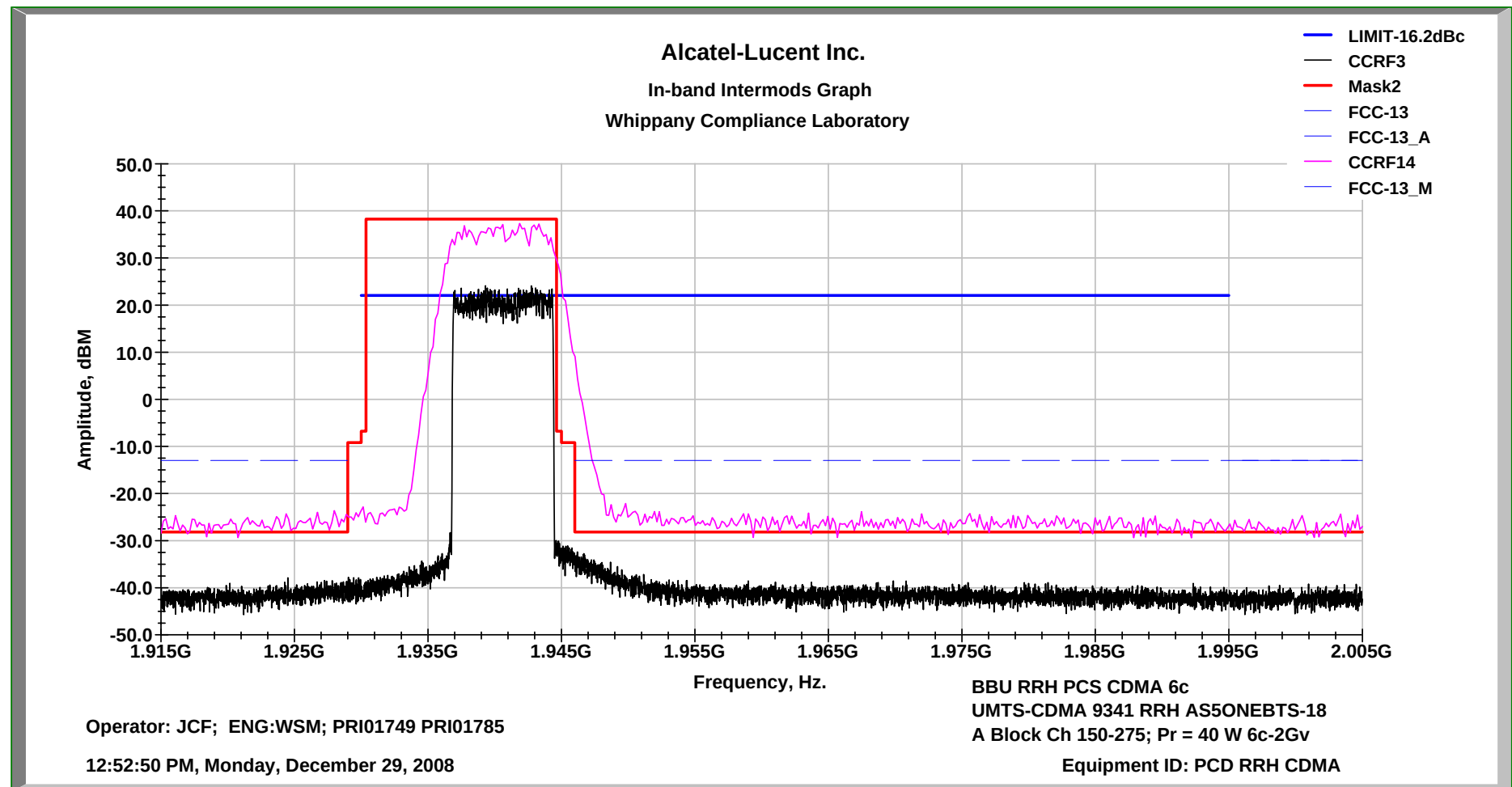
6c

2Gv

w/EDPD

40W Total



In-band Inter-modulation PCS Ch A5 150-275 6c 2Gv w/EDPD 40W Total

Occupied Bandwidth

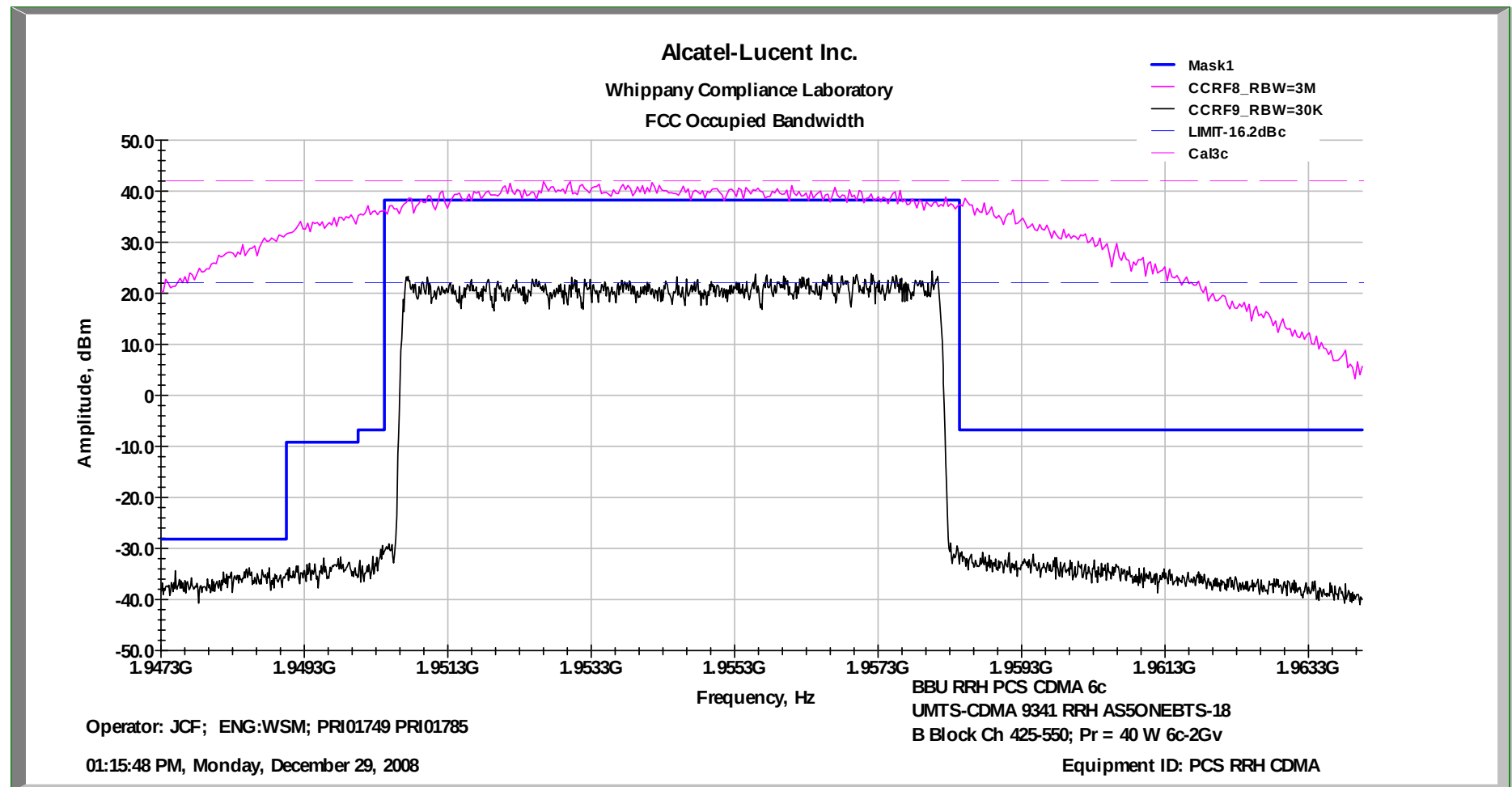
PCS Ch B1 425-550

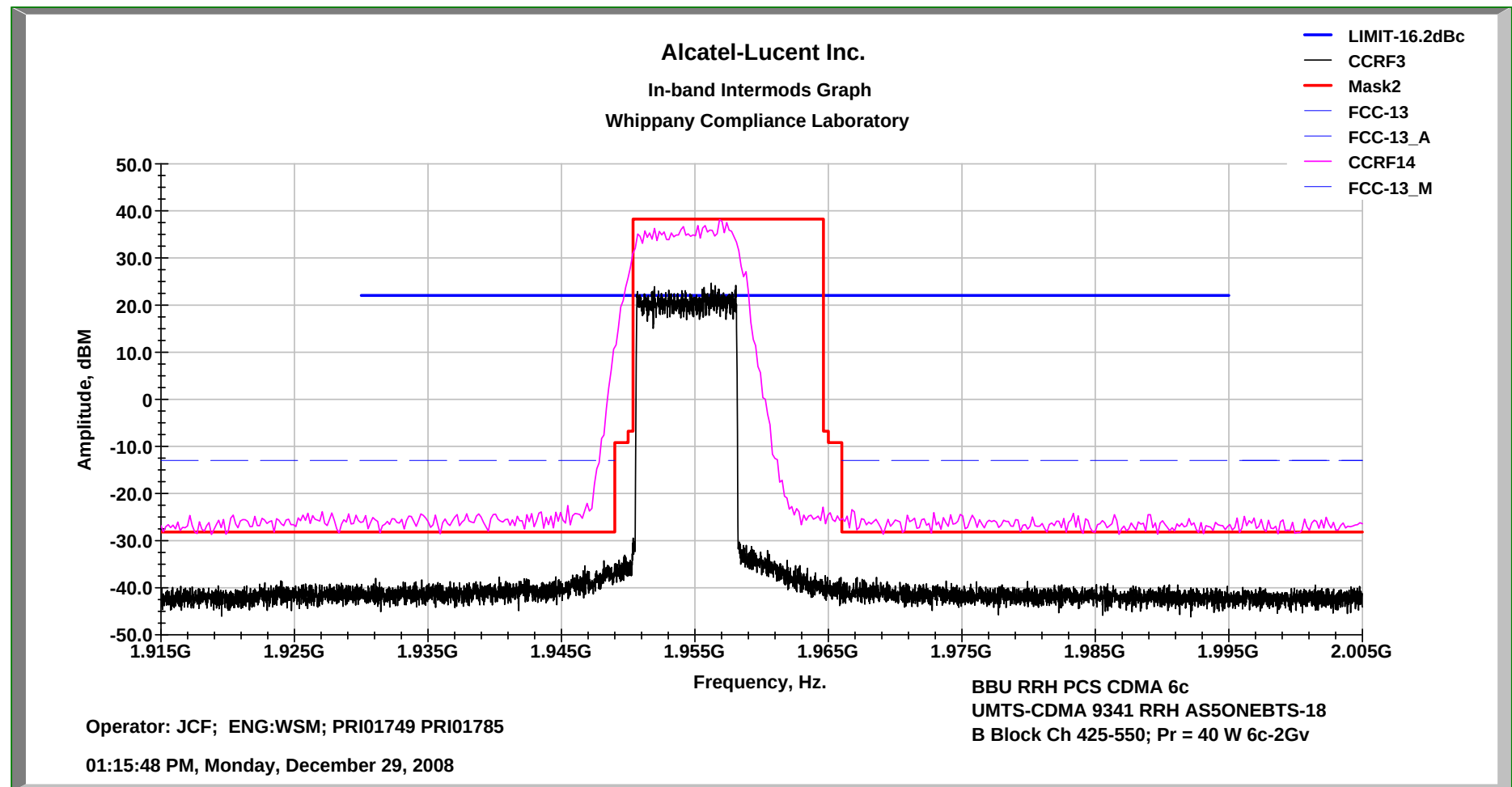
6c

2Gv

w/EDPD

40W Total



In-band Inter-modulation PCS Ch B1 425-550 6c 2Gv w/EDPD 40W Total

Occupied Bandwidth

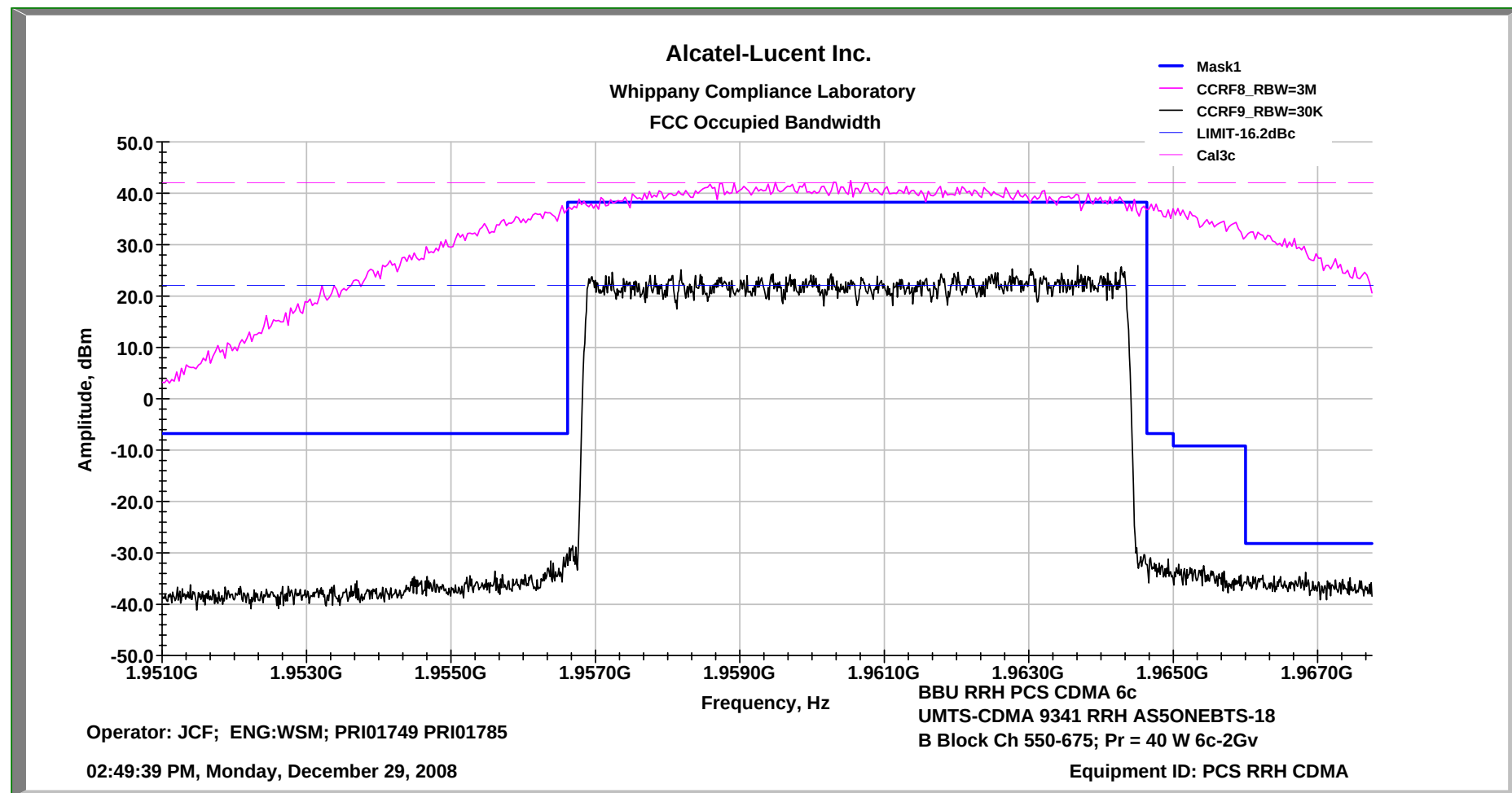
PCS Ch B5 550-675

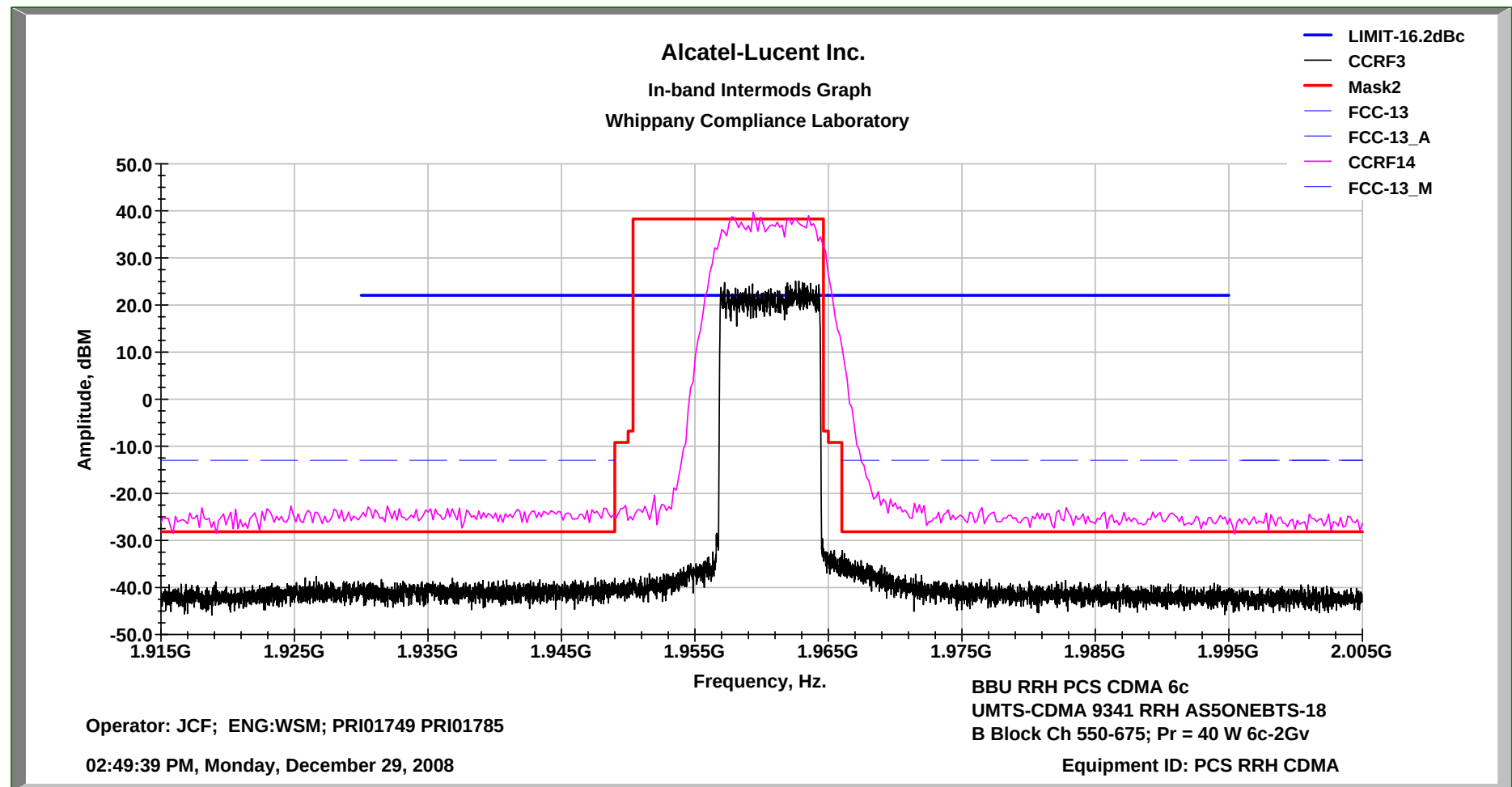
6c

2Gv

w/EDPD

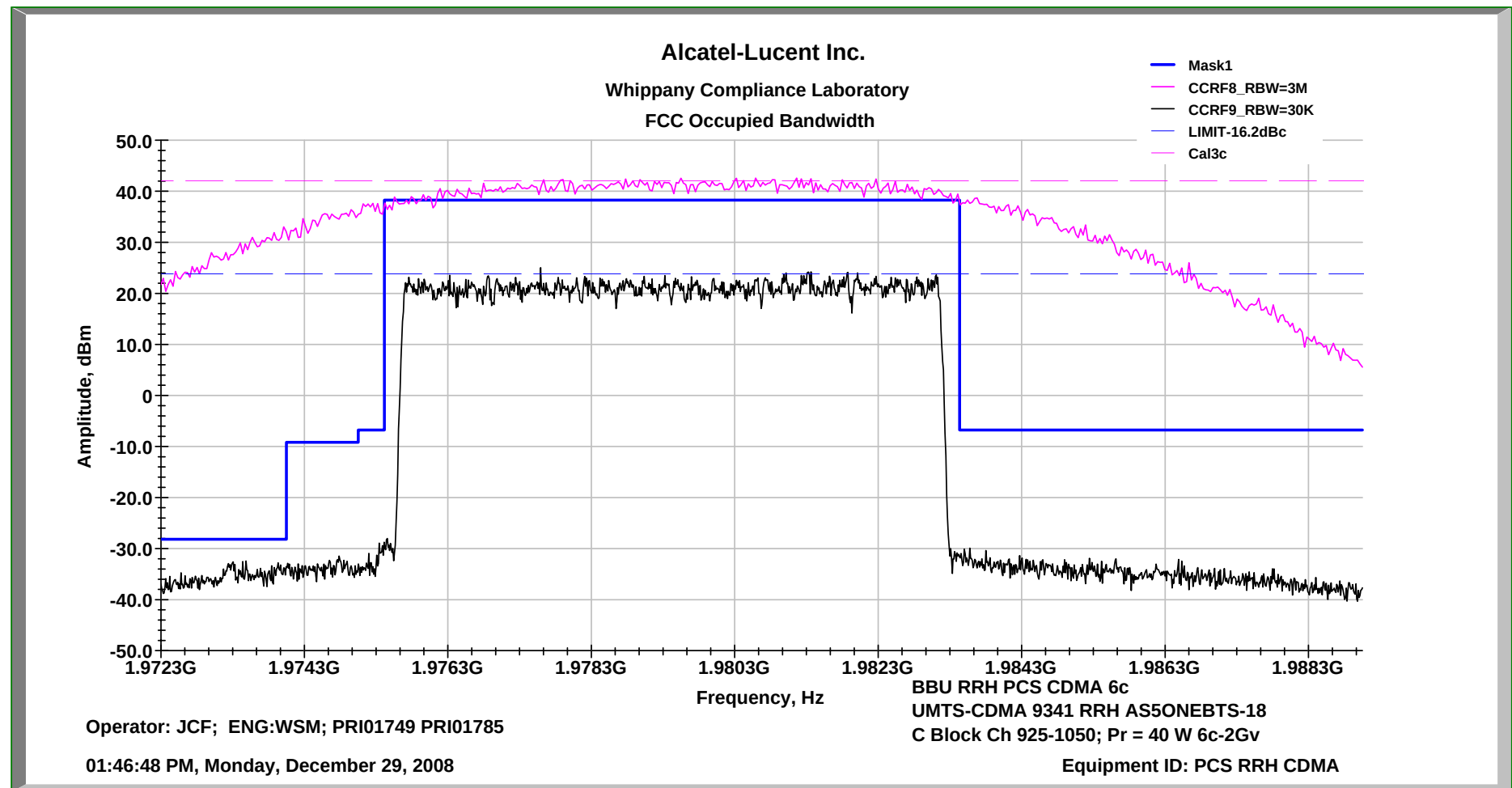
40W Total

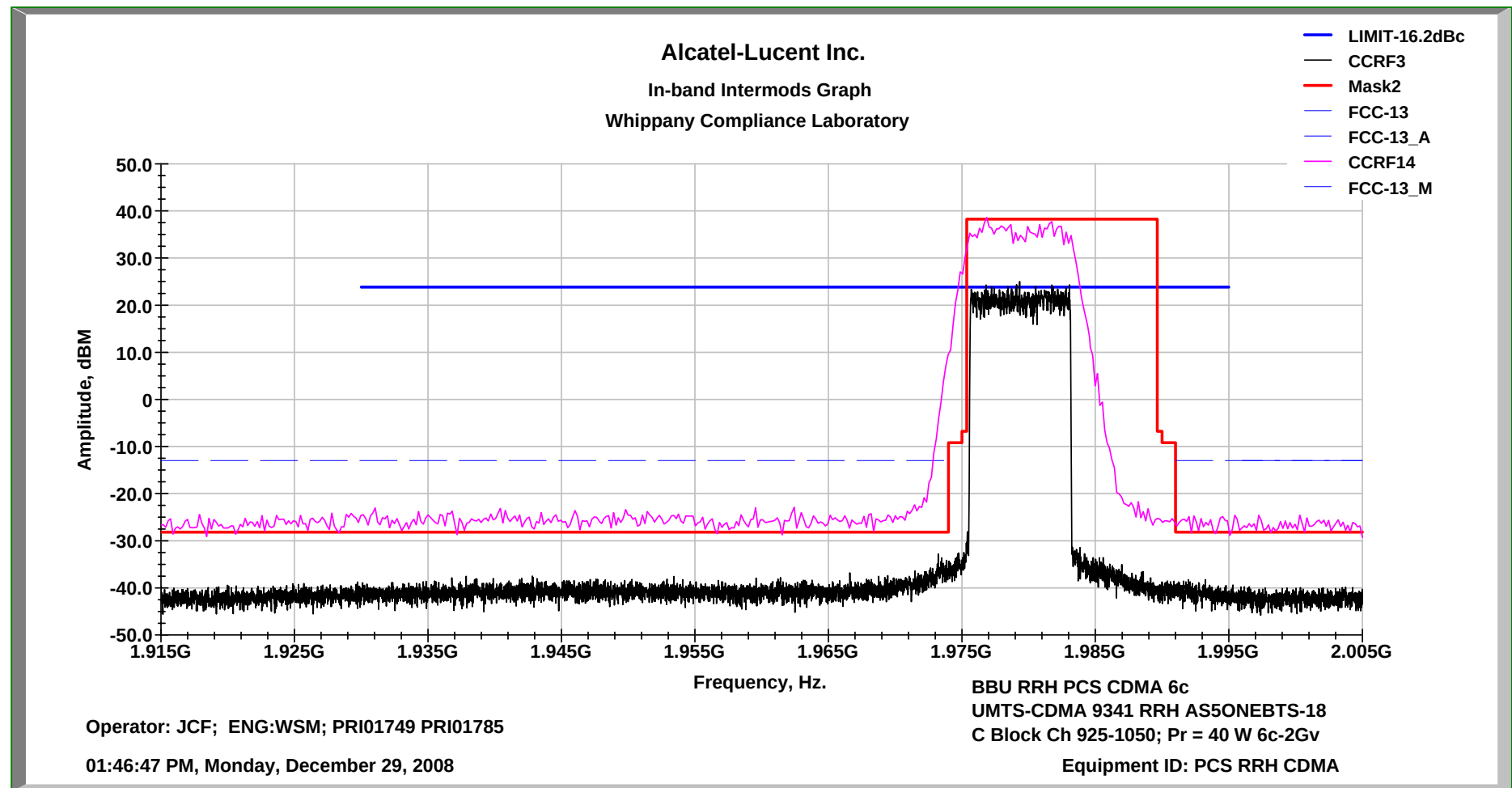


In-band Inter-modulation PCS Ch B5 550-675 6c 2Gv w/EDPD 40W Total

Occupied Bandwidth

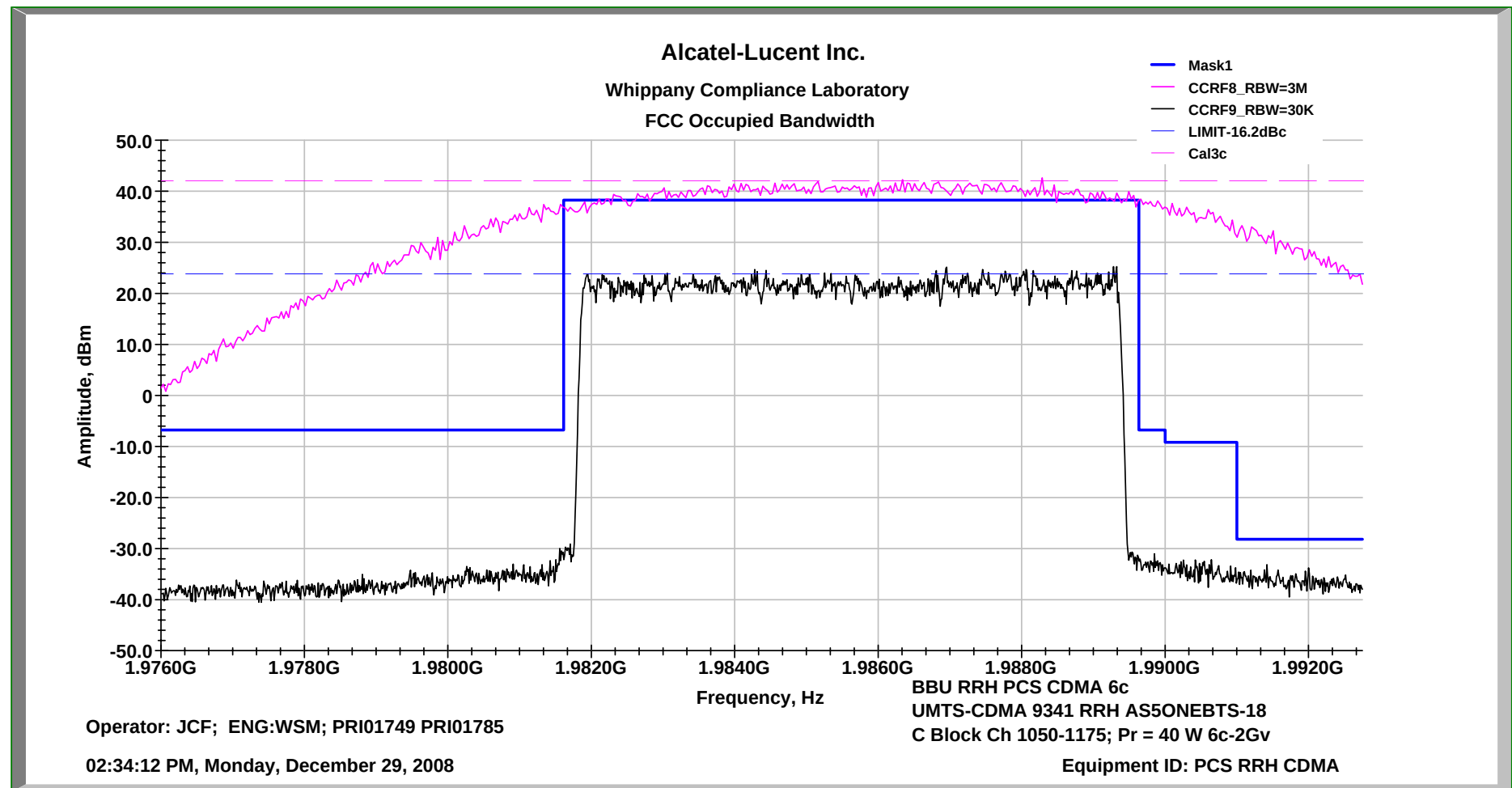
PCS Ch C1 925-1050 6c 2Gv w/EDPD 40W Total

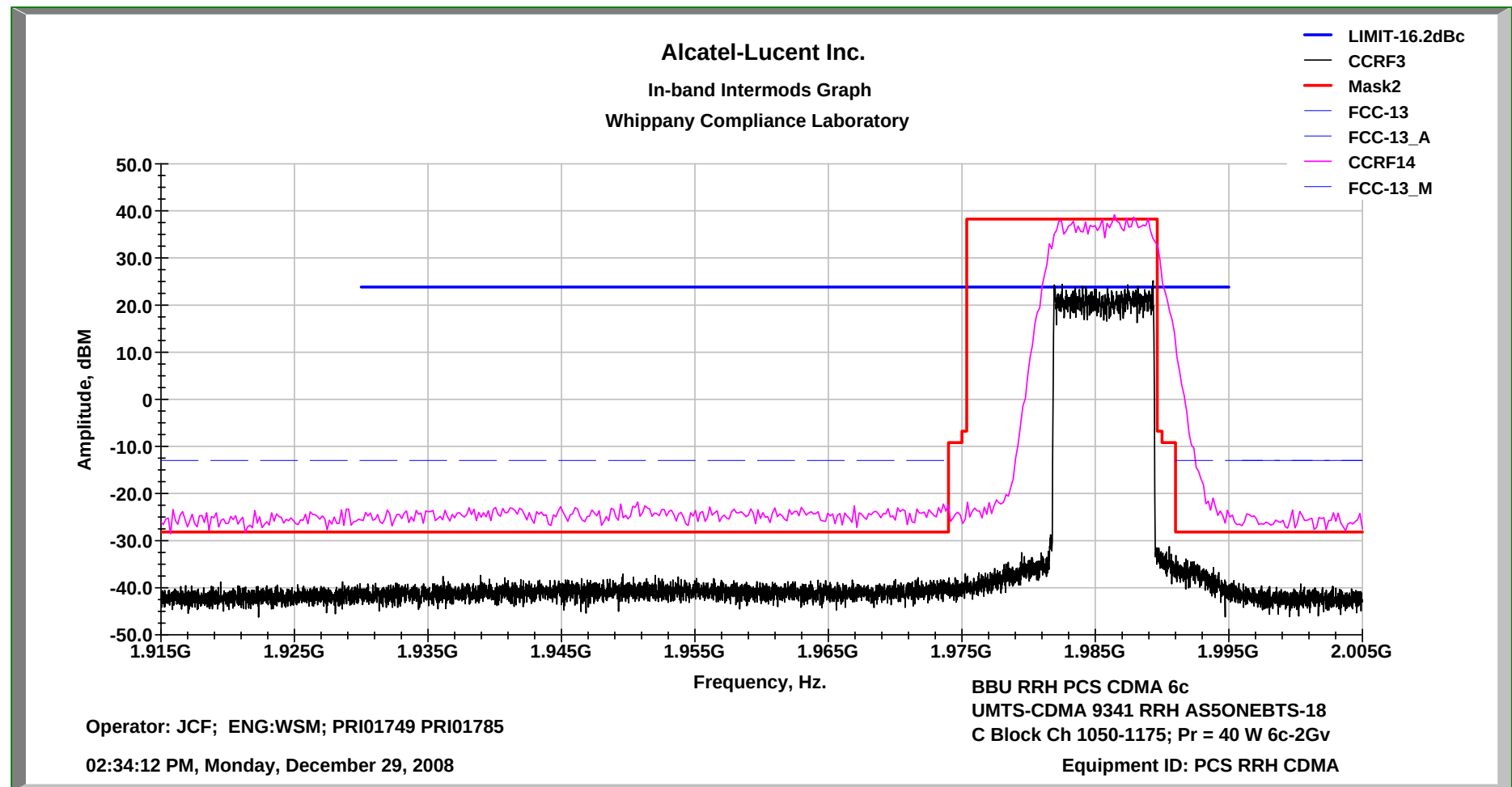


In-band Inter-modulation PCS Ch C1 925-1050 6c 2Gv w/EDPD 40W Total

Occupied Bandwidth

PCS Ch C5 1050, 1175 6c 2Gv w/EDPD 40W Total



In-band Inter-modulation PCS Ch C5 1050-1175 6c 2Gv w/EDPD 40W Total

**Seven Carrier Occupied Bandwidth Data
PCS Blocks A, D, B, E, F, and C
Left side of Block, Right side of Block
And
PCS Band Intermodulation**

Occupied Bandwidth

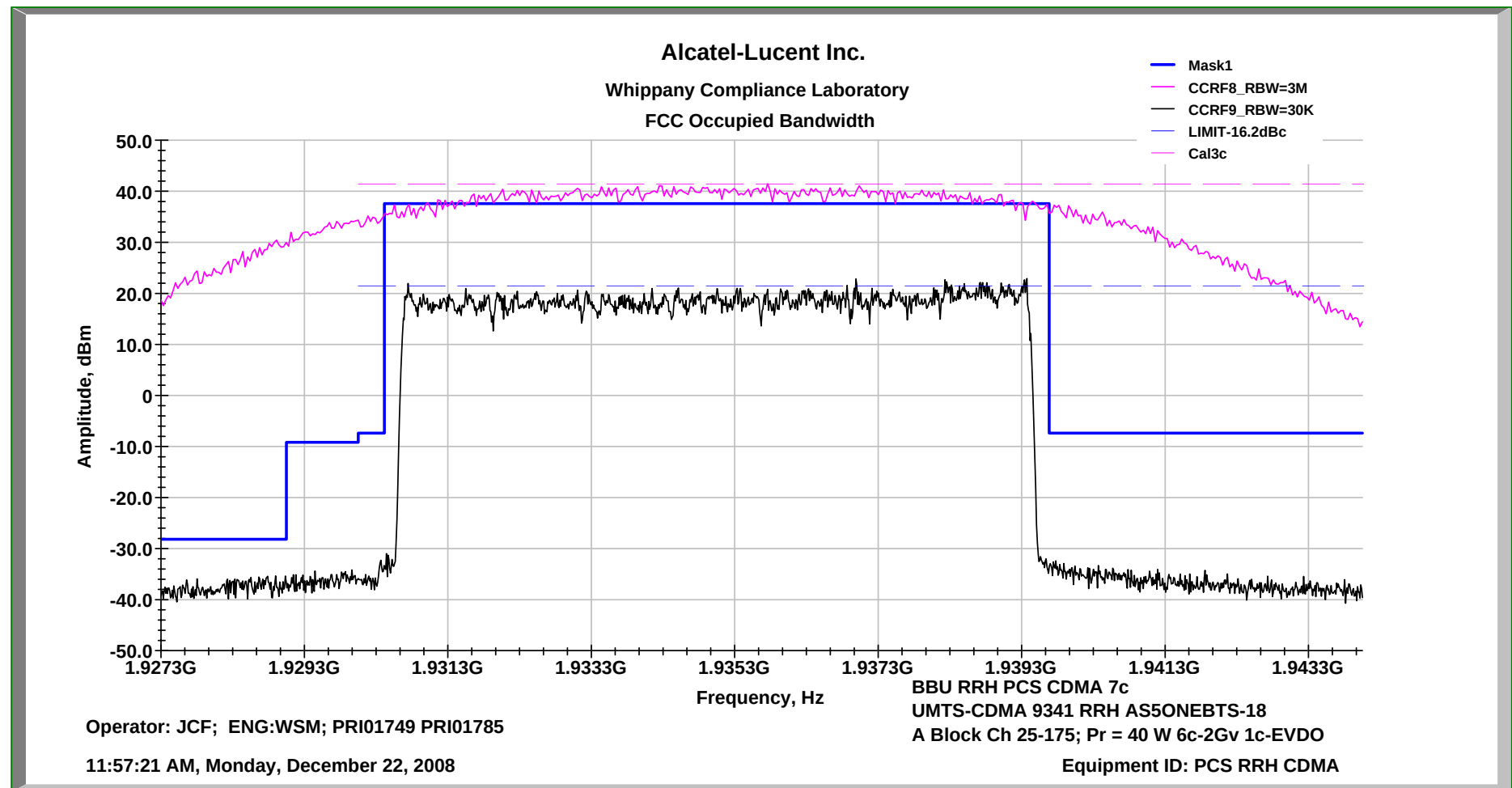
PCS Ch A1 25- 175

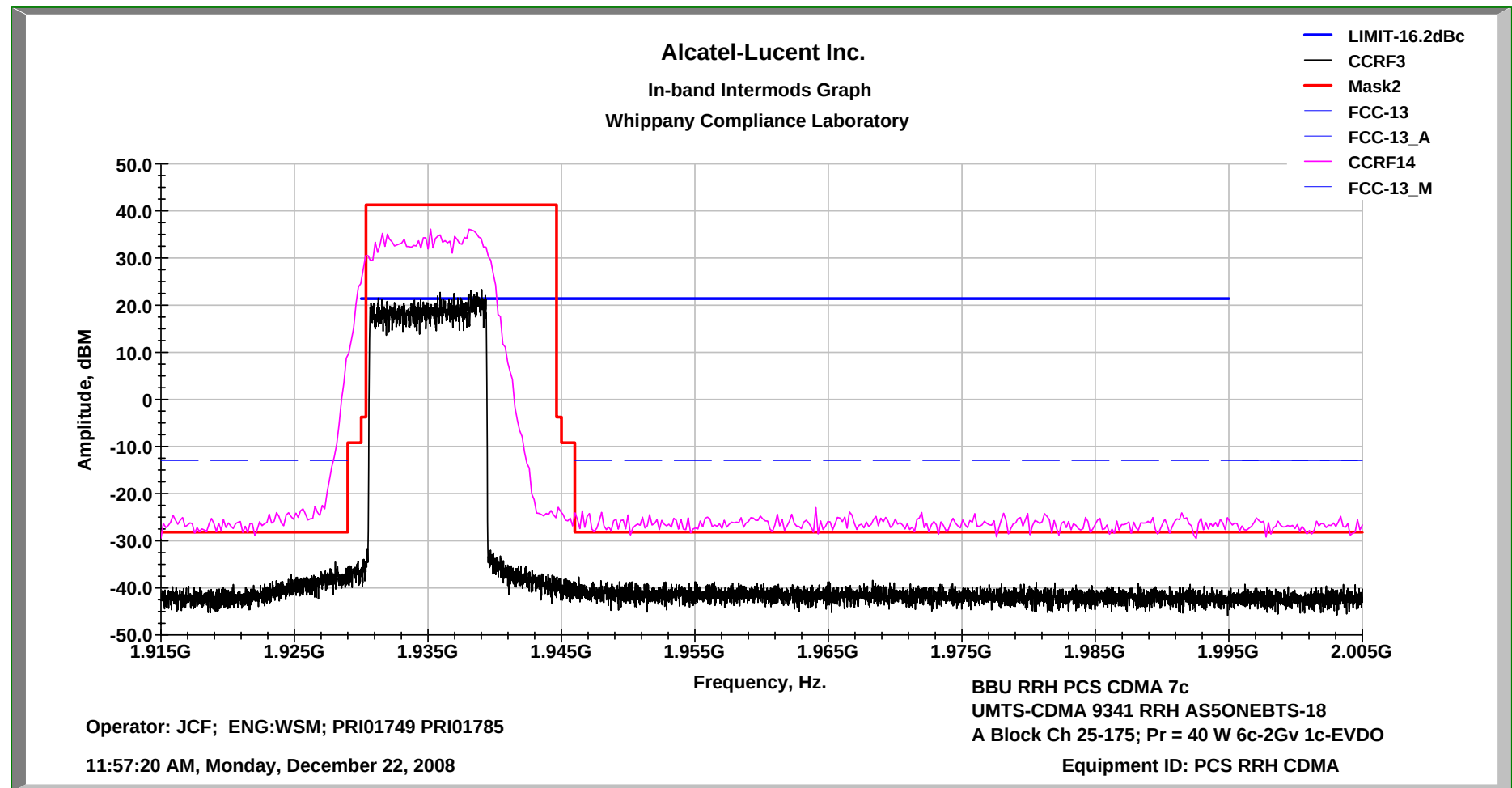
7c

2Gv

w/EDPD

40W Total



In-band Inter-modulation PCS Ch A1 25- 175 7c 2Gv w/EDPD 40W Total

Occupied Bandwidth

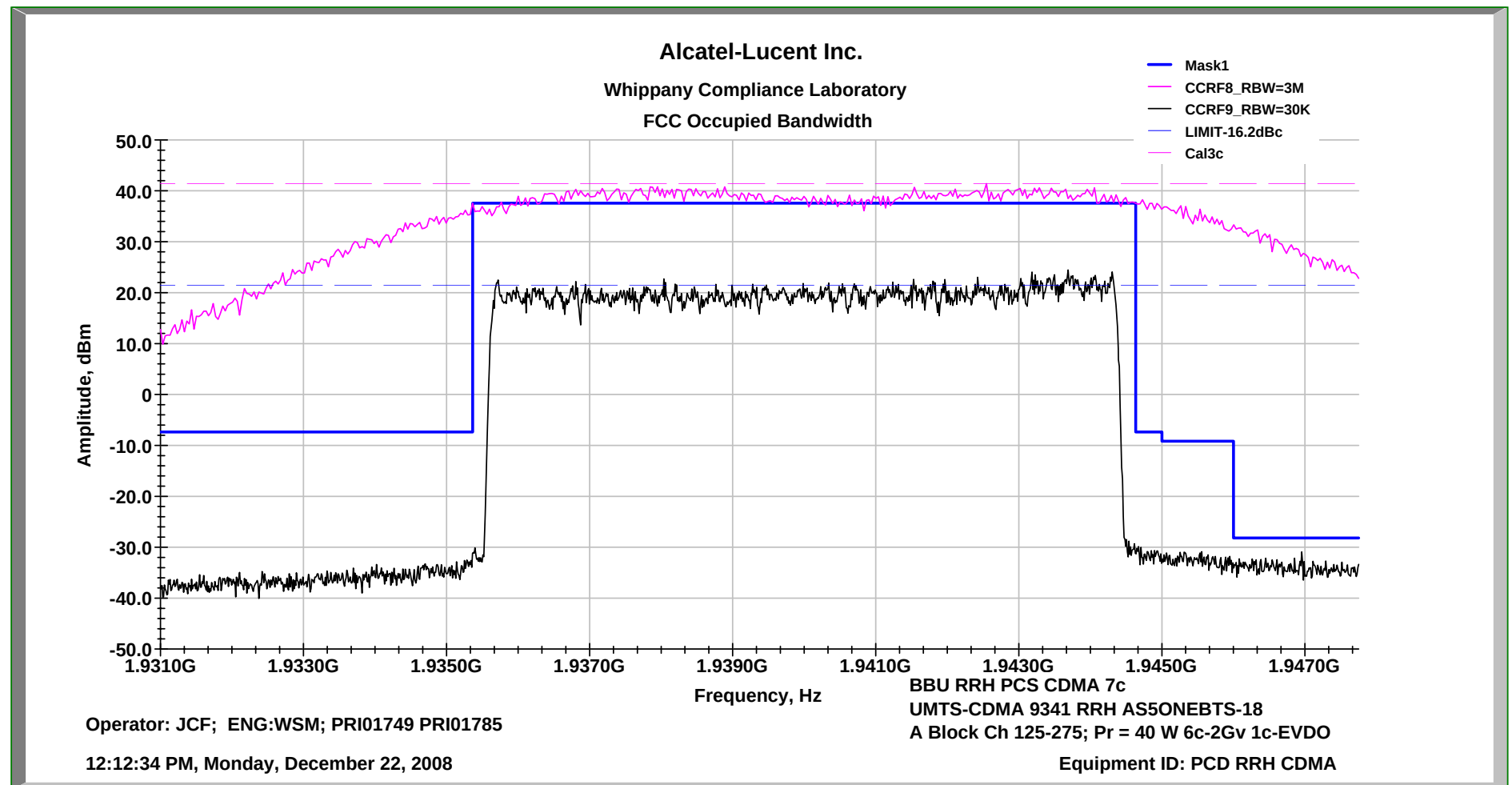
PCS Ch A5 125-275

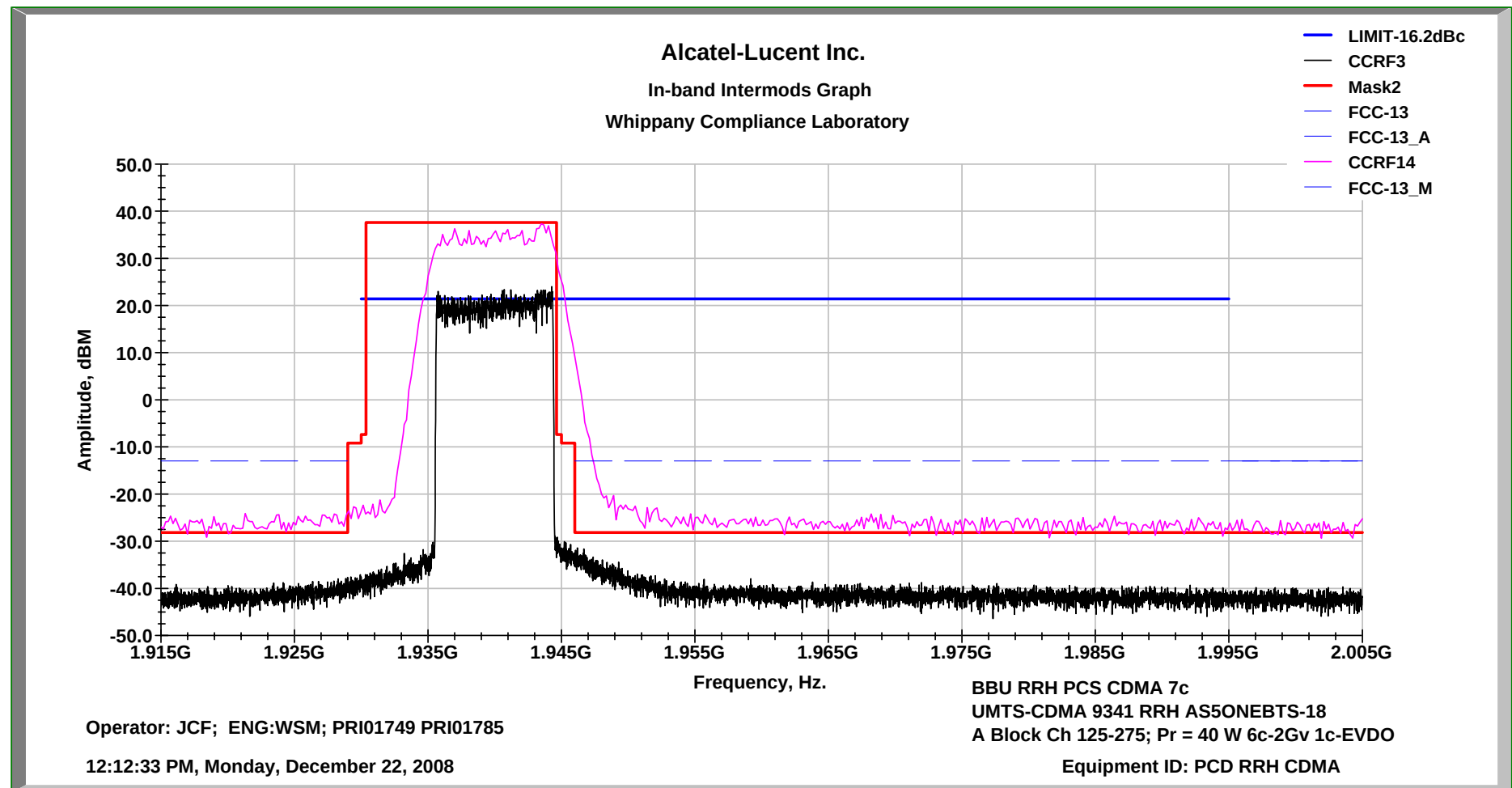
7c

2Gv

w/EDPD

40W Total



In-band Inter-modulation PCS Ch A5 125-275 7c 2Gv w/EDPD 40W Total

Occupied Bandwidth

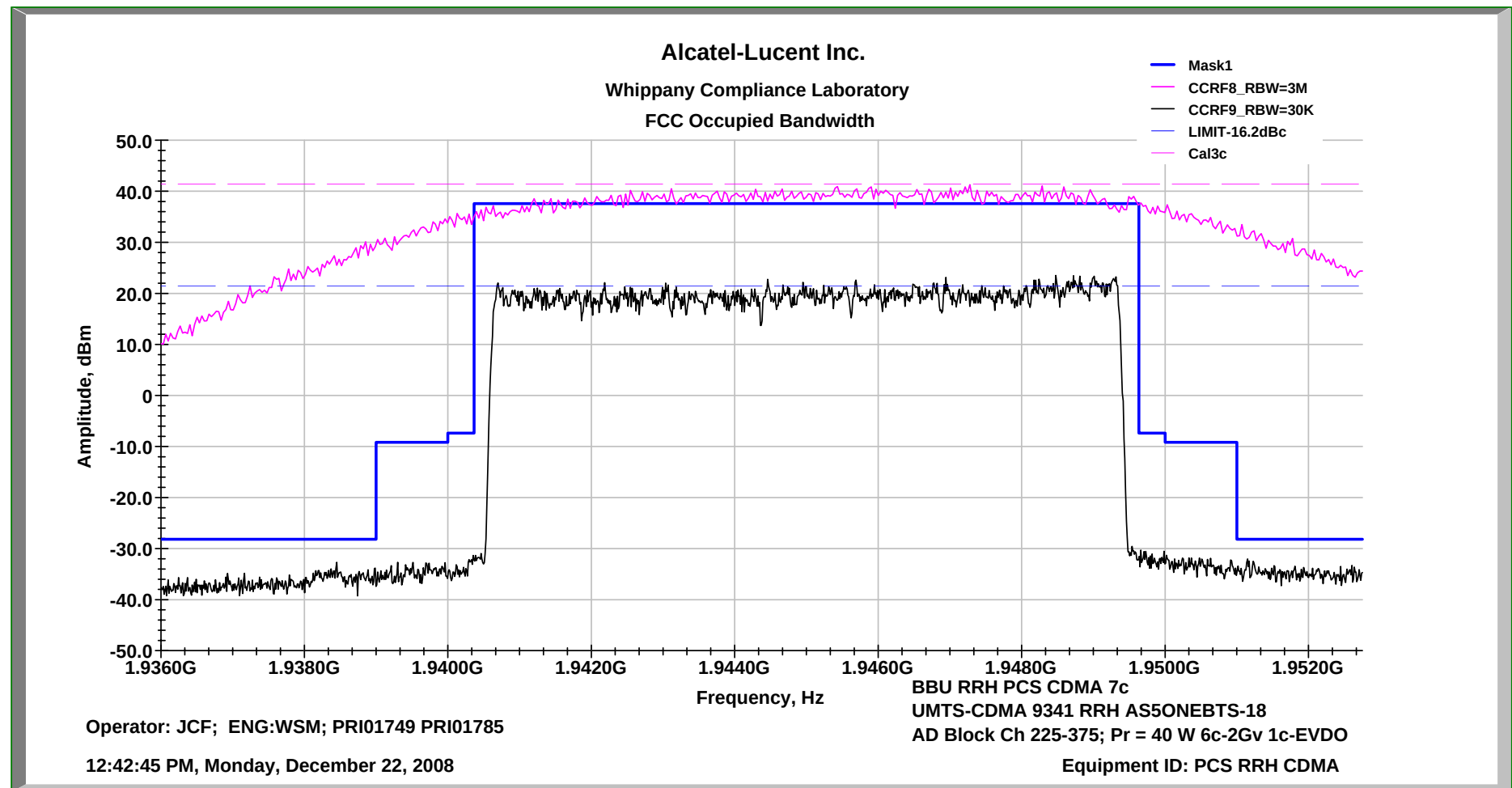
PCS Ch AD 225-350

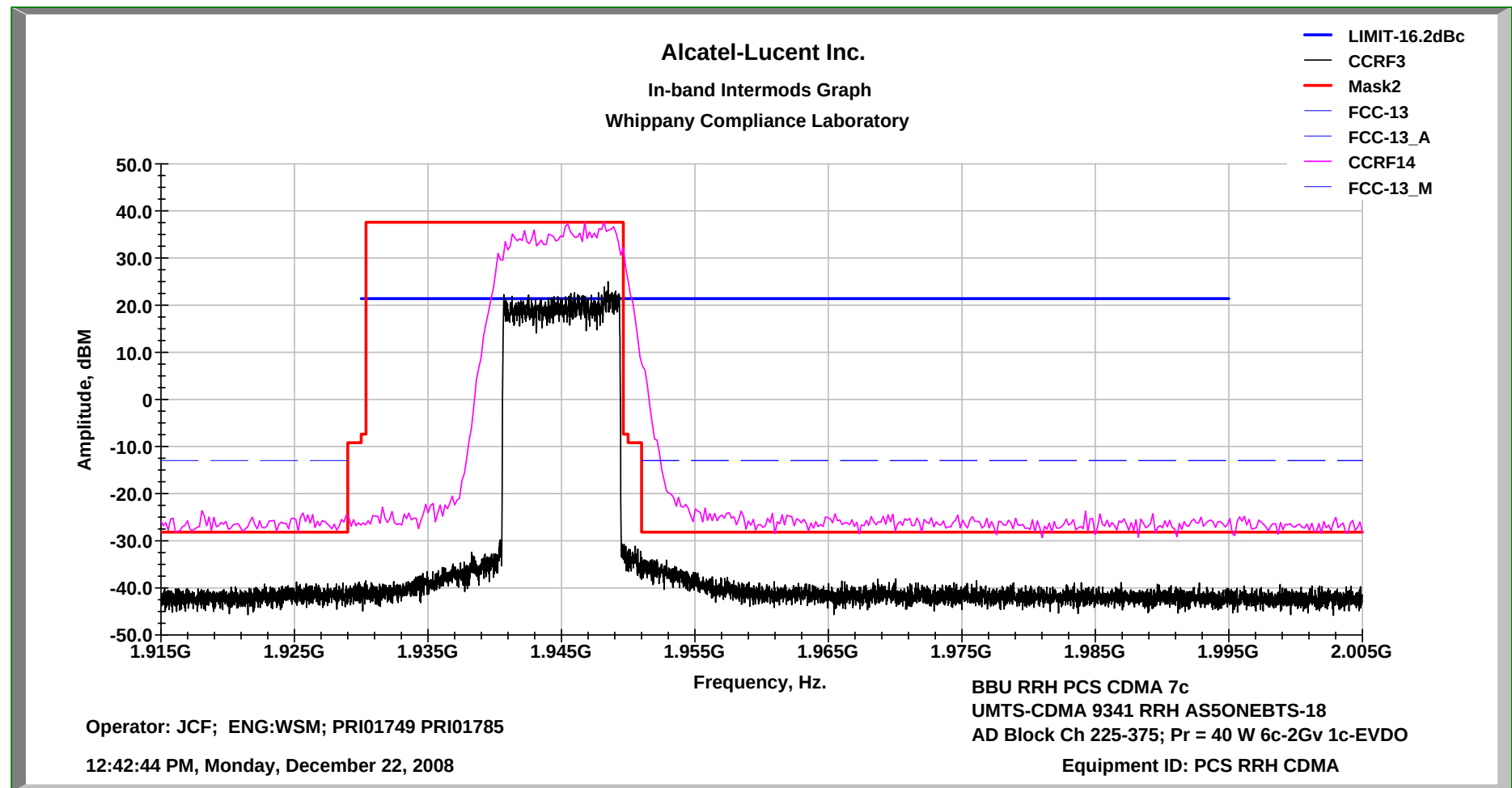
7c

2Gv

w/EDPD

40W Total



In-band Inter-modulation PCS Ch AD 225-350 7c 2Gv w/EDPD 40W Total

Occupied Bandwidth

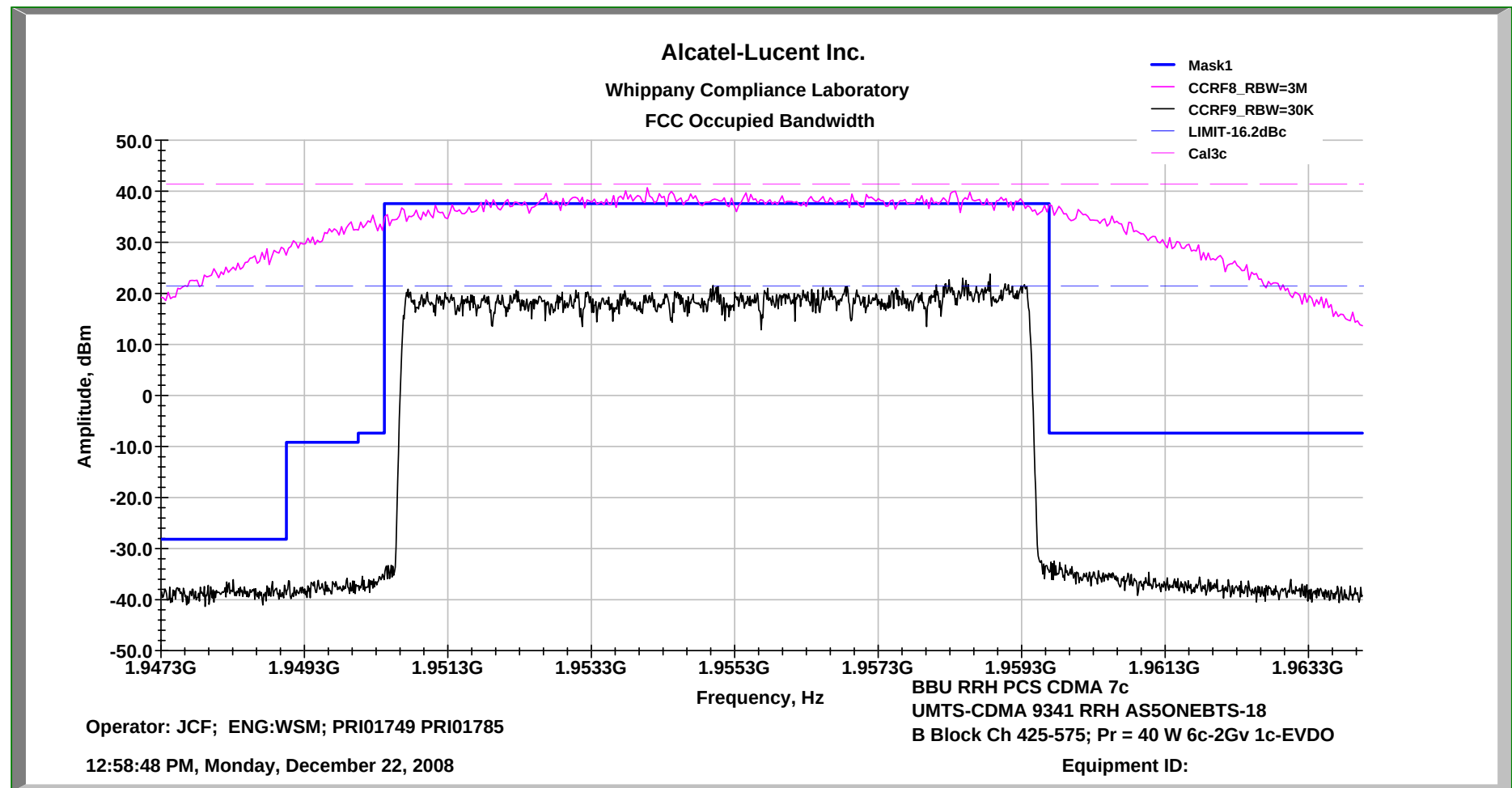
PCS Ch B1 425-575

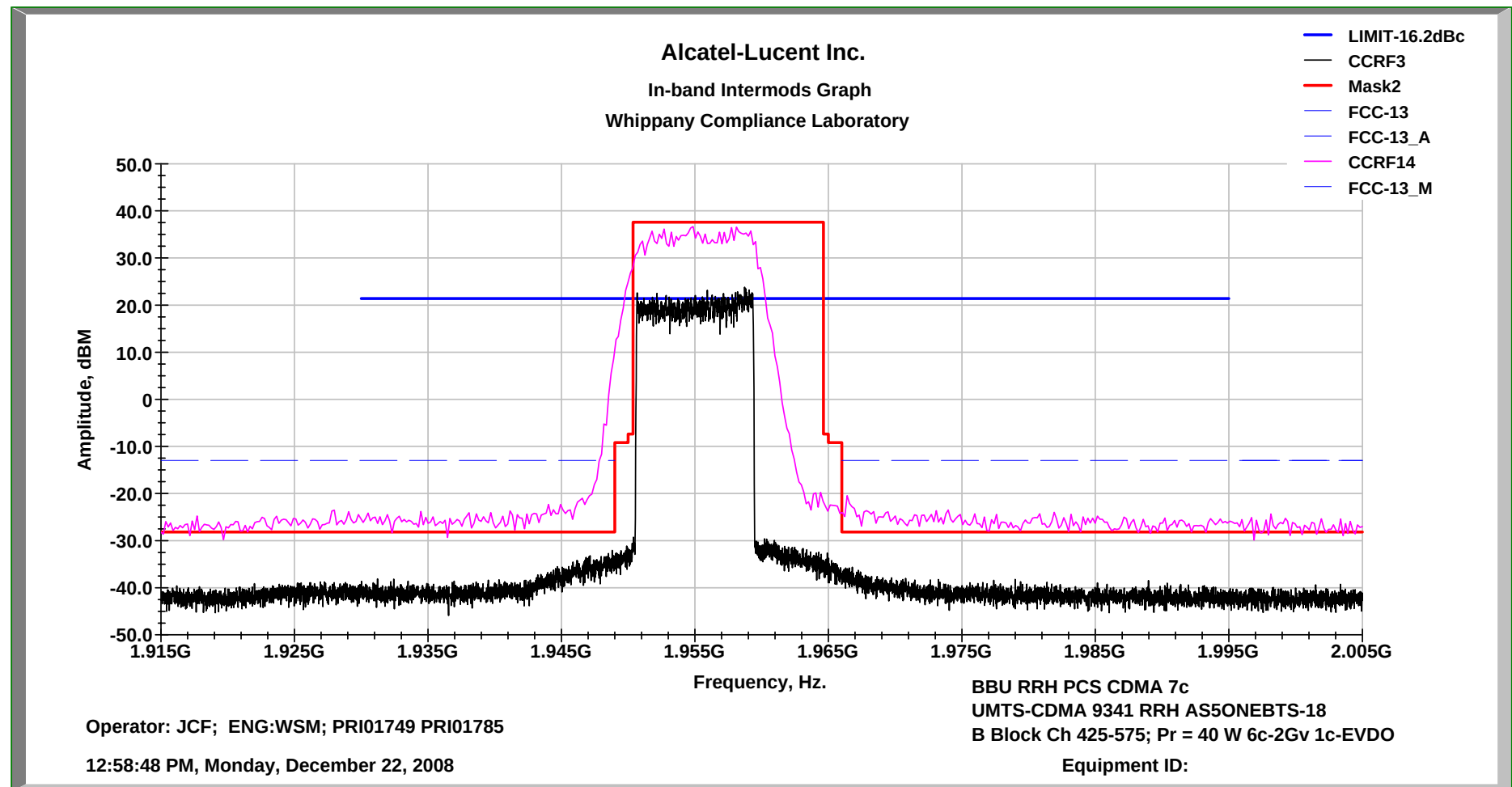
7c

2Gv

w/EDPD

40W Total



In-band Inter-modulation PCS Ch B1 425-575 7c 2Gv w/EDPD 40W Total

Occupied Bandwidth

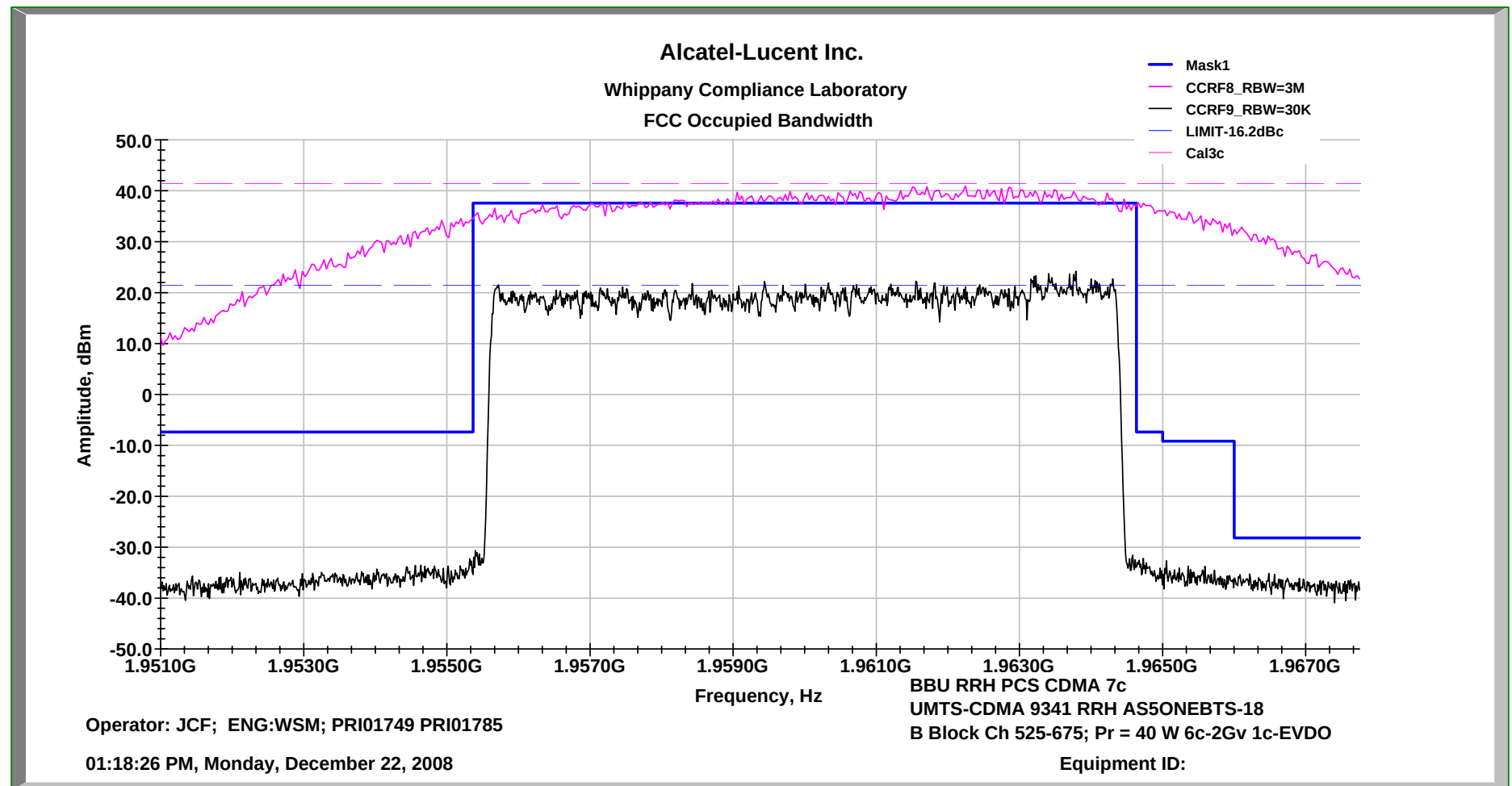
PCS Ch B5 525-675

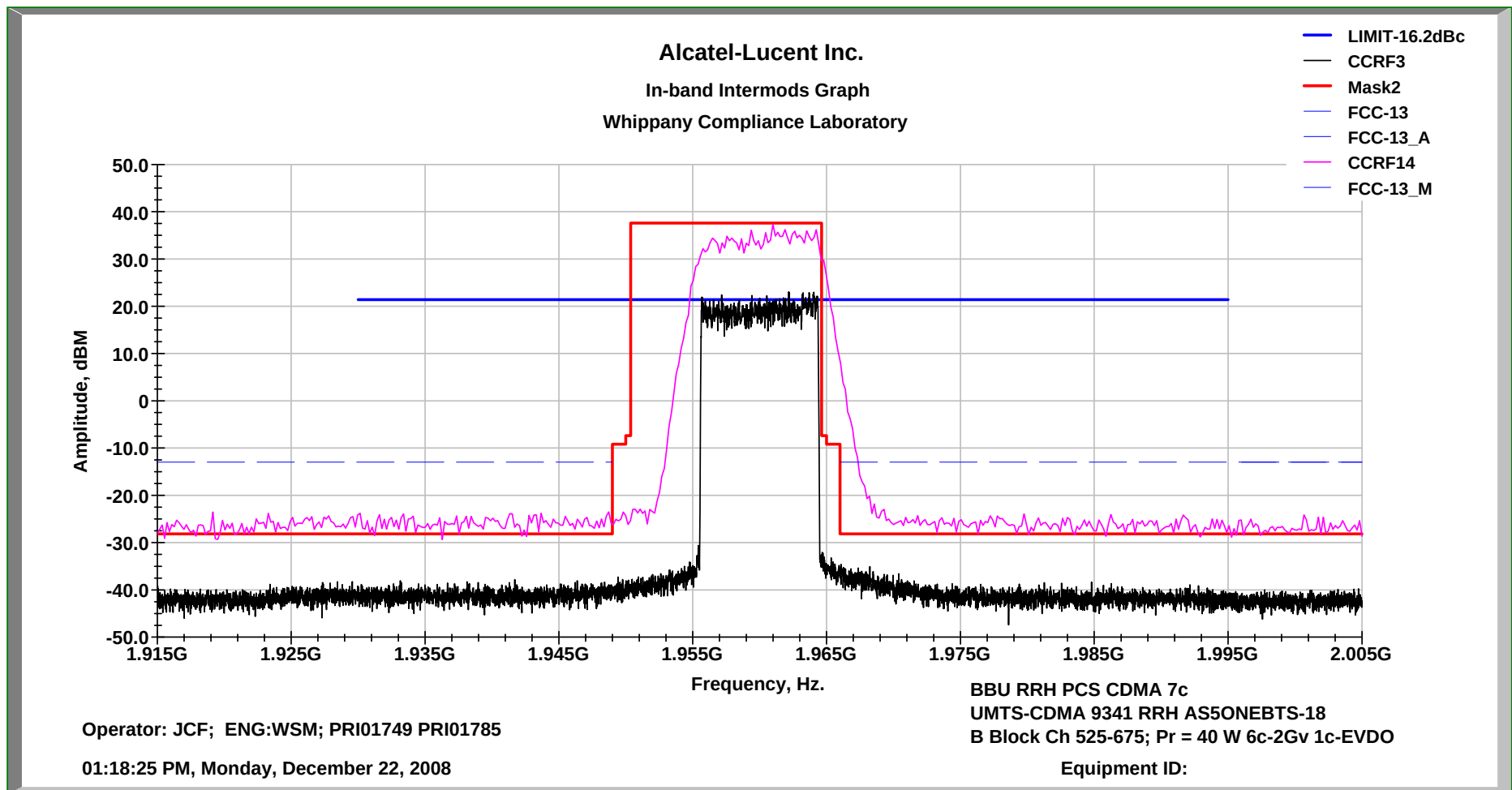
7c

2Gv

w/EDPD

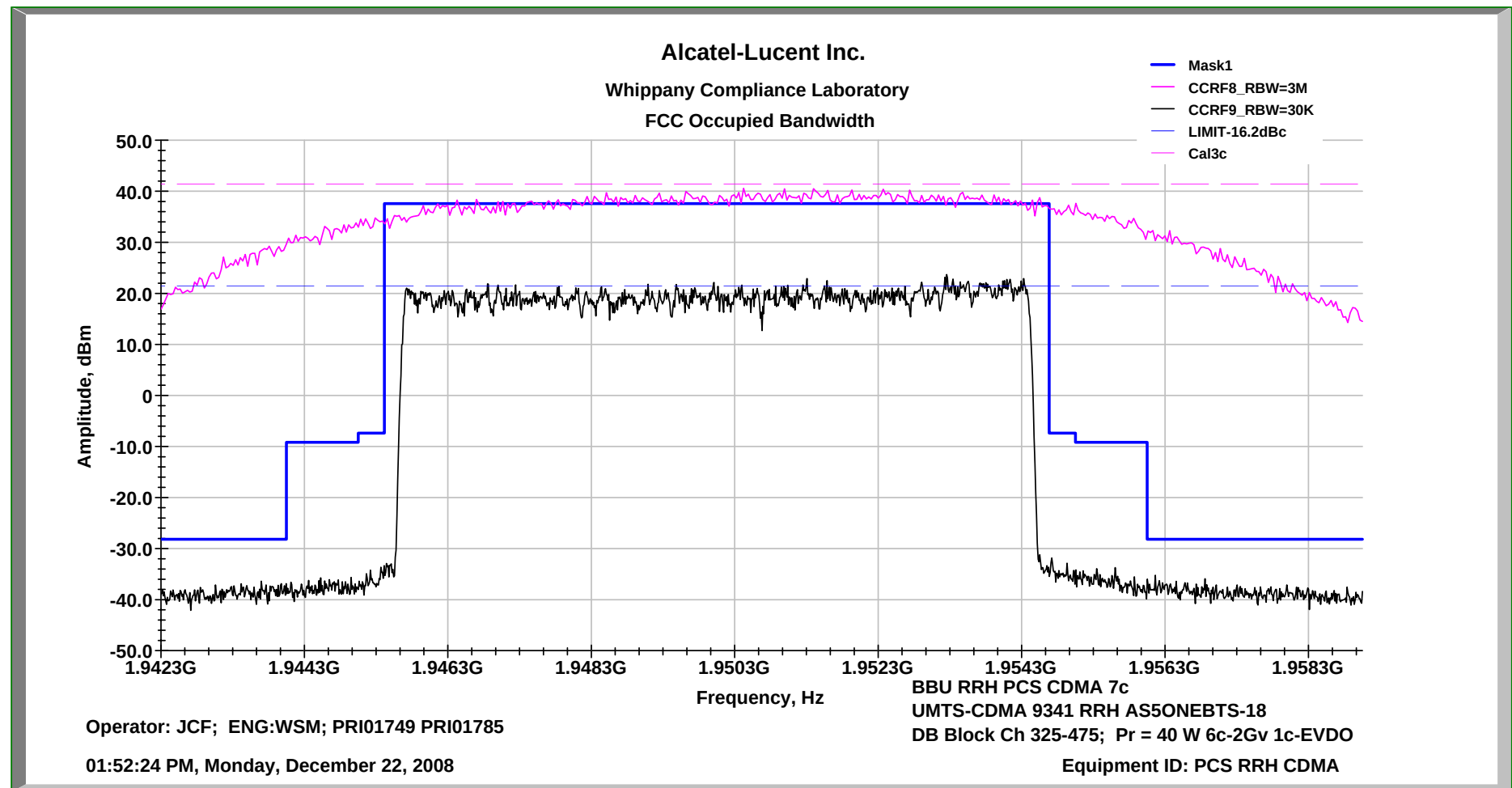
40W Total



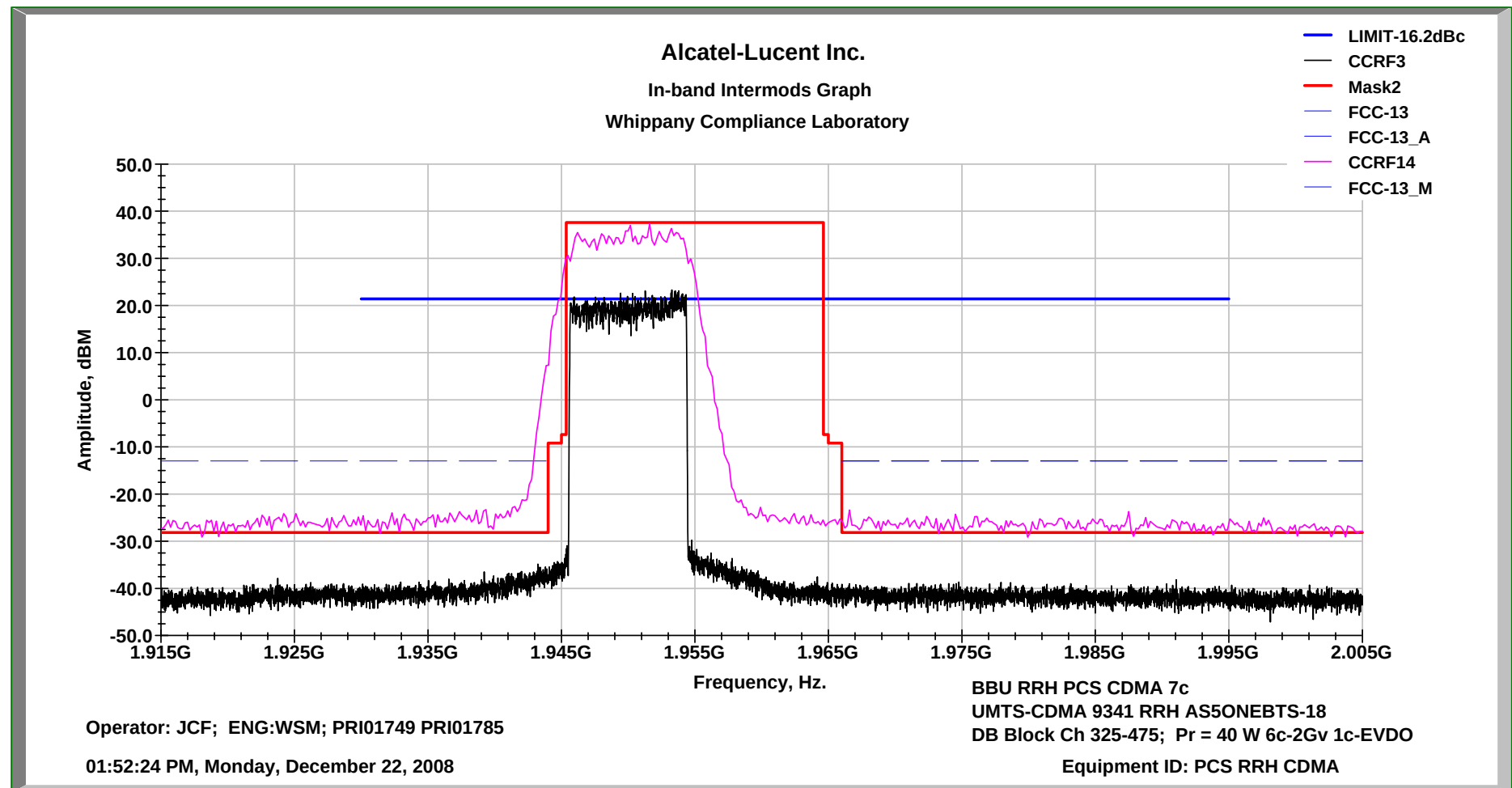
In-band Inter-modulation PCS Ch B5 525-675 7c 2Gv w/EDPD 40W Total

Occupied Bandwidth

PCS Ch DB 325-475 7c 2Gv w/EDPD 40W Total



In-band Inter-modulation PCS Ch DB 325-475 7c 2Gv w/EDPD 40W Total



Occupied Bandwidth

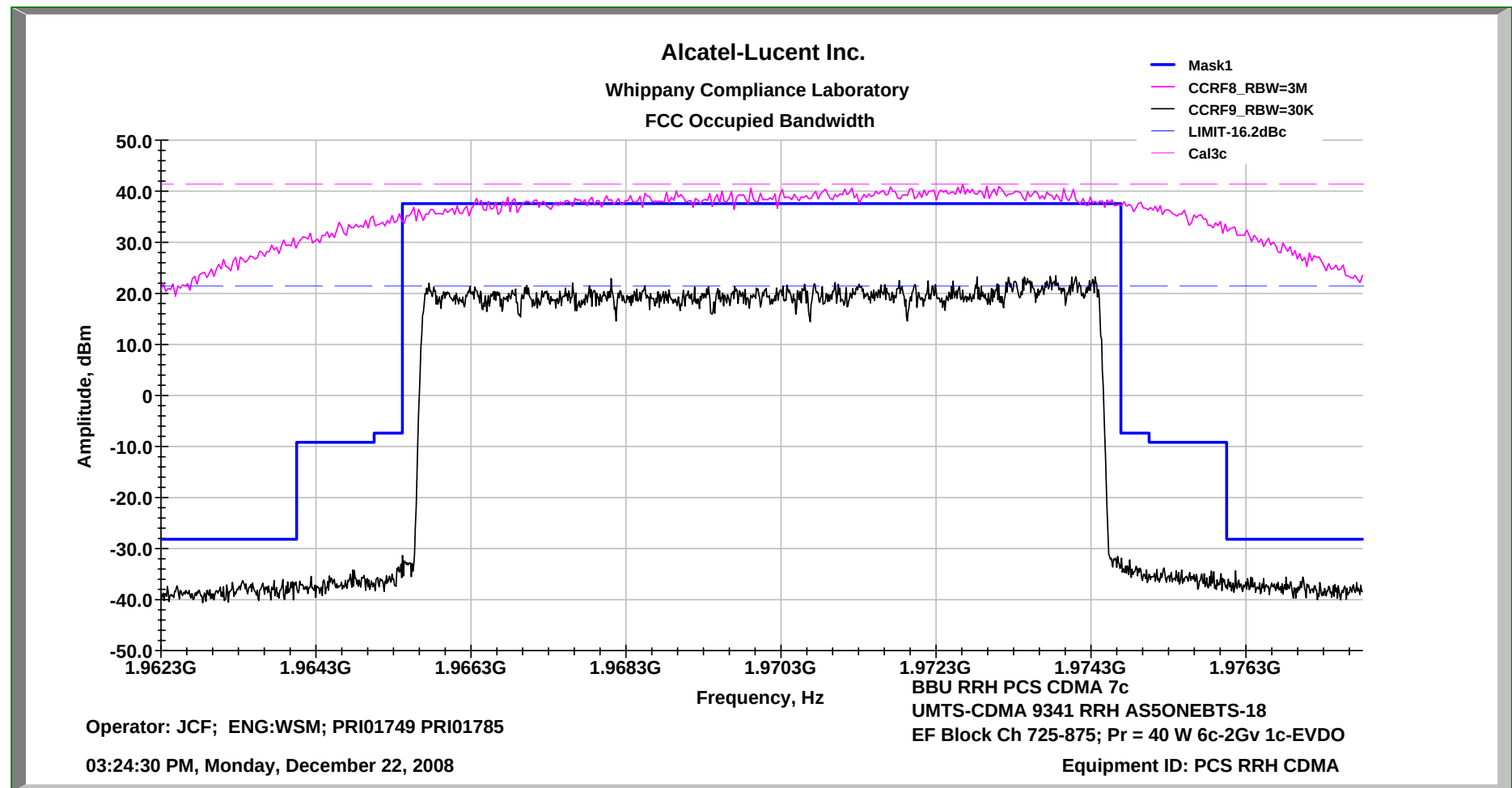
PCS Ch EF 725-875

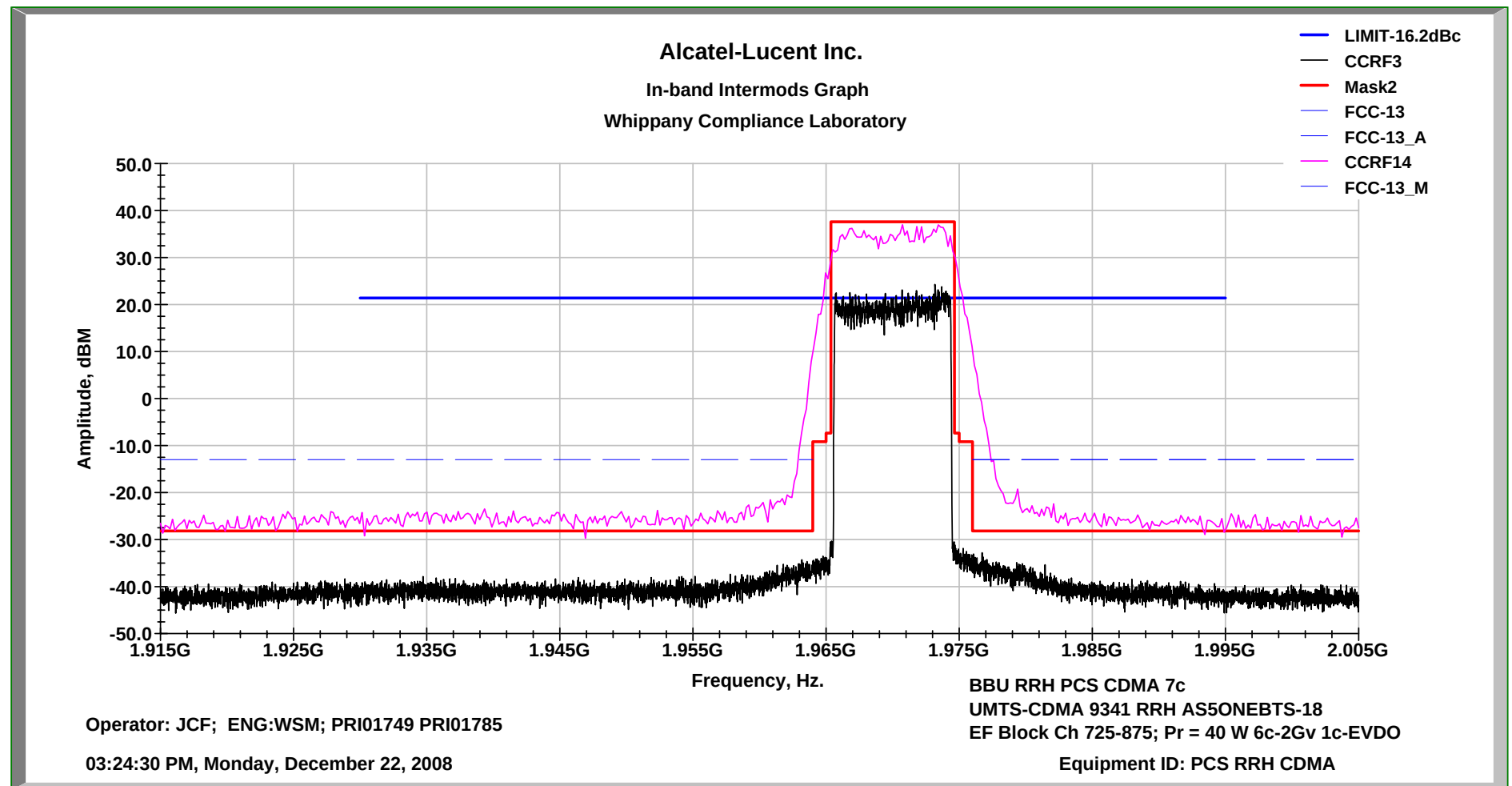
7c

2Gv

w/EDPD

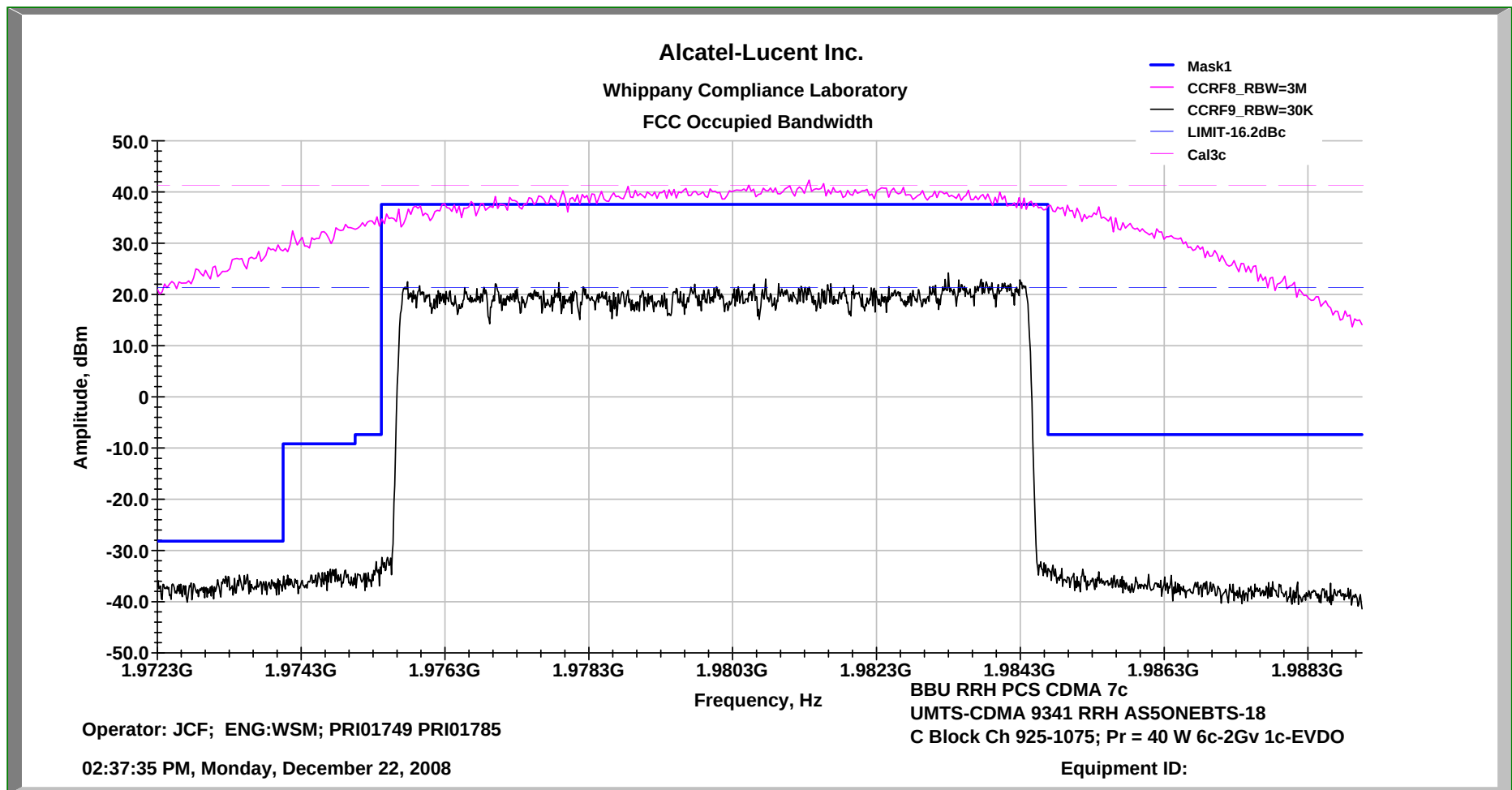
40W Total

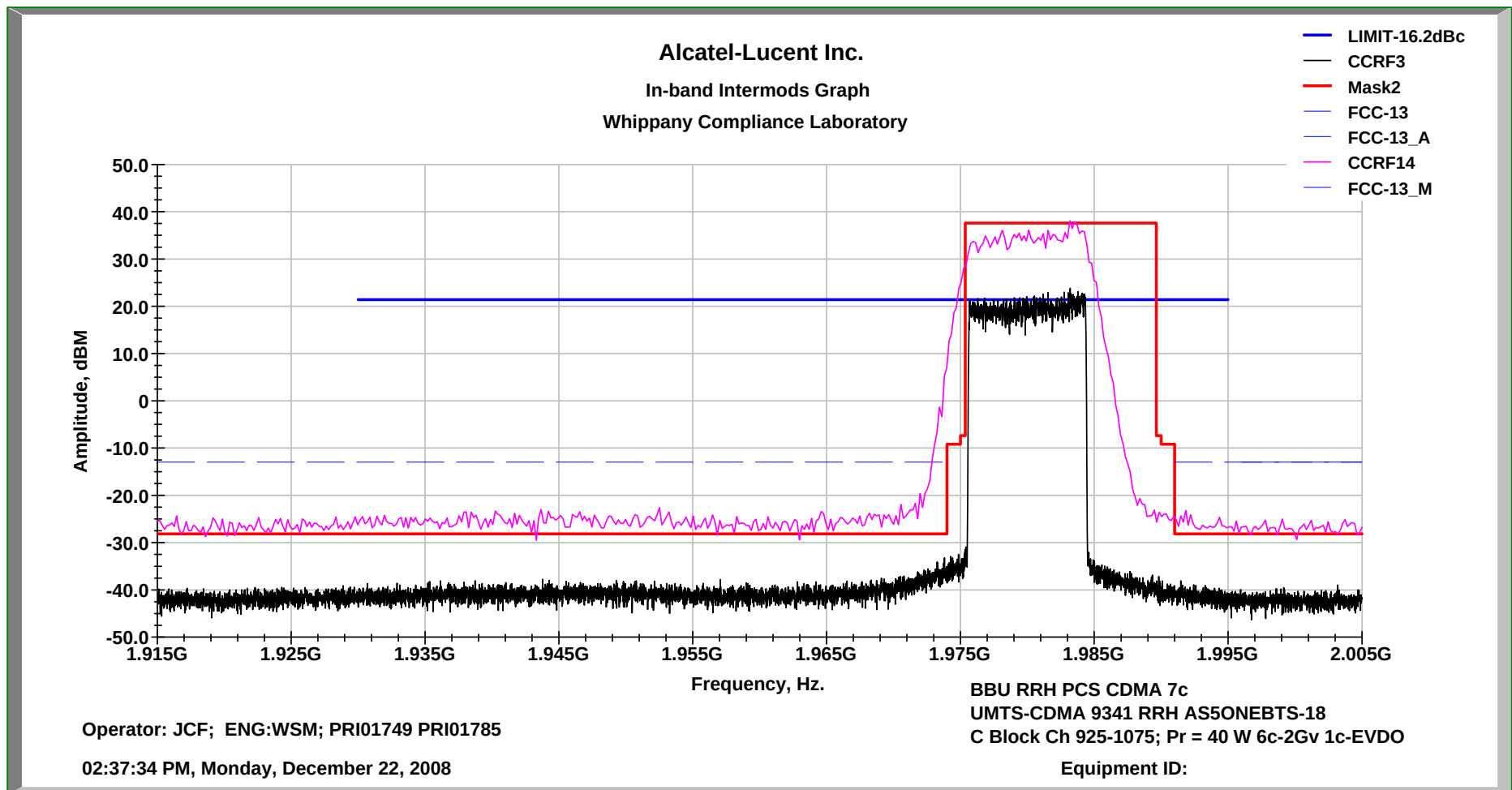


In-band Inter-modulation PCS Ch EF 725-875 7c 2Gv w/EDPD 40W Total

Occupied Bandwidth

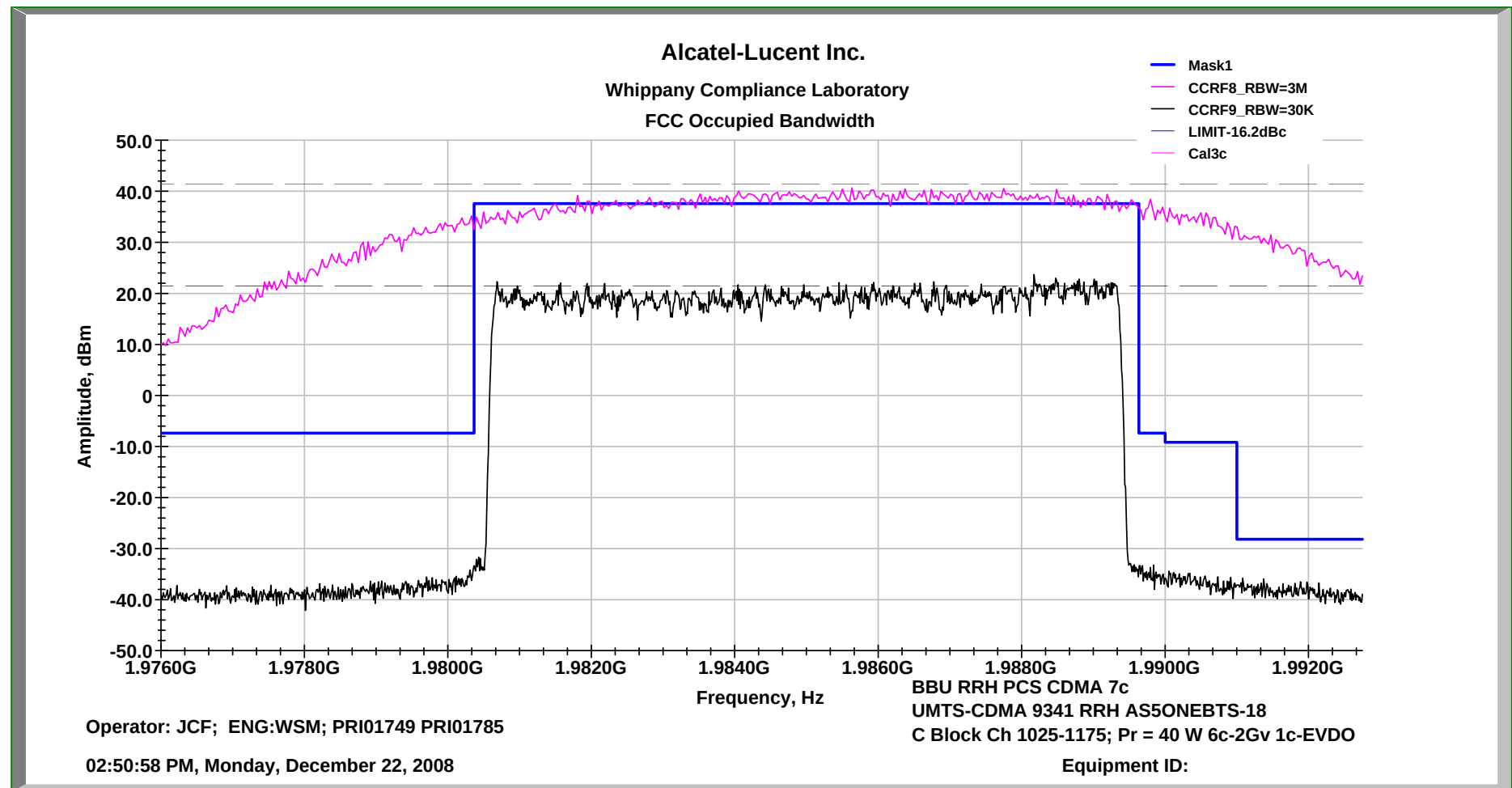
PCS Ch C1 925-1075 7c 2Gv w/EDPD 40W Total



In-band Inter-modulation PCS Ch C1 925-1075 7c 2Gv w/EDPD 40W Total

Occupied Bandwidth

PCS Ch C5 1025-1175 7c 2Gv w/EDPD 40W Total



In-band Inter-modulation PCS Ch C5 1025-1175 7c 2Gv w/EDPD 40W Total