



Enclosure Base and Ports



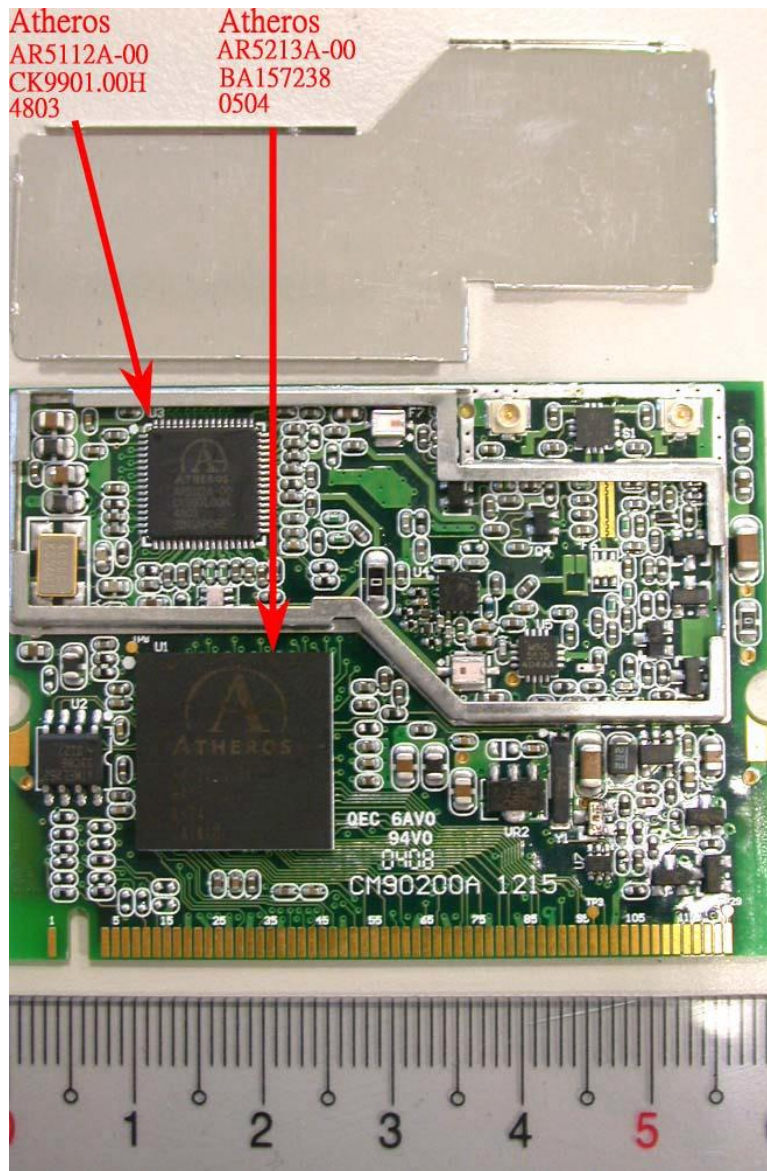
Enclosure Assembly



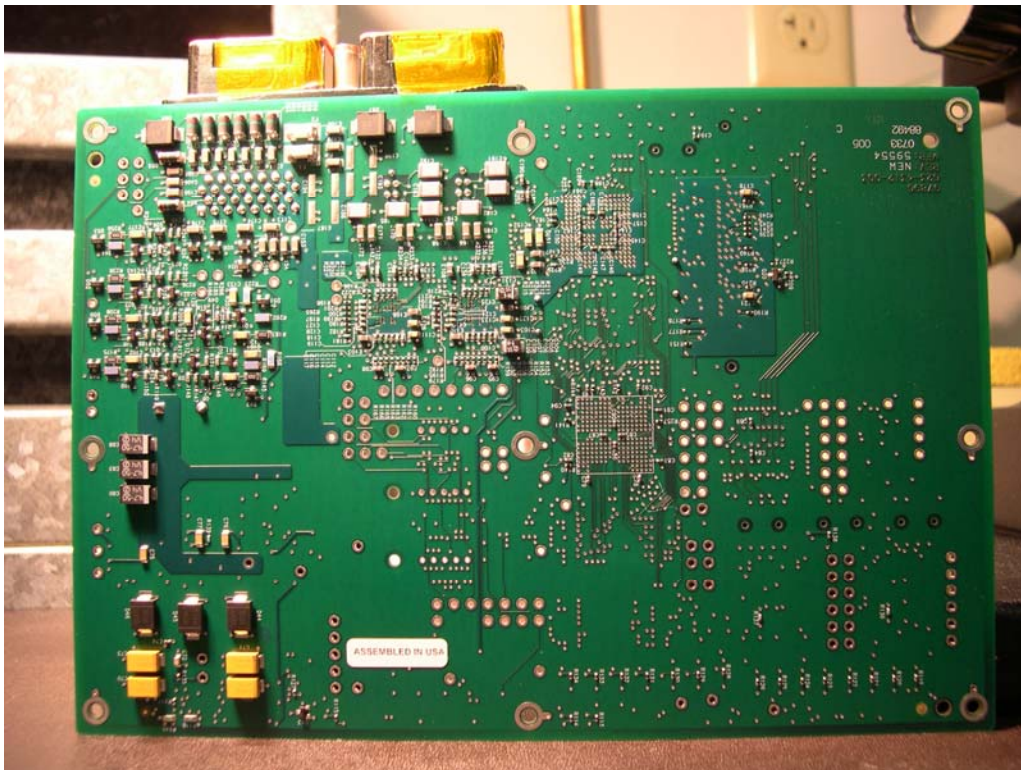
Module Host Detail



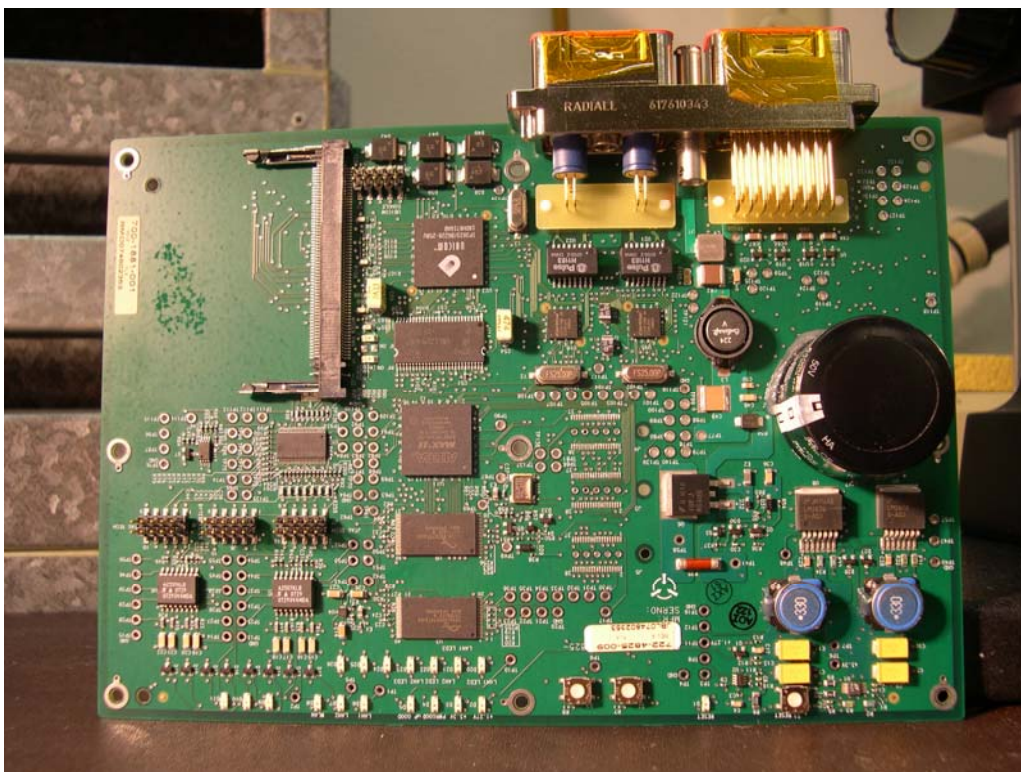
Module Internal Bottom



Module Internal Top



Submittal Photo PCB Bottom



Submittal Photo PCB Top



Submittal Photo WLU back