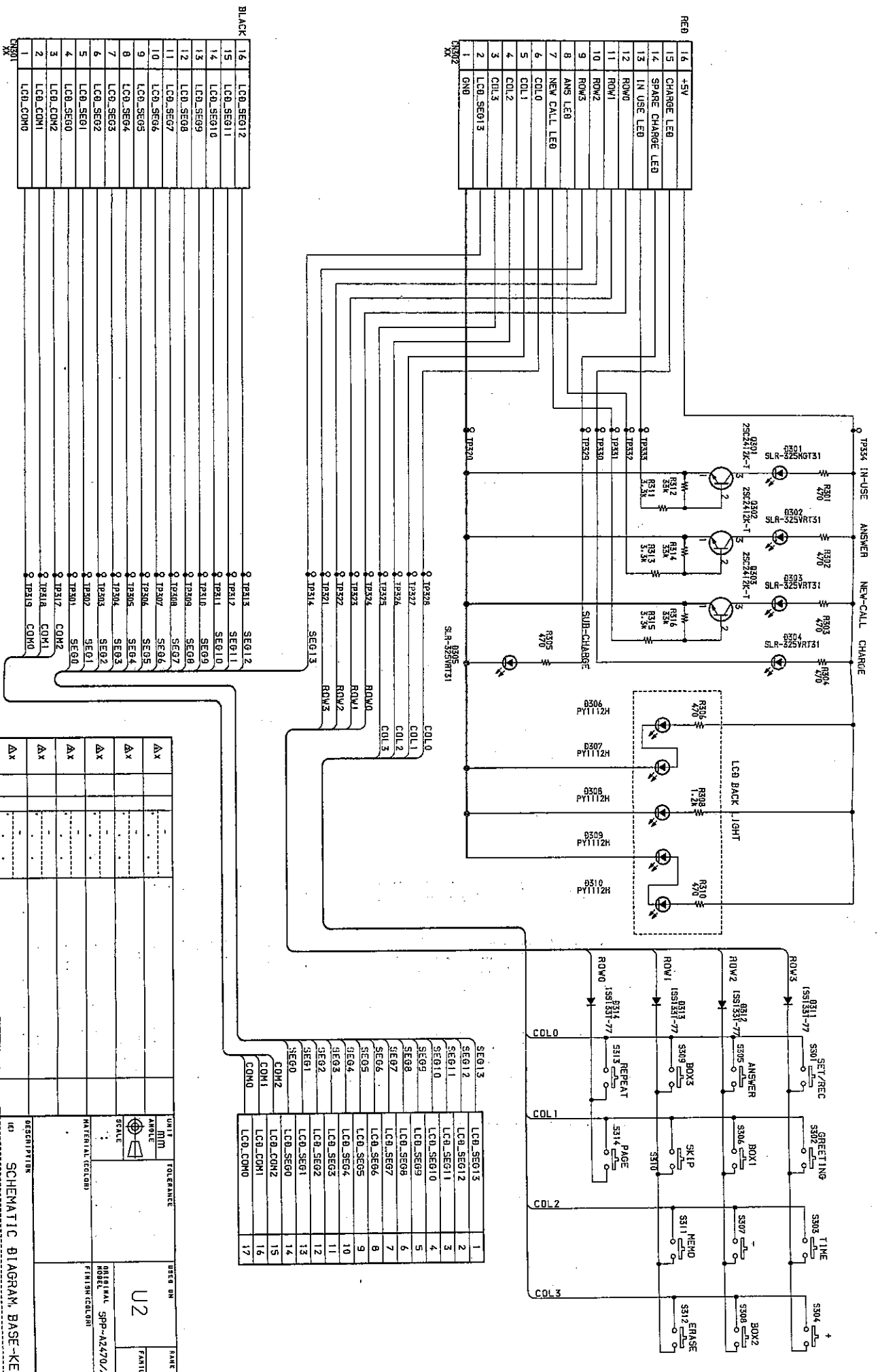
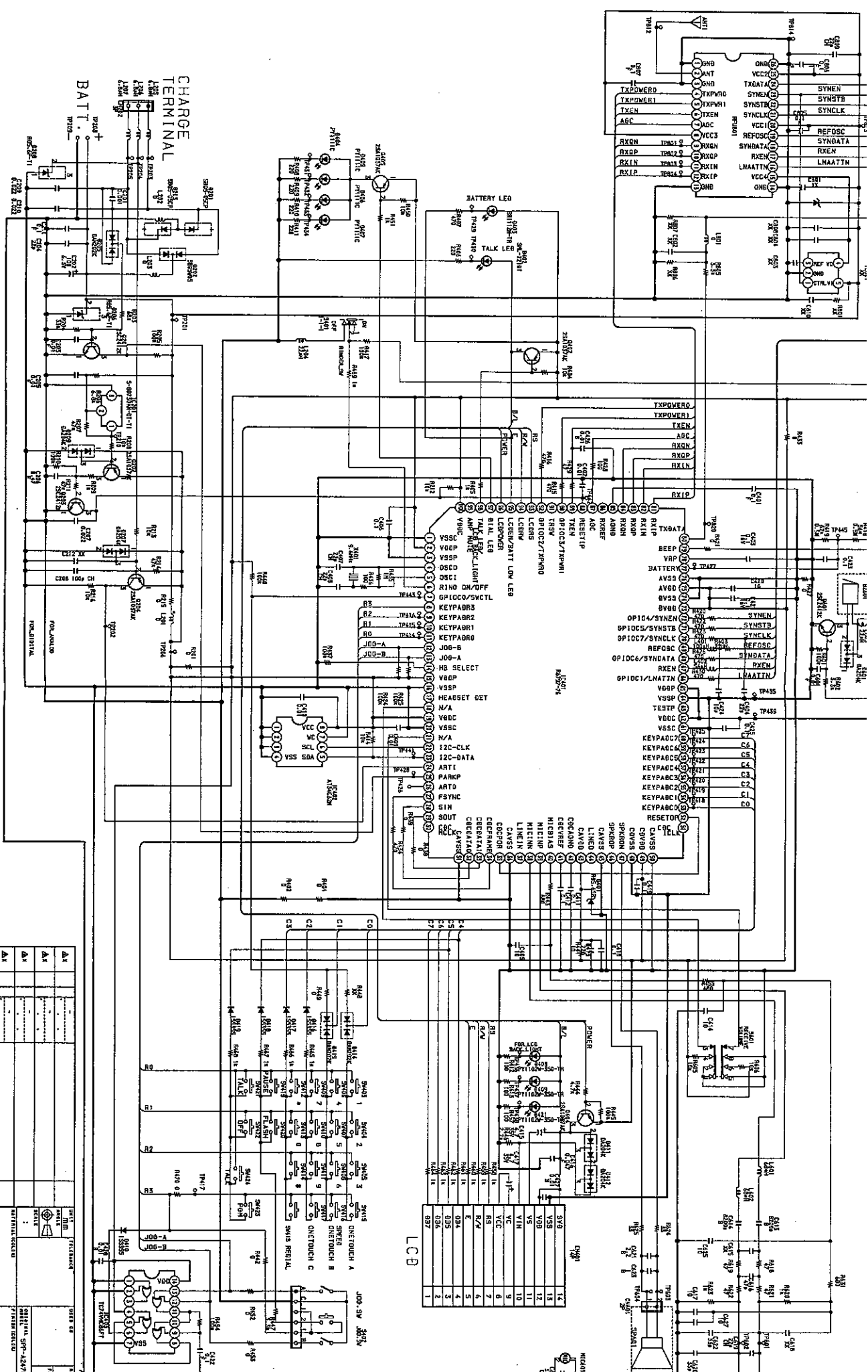


EXHIBIT D

Circuit Diagram





[illegible]