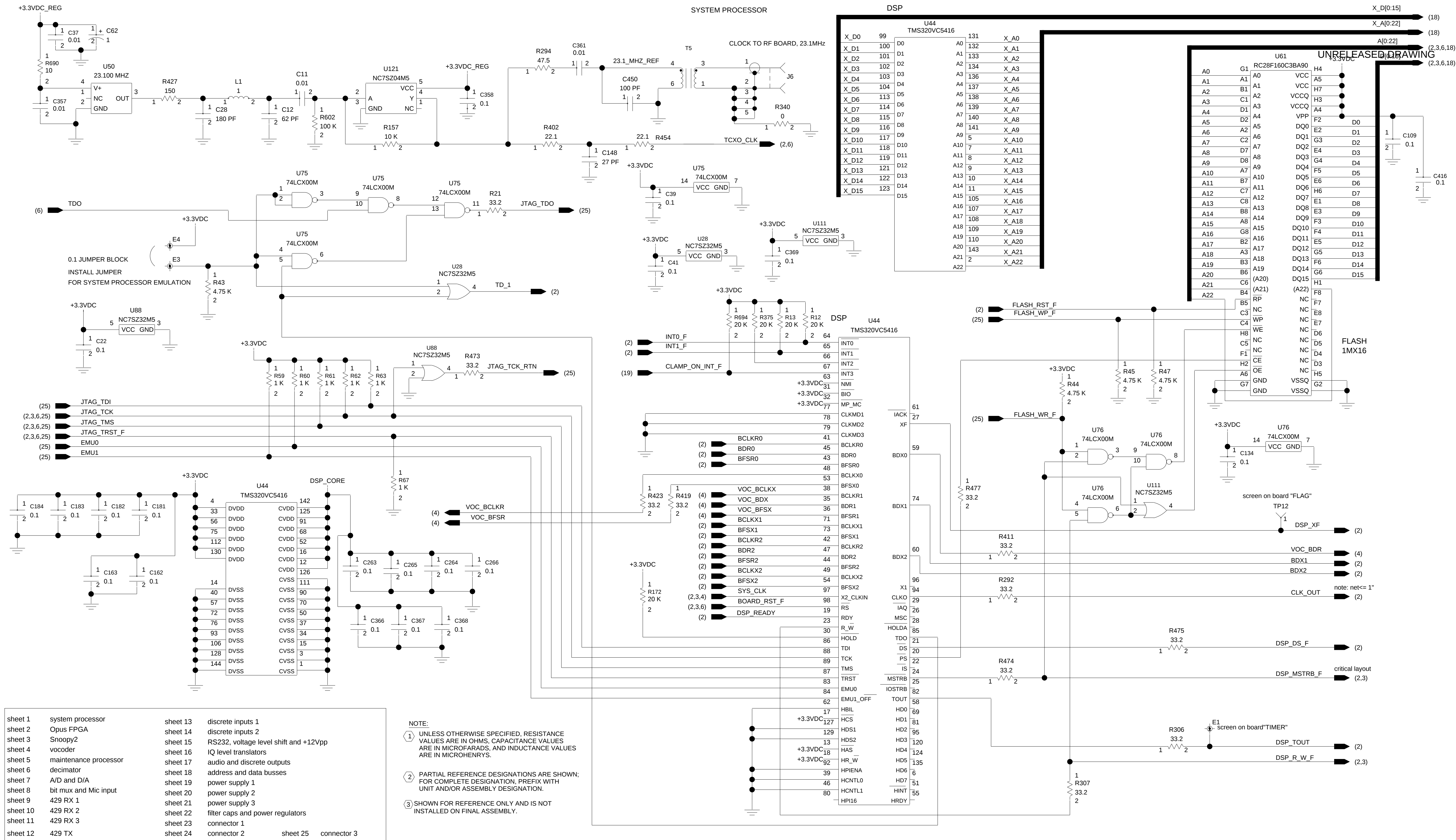


REVISIONS			
LTR	DESCRIPTION	DATE	APVD
-		2003-01-09	



sheet 1	system processor	sheet 13	discrete inputs 1	sheet 25	connector 3
sheet 2	Opus FPGA	sheet 14	discrete inputs 2		
sheet 3	Snoopy2	sheet 15	RS232, voltage level shift and +12Vpp		
sheet 4	vocoder	sheet 16	IQ level translators		
sheet 5	maintenance processor	sheet 17	audio and discrete outputs		
sheet 6	decimator	sheet 18	address and data busses		
sheet 7	A/D and D/A	sheet 19	power supply 1		
sheet 8	bit mux and Mic input	sheet 20	power supply 2		
sheet 9	429 RX 1	sheet 21	power supply 3		
sheet 10	429 RX 2	sheet 22	filter caps and power regulators		
sheet 11	429 RX 3	sheet 23	connector 1		
sheet 12	429 TX	sheet 24	connector 2		

- NOTE:
- UNLESS OTHERWISE SPECIFIED, RESISTANCE VALUES ARE IN OHMS, CAPACITANCE VALUES ARE IN MICROFARADS, AND INDUCTANCE VALUES ARE IN MICROHENRYS.
 - PARTIAL REFERENCE DESIGNATIONS ARE SHOWN; FOR COMPLETE DESIGNATION, PREFIX WITH UNIT AND/OR ASSEMBLY DESIGNATION.
 - SHOWN FOR REFERENCE ONLY AND IS NOT INSTALLED ON FINAL ASSEMBLY.

NOTICE: THIS DOCUMENT CONTAINS TECHNICAL DATA, WHICH MAY BE RESTRICTED FOR EXPORT UNDER THE INTERNATIONAL TRAFFIC IN ARMS REGULATIONS (ITAR) OR THE EXPORT ADMINISTRATION REGULATIONS (EAR). VIOLATIONS OF THESE EXPORT LAWS MAY BE SUBJECT TO FINES AND PENALTIES UNDER THE ARMS EXPORT CONTROL ACT (22 U.S.C. 2778)

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THIS DRAWING IS CREATED FROM DIGITAL DATA

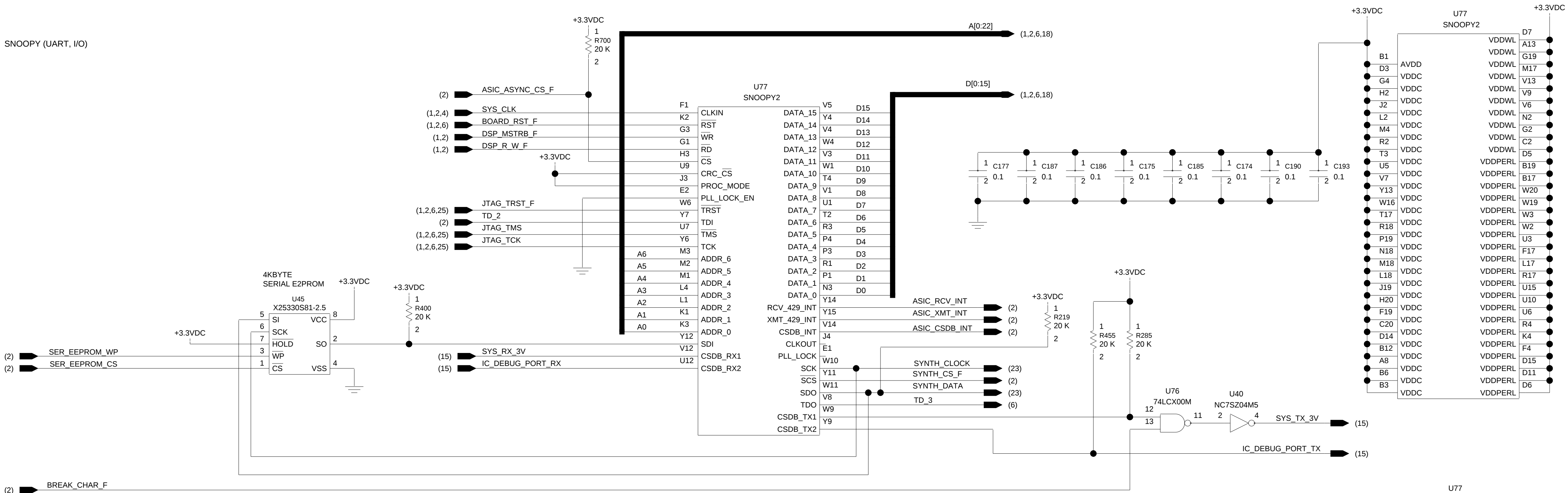
ROCKWELL COLLINS
PROPRIETARY INFORMATION.

PREP	2003-01-09	ROCKWELL COLLINS, INC		
R. COLLETTE		400 COLLINS RD. NE CEDAR RAPIDS, IA 52498		
CHK	2003-01-09	SCHEMATIC DIAGRAM,		
G. RUPPEL		DSP/POWER SUPPLY		
ENG APVD	2003-01-09			
R. ELLIOTT				
NEXT ASSY PART NO.	828-1437-002	SIZE	CAGE CODE	DWG NO.
EQUIPMENT TYPE NO.	VHF-920A	D	4V792	828-6437-002
		SCALE	NONE	REV LTR
				-
		SHEET 1 OF 25		

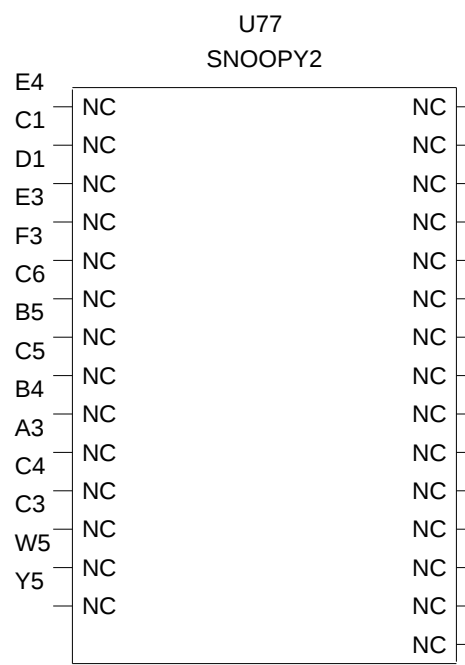
DIGITAL DATA ONLY NO MASTER TRACING

NFP REL CR_MB_

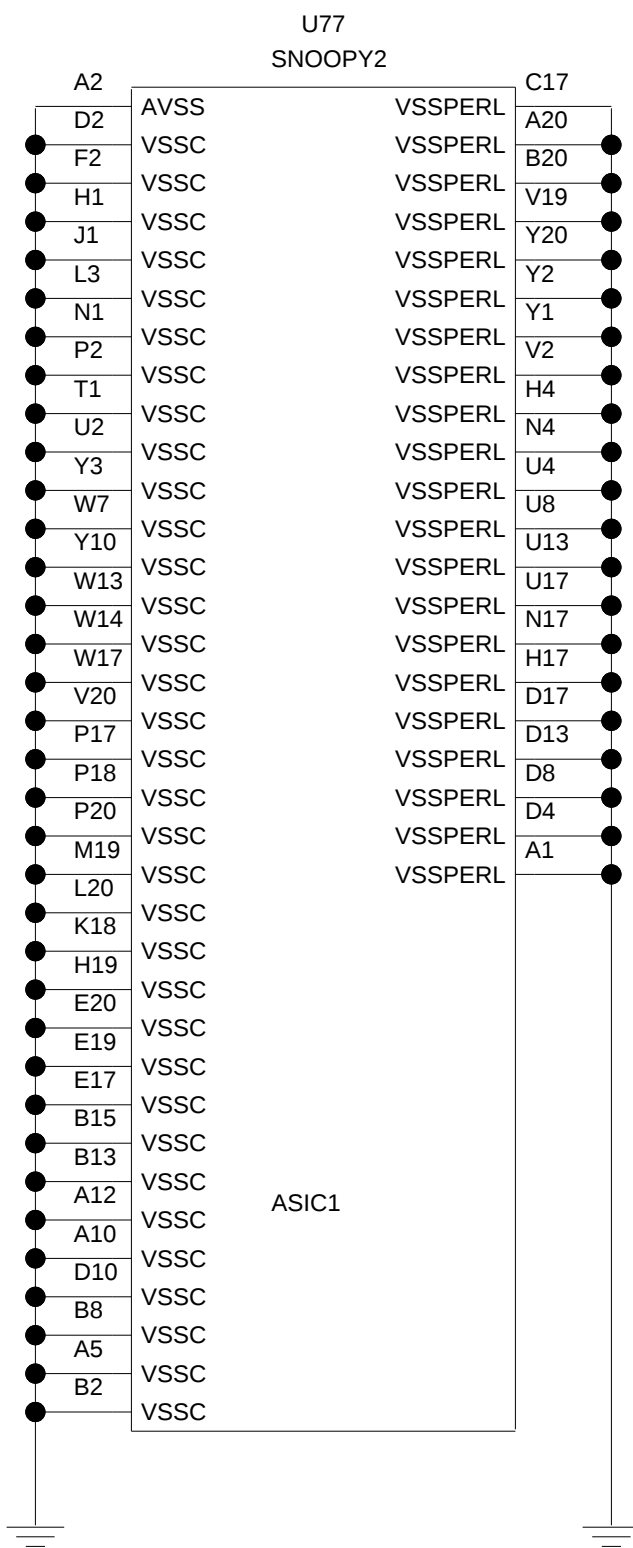
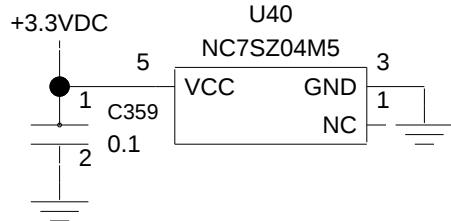
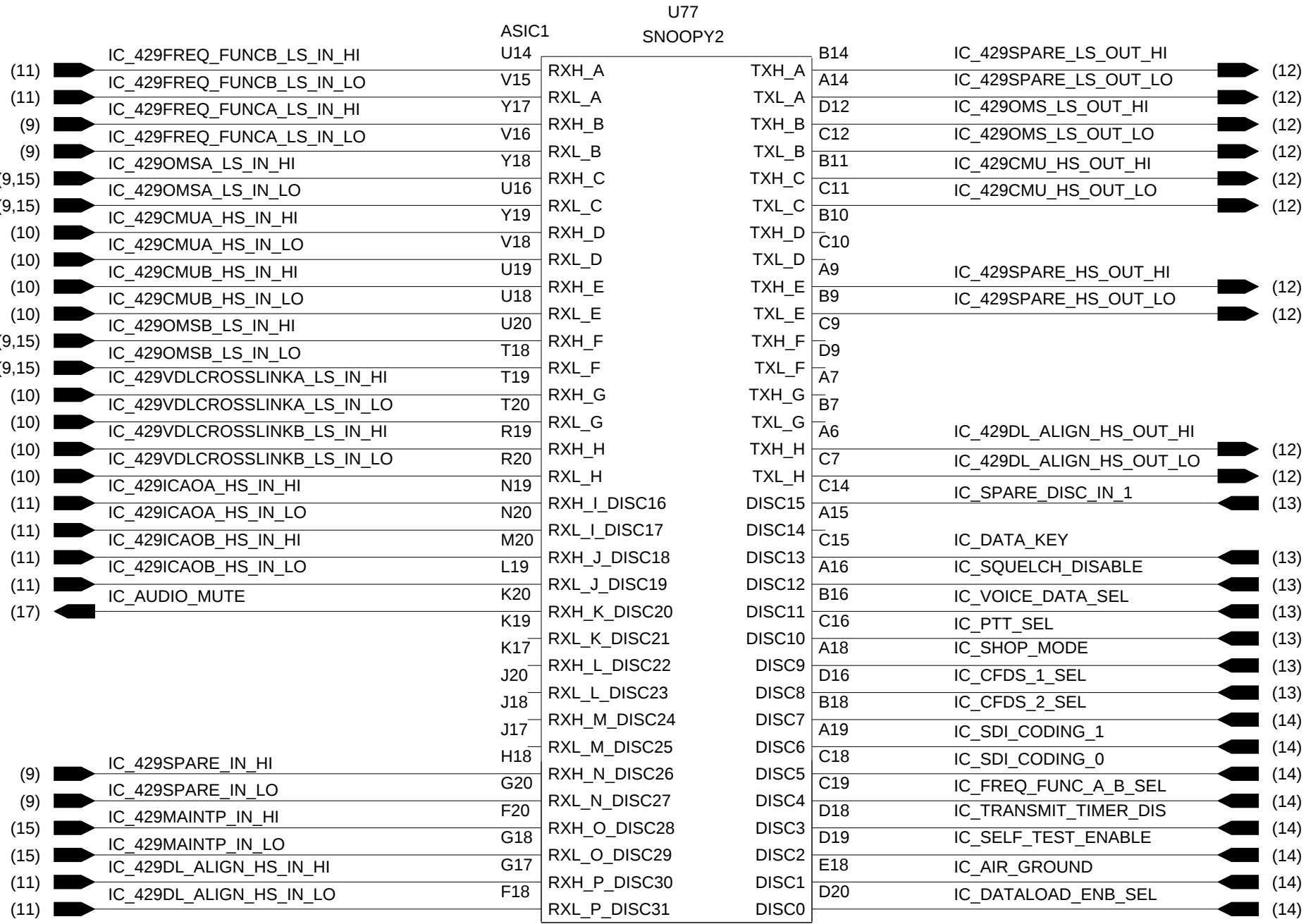
SNOOPY (UART, I/O)



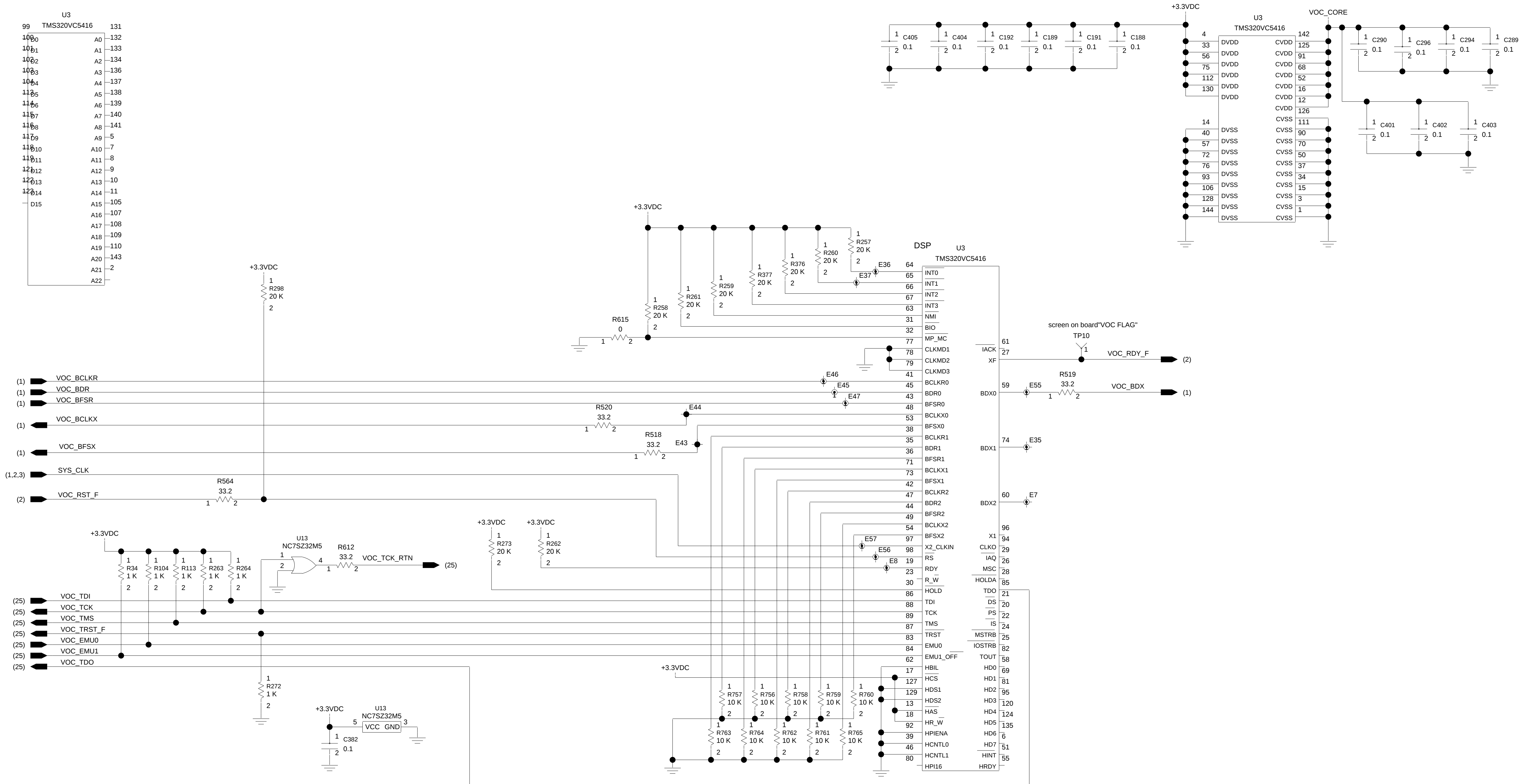
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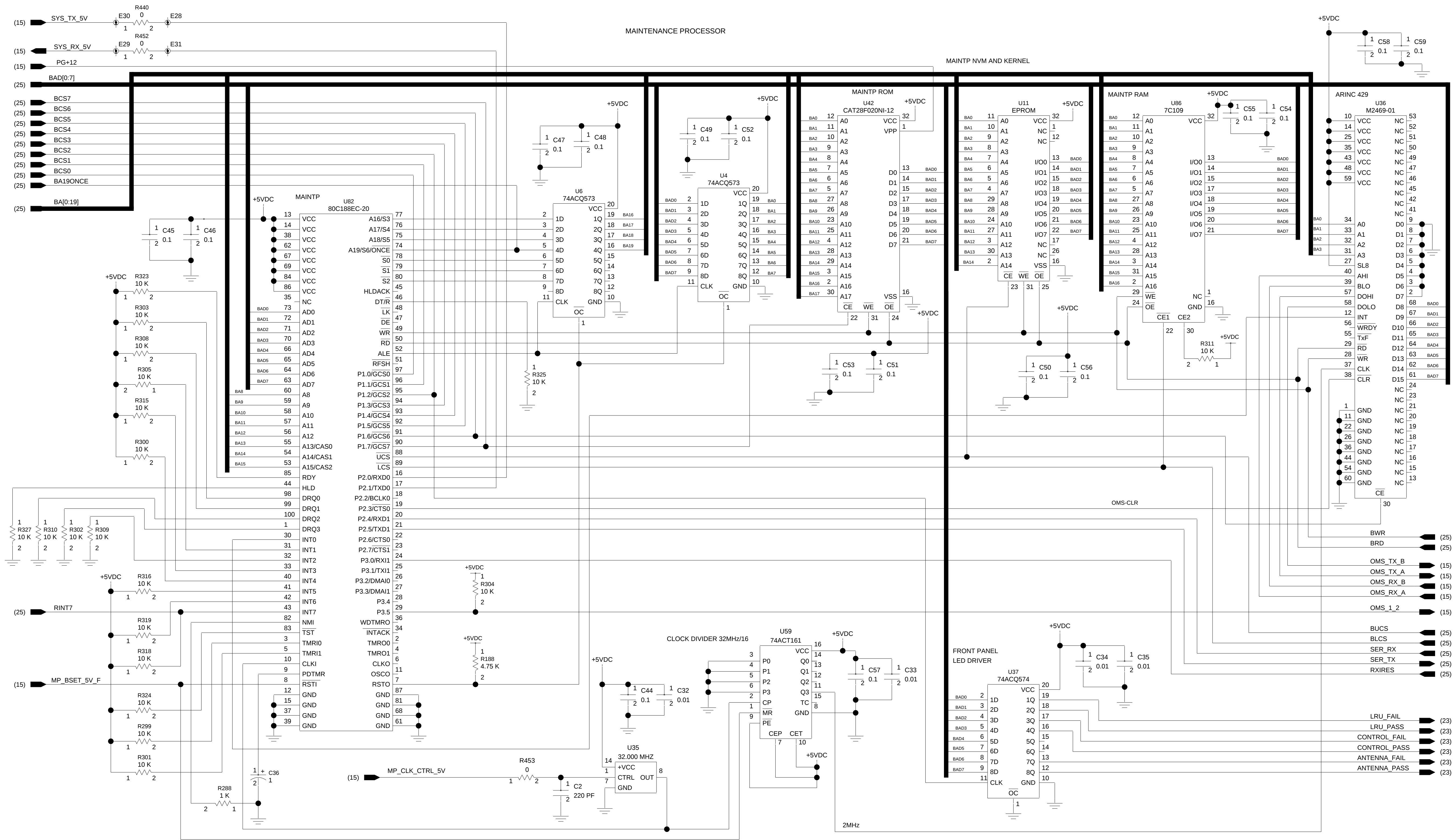


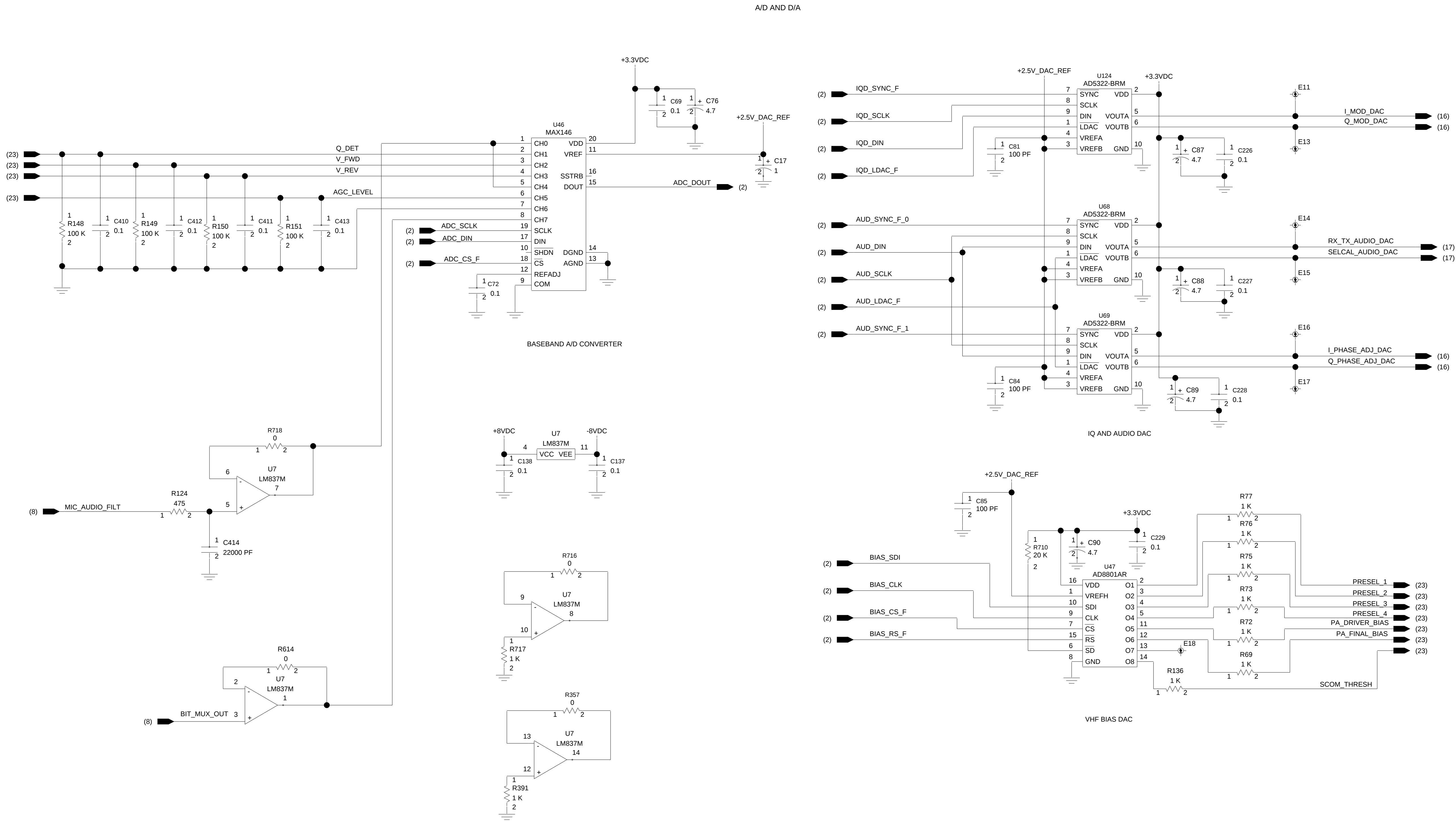
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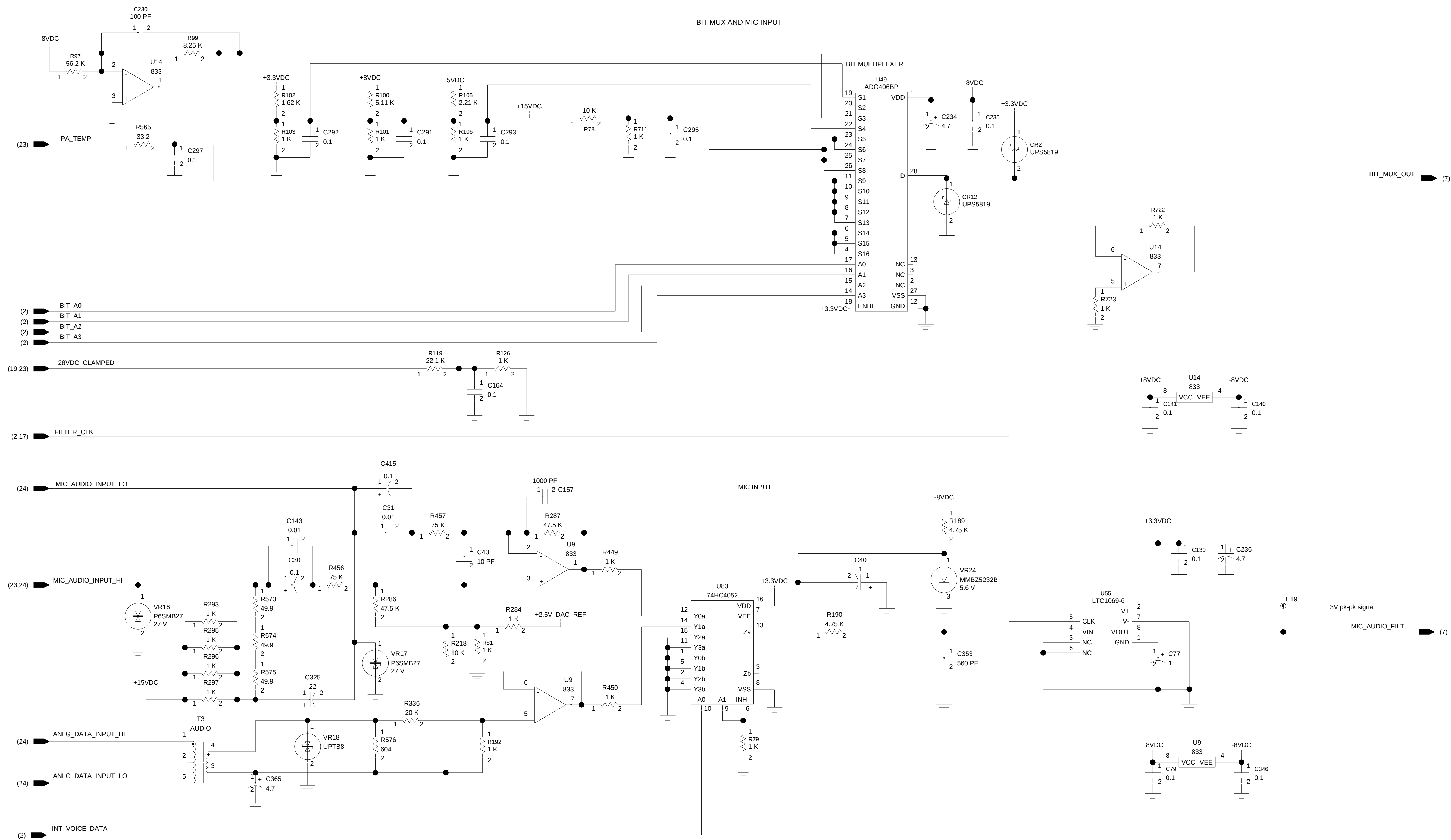


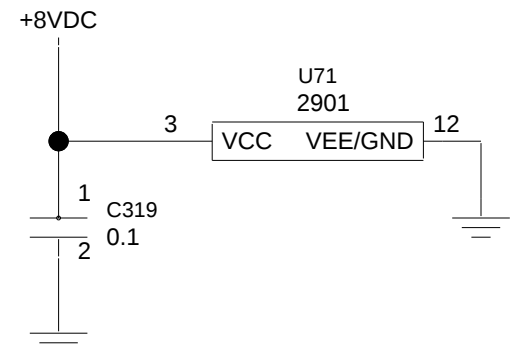
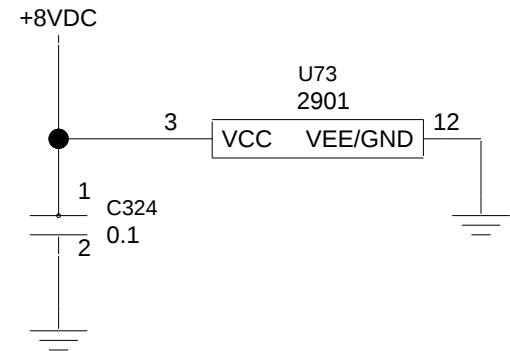
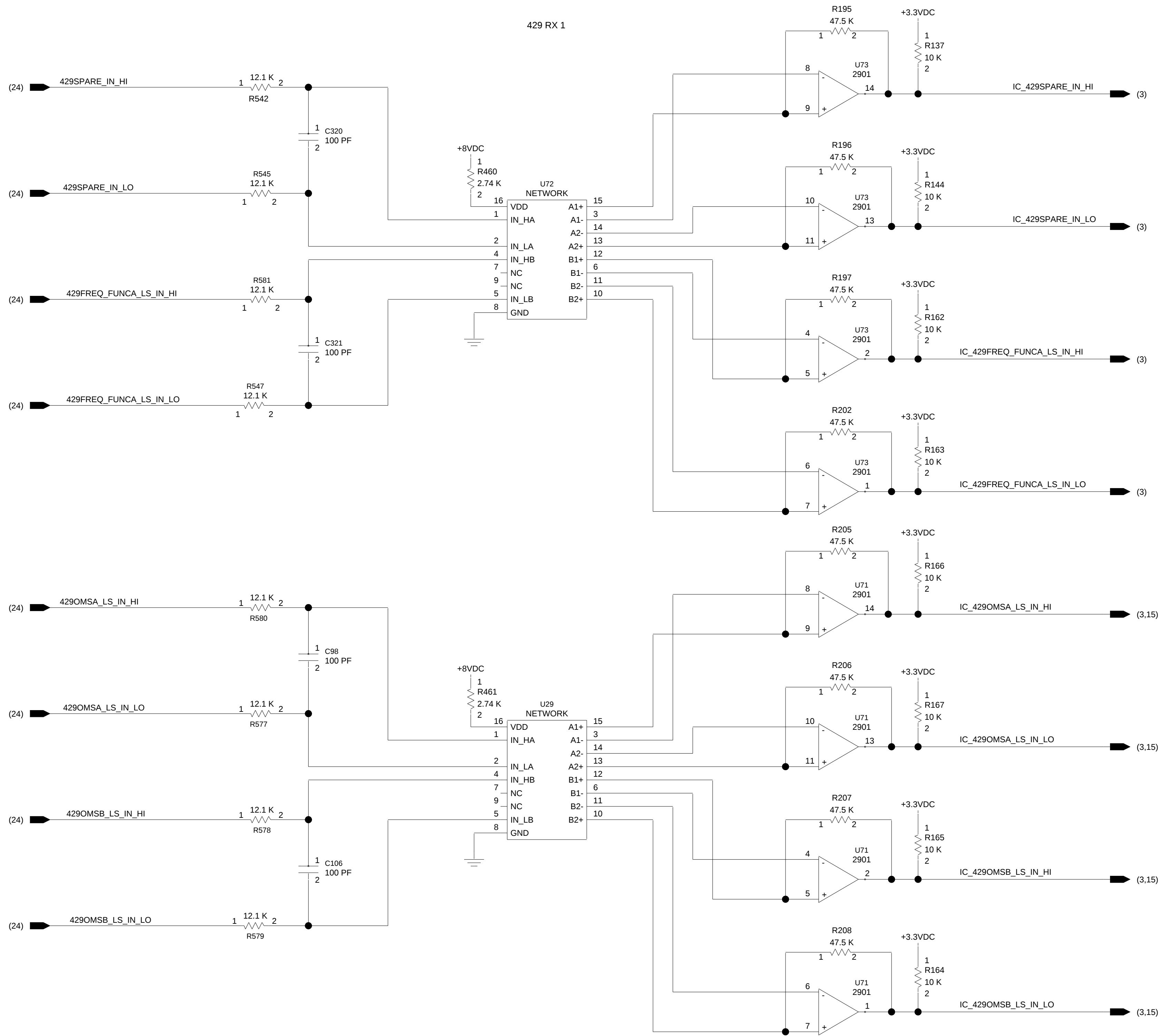
VOCODER

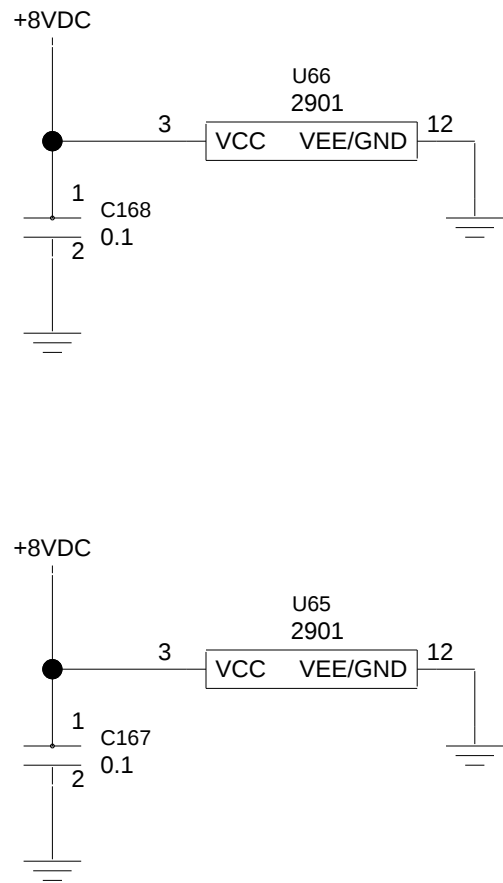
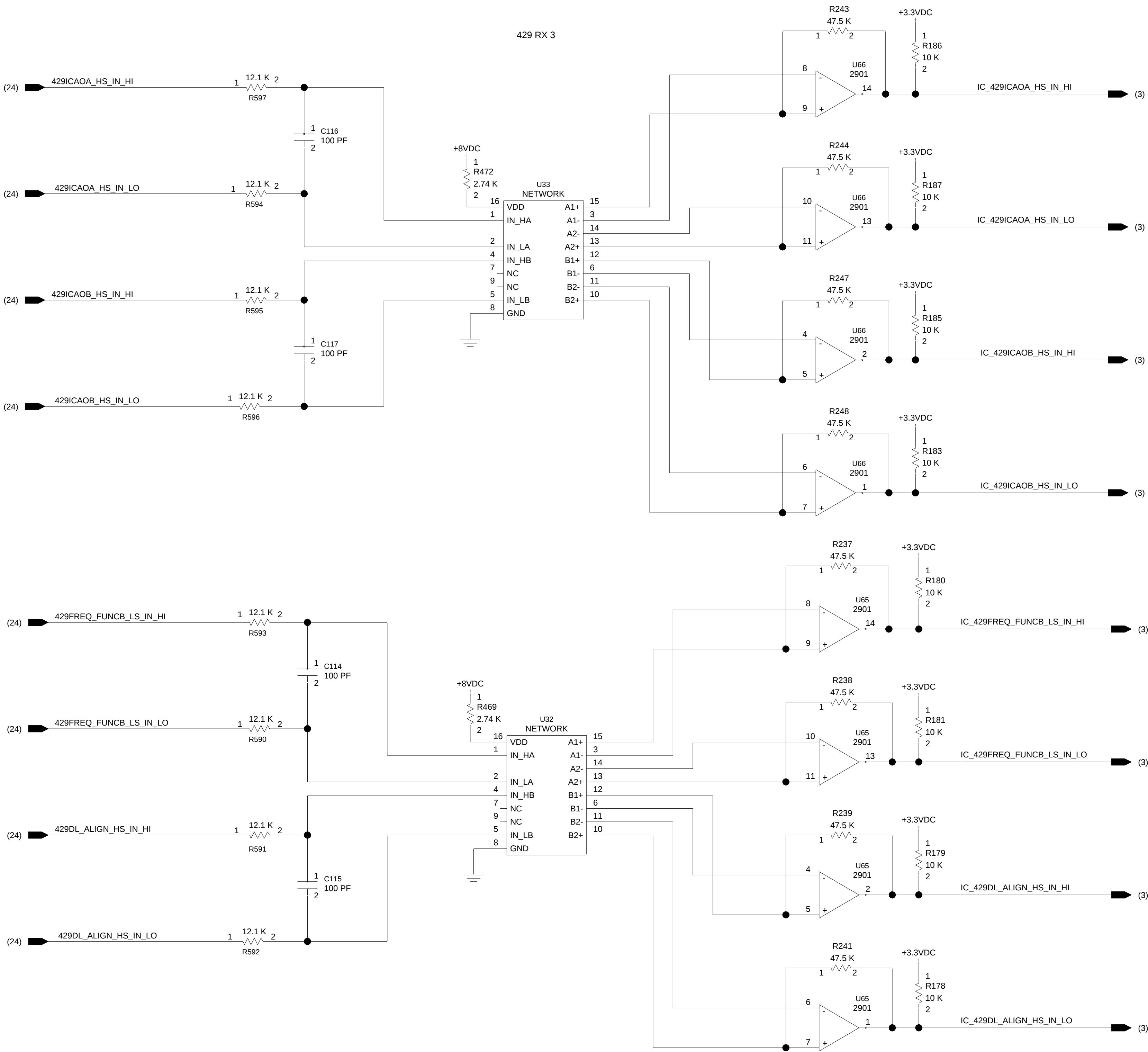


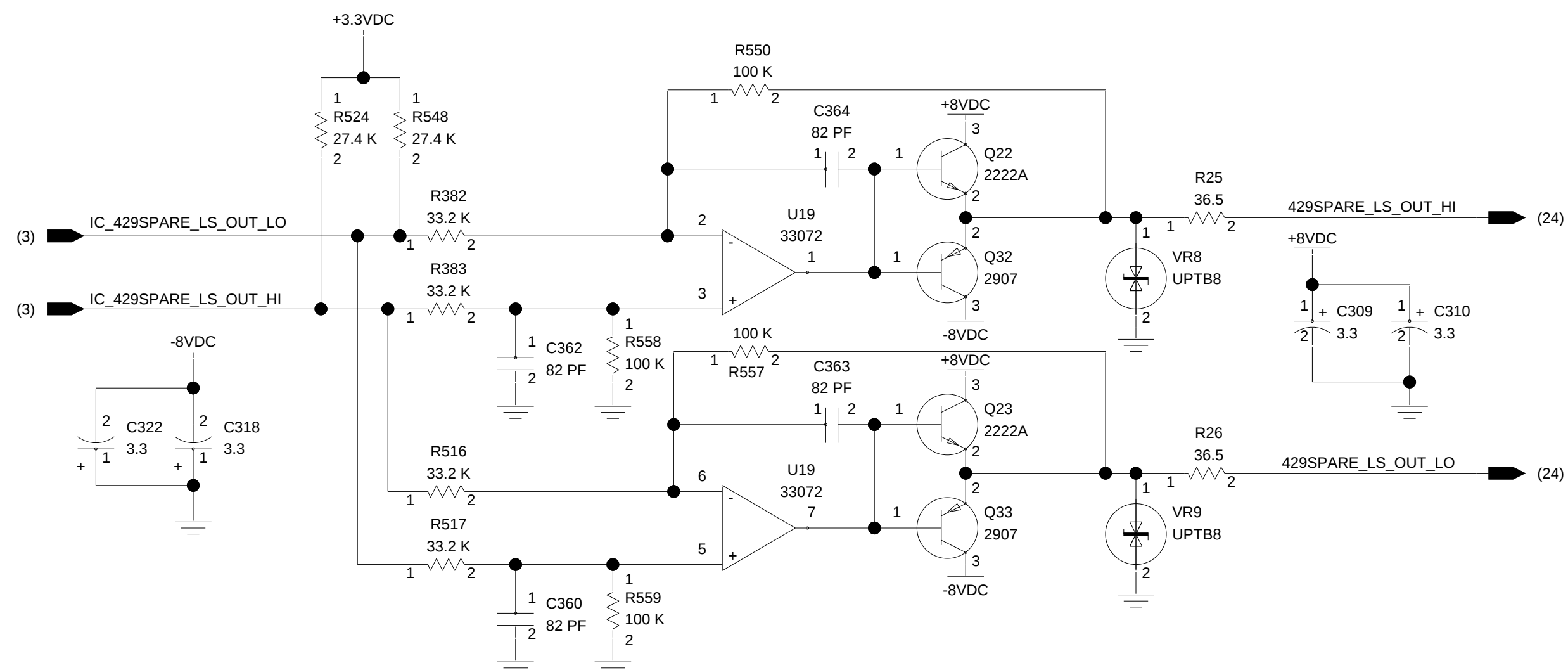
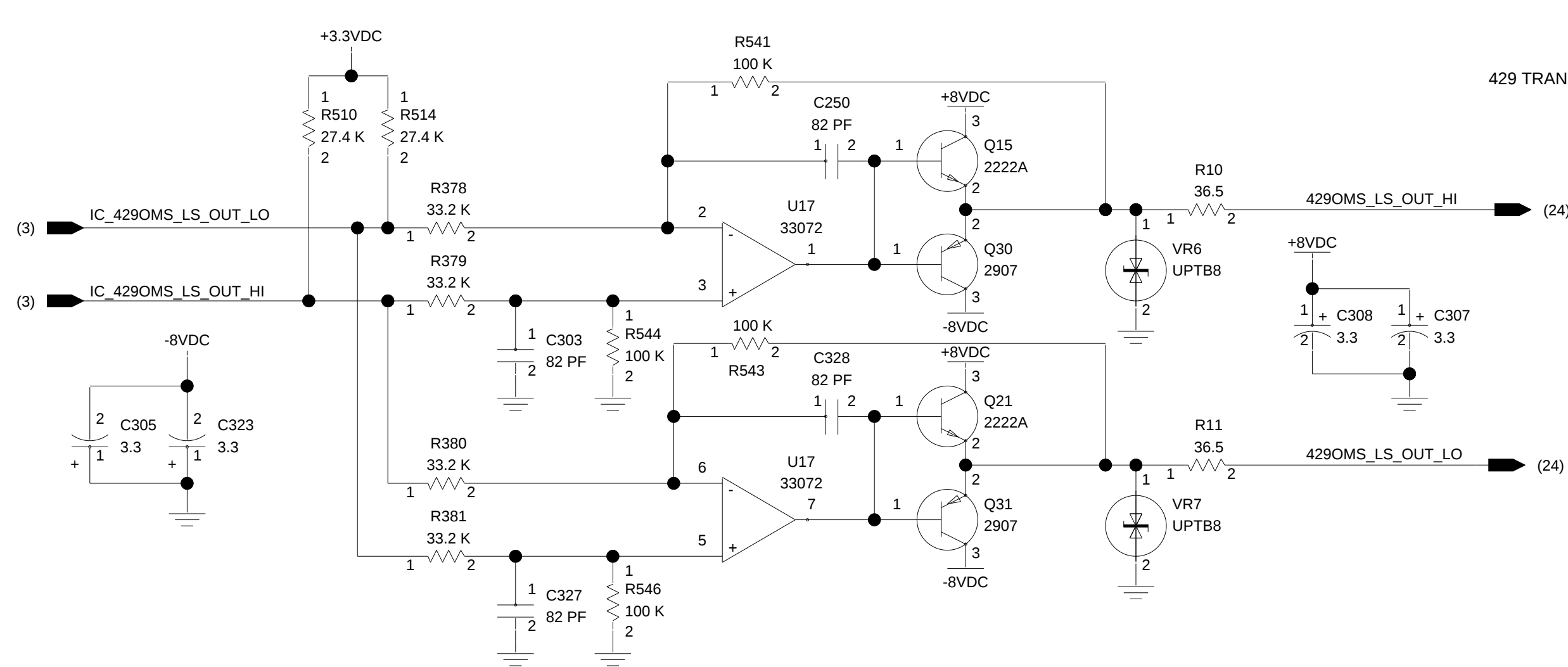




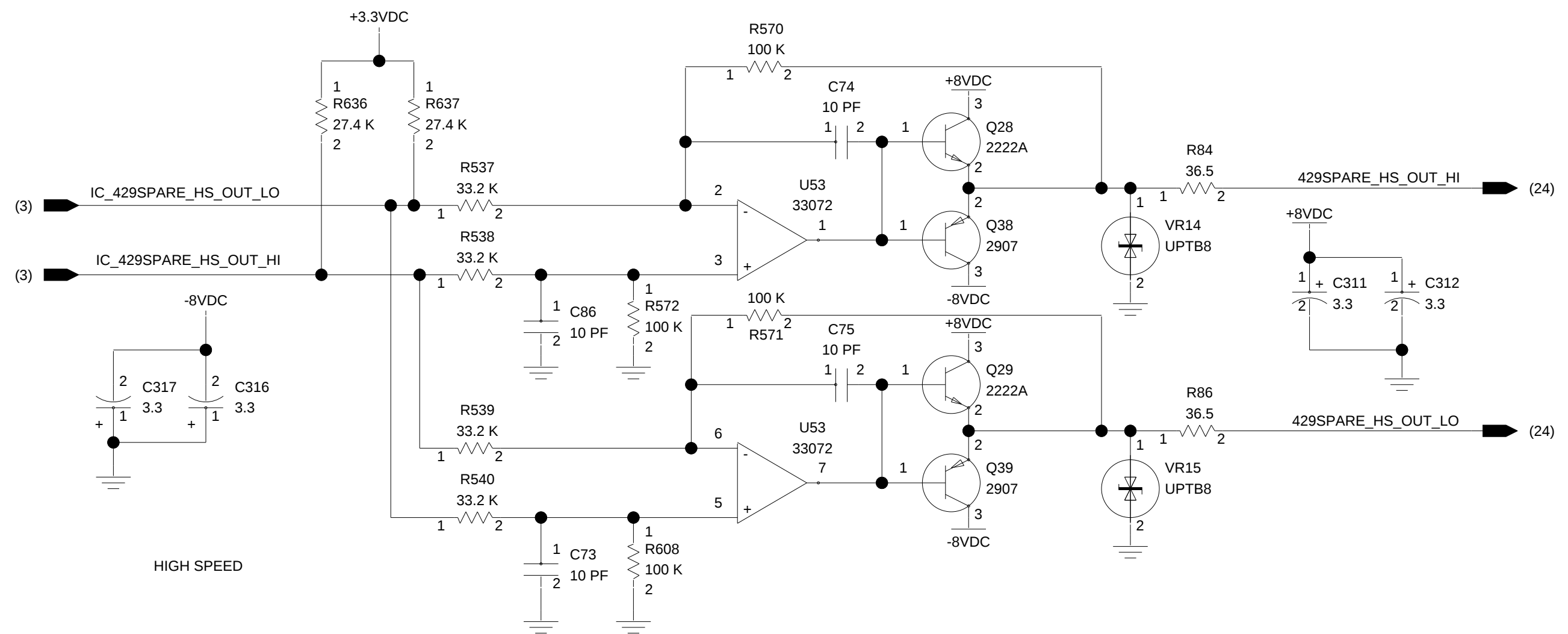
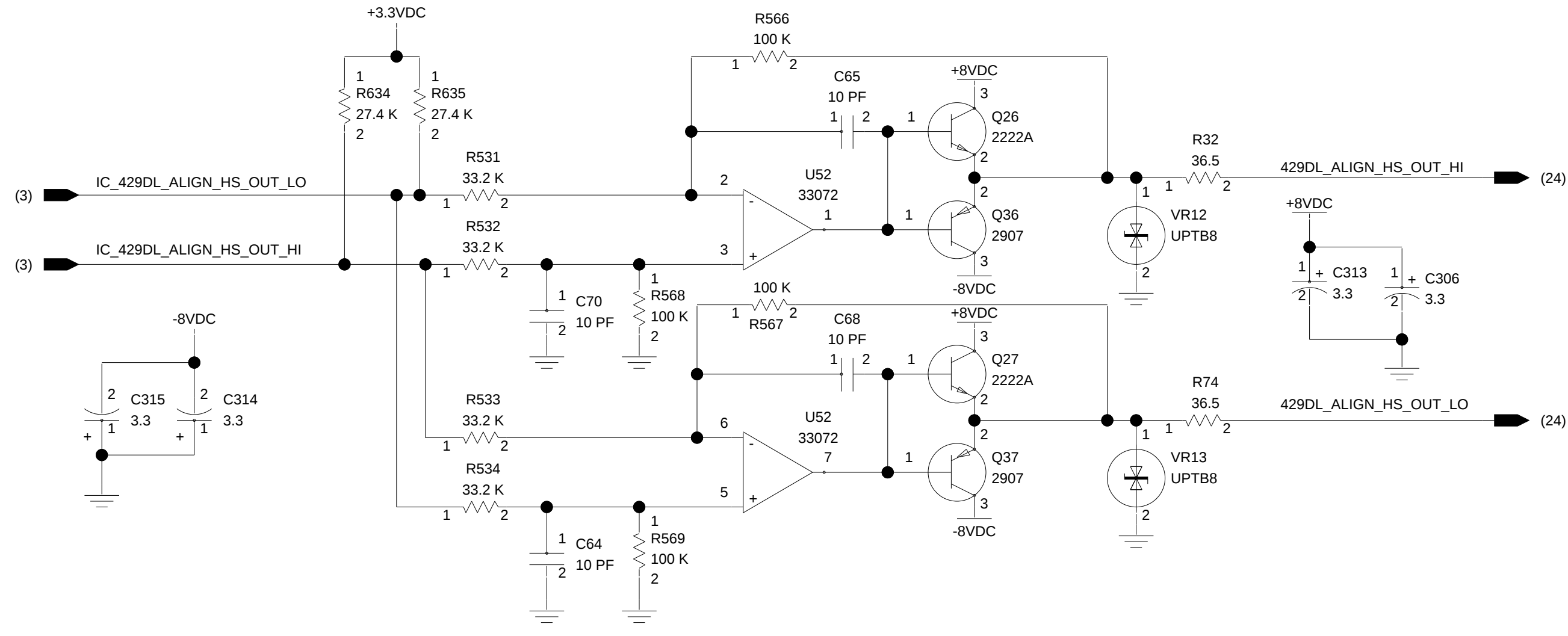
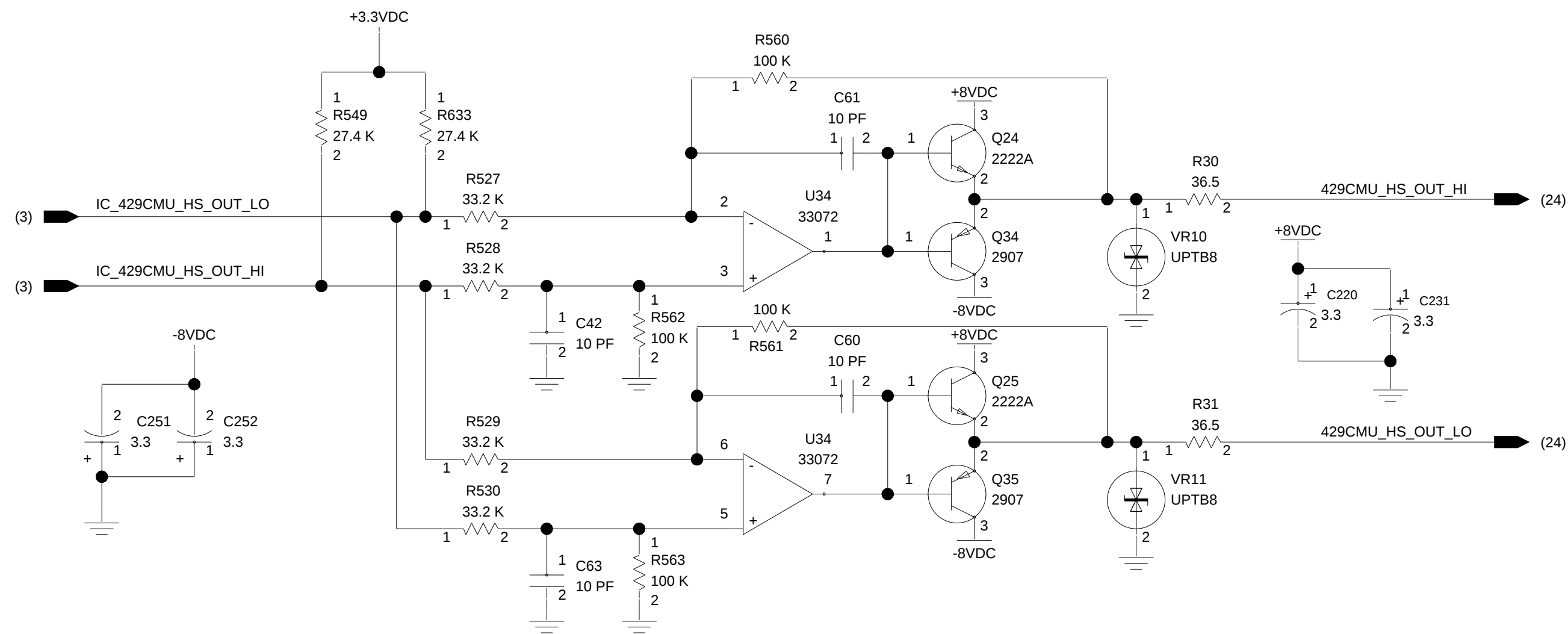
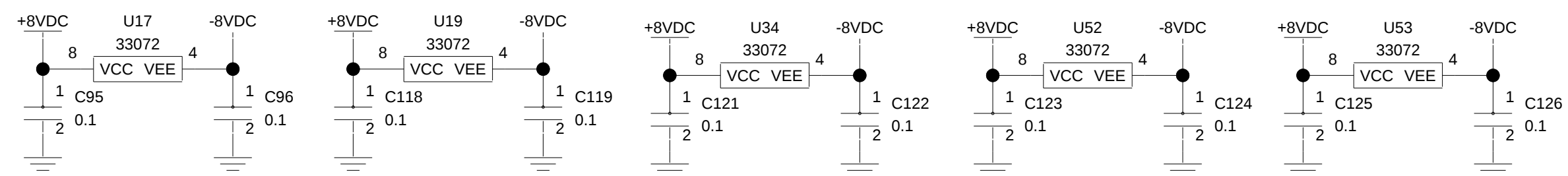




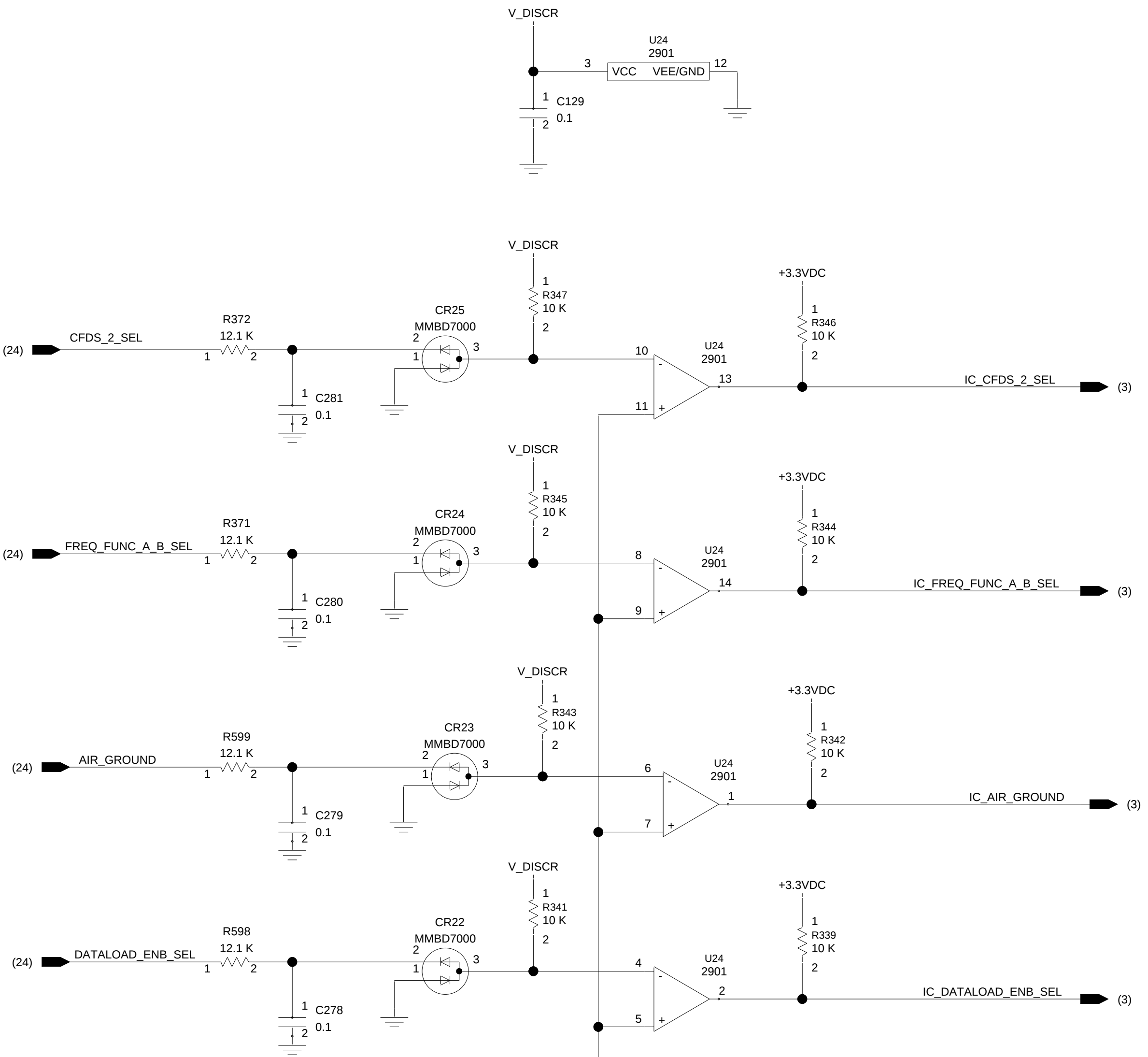
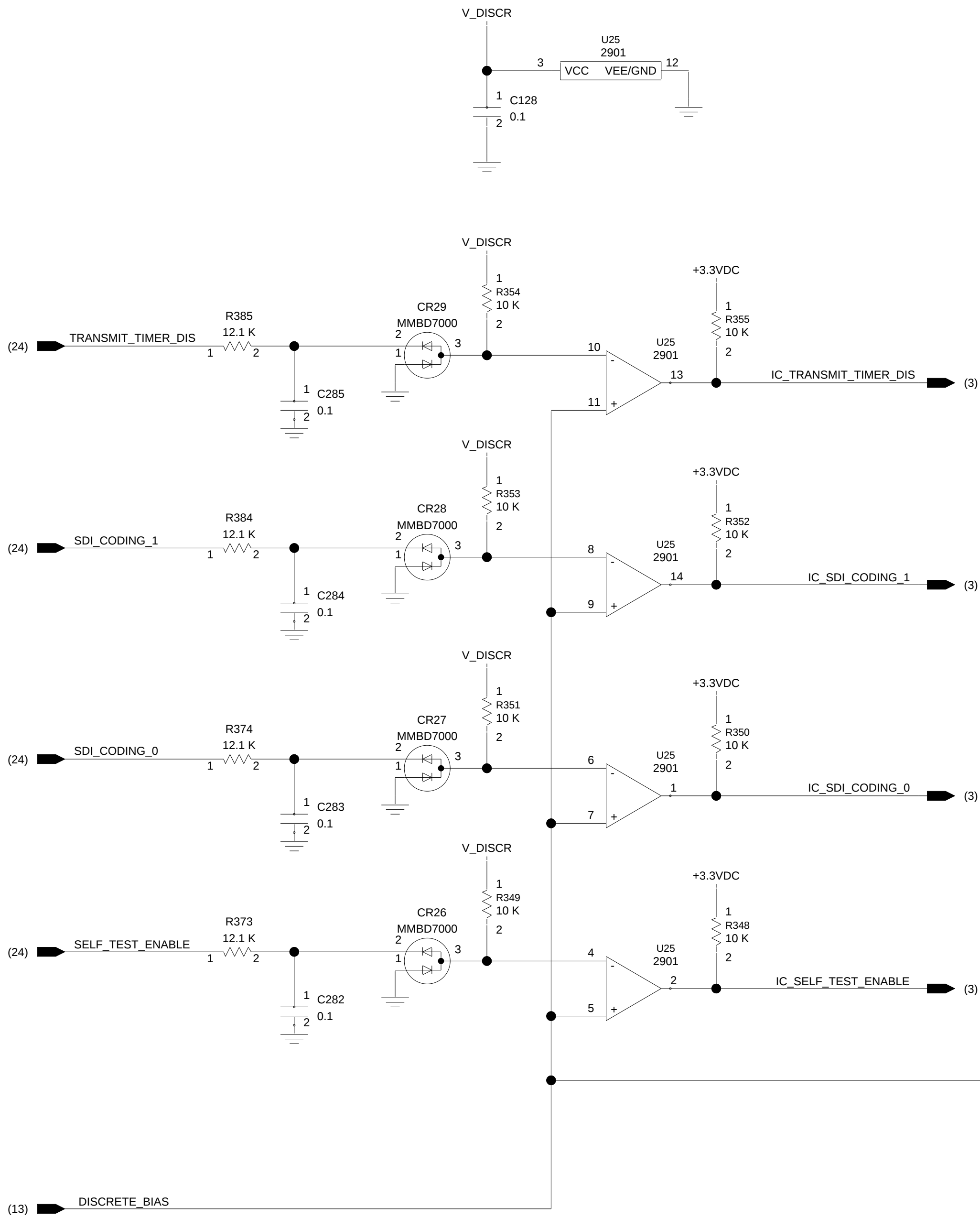




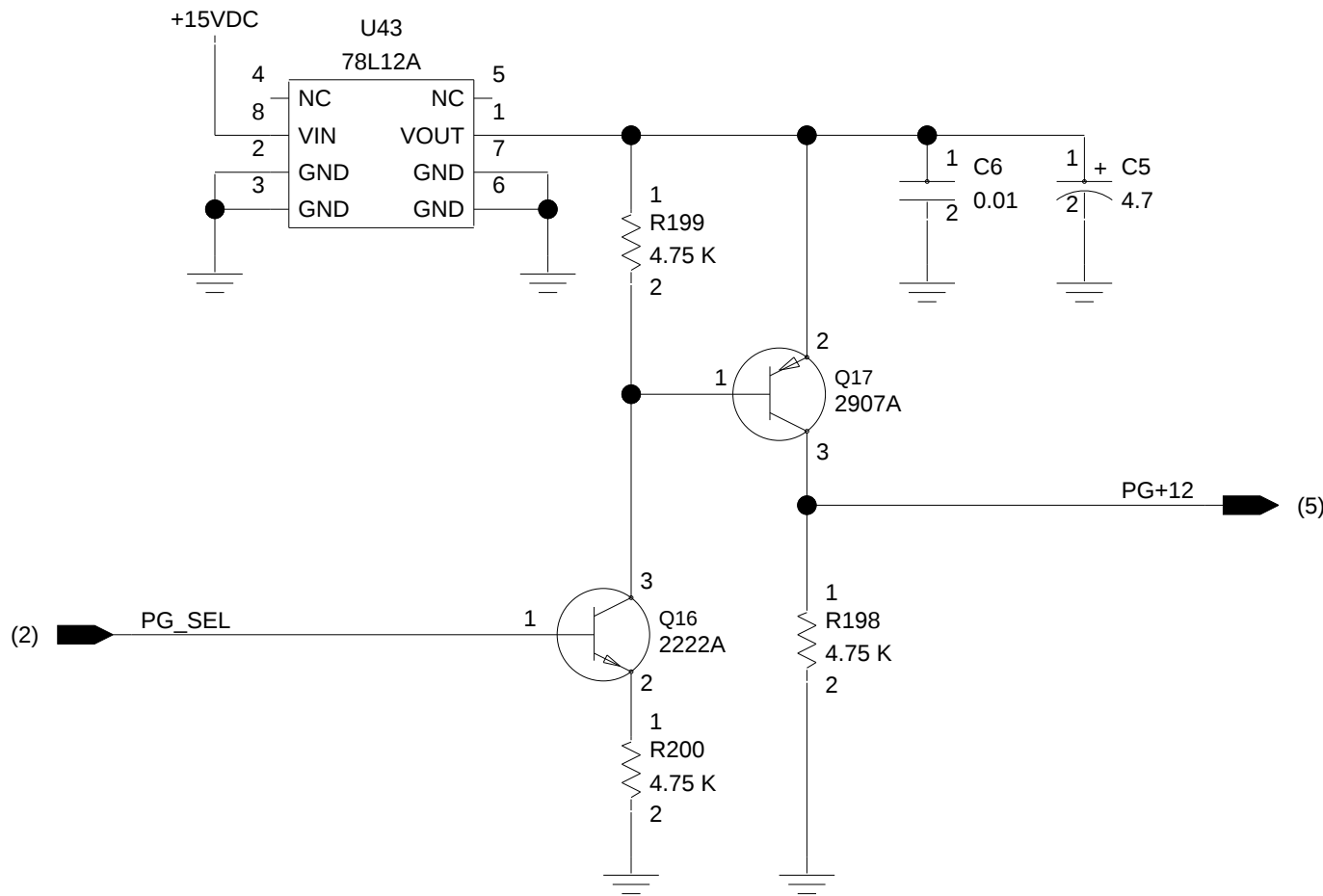
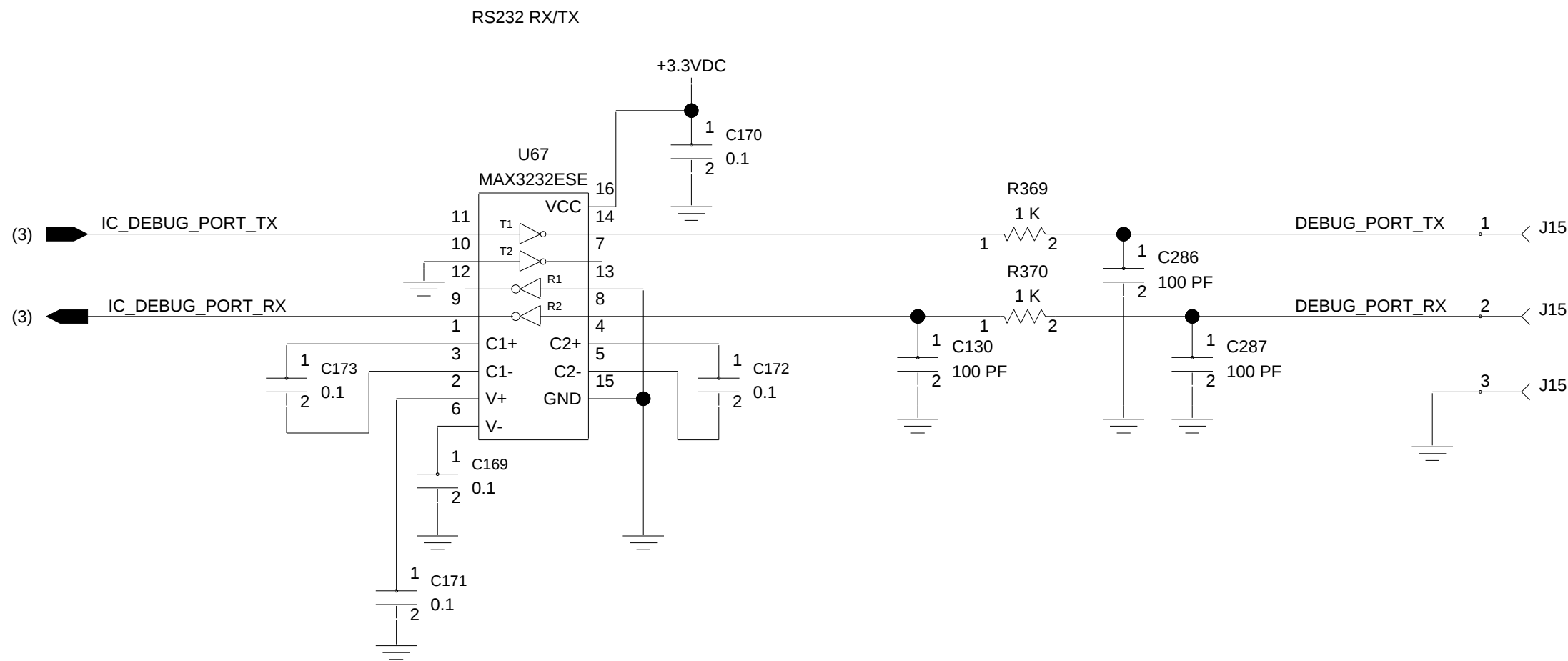
LOW SPEED



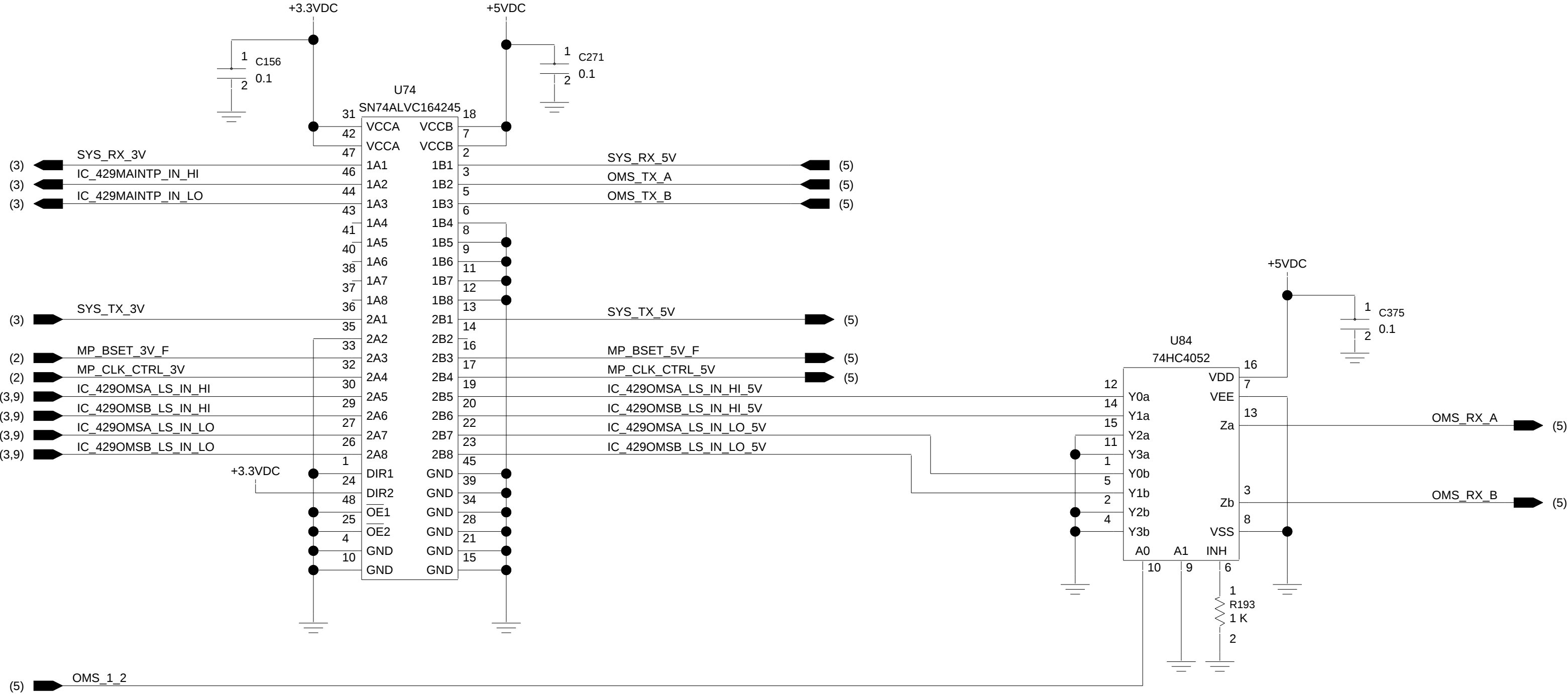
DISCRETE INPUTS 2



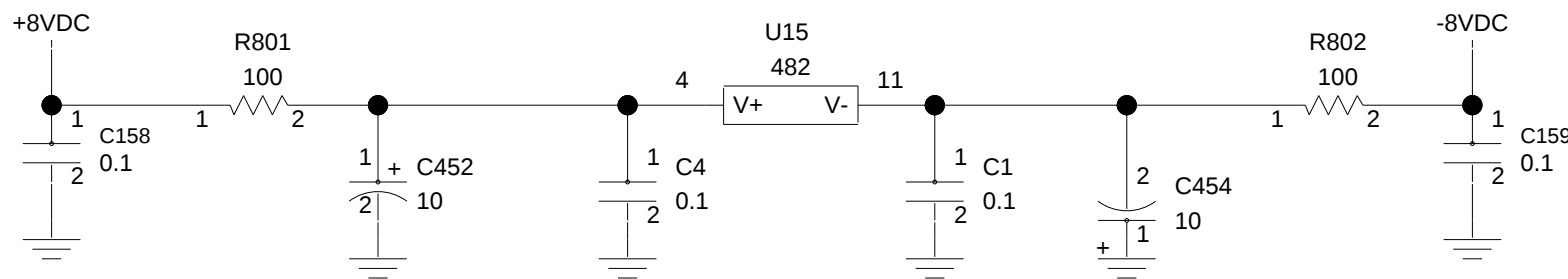
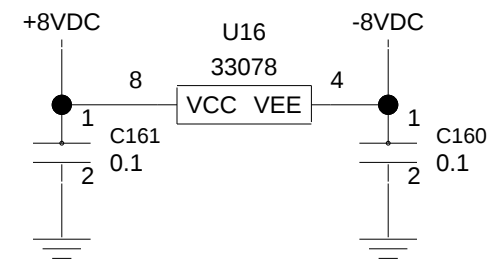
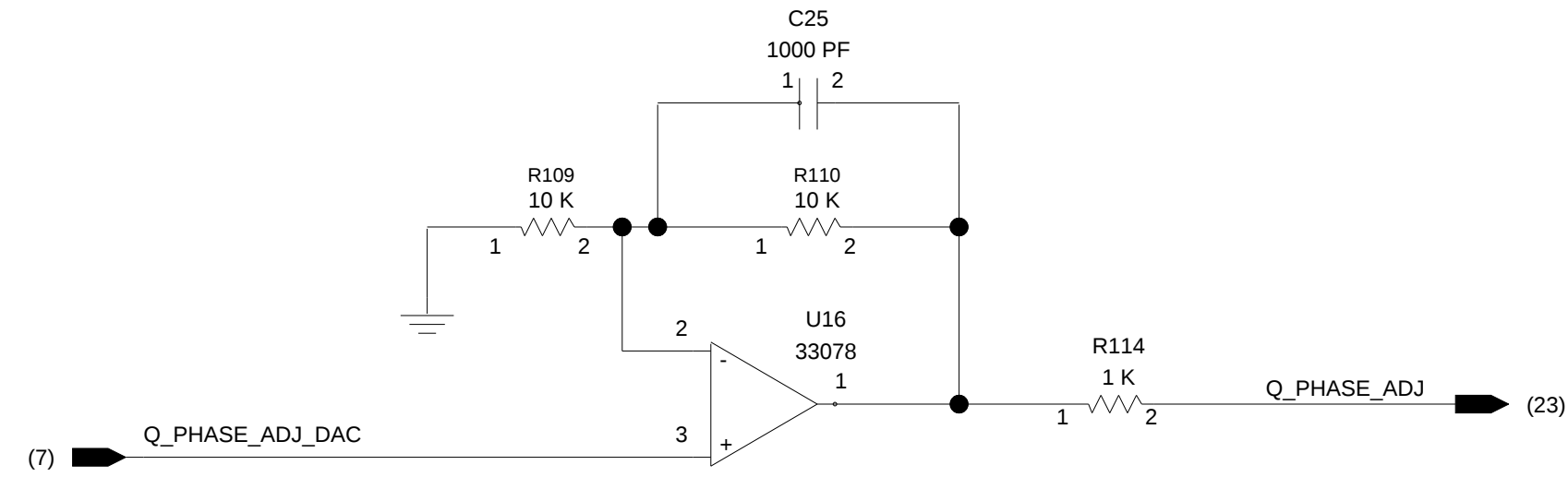
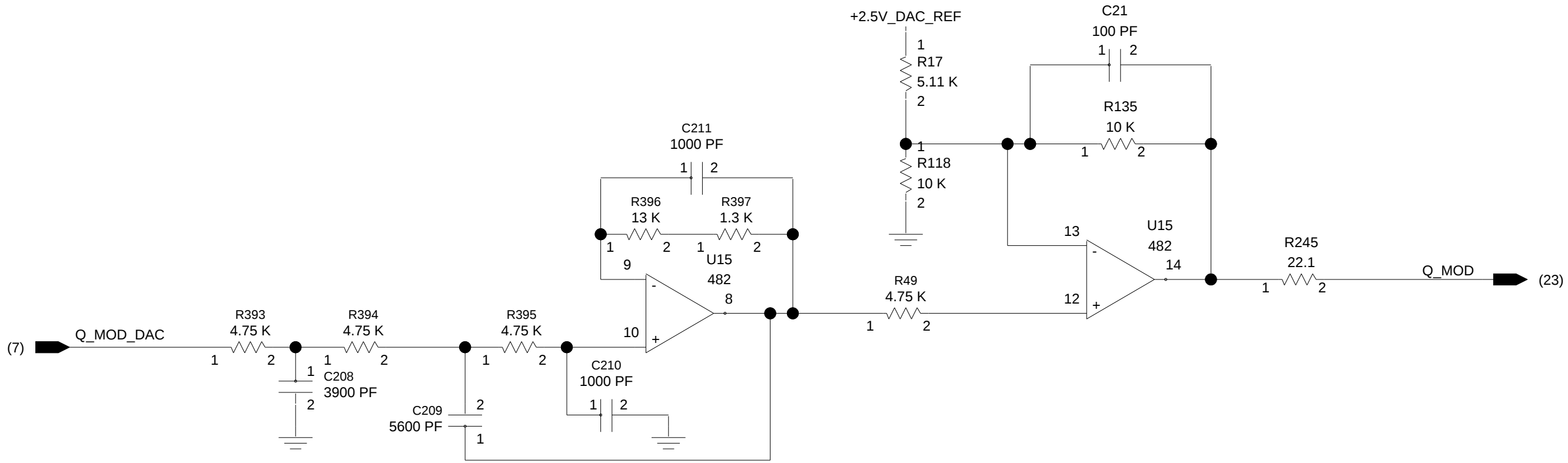
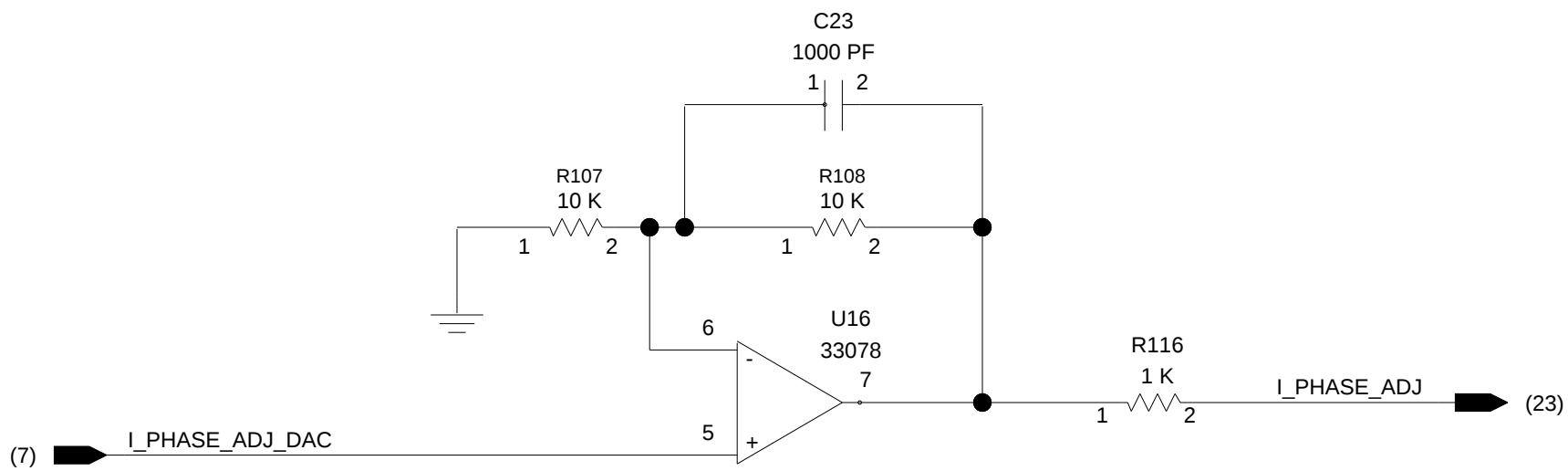
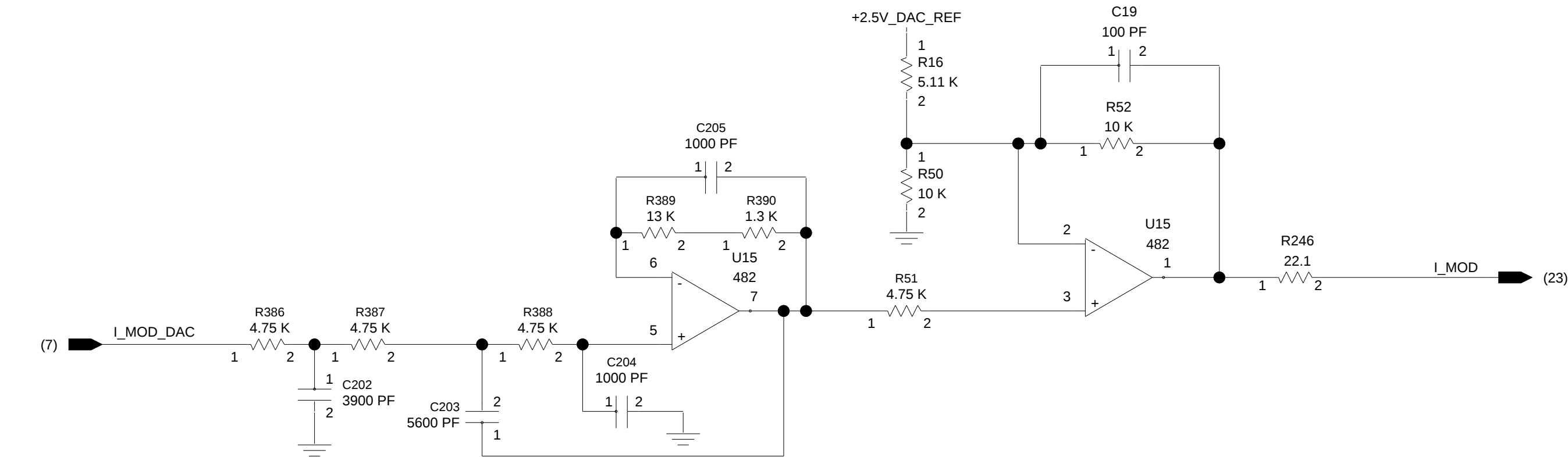
RS232 RX TX, VOLTAGE LEVEL SHIFT, +12Vpp for Flash programming



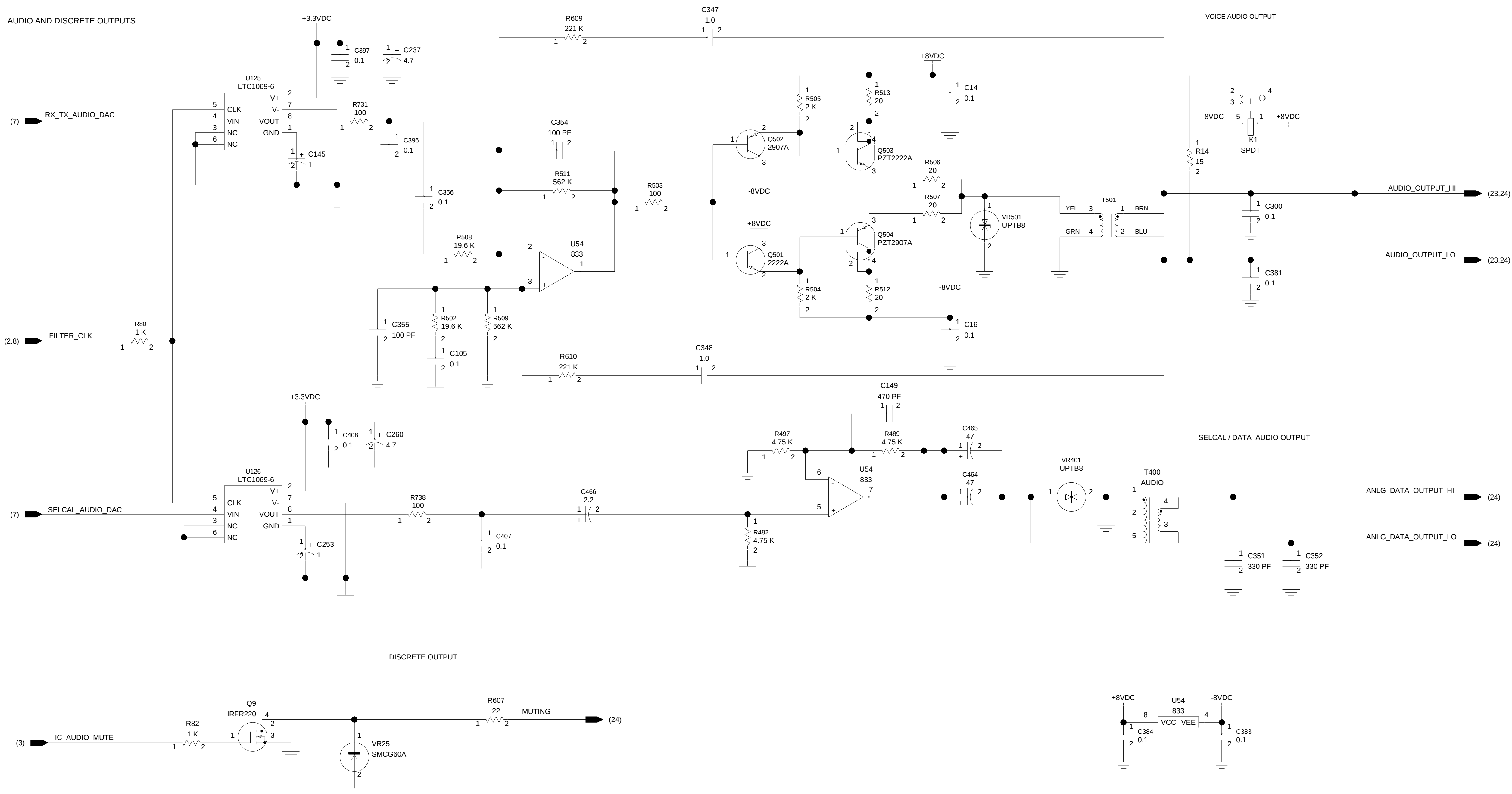
SNOOPY MAINTP voltage level shift



IQ level translators

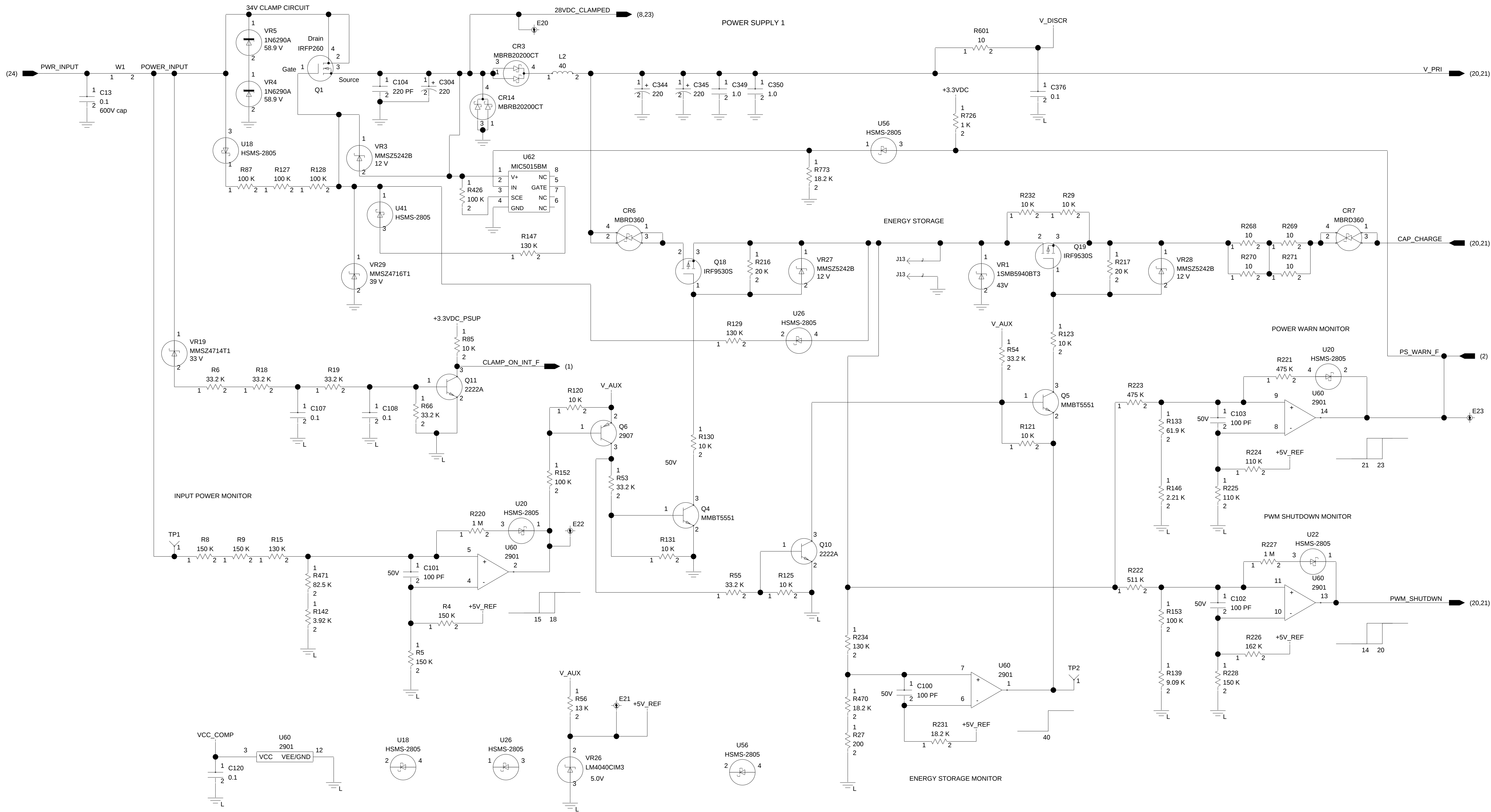


AUDIO AND DISCRETE OUTPUTS

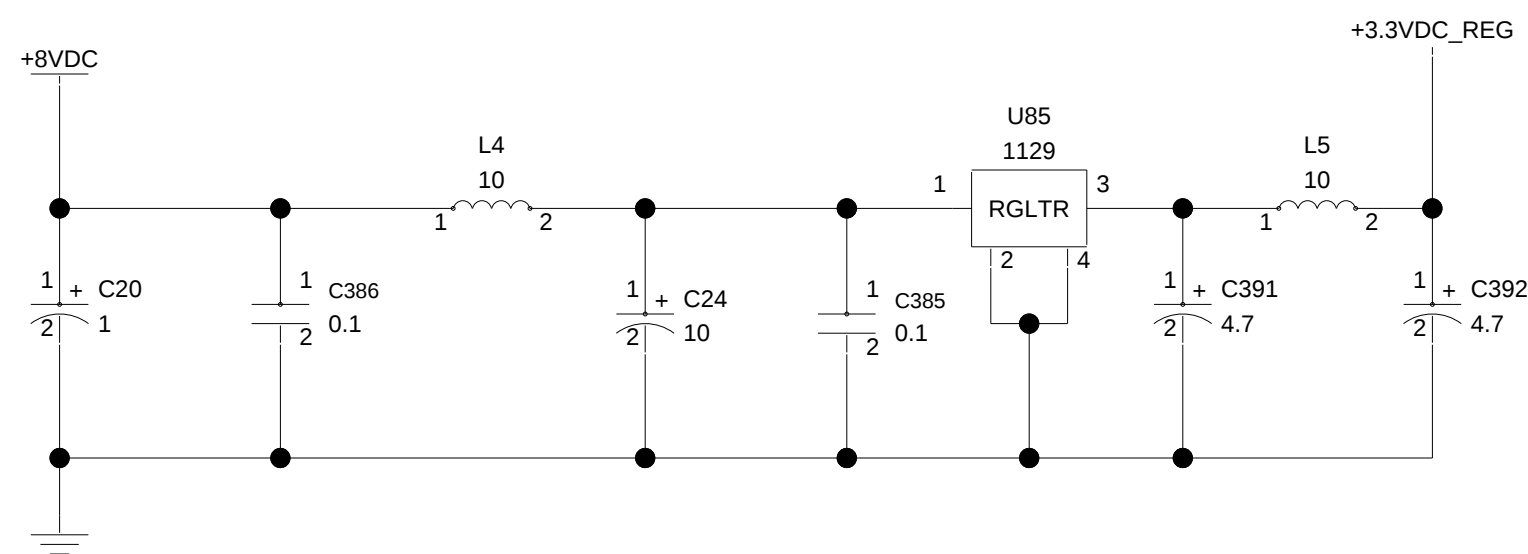
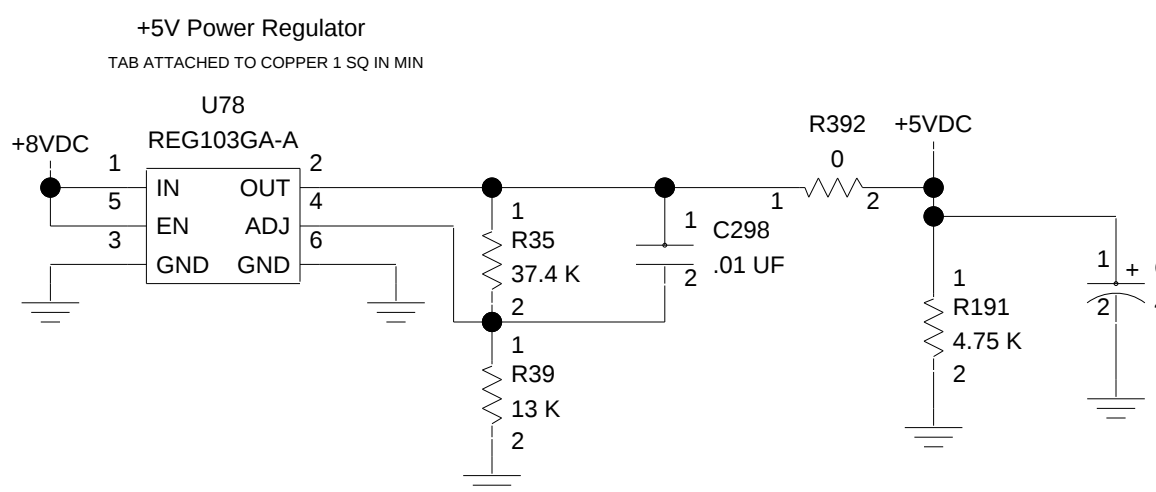
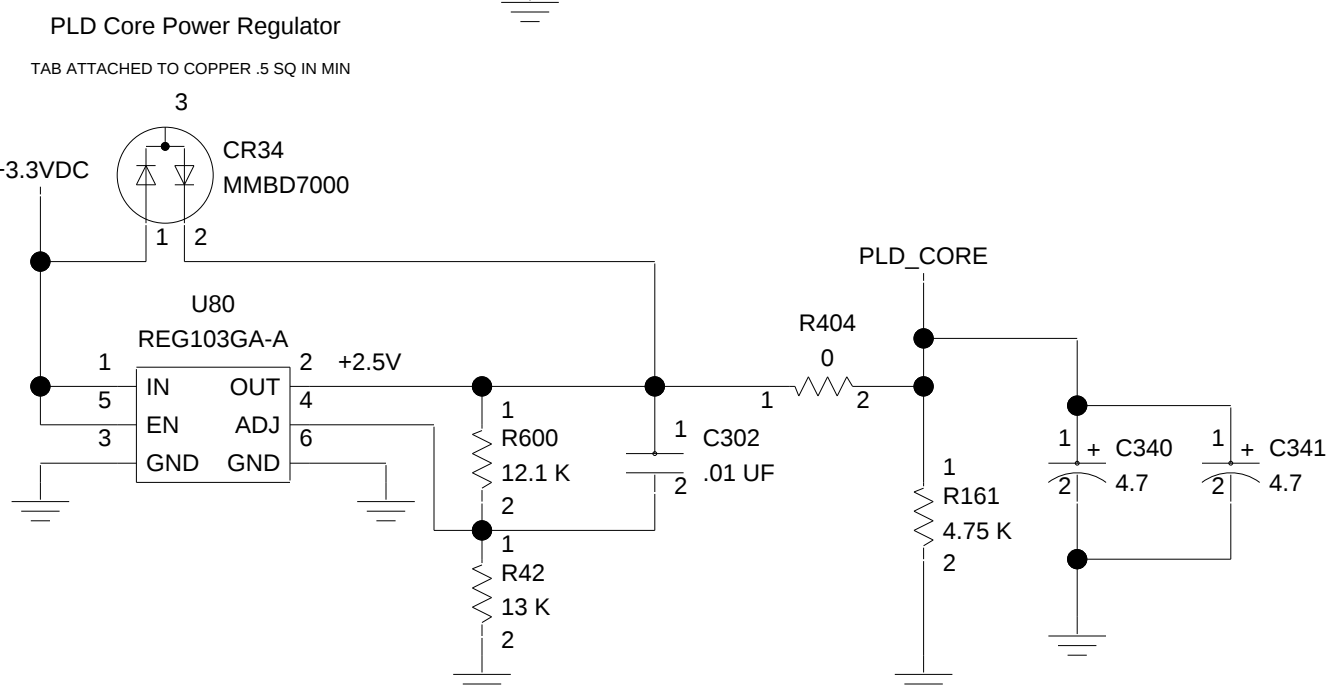
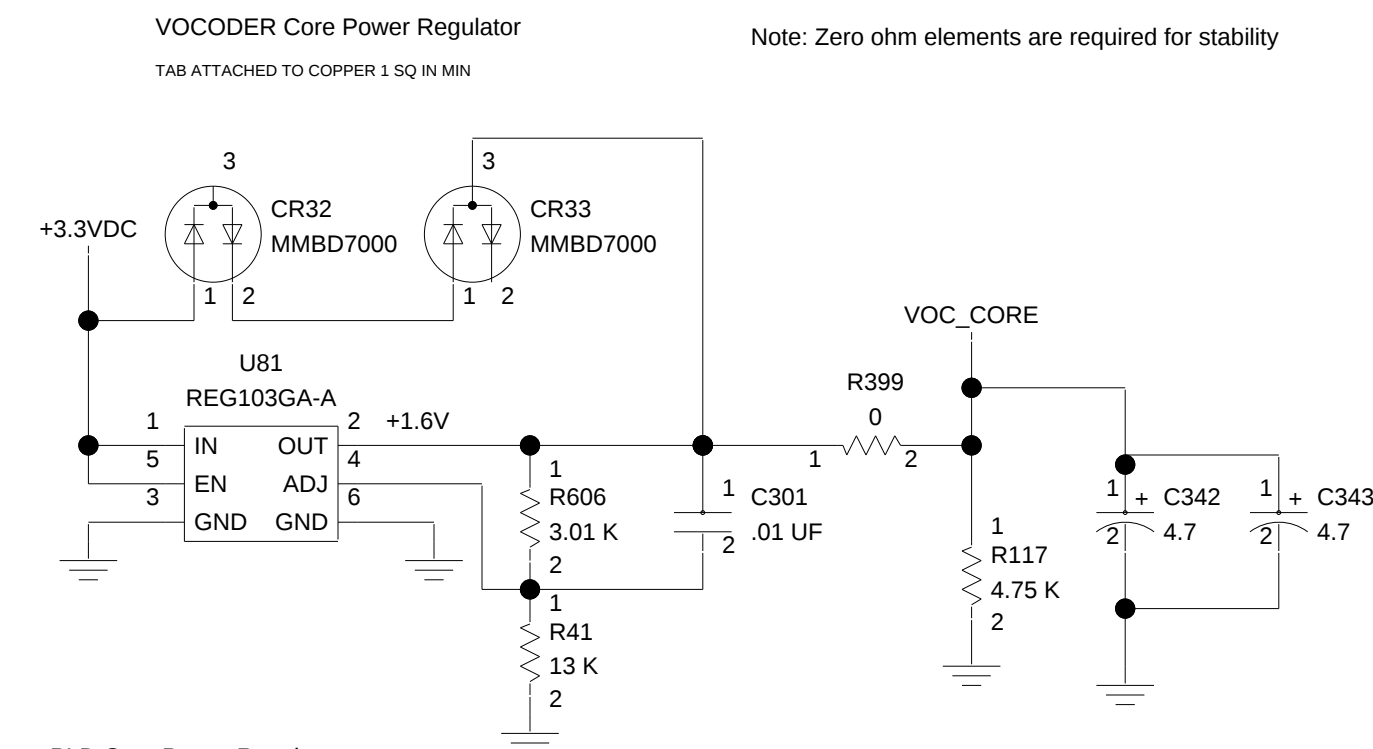
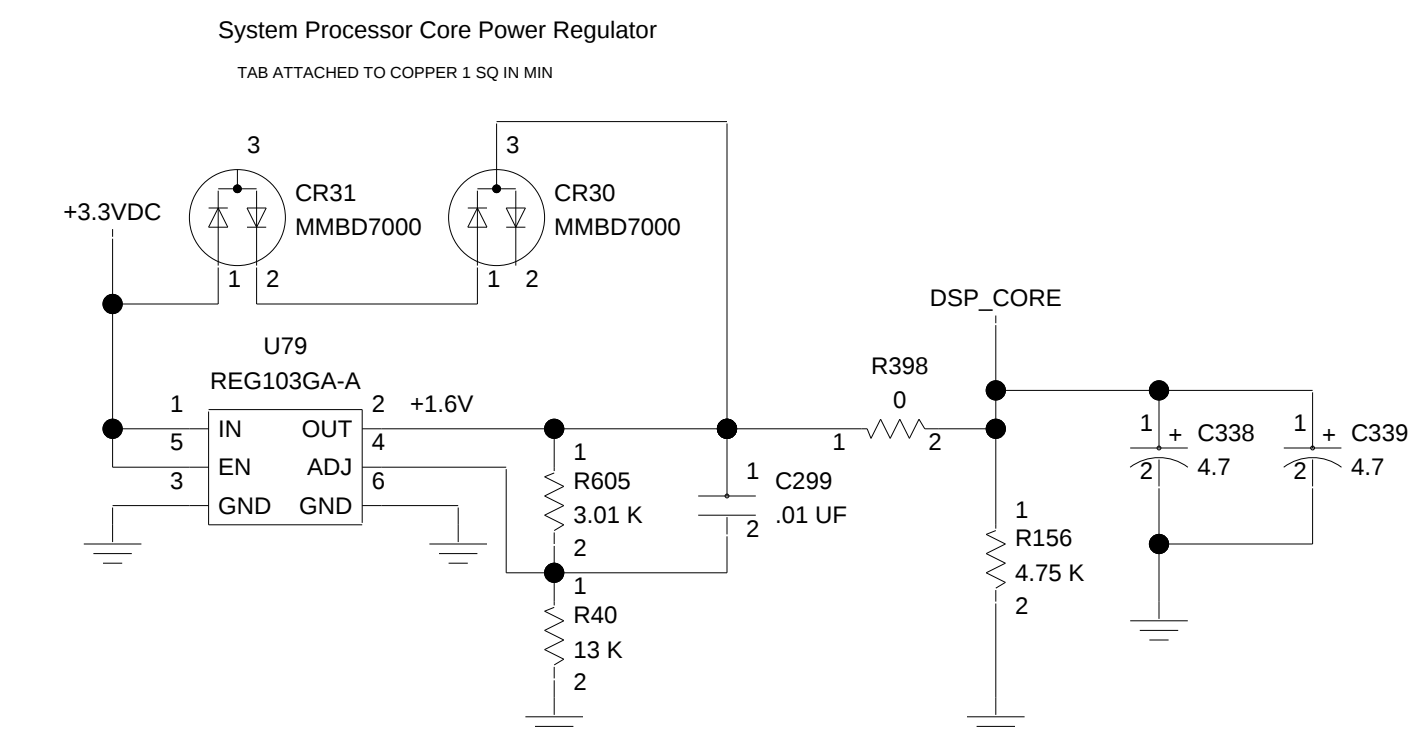
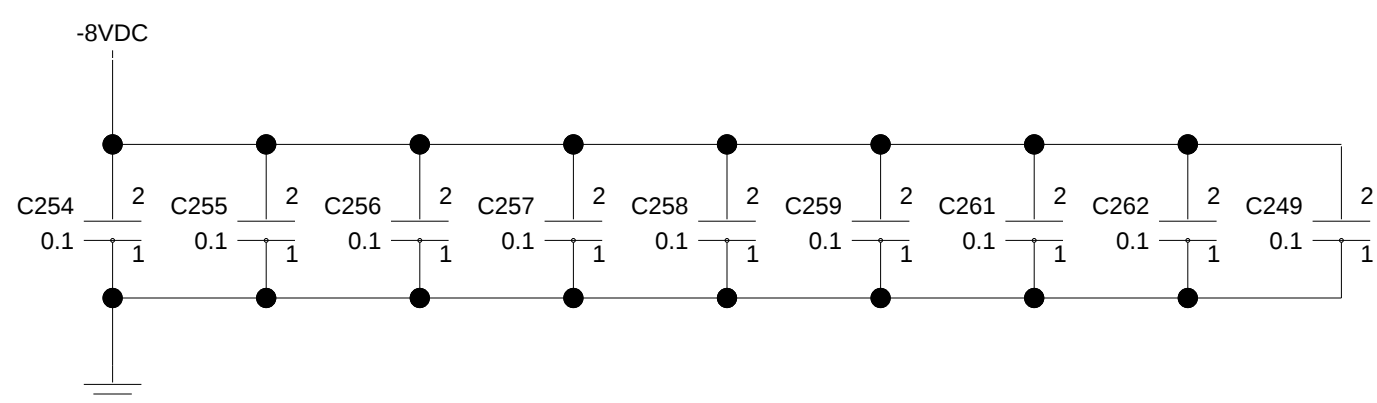
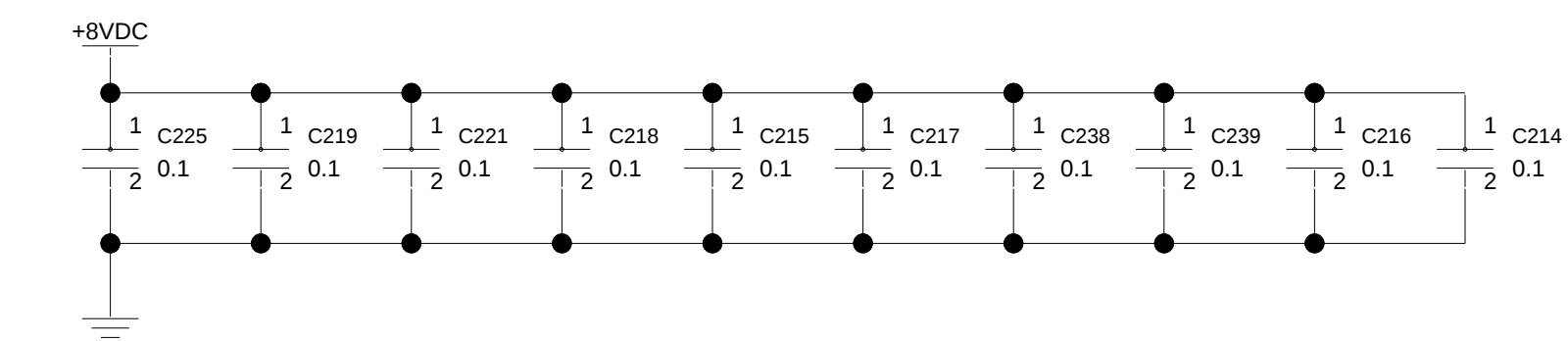
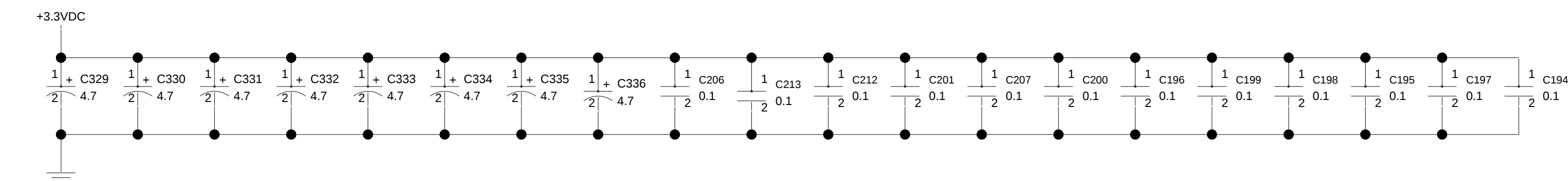


ADDRESS AND DATA BUSES





FILTER CAPS AND POWER REGULATORS

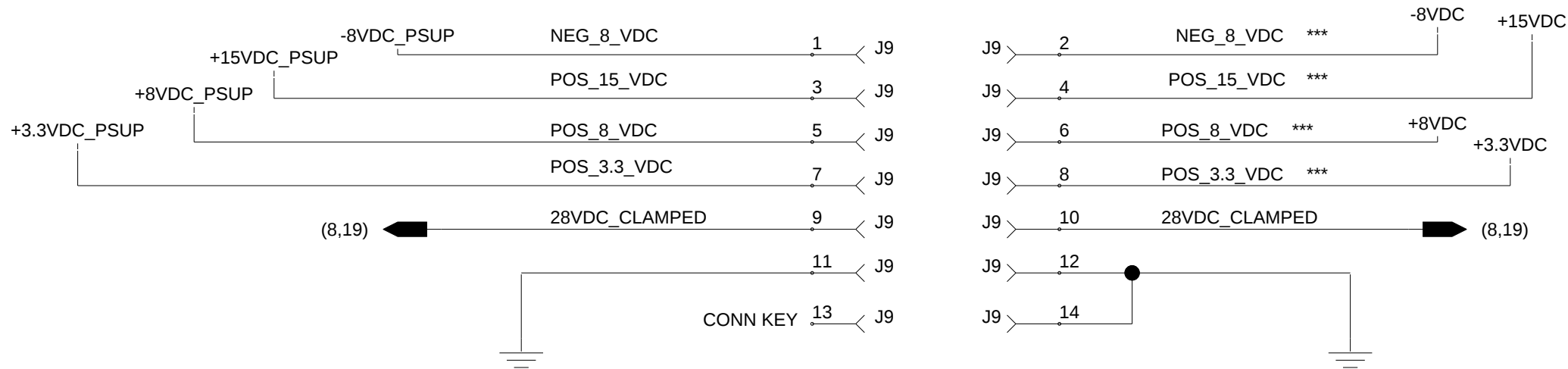


CONNECTOR 1

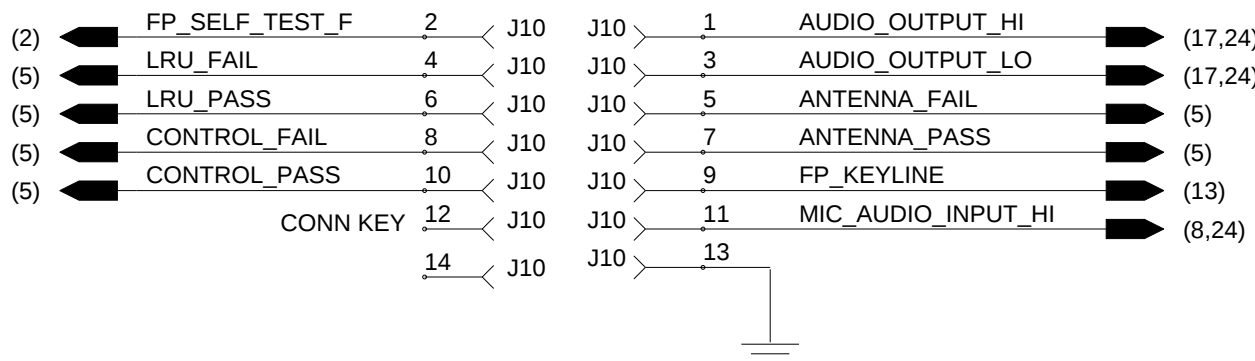
FROM POWER SUPPLY TO RF MODULE



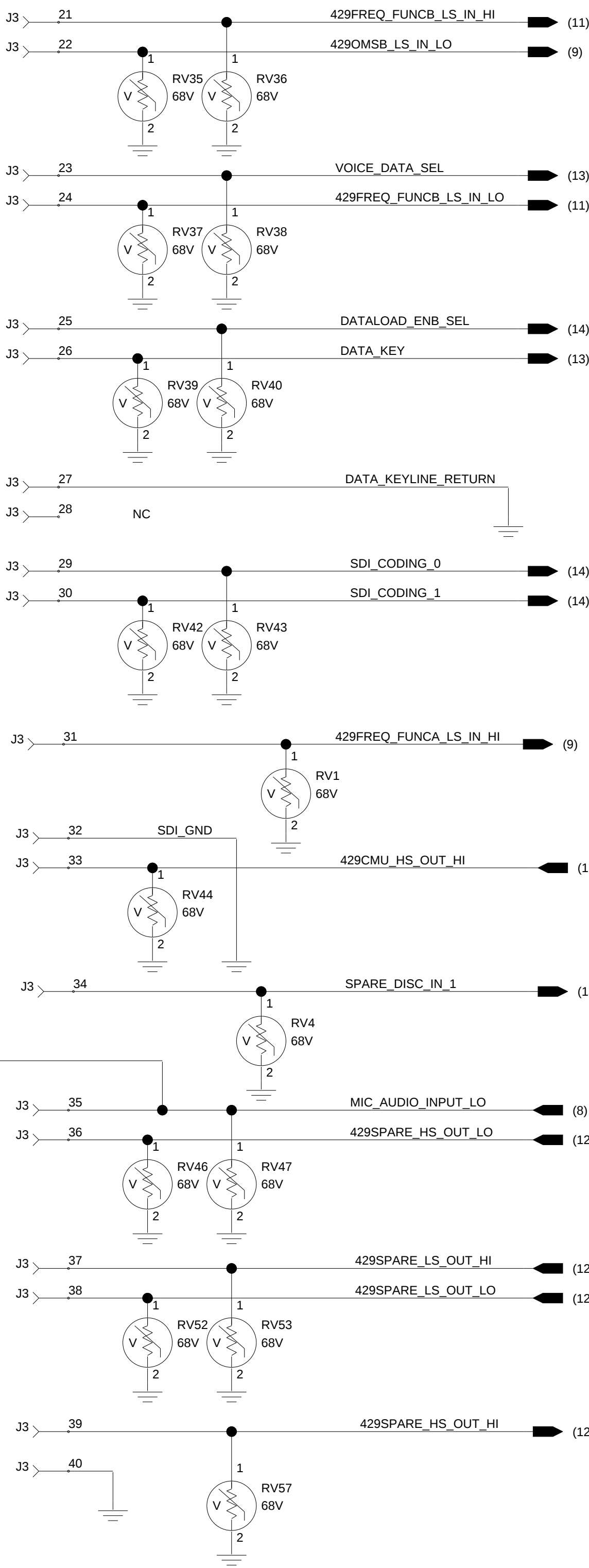
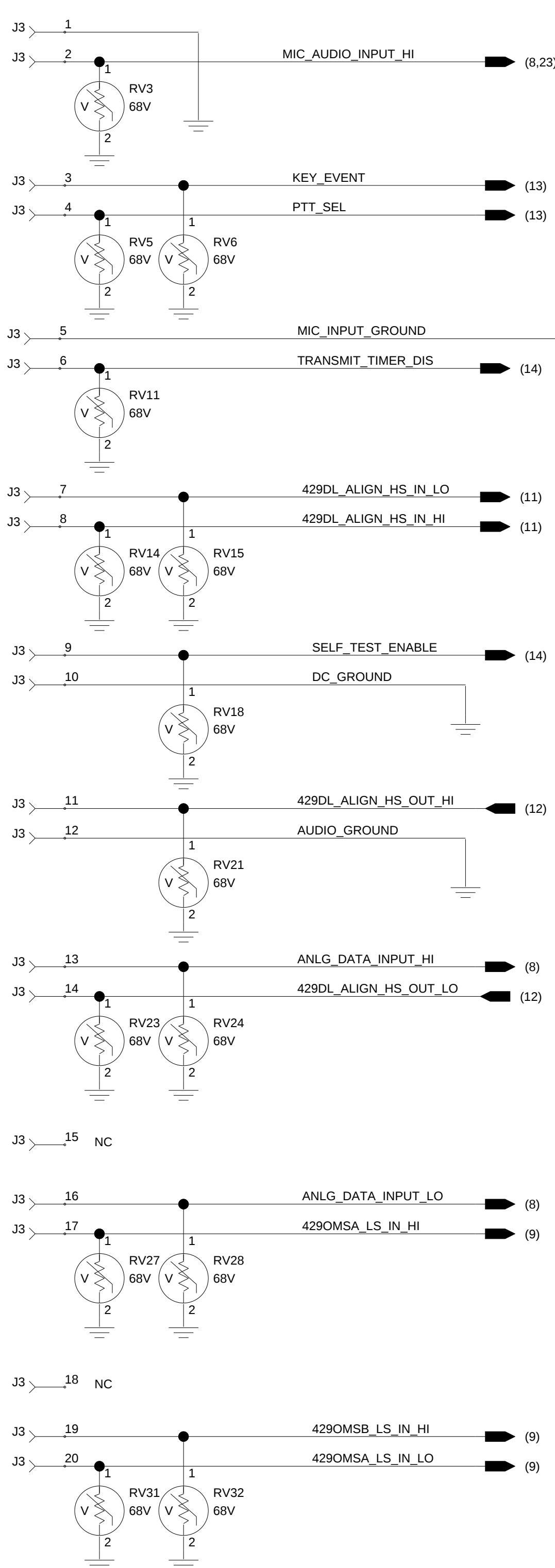
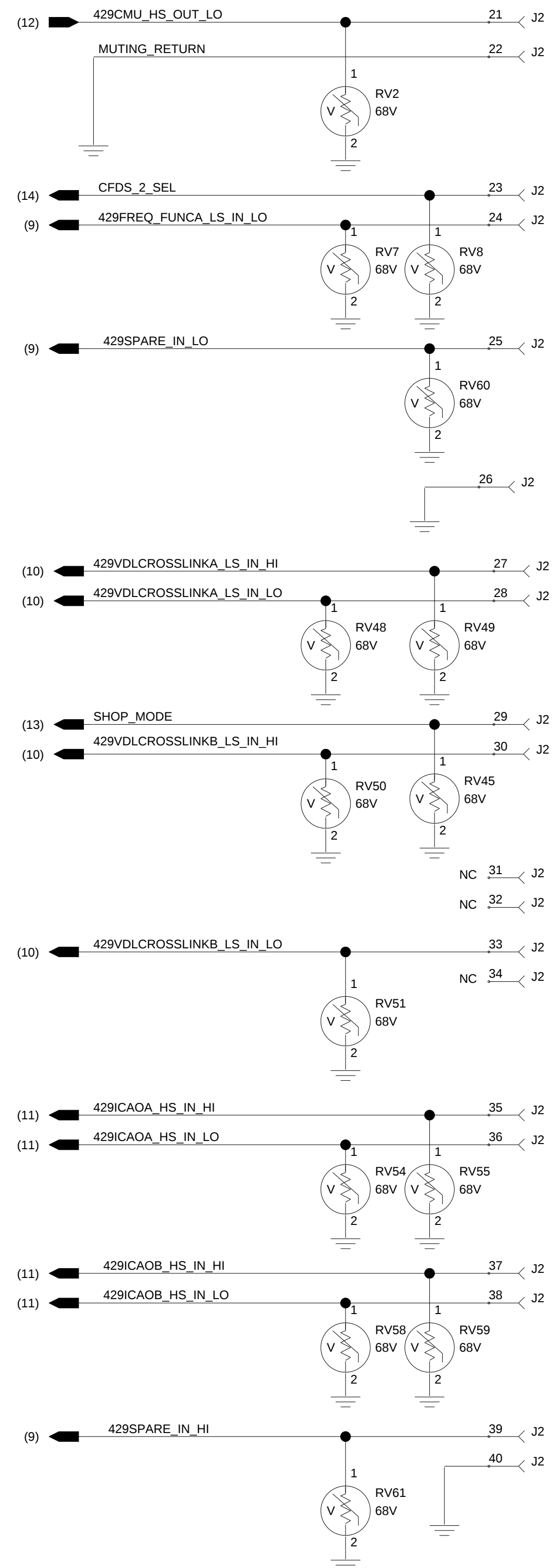
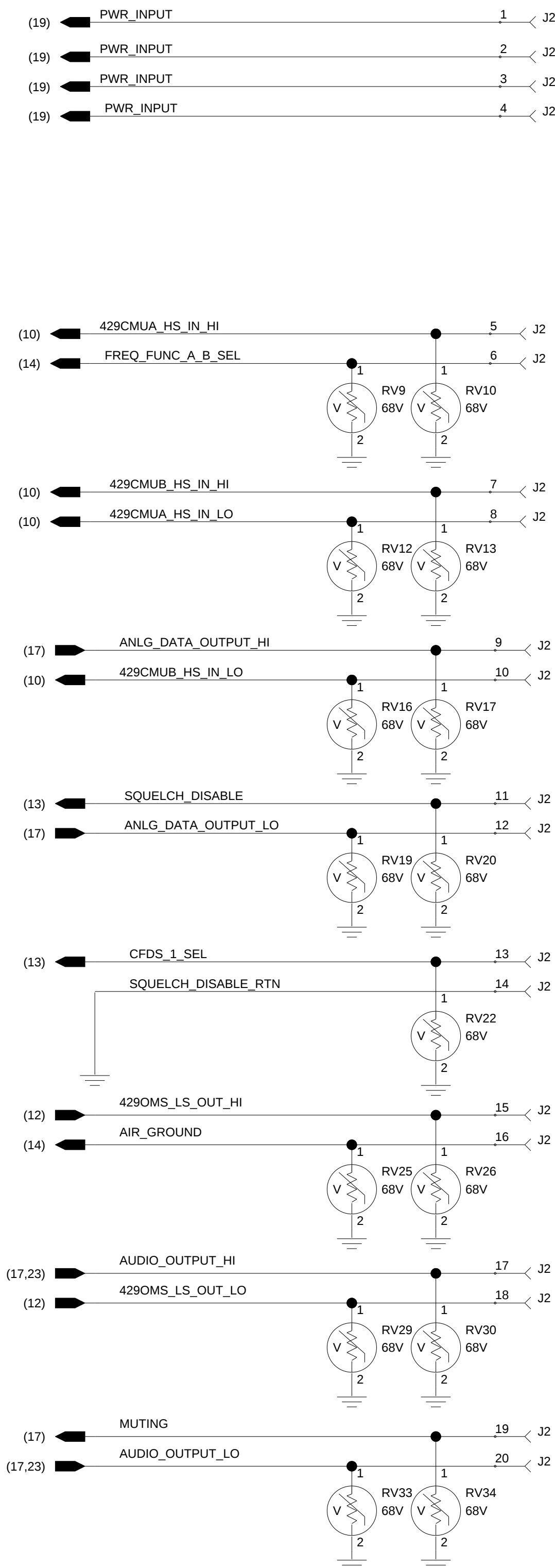
*** RF MODULE LOOP AROUND



LED board conn

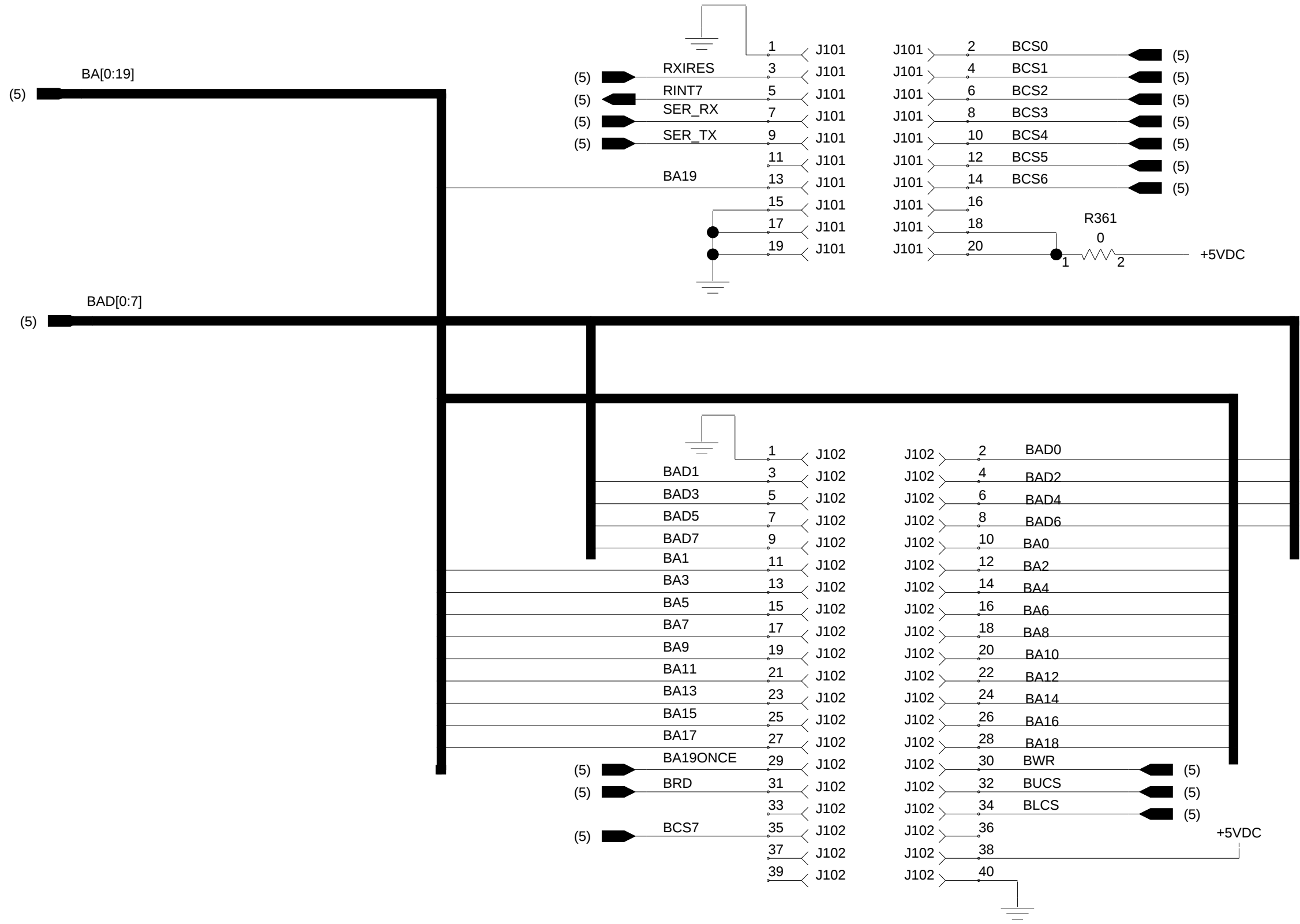


CONNECTOR 2

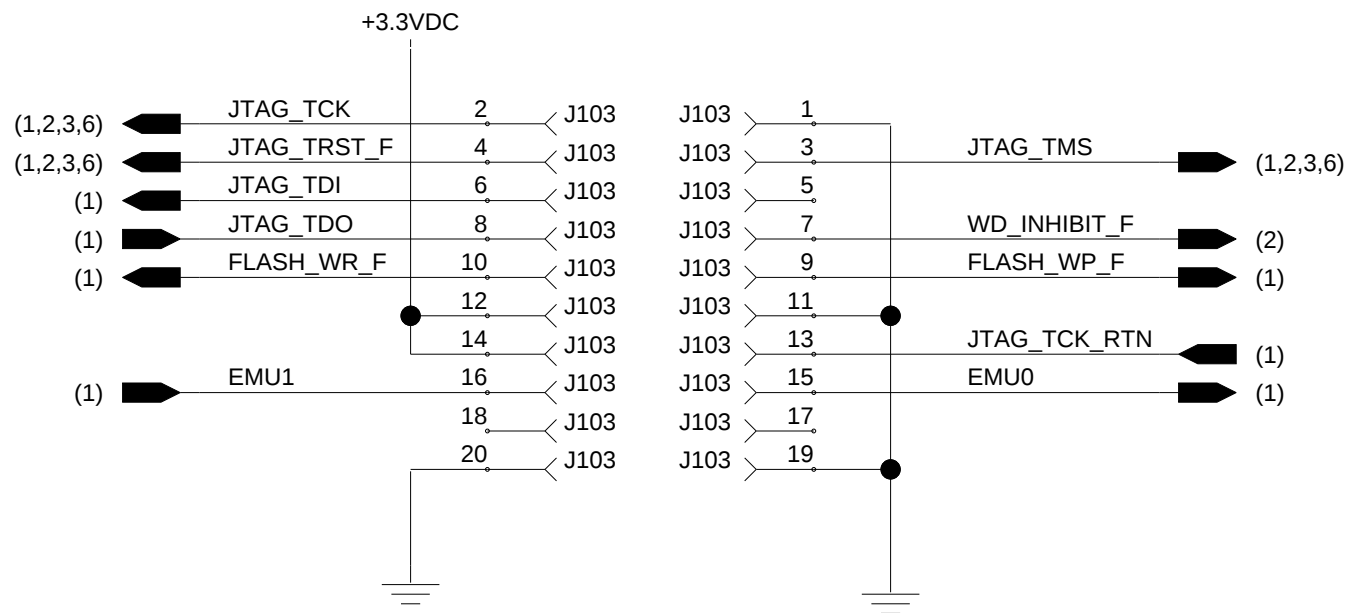


BASED ON INTERCONNECT PIN ASSIGNMENTS

CONNECTOR 3



SYSTEM PROCESSOR JTAG PORT
CPN 859-5534-220 SMT
20 PIN 0.050 SOCKET



VOCODER JTAG PORT
CPN 859-5534-220 SMT
20 PIN 0.050 SOCKET

