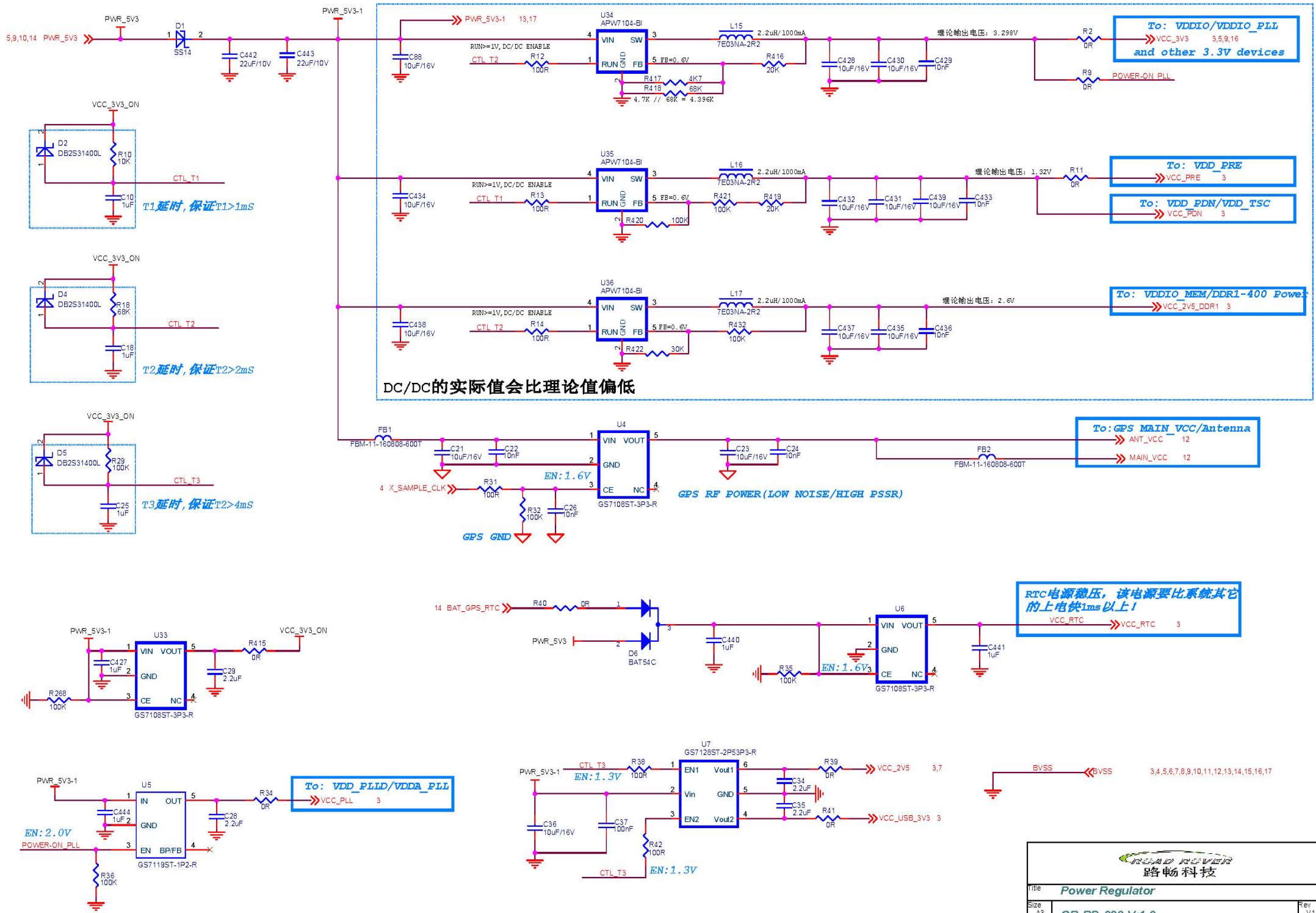


U8A

X_GPIO0_0
X_GPIO0_1
X_GPIO0_2/SCL_1
X_GPIO0_3/SDA_1
X_GPIO0_4/PWM0
X_GPIO0_5/PWM1
X_GPIO0_6/PWM2
X_GPIO0_7/PWM3

X_GPIO0_8/WARM_RST_B
X_GPIO0_9/TMS/EF_CS_B
X_GPIO0_10/TDI/EF_DIN
X_GPIO0_11/TDO/EF_DOUT
X_GPIO0_12/NSRST
X_GPIO0_13/RTCK/EF_PGM
X_GPIO0_14/NTRST
X_GPIO0_15/TCK/EF_SCLK

X_GPS_PPS/X_GPIO1_21
X_GPS_SGN/X_GPIO3_20
X_GPS_MAG/X_GPIO3_21
X_GPS_SAMPLE_CLK/X_GPIO3_22
X_GPS_CLK/X_GPIO4_28

X_CK0_0

X_RESET_B
X_RST_OUT_B
X_RTC_RST_B
X_PWR_EN
X_LVR_PDN
X_RTC_ALARM

X_XIN
X_XOUT
X_XINW
X_XOUTW

X_TEST_MODE[0]
X_TEST_MODE[1]
X_TEST_MODE[2]
X_TEST_MODE[3]
X_TEST_MODE[4]
X_TEST_MODE[5]
X_SCAN_EN

SIRFprima

C1 X GPIO0_0
C2 X GPIO0_1
D2 X GPIO0_2
D1 X GPIO0_3
Y14
AC14 X GPIO0_5
AB14
AA14 X GPIO0_7

J21 X GPIO0_8
J22 X GPIO9
J23 X GPIO10
J20 X GPIO11
H21 X GPIO12
H20 X GPIO13
E20 X GPIO14
E21 X GPIO15

B1 X GPIO1_21
B3 X GPS_SGN
A2 X GPS_MAG
A1 X SAMPLE_CLK
B2 X GPS_RF_CLK

AC13

W10 X RESET_B
Y11 X RTC_RST_B
AA9 X PWR_EN
Y13 X LVR_PDN
AA11

AB12 XIN
AC12 XOUT
AB9 XINW
AC9 XOUTW

W11 X TEST_MODE0
Y12 X TEST_MODE1
Y10 X TEST_MODE2
Y9 X TEST_MODE3
W7 X TEST_MODE4
AA12 X TEST_MODE5
AA13 R70

X GPIO0_0 R50 10K VDD_IO
X GPIO0_8 R51 10K VDD_IO
X RTC_RST_B R52 10K VDD_RTCIO
X LVR_PDN R53 10K VDD_LVR

Software Reset

GPS Data JTAG Stream

X_GPIO0_1 INAND_nRST 16

X_GPIO0_5 TP TP1

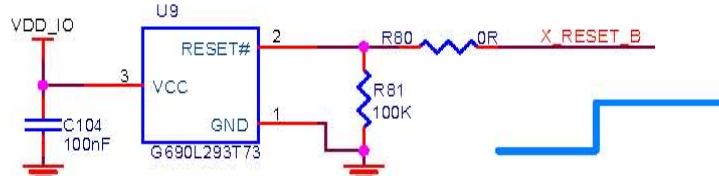
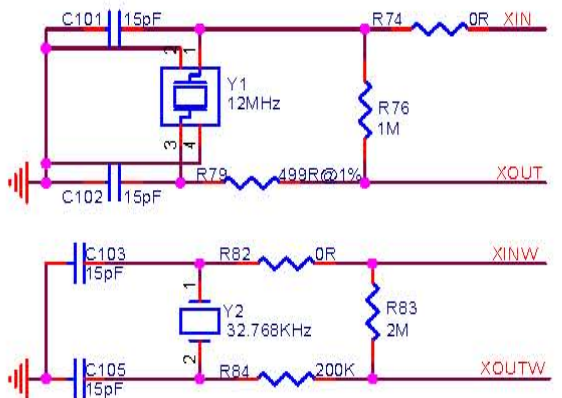
X_GPIO1_21 GPS_RF_RST 12

X_GPIO0_7 R67 0R PWM3_BACKLIGHT_CTL 8
R68 4K7-NC
读PWM在CPU内部有下拉

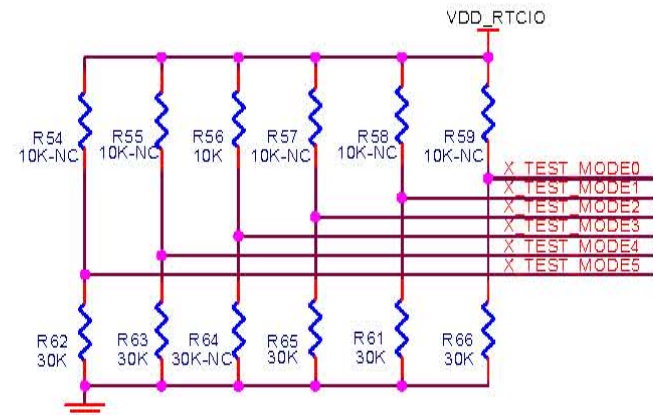
X_PWR_EN:
High Active

时钟/谐振

Boot config



550ms > Reset time > 140ms

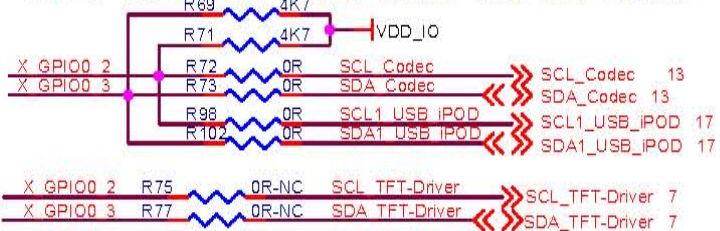


Default Boot: Embedded ROM+2KB page MLC
NAND (8-bit) Test Mode[5:0]=001000
Option Boot: 4K/Page mLC Flash Test Mode[5:0]=001001
JTAG Mode: Test Mode[5:0]=110000 (用于第一次烧录SLC)
Nboot: 注意, 用ROM_Boot可以直接通过UART烧录, 不需JTAG)

Boot Configure for three options

Boot configuration	X_TEST_MODE[5:0]
Boundary Scan (JTAG)	6b110000
SLC Nand Boot 2KB Page 8 Bit	6b000101
MLC Nand Boot 2KB Page 8 Bit Default	6b001000
INAND Boot	6b001100
EFuse	6b110010
MLC Nand Boot 4KB Page 8 Bit Option	6b001001

I2C-1 Using for Audio Codec and AMP Board



2,3,5,6,7,8,9,10,11,12,13,14,15,16,17

路畅科技

title **Boot Config/Reset**

Size A4

Date Monday, December 12, 2011

Sheet 4 of 18

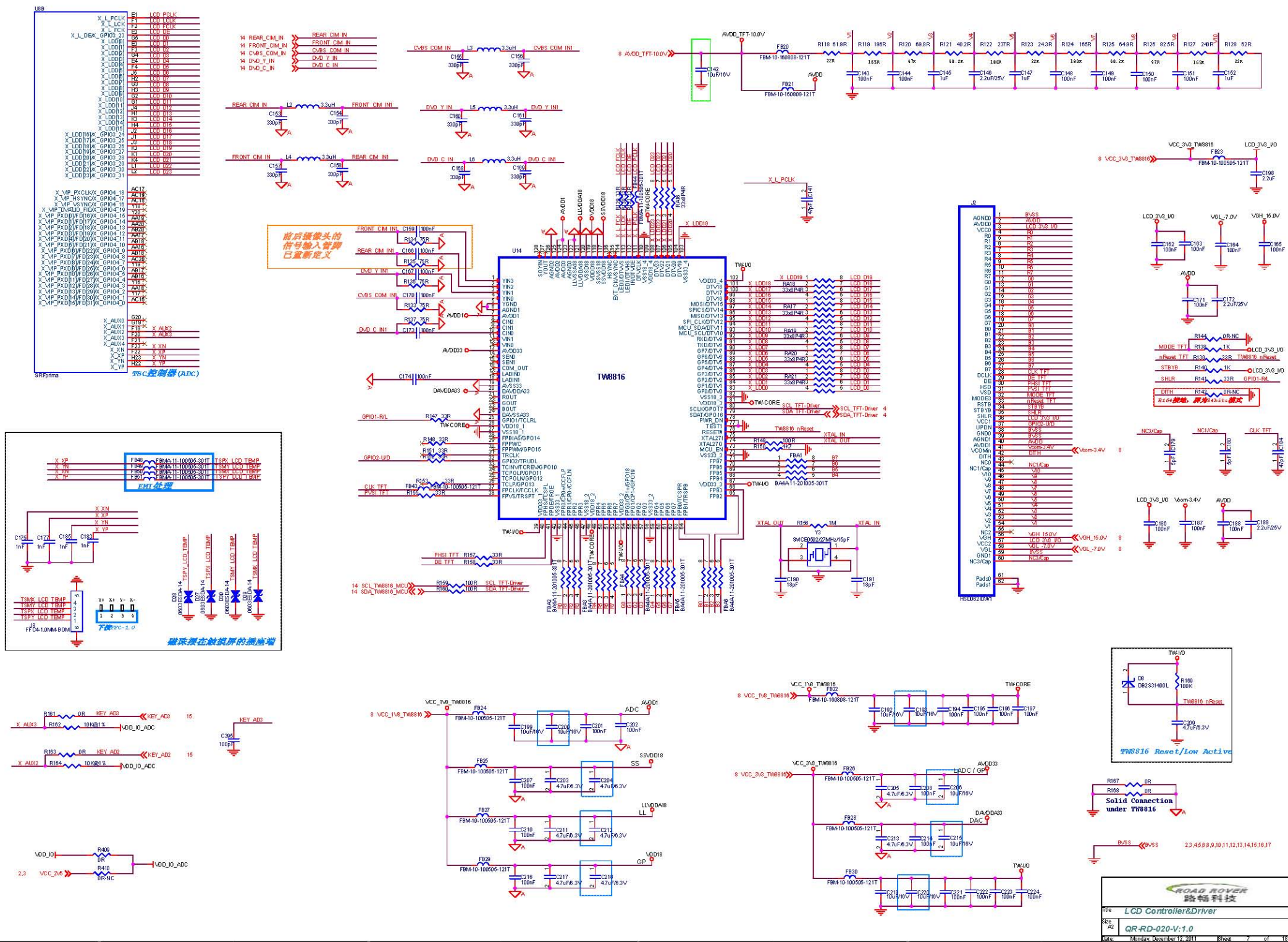
Rev V1.2

QR-RD-020-V:1.0

SIRF prima

SDIO-1

Date: Monday, December 12, 2011 Sheet 5 of 18



EMI Shield

The PWM output current of mcu is about 2mA

From AMP Board MCU

14 PWM_Backlight_MCU

From CPU

4 PWM3_BACKLIGHT_CTL

EMI Shield

VSW信号要被屏蔽包地，而且走线要尽量短！

Handstar: 15v
Tianma: 22v

正负压

仓库10uF/0805/Y5V的电容
耐压可以做到16V

默认是关屏的

必须采用电解100uF电容，否则电感会产生“吱吱”杂音

$I = 104\text{mV} / R_{fb}$

LED_POWER (静态)
Handstar LCD:
1. 6.2inch: $R_{fb}=0.68R$;
2. 7.0inch: $R_{fb}=0.68R$;
3. 8.0inch: $R_{fb}=0.5R$.

Vcom fine turning

Handstar LCD:
1. 6.2inch: $V_{com}=3.4V$;
2. 7.0inch: $V_{com}=3.9V$;
3. 8.0inch: $V_{com}=4.3V$.

LCD BACKLIGHT		
Size A4	QR-RD-020-V:1.0	Rev V1.2
Date: Monday, December 12, 2011	Sheet 8	of 18

U8C

X_TXD_0
X_RXD_0/X_GPIO3_15
X_TXD_1
X_RXD_1/X_GPIO3_16
X_TXD_2/X_GPIO0_16
X_RXD_2/X_GPIO0_17

X_SDA_0/X_GPIO3_18
X_SCL_0/X_GPIO3_19

X_SPI_EN_0/X_GPIO0_28
X_SPI_CLK_0/X_GPIO0_29
X_SPI_DIN_0/X_GPIO0_30
X_SPI_DOUT_0/X_GPIO0_31
X_SPI_EN_1/X_GPIO1_22
X_SPI_CLK_1/X_GPIO1_25
X_SPI_DIN_1/X_GPIO1_24
X_SPI_DOUT_1/X_GPIO1_23

X_USCLK_0/I2S_EXTCLK/X_GPIO0_18
X_UTXD_0/X_GPIO0_19
X_URXD_0/X_GPIO0_20
X_UTFS_0/USB1_UTMI_DRVVBUS/X_GPIO0_21
X_URFS_0/I2S_DOUT[1]/X_GPIO0_22
X_USCLK_1/X_GPIO0_23
X_UTXD_1/X_GPIO0_24
X_URXD_1/X_GPIO0_25
X_UTFS_1/X_GPIO0_26
X_URFS_1/I2S_DOUT[2]/X_GPIO0_27
X_USCLK_2/X_GPIO4_29
X_UTXD_2/X_GPIO4_30
X_URXD_2/X_GPIO4_31
X_UTFS_2/X_GPIO1_26
X_URFS_2/X_GPIO1_27

X_CK0_1/I2S_MCLK/X_GPIO1_20
X_AC97_BIT_CLK/I2S_BCLK
X_AC97_SYNC/I2S_LRCLK
X_AC97_DIN/I2S_DIN
X_AC97_DOUT/I2S_DOUT[0]

X_USB_CLK
X_USB_DAT[0]
X_USB_DAT[1]
X_USB_DAT[2]
X_USB_DAT[3]
X_USB_DAT[4]
X_USB_DAT[5]
X_USB_DAT[6]
X_USB_DAT[7]
X_USB_DIR
X_USB_NXT
X_USB_STP

X_USB_VBUS
X_USB_DP
X_USB_DN
X_USB_ID
X_USB_RREFEXT
X_USB_TEST_GPANAIO

SIRFprima

AB1 Prima TXD0 R198 100R TXD0 RADAR TXD0_RADAR 14
AA1 Prima RXD0 R200 100R RXD0 RADAR RXD0_RADAR 14
W3 Prima TXD1 R202 100R-NC X TXD 1 X_TXD_1 14
Y3 Prima RXD1 R205 100R-NC X RXD 1 X_RXD_1 14
AB3 Prima TXD2 R206 100R PC TXD PC_TXD 14
AC3 Prima RXD2 R207 100R PC RXD PC_RXD 14

Debug or BT

E22 X SDA 0
E23 X SCL 0

Y21 GPS_SPI_CS 12
AC21 GPS_SPI_CLK 12
AA21 X SPI DIN 0
AB21 GPS_SPI_DOUT 12
Y22 X SPI EN 1
AC22 X SPI CLK 1
AA22 X SPI DIN 1
AB22 X SPI DOUT 1

GPS Control Stream

AC1 GPIO0_18
AC2 USP TXD0 R223 100R USP TXD0 IP0D USP_TXD0_IP0D 14
AA2 USP RXD0 R224 100R USP RXD0 IP0D USP_RXD0_IP0D 14
AB2 GPIO0_21
AA3
AC4
AA4 USP TXD1 R226 100R USP TXD1 RES USP_TXD1_RES 14
Y4 USP RXD1 R226 100R USP RXD1 RES USP_RXD1_RES 14
AB4
W5
AC15

AB15 USP TXD2 R230 100R-NC Prima TXD1 BT Prima_TXD1_BT 13
W15 USP RXD2 R232 100R-NC Prima RXD1 BT Prima_RXD1_BT 13
Y15

AA15 USP TXD2 R423 100R USP TXD2 MainAMP BT USP_TXD2_MainAMP_BT 14
USP RXD2 R424 100R USP RXD2 MainAMP BT USP_RXD2_MainAMP_BT 14 2011-4-21

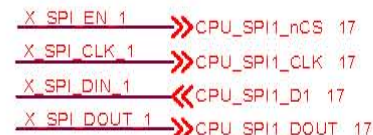
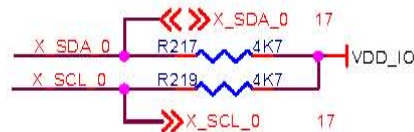
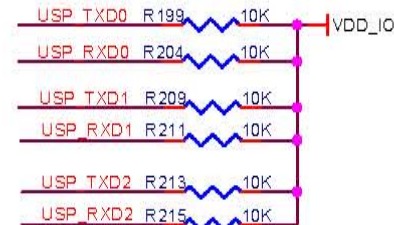
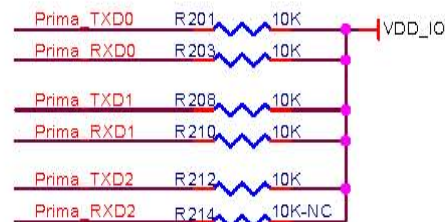
W4 I2S MCLK I2S_MCLK 13
W1 I2S BCLK I2S_BCLK 13
W2 I2S LRCLK I2S_LRCLK 13
Y2 I2S DIN I2S_DIN 13
Y1 I2S DOUT I2S_DOUT 13

I2S Ports

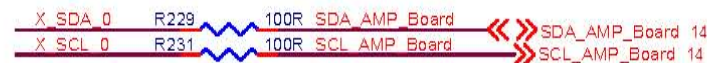
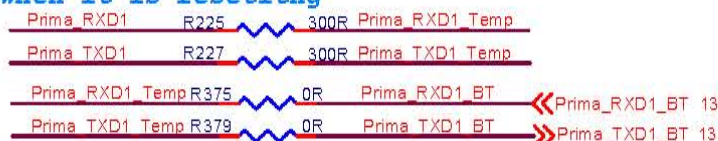
M1
L3
L4
M3
M4
M2
N1
N3
N2
L5
L4
N4
M5

Y5 X USB VBUS
AC5 X USB DP X_USB_DP 10
AB5 X USB DN X_USB_DN 10
AA5 X USB ID
AA6 R239 6.04K@1%
W6

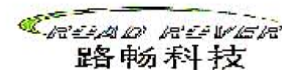
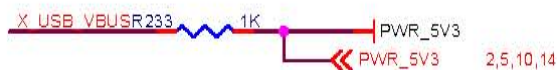
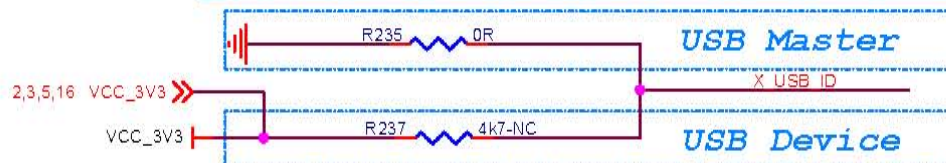
2,3,4,5,6,7,8,10,11,12,13,14,15,16,17

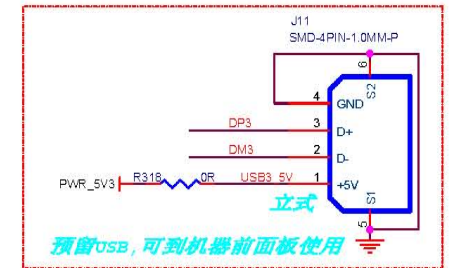
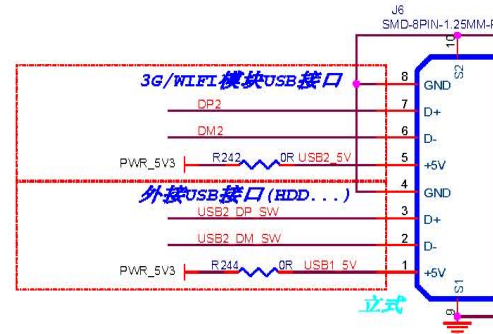
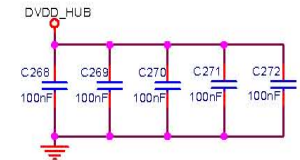
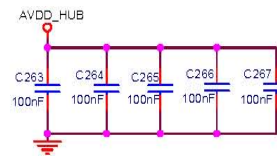
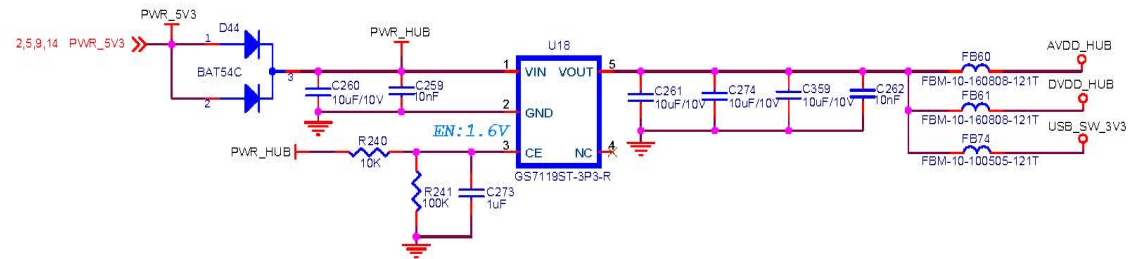
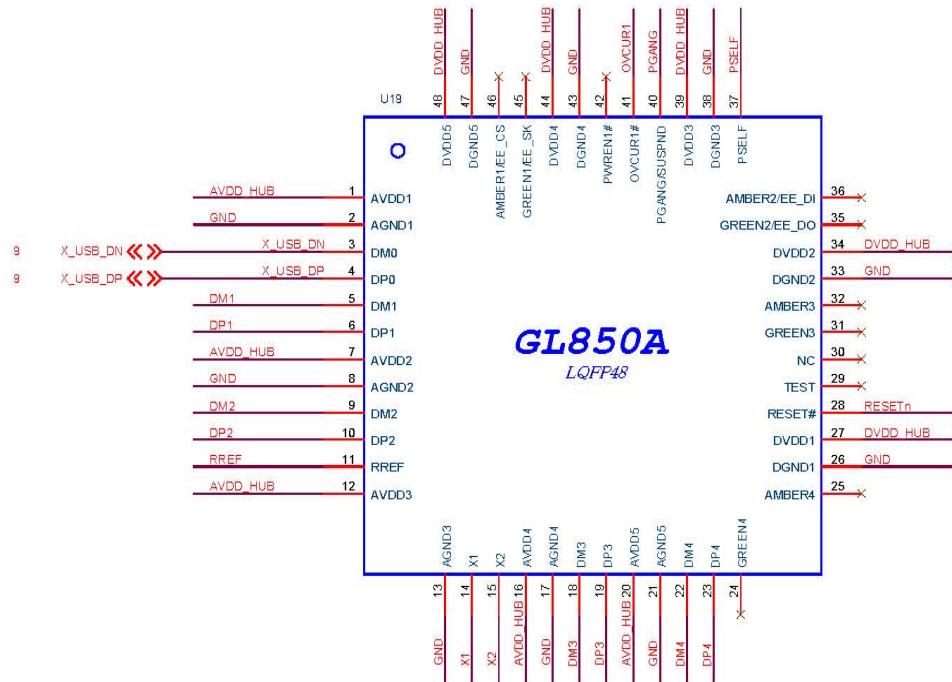


Prevent the current of CPU UART charge into Bluetooth when it is reseting

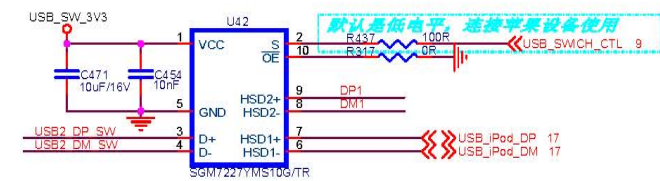
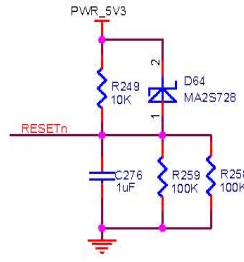
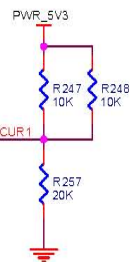
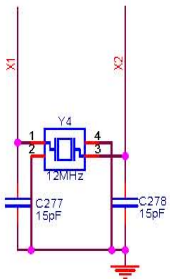
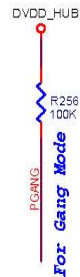
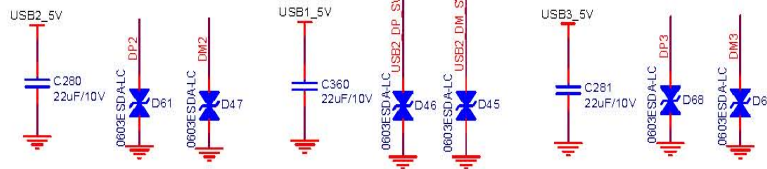


CPU与AMP板通信I2C



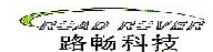


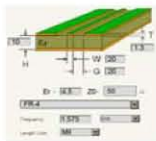
假如USB走线不顺、交叉，组与组之间是可以互换调整的（通过调整原理图）。



FUNCTION TABLE

OE	S	HSD1+, HSD1-	HSD2+, HSD2-
0	0	ON	OFF
0	1	OFF	ON
1	*	OFF	OFF





TCXO

Caution:

1. This page is RF module, so should pay more attention to PCB layout. The RF Z0 is 50ohm, the drawing should fit RF rules and RF portion must be put into one shielding case.
2. The value of components which marked asterisk "*" may be tuned in different product. These components are in RF matching nets, their values are effected by the PCB material parameter, tolerance of component, and so on.
3. The idiosyncrasy and tolerance of passive components such as capacitors, inductances, has been appointed. These components just can be substituted by which are more high precision.
4. There are many DC filtering capacitors in the schematic, please put them close to the source pins. Putting some ground vias near to the ground pin and in ground panel is well for DC filtering and EMI rejection also.

RF Microstrip
Z0= 50 ohm



EMI Shield

For good grounding, put 9 vias under RF IC die ground pad.

假如使用硬件RC复位时:
(1)、R=510K;
(2)、C=0.56uF;

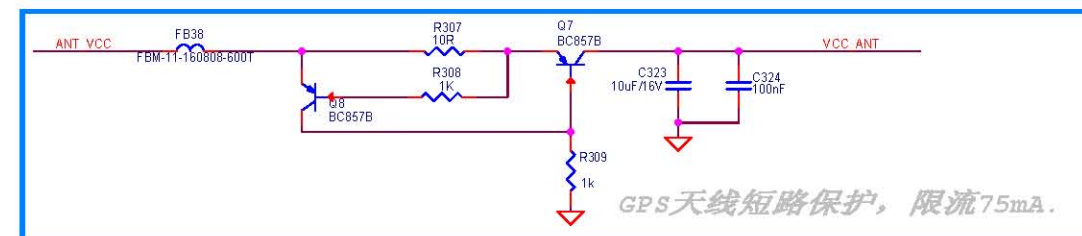
Digital IF Frequency: 4.092MHZ
RF_CLK: 16.367667MHZ

1uF ceramic capacitance must be X7R material, that will be better performance

- 1、GPS RF Receiver core
- 2、Must be placed into RF shielding case

External antenna connector(IPEX): SAWF1 亦可用: FAR-F6KA-1G5754-L4AJ

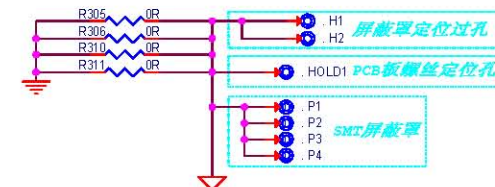
RF Microstrip
Z0= 50 ohm



GPS天线短路保护, 限流75mA.

BVSS << BVSS

2,3,4,5,6,7,8,9,10,11,13,14,15,16,17



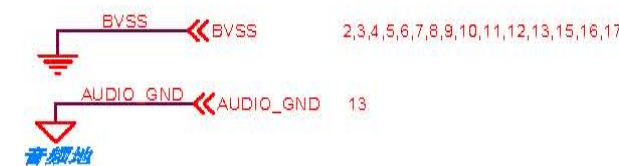
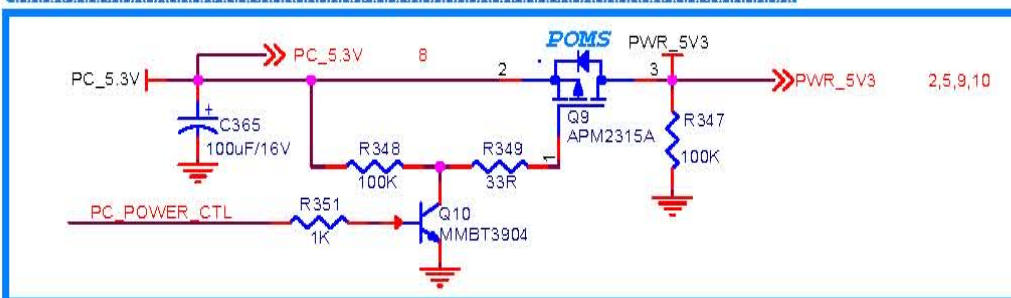
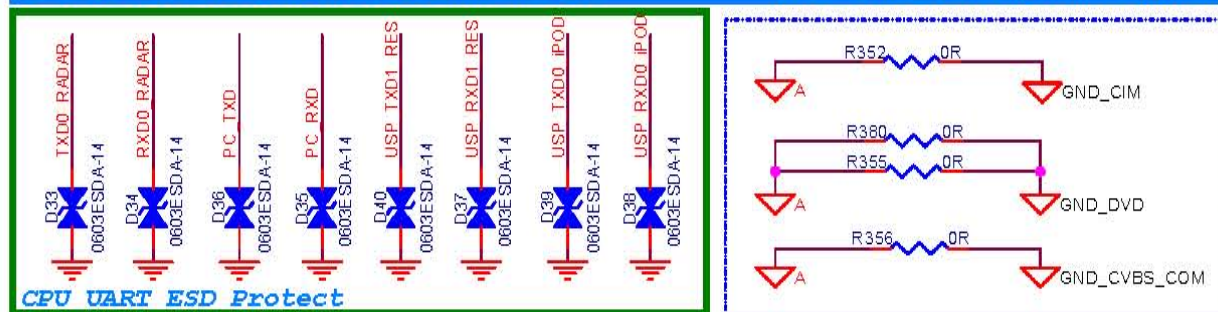
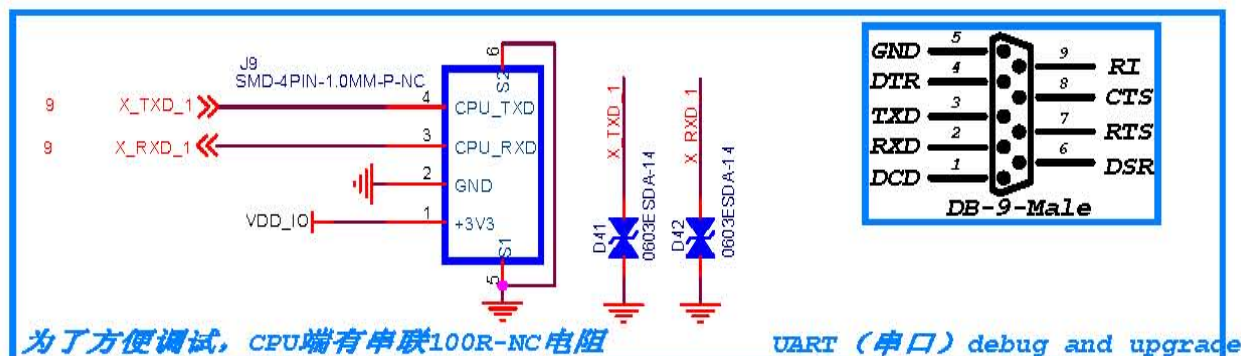
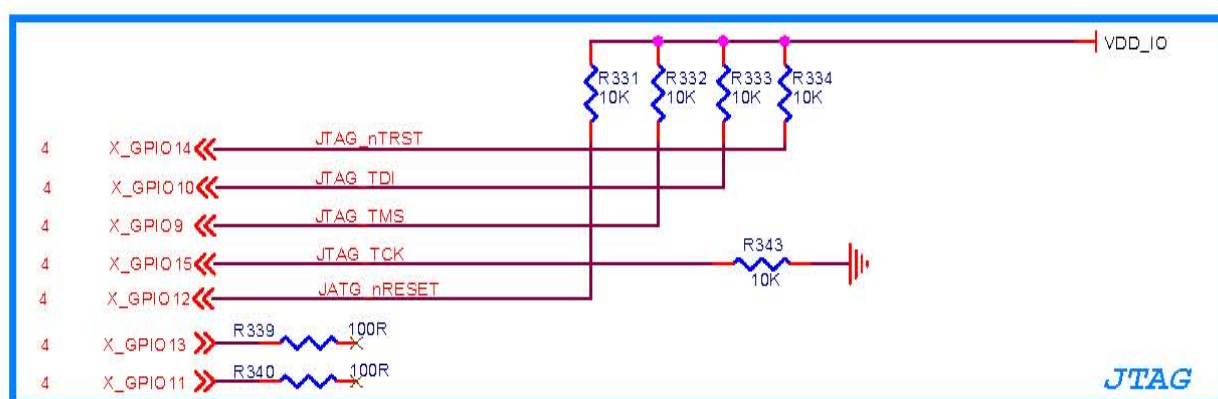
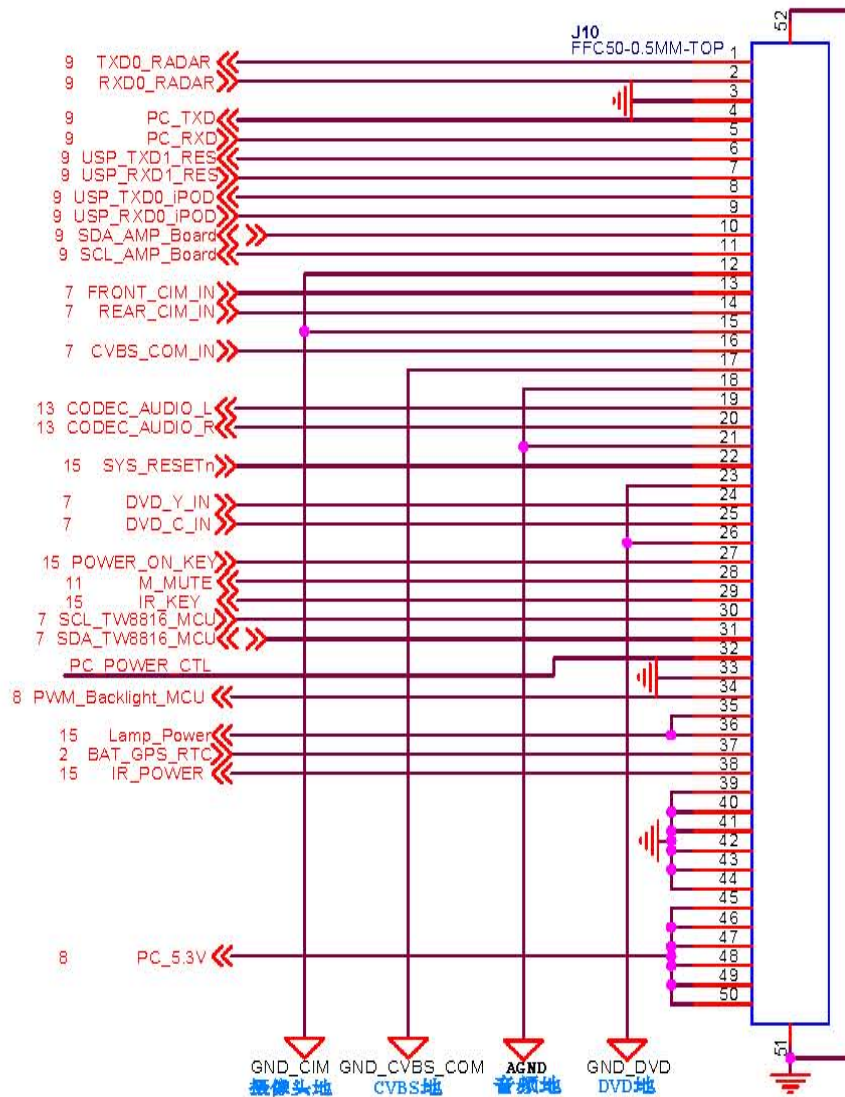
ROAD ROVER
路畅科技

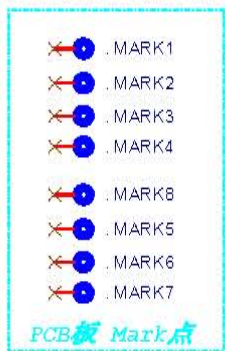
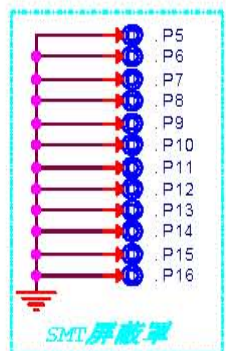
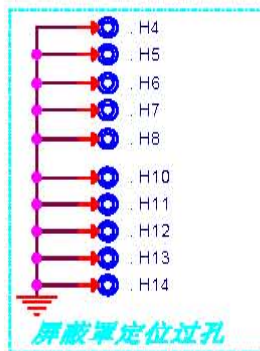
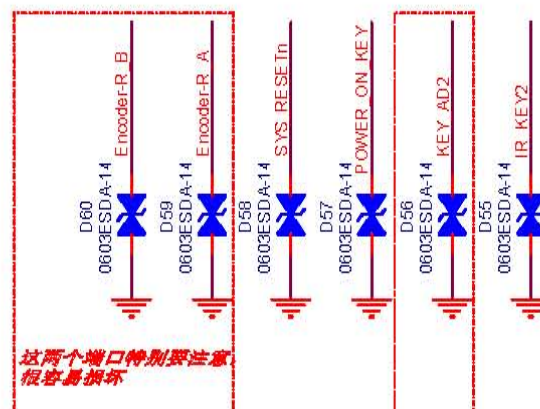
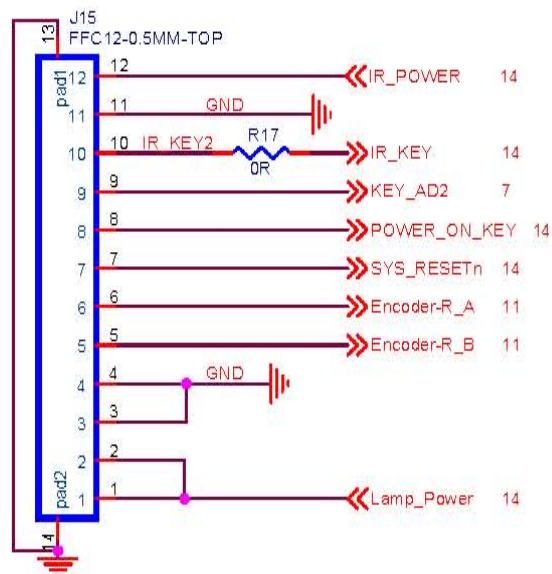
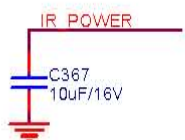
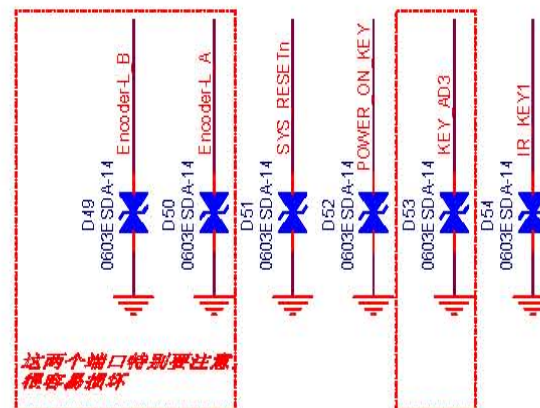
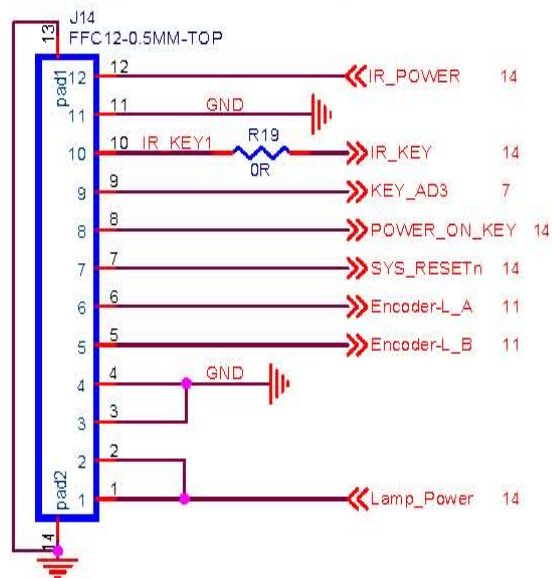
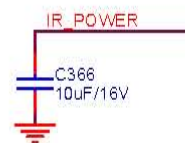
title GPS RF

Size A3 QR-RD-020-V:1.0

Rev V1.2

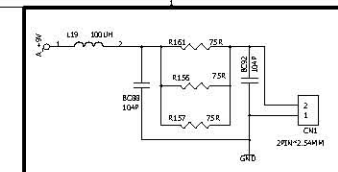
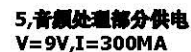
Date: Monday, December 12, 2011 Sheet 12 of 18





Title Key Connector		
Size A4	QR-RD-020-V:1.0	Rev V1.2
Date: Monday, December 12, 2011	Sheet 15	of 18

2.收音天线供电
 $V=BATT-0.3V, I<400MA$



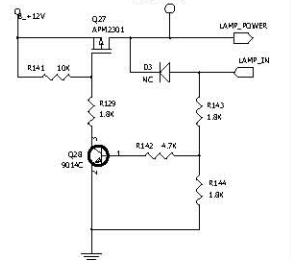
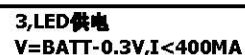
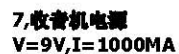
DVD_A5V:V=5.2V, I=1.5A.DVD_1.8V:V=1.8V,I=500MA.DVD_3.3V:V=3.3V,I=800MA

6,DVD电源

DVD_A5V:V=5.2V, I=1.5A.DVD_1.8V:V=1.8V,I=500MA.DVD_3.3V:V=3.3V,I=800MA

6,DVD电源

V=5.25V, I_{max}=4A, VCC=14V, SW=200Khz



The circuit diagram shows a power supply section. It starts with a +1.2V input connected through a resistor R107 (1K) to a diode D1 (1N4148). The output of D1 goes through a capacitor C13 (7µF 25V) to ground. A feedback loop from the output passes through a network of resistors (R102, R103, R104, R105, R106) and capacitors (C14, C15, C16, C17) before reaching the non-inverting input of op-amp U6 (LM324). The op-amp's output drives the base of transistor Q19 (9014C), which is part of a Darlington pair with Q21 (9014C). Transistor Q22 (9014C) provides the main output current to the load, controlled by the base-emitter junction of Q21. Various other components like R101, R108, R109, R110, R111, R112, R113, R114, R115, R116, R117, R118, R119, R120, R121, R122, R123, R124, R125, R126, R127, R128, R129, R130, R131, R132, R133, R134, R135, R136, R137, R138, R139, R140, R141, R142, R143, R144, R145, R146, R147, R148, R149, R150, R151, R152, R153, R154, R155, R156, R157, R158, R159, R160, R161, R162, R163, R164, R165, R166, R167, R168, R169, R170, R171, R172, R173, R174, R175, R176, R177, R178, R179, R180, R181, R182, R183, R184, R185, R186, R187, R188, R189, R190, R191, R192, R193, R194, R195, R196, R197, R198, R199, R200, R201, R202, R203, R204, R205, R206, R207, R208, R209, R210, R211, R212, R213, R214, R215, R216, R217, R218, R219, R220, R221, R222, R223, R224, R225, R226, R227, R228, R229, R230, R231, R232, R233, R234, R235, R236, R237, R238, R239, R240, R241, R242, R243, R244, R245, R246, R247, R248, R249, R250, R251, R252, R253, R254, R255, R256, R257, R258, R259, R260, R261, R262, R263, R264, R265, R266, R267, R268, R269, R270, R271, R272, R273, R274, R275, R276, R277, R278, R279, R280, R281, R282, R283, R284, R285, R286, R287, R288, R289, R290, R291, R292, R293, R294, R295, R296, R297, R298, R299, R300, R301, R302, R303, R304, R305, R306, R307, R308, R309, R310, R311, R312, R313, R314, R315, R316, R317, R318, R319, R320, R321, R322, R323, R324, R325, R326, R327, R328, R329, R330, R331, R332, R333, R334, R335, R336, R337, R338, R339, R340, R341, R342, R343, R344, R345, R346, R347, R348, R349, R350, R351, R352, R353, R354, R355, R356, R357, R358, R359, R360, R361, R362, R363, R364, R365, R366, R367, R368, R369, R370, R371, R372, R373, R374, R375, R376, R377, R378, R379, R380, R381, R382, R383, R384, R385, R386, R387, R388, R389, R390, R391, R392, R393, R394, R395, R396, R397, R398, R399, R400, R401, R402, R403, R404, R405, R406, R407, R408, R409, R410, R411, R412, R413, R414, R415, R416, R417, R418, R419, R420, R421, R422, R423, R424, R425, R426, R427, R428, R429, R430, R431, R432, R433, R434, R435, R436, R437, R438, R439, R440, R441, R442, R443, R444, R445, R446, R447, R448, R449, R450, R451, R452, R453, R454, R455, R456, R457, R458, R459, R460, R461, R462, R463, R464, R465, R466, R467, R468, R469, R470, R471, R472, R473, R474, R475, R476, R477, R478, R479, R480, R481, R482, R483, R484, R485, R486, R487, R488, R489, R490, R491, R492, R493, R494, R495, R496, R497, R498, R499, R500, R501, R502, R503, R504, R505, R506, R507, R508, R509, R510, R511, R512, R513, R514, R515, R516, R517, R518, R519, R520, R521, R522, R523, R524, R525, R526, R527, R528, R529, R530, R531, R532, R533, R534, R535, R536, R537, R538, R539, R540, R541, R542, R543, R544, R545, R546, R547, R548, R549, R550, R551, R552, R553, R554, R555, R556, R557, R558, R559, R560, R561, R562, R563, R564, R565, R566, R567, R568, R569, R570, R571, R572, R573, R574, R575, R576, R577, R578, R579, R580, R581, R582, R583, R584, R585, R586, R587, R588, R589, R590, R591, R592, R593, R594, R595, R596, R597, R598, R599, R600, R601, R602, R603, R604, R605, R606, R607, R608, R609, R610, R611, R612, R613, R614, R615, R616, R617, R618, R619, R620, R621, R622, R623, R624, R625, R626, R627, R628, R629, R630, R631, R632, R633, R634, R635, R636, R637, R638, R639, R640, R641, R642, R643, R644, R645, R646, R647, R648, R649, R650, R651, R652, R653, R654, R655, R656, R657, R658, R659, R660, R661, R662, R663, R664, R665, R666, R667, R668, R669, R670, R671, R672, R673, R674, R675, R676, R677, R678, R679, R680, R681, R682, R683, R684, R685, R686, R687, R688, R689, R690, R691, R692, R693, R694, R695, R696, R697, R698, R699, R700, R701, R702, R703, R704, R705, R706, R707, R708, R709, R710, R711, R712, R713, R714, R715, R716, R717, R718, R719, R720, R721, R722, R723, R724, R725, R726, R727, R728, R729, R730, R731, R732, R733, R734, R735, R736, R737, R738, R739, R740, R741, R742, R743, R744, R745, R746, R747, R748, R749, R750, R751, R752, R753, R754, R755, R756, R757, R758, R759, R760, R761, R762, R763, R764, R765, R766, R767, R768, R769, R770, R771, R772, R773, R774, R775, R776, R777, R778, R779, R780, R781, R782, R783, R784, R785, R786, R787, R788, R789, R790, R791, R792, R793, R794, R795, R796, R797, R798, R799, R800, R801, R802, R803, R804, R805, R806, R807, R808, R809, R810, R811, R812, R813, R814, R815, R816, R817, R818, R819, R820, R821, R822, R823, R824, R825, R826, R827, R828, R829, R830, R831, R832, R833, R834, R835, R836, R837, R838, R839, R840, R841, R842, R843, R844, R845, R846, R847, R848, R849, R850, R851, R852, R853, R854, R855, R856, R857, R858, R859, R860, R861, R862, R863, R864, R865, R866, R867, R868, R869, R870, R871, R872, R873, R874, R875, R876, R

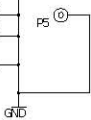
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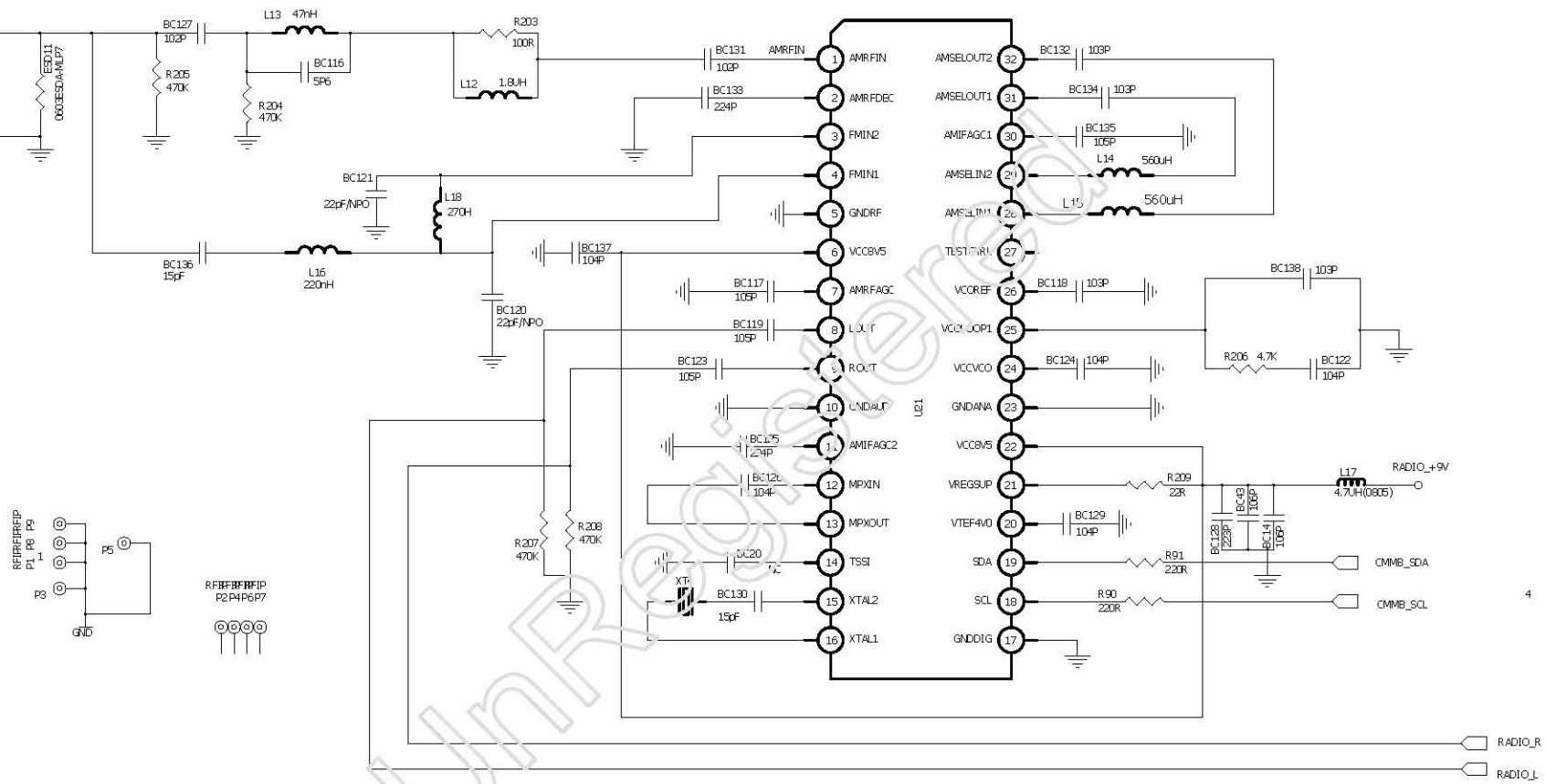
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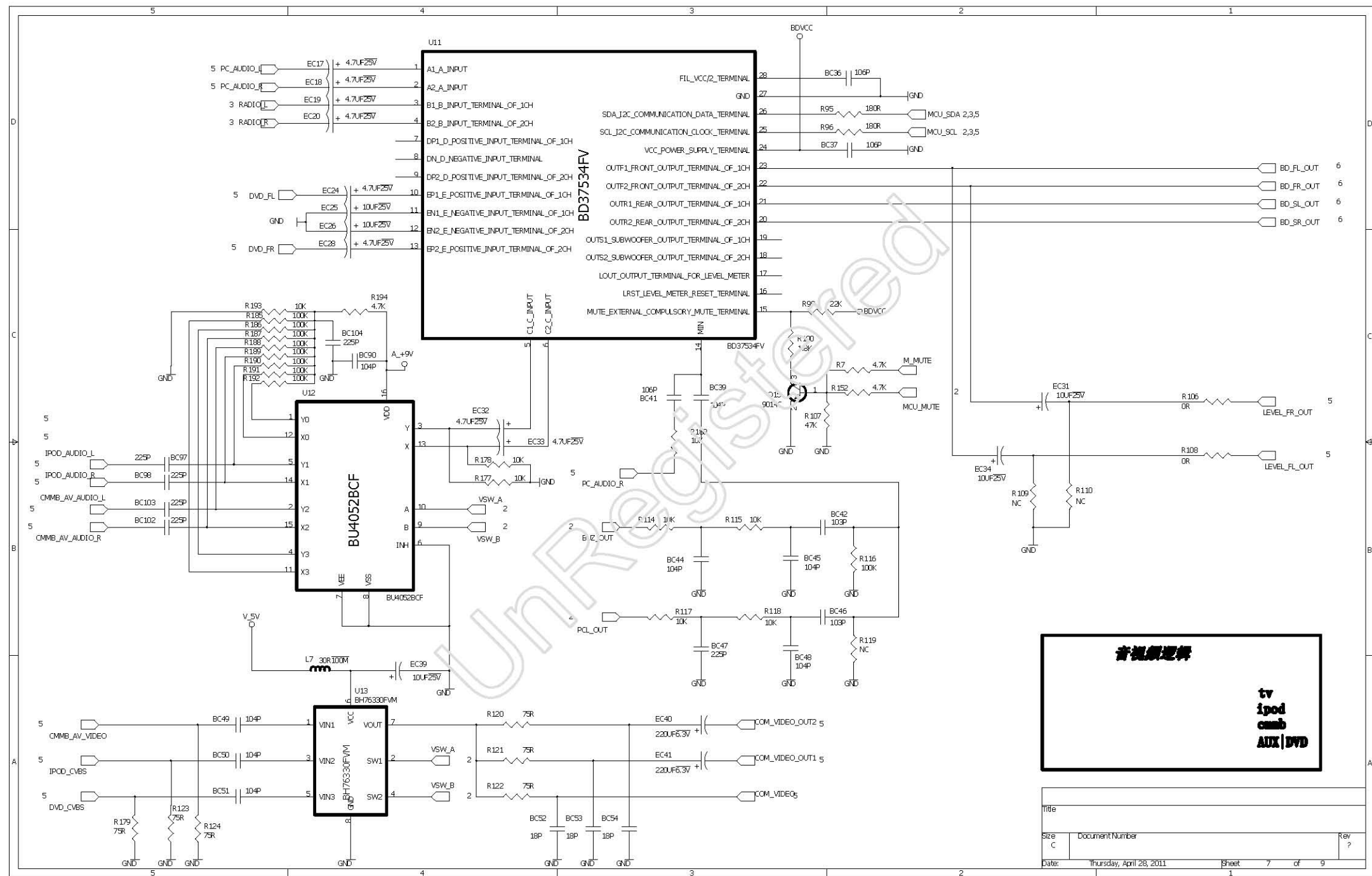


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