

June 23, 2008 ITPD-08-F005A

To: Mr. Gregory Czumak / PCTEST TCB

Re: FCC ID: ACJ9TGCF-523

Applicant: Panasonic Corporation of North America

Correspondence Reference Number: ACJ80337 Confirmation Number: 804170337-40 Date of Original Email: June 12, 2008

This is in response to your request for additional information.

- Please submit the Schematics, Block Diagrams and Operational Descriptions for the DTS and NII applications.
 <u>Answer</u>: I have been advised by ITPD factory that Intel has provided PCTEST with the requested Model
 512AN_MMW confidential documents to support application filing for FCC Parts 15C (DTS) and Part 15E (NII).
- 2. Please verify compliance of the NII transmitter with 15.407(c).

 <u>Answer:</u> Refer to provided confidential documents, which includes description how NII transmitter automatically discontinue transmission in case of either absence of information to transmit or operational failure.
- 3. The Cover Letter and MPE Report list the BT antenna gain as 3.69 dBi, however, the end of the Photo Exhibit lists it as 3.46 dBi. Please clarify, and revise, as necessary.

 Answer: Under separate e-mail attached I am sending marked-up antenna location photographs and amended list of associated antenna gains, which correctly stated the BT antenna gain is 3.39 dBi.
- 4. Please provide a photo showing the distance of each antenna from the bottom of the pc, in order to justify the mobile classification wrt RFx.
 - <u>Answer:</u> Refer to the above referred marked-up antenna location photographs. All transmitter antennas are located greater than 20 cm between all the antennas and all person's body (excluding extremities of hands, wrist and feet) during wireless modes of operation.
- 5. FYI: in the future, please be sure that the licensed transmitter block diagram(s) show all of the clock/oscillator values, as required by the FCC Rules.
 - <u>Answse:</u> I have again requested our factory and their employed transmitter manufactures to declare the employed clocks/oscillators frequencies on all provided block and schematic diagrams.

Sincerely yours,

Richard Mullen

Richard Mullen Group Manager