

Description of Peripherals

Power Supply	: Voltage	: AC 100 - 240 V, 50 / 60 Hz
	: Power Consumption	: 3.7-1.5A
Video Input Signal	: 1 Vp-p, sync negative, 75 ohms terminated	
S-Video Input Signal	: Y (luminance signal), 1 Vp-p, 75 ohms terminated	
	: C (chrominance signal), 0.286 Vp-p, 75 ohms terminated	
RGB/YPBPR Input Signal		
Video Signal	: RGB Analog (0.7 Vp-p, 1.0 Vp-p with sync on green, 75 ohms) Unlimited numbers of colors	
	: YPBPR (Y: 1Vp-p, PB,PR: 0.7Vp-p 75 ohms)	
Sync Signal	: H/V separate, H/V composite, or Sync-on-Green	
H-Frequency	: 24 – 110 kHz (TTL Level)	
V-Frequency	: 50 - 86 Hz (TTL Level)	
Terminals	: S-Video Input	: Mini DIN 4-pin × 1
	: Video Input	: RCA pin × 1
	: Video Audio Input	: M3 stereo mini pin × 1
	: Serial Port (RS-232C)	: Mini DIN 8-pin × 1
	: Mouse Terminal	: 13-pin round connector × 1
	: RGB Input 1	: D-Sub mini 15-pin × 1
	: RGB/ YPBPR Input 2	: D-Sub mini 15-pin × 1
	: RGB/ YPBPR Audio Input	: M3 stereo mini pin × 1
	: Audio Output	: M3 stereo mini pin × 1
	: USB Port	: USB B-Type 4-pin × 1
Operating Temperature	: 41 ° F - 104 ° F (5 - 40)	
Operating Humidity	: 10 % - 80 %	

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Performance Explanation of LCD Projector

This projector is equipped with the function to project video images onto a large screen
Using monitor signal from PCs or video signal from VCRs, etc.

And the audio signal from PC or VCR is output by projector's internal speaker.

The following describes how the video signal is processed.

1. Video Mode, Video signal which is input to S-VIDEO input terminal or VIDEO input terminal is Supplied to Digital block. In the digital block, Video signal is converted to RGB signal. And Synchronizing signals which are separated by Digital Video Decoder is supplied to LCD drive timing control circuit.
2. RGB Mode, the monitor signal which is supplied to monitor input terminal from a PC, is supplied to digital block and output RGB signal.
Synchronizing signal, which is produced by the Synchronizing signal processing, is added to LCD drive timing control circuit and system control circuit.
3. Synchronizing signal, is used to process synchronization of system control and LCD drive.
4. PLL oscillation clock of LCD drive timing control circuit and OSD clock which is supplied to signal process circuit are adjusted to the proper frequency for each PC by the system control circuit which distinguishes the input Synchronizing signal.