LPF

ANT SW
D201,202
HSU277
Q201
2SA1162
Q202
2SC2712

FT201
FL-201

BPF

LNA

IC202
RF105

IC203
RF105

20dB
10dB
8dB

TX POWER CONT

MIXER

BUF

BUF

VGA

VGA

LPF

LPF

LPF

LPF

VGA

VGA

IC201
REGURATOR
TK11238BMCL

PLL VCO

PLL DATA

TX DATA

REF OSC

AGC

JK-1040
22P

CHG CONTACT
CHG IN
DET
2SC2712

Q6
CHG IN
DET
2SC2712

Q9
AVR
2SC1618

Q4
AVR
2SD1080

D2,10,11
CHG CONT
2SD471,2SC2712

BUFF
Q12
2SC2712

IC5
RH5VL33AA

RESET

V_CPU

5V

JK-234
J3

9V

D2
STATUS

DRIVE

JK-940
22P

5V

TX DATA

PLL DATA

REF OSC

Q OUT

AGC

I OUT

IC1
EEPROM
24LC64

CPU

9.600MHz
X1

DATA

SS
ENGINE

IC7
ASIC

CODEC

AMP
Q5
2SC2712

Q21
AMP
2SC2712

S1
PAGE

S2
TONE

V_CPU

T1

RL SW
Q7
2SC2712

RL1

IC5
BELL
DET

TEL JACK

J2
(2P)

LPF

ANT SW
D601,602 HSU277
Q601 2SA1162
Q602 2SC2712

BPF
FT601 FL-040

LNA

BUF

MIXER

BUF

IC602 RF105

20dB
10dB
0dB

IC603 RF106

VGA   VGA

LPF   LPF
LPF   LPF

VGA   VGA

PLL VCO

REGURATOR
TK11230BMCL
IC601

REF OSC

PLL DATA

TX DATA

TX POWER CONT

AGC

JK-1040 22P   PWB: RF

JK-940 22P   PWB: MAIN

TX DATA   V_BATT   PLL DATA   REF OSC   Q OUT   AGC   I OUT

CHG CONTACT
−   +

BT901
BT-334

(2P)
(2P)

V_BATT

CHG_DET
Q511 2SC2712

BATT_LOW DET
IC505 RHSVL35CA

BATT_OFF DET
IC503 RHSVL20AA

IC506 EEPROM 24LC84

CPU

DATA

SS ENGINE

IC504 ASIC

X501
9.600MHz

CODEC

SCAN

1	2	3	MUTE
4	5	6	
7	8	9	
*	0	#	
TALK	FLASH R/P	MEM	
A	B	C	D
		V/D	V/U

D501 STATUS
V_BATT

Q505 AMP 2SC2712

Q514 AMP 2SC2712

Q515 AMP 2SC2712

Q508 DRIVE 2SC2712

V_BATT

J504 JK-802

MC591 MK-475

SP591 YY-901

SP901 SP-363

			UNIDEN No.	MODEL No.
REV.No.	DESIGN	DRAWN.BY	UC604ZH	
	CHECK.BY	APPRO.BY	TITLE	
			PORT BLOCK DIAGRAM	
			DRAWING No.	
			UNIDEN CORP.	