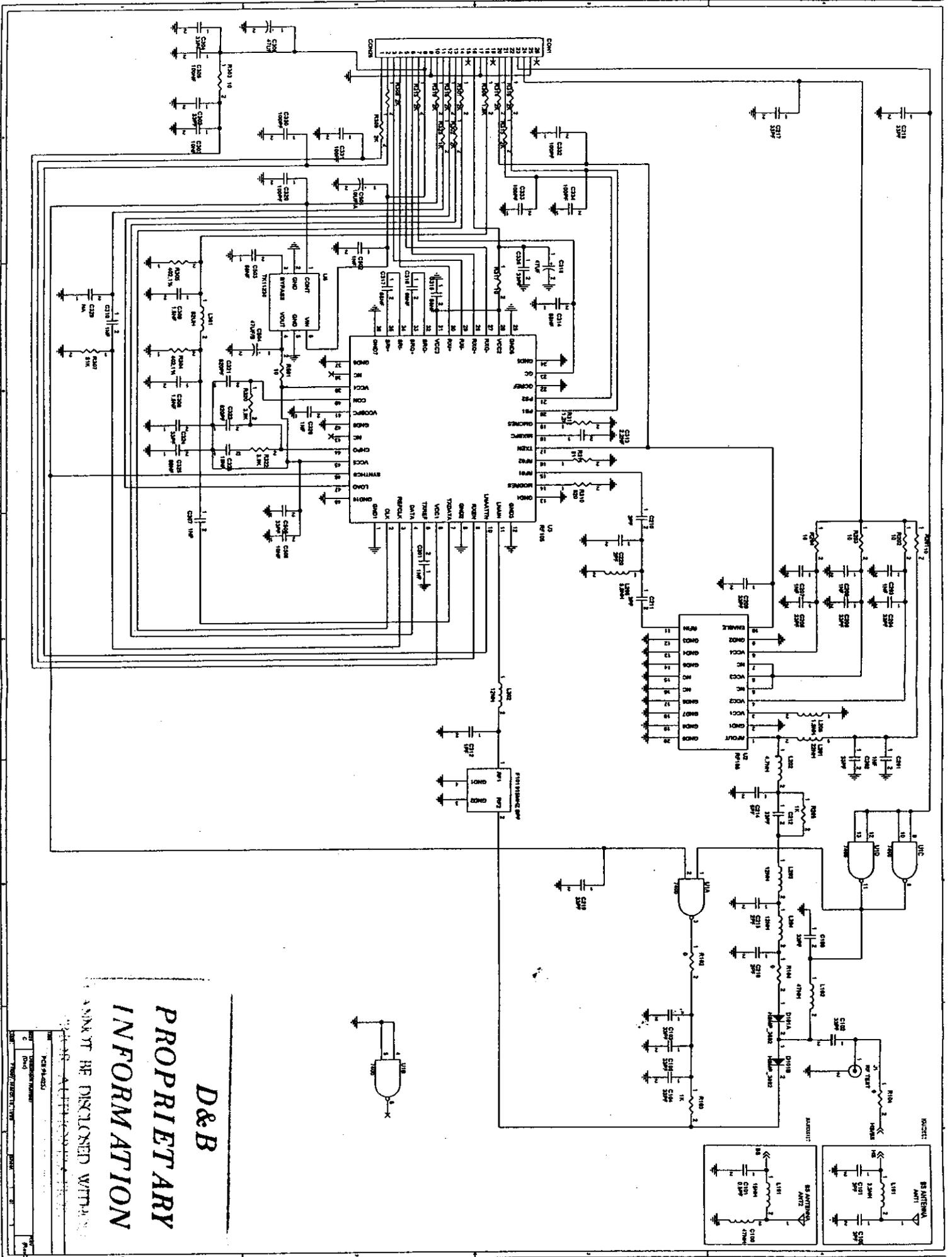


EXHIBIT D

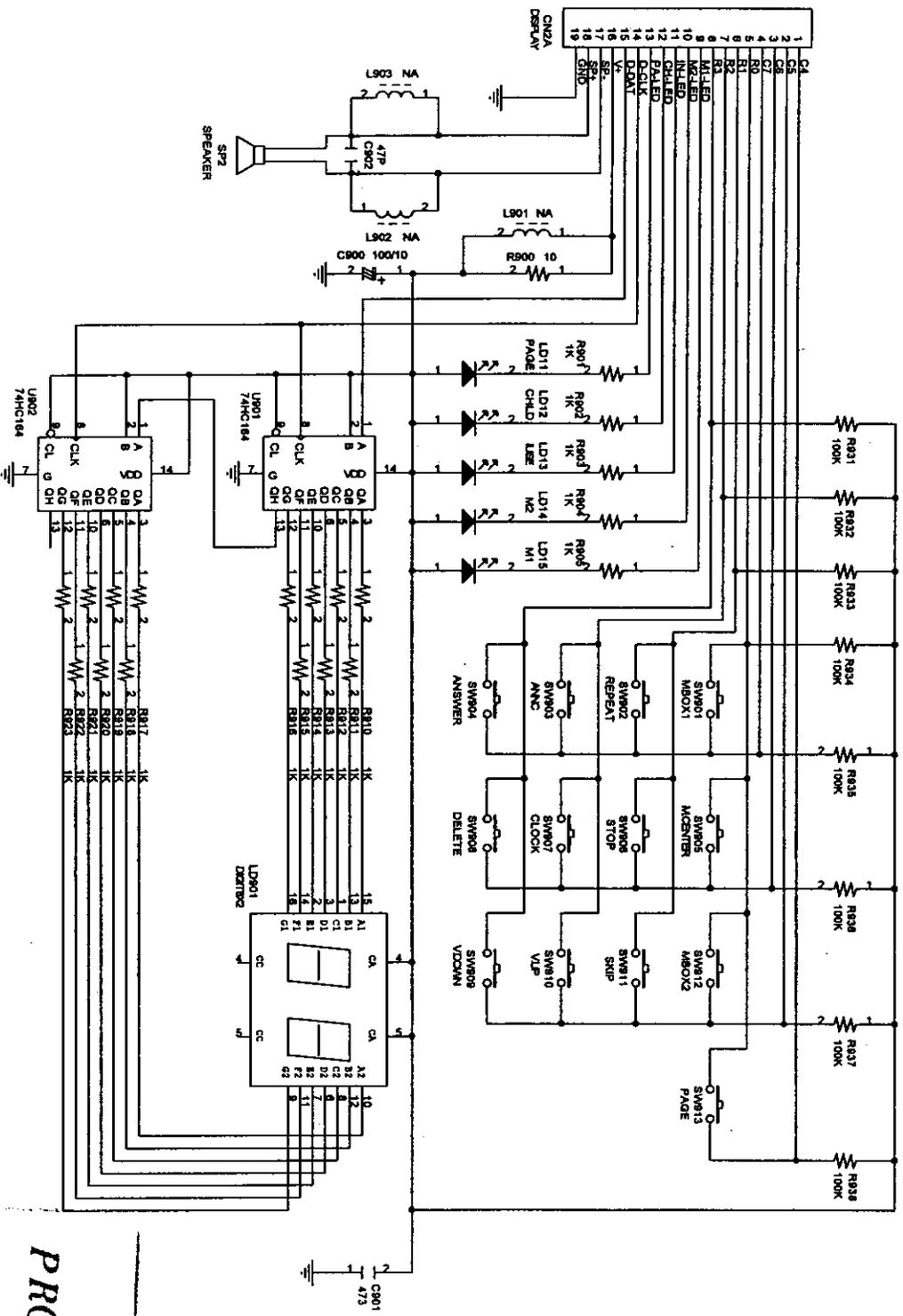
Circuit Diagram



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INFORMATION

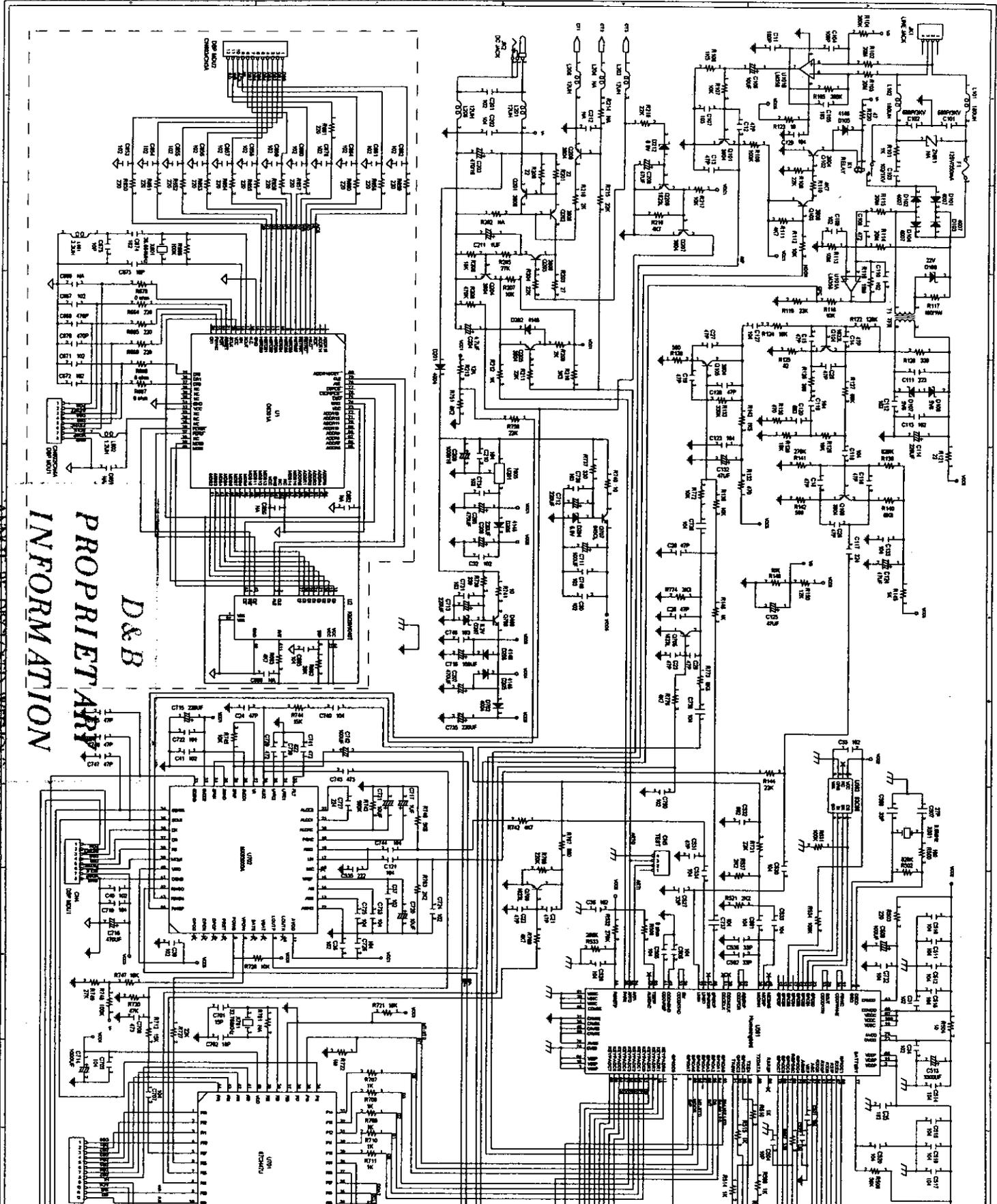
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 WRITTEN PERMISSION

REV	DATE	DESCRIPTION
1	10/1/50	DESIGN
2	10/1/50	REVISED
3	10/1/50	REVISED



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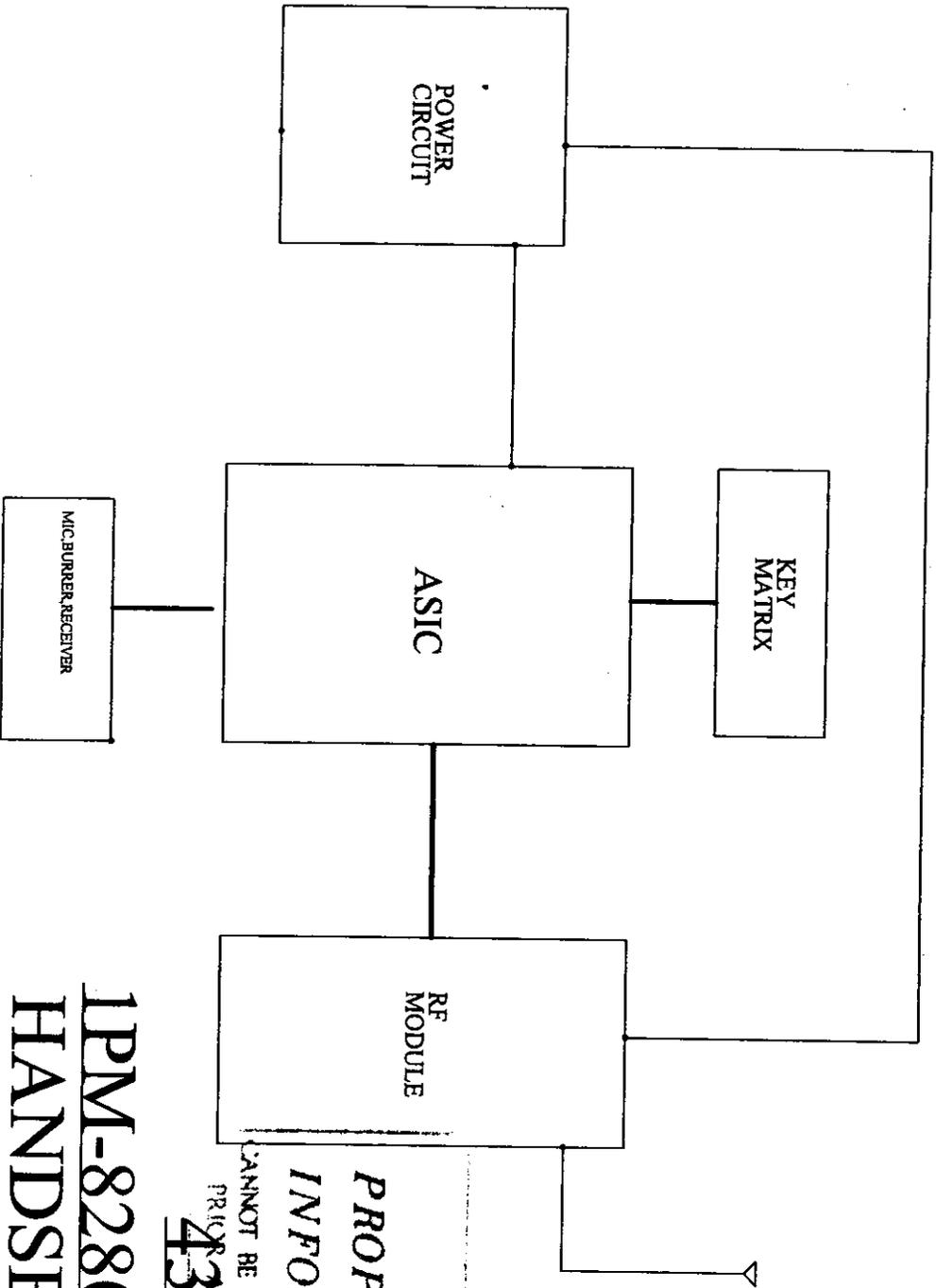
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EXHIBIT E

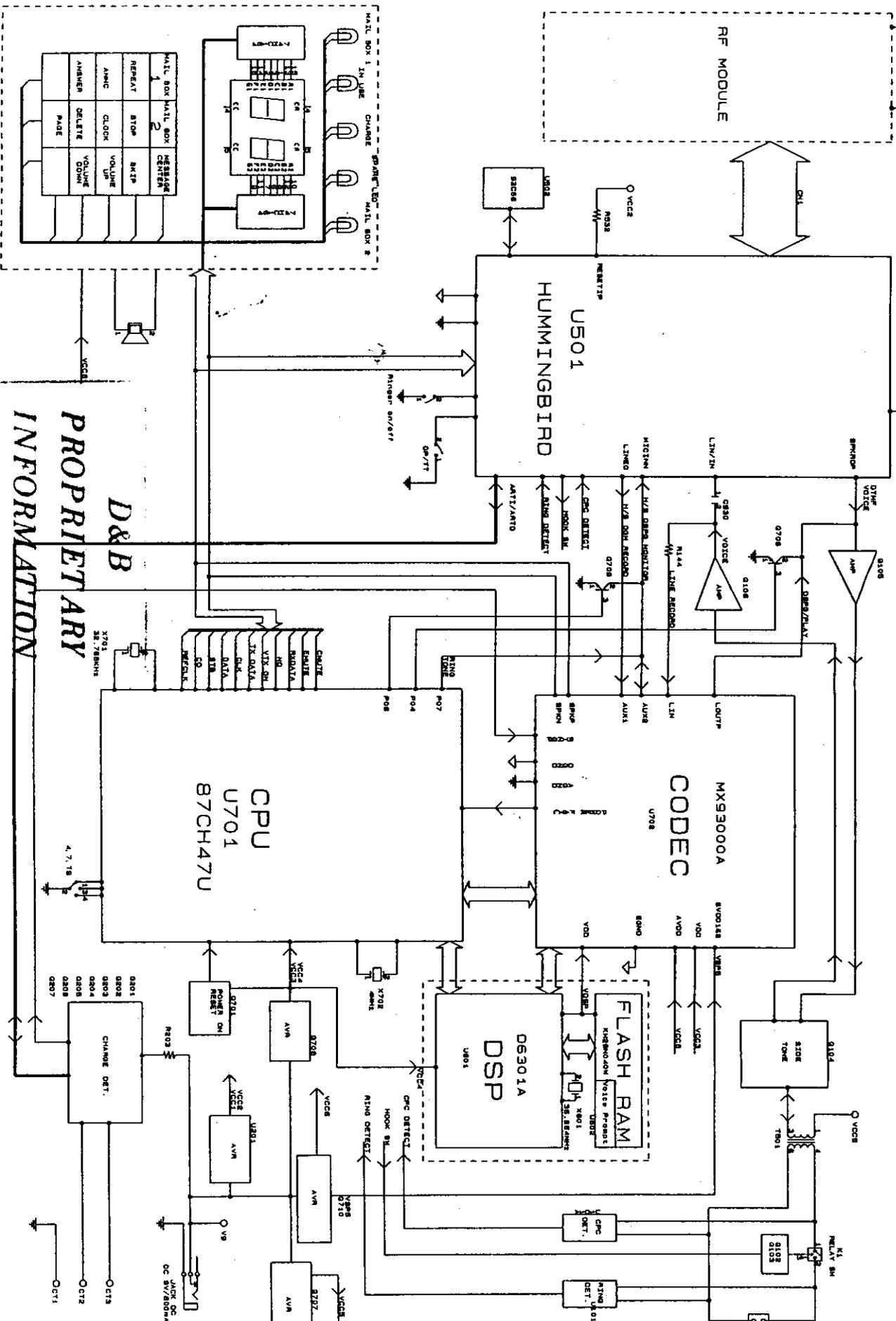
Block Diagram



IPM-828073-F51
 HANDSET
 BLOCK
 DIAGRAM

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 43-733

DB-8210 (43-733) BASE BLOCK DIAGRAMS



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