

- 2.983(d) (7) Complete Circuit Diagrams: The circuit diagram is included as EXHIBIT #7. The block diagram is included as EXHIBIT #5.
- (8) Instruction book. The instruction manual is included as EXHIBIT #8.
- (9) Tune-up procedure. The tune-up procedure is given in EXHIBIT #10.
- (10) Description of all circuitry and devices provided for determining and stabilizing frequency is included in the circuit description in the instruction manual.

2.983 (11) Description of any circuits or devices employed for suppression of spurious radiation, for limiting modulation, and for limiting power. In addition to the interstage filtering the multi-section low pass filter made up of L12, L11, C40, C47, C95, C46, C94, C45, & C26.

Limiting Modulator:

The transmitter audio limiting circuitry is contained in the loop filter IC01.

Limiting Power: There is no provision for limiting power.

(12) Digital modulation. This unit does NOT use digital modulation.

2.983(e) The data required by 2.985 through 2.997 is submitted below.

2.985(a) RF power output.
 RF power is measured by connecting a 50 ohm, resistive wattmeter to the RF output connector. With a nominal battery voltage of 13.6V, and the transmitter properly adjusted the RF output measures:

HIGH POWER

INPUT POWER: (13.2V)(1.26A) = 16.6 Watts
 OUTPUT POWER: 5.0 Watts Efficiency: 30%

LOW POWER

INPUT POWER: (13.2V)(0.36) = 4.75 Watts
 OUTPUT POWER: 0.90 Watts Efficiency: 19%



