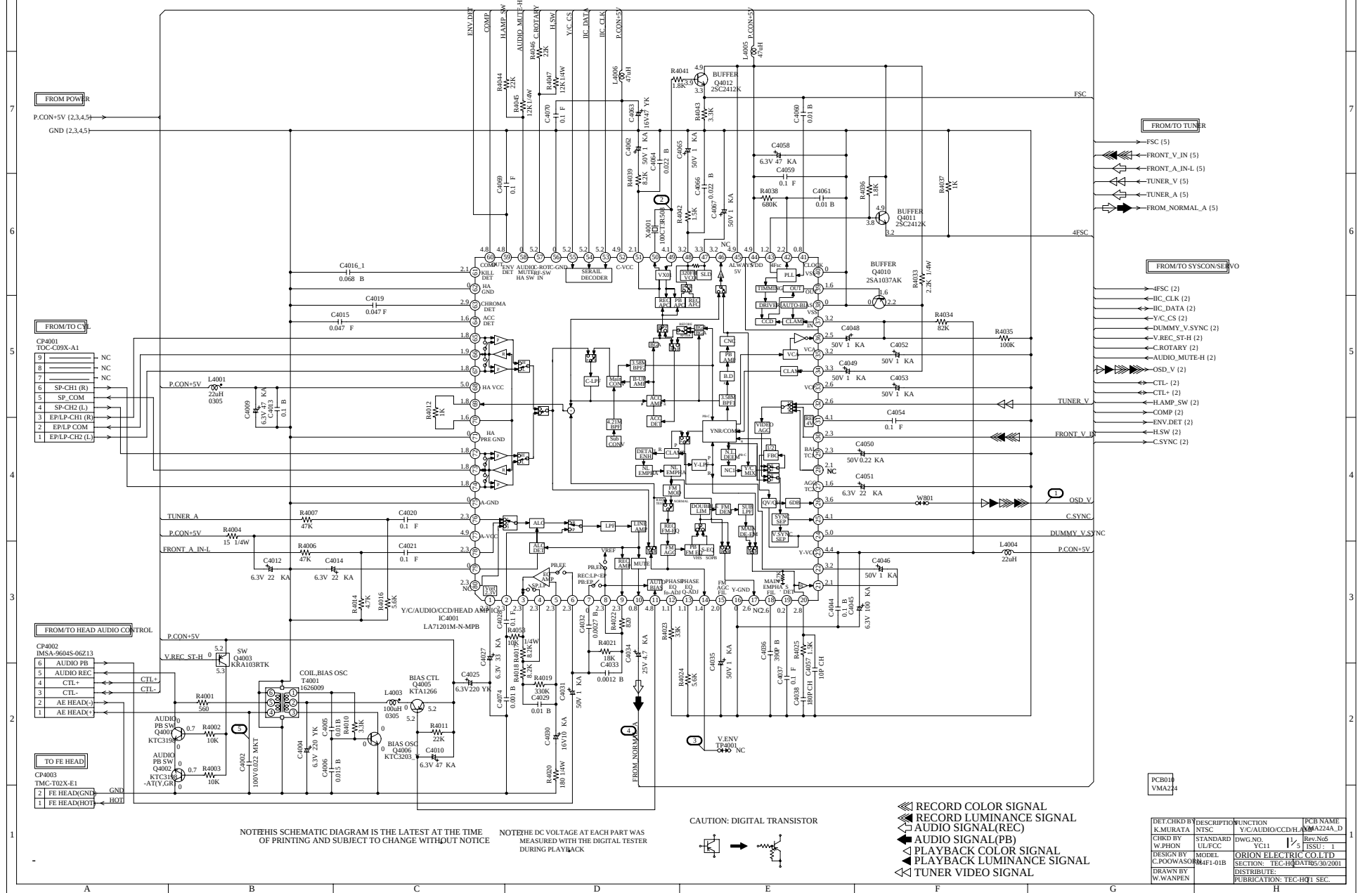


Y/C/AUDIO/CCD/HEAD AMP SCHEMATIC DIAGRAM (SYSCON PCB)

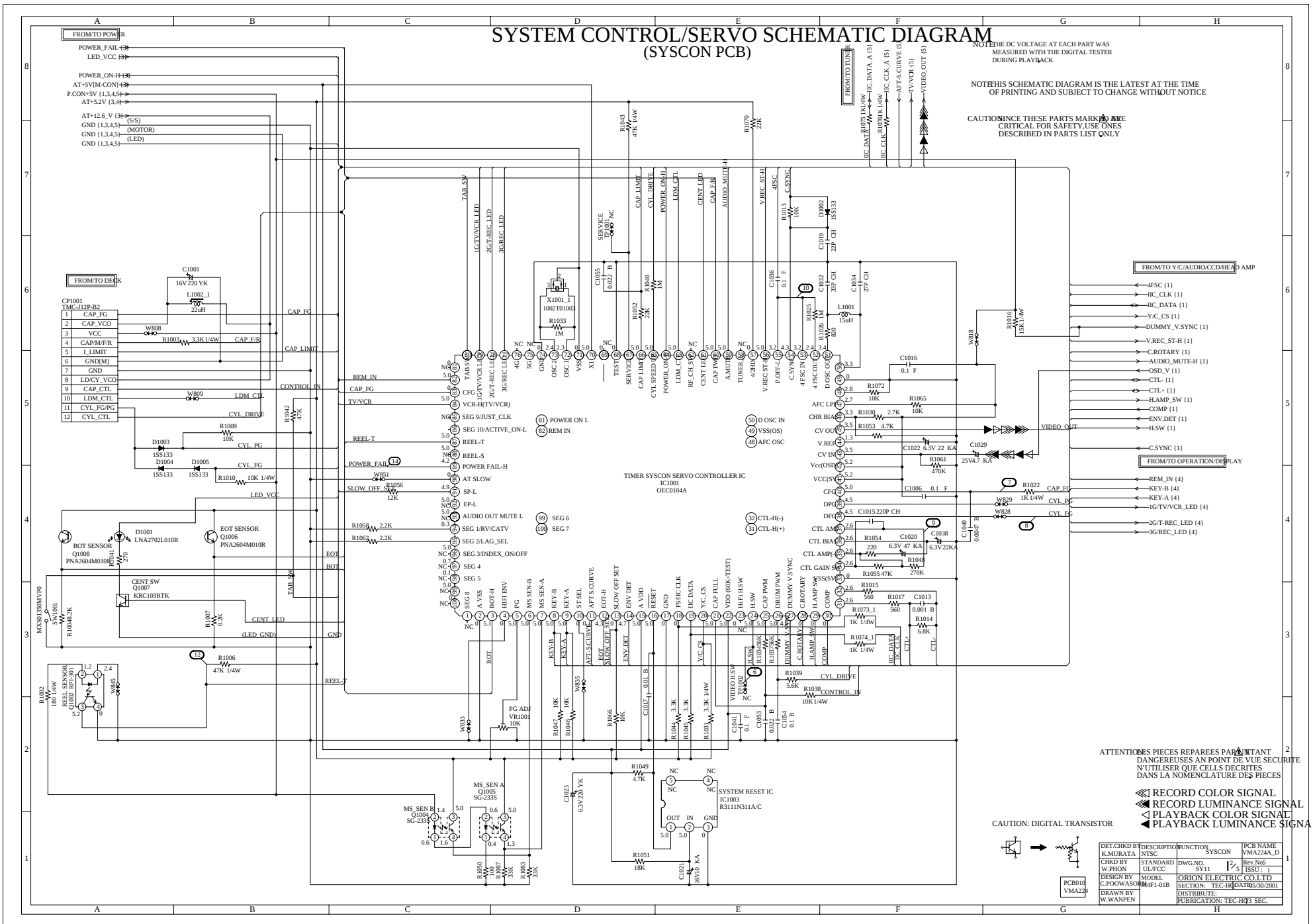


SYSTEM CONTROL/SERVO SCHEMATIC DIAGRAM

NOTE: THE DC VOLTAGE AT EACH PART WAS MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK.

NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE.

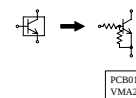
CAUTION SINCE THESE PARTS MARKED BY CRITICAL FOR SAFETY, USE ONES DESCRIBED IN PARTS LIST ONLY



ATTENTION: DES PIÈCES RÉPARÉES PAR UN
DANGEREUSES AN POINT DE VUE SECURITE
N'UTILISER QUE CELLS DECRITES
DANS LA NOMENCLATURE DES PIÈCES

◀◀	RECORD COLOR SIGNAL
◀◀	RECORD LUMINANCE SIGNAL
◀	PLAYBACK COLOR SIGNAL
◀	PLAYBACK LUMINANCE SIGNAL

CAUTION: DIGITAL TRANSISTOR



DET.CHKD BY K.MURATA	DESCRIPTION NTSC	FUNCTION SYSCON	PCB NAME VMA224A.D
CHKD BY W.PHON	STANDARD UL/FCC	DWG.NO. SY11	Rev.No5 ISSU: 1
DESIGN BY C.POOWASORN	MODEL M4F1-01B	ORION ELECTRIC CO., LTD SECTION: TEC-HQ DATA5/30/2001	
DRAWN BY W.WANPEN	DISTRIBUTE: PUBICATION: TEC-HQ3 SEC.		

[illegible]

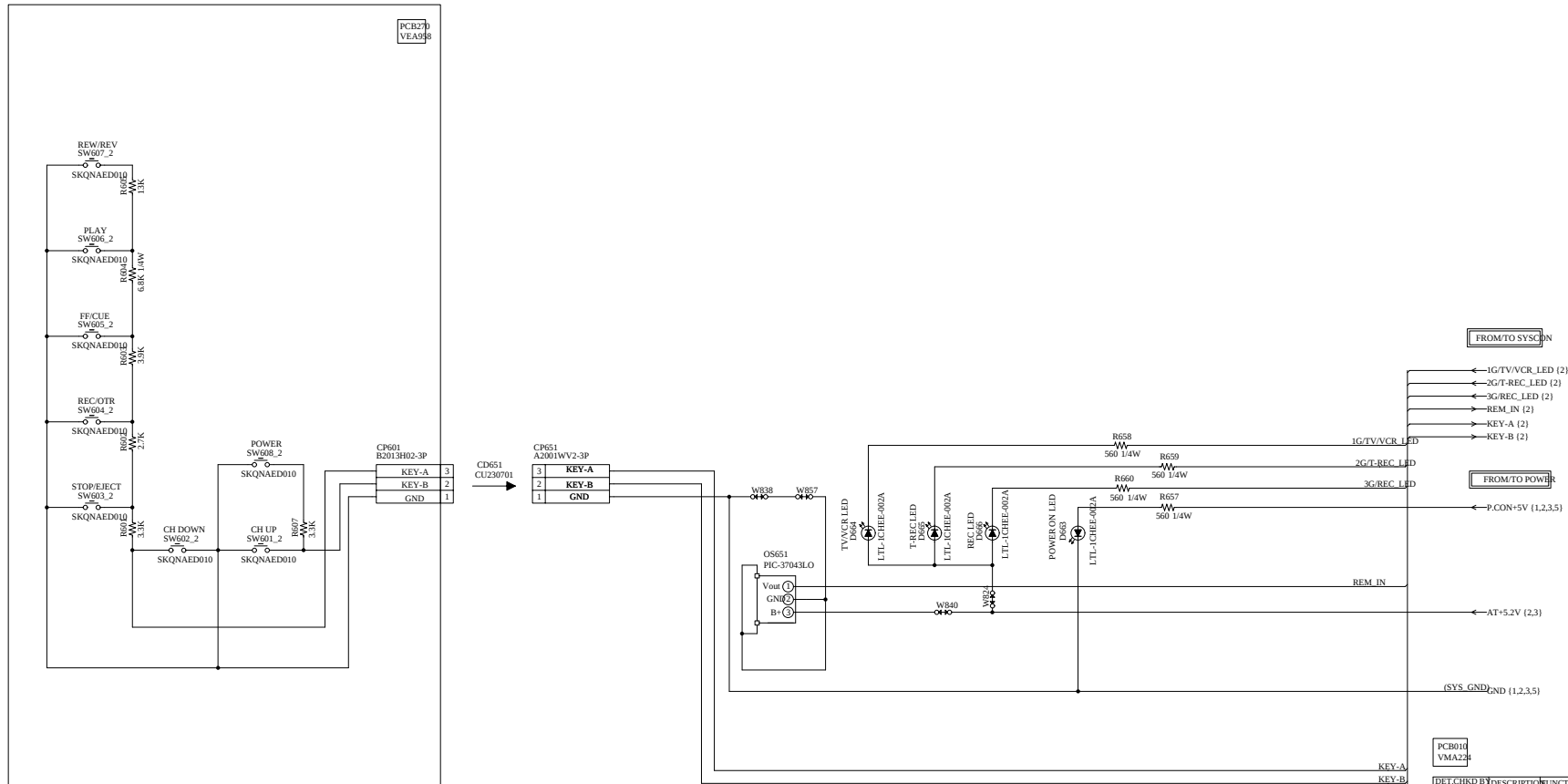
The diagram shows a dependent current source represented by a square box containing a diamond shape with an arrow pointing upwards. Two terminals are on the left, and two are on the right. A thick black arrow points to the right, where the equivalent circuit is shown. This circuit consists of a voltage source (represented by two parallel lines of unequal length) in series with a resistor (represented by a zigzag line). The voltage source's positive terminal is at the top, and the resistor is connected in series with it.

DET.CHKD BY K.MURATA	DESCRIPTION NTSC	FUNCTION POWER	PCB NAME VMA224A.D
CHKD BY W.PHON	STANDARD UL/FCC	DWG.NO. PW11	Rev.No. ISSU: 1
DESIGN BY C.POOWASORN	MODEL M4F1-01B	ORION ELECTRIC CO., LTD	
DRAWN BY W.WANPEN	SECTION: TEST-HQ/DATE: 05/30/2001		
DISTRIBUTE:			
PUBICATION: TEC-HQ/1 SEC.			

OPERATION/DISPLAY SCHEMATIC DIAGRAM (SYSCON PCB)

8
7
6
5
4
3
2
1

8
7
6
5
4
3
2
1



NOTE: THE DC VOLTAGE AT EACH PART WAS
MEASURED WITH THE DIGITAL TESTER
DURING PLAYBACK

NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME
OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE

DET. CHKD BY	DESCRIPTION	FUNCTION	PCB NAME
K. MURATA	NTSC	OPERATION/DISPLAY	MA224A_D
GHKD BY	STANDARD	DWG. NO.	Rev. No.
W. PHON	ULPCC	OP11	ISSU. 1
DESIGN BY	MODEL	ORION ELECTRIC CO. LTD.	SECTION: TEC-HQ
C. POOWASORN	M4F1-01B	DATE: 05/30/2001	DATE: 05/30/2001
DRAWN BY	DISTRIBUTE:		
W. WANPEN	PUBLICATION: TEC-HQ		

TUNER SCHEMATIC DIAGRAM (SYSCON PCB)

Legend:

- RECORD LUMINANCE SIGNAL
- RECORD COLOR SIGNAL
- AUDIO SIGNAL (REC)
- AUDIO SIGNAL (PB)
- TUNER VIDEO SIGNAL
- PLAYBACK LUMINANCE SIGNAL
- PLAYBACK COLOR SIGNAL

DET. CHK'D BY	DESCRIPTION	FUNCTION	PCB NAME
K. MURATA	NTSC	TUNER	VMA224A_D
7. KIKO BY	STANDARD		Rev. No.
W. PHON	UL/FCC		TU11
BY MODEL			ISSU: 1
C. POOWASORN	4F1-01B		ORION ELECTRIC CO. LTD
DRAWN BY			SECTION: TEC-HQ
W. WANPEN			DISTRIBUTE: DAT05/30/2001
			PUBLICATION: TEC-HQ3 SEC.

◀ RECORD LUMINANCE SIGNAL (Y)
◀ RECORD COLOR SIGNAL (CB, CR)
◀ AUDIO SIGNAL (REC)
◀ AUDIO SIGNAL (PB)
◀ TUNER VIDEO SIGNAL
◀ PLAYBACK LUMINANCE SIGNAL (Y)
◀ PLAYBACK COLOR SIGNAL (CB, CR)

DET.CHKD BY K.MURATA	DESCRIPTION NTSC	FUNCTION TUNER	PCB NAME VMA224A_D
CHKD BY W.PHON	STANDARD UL/FCC	DWG.NO. TU11	Rev.No5 ISSU: 1
GENERAL BY C.POO WASORN	MODEL M4F1-01B	ORION ELECTRIC CO. LTD SECTION: TEC-HQ DATE: 5/30/2001	
DRAWN BY W.WANPEN		DISTRIBUTE: PUBLICATION: TEC-HQ 3 SEC.	