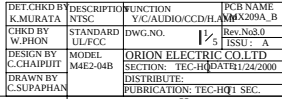


(SYSCON PCB)

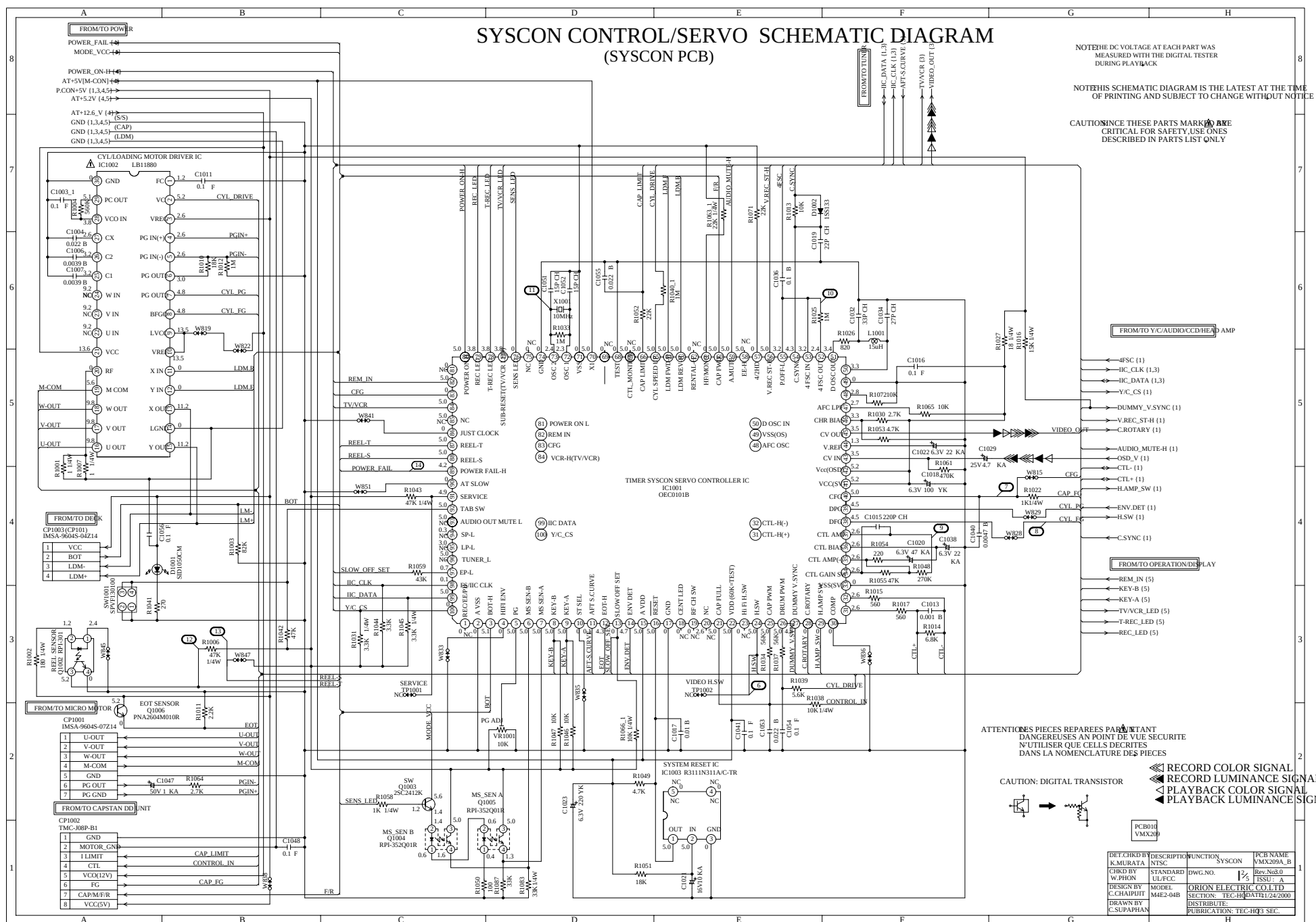


SYSCON CONTROL/SERVO SCHEMATIC DIAGRAM

NOTE: THE DC VOLTAGE AT EACH PART WAS MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK

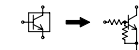
NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE

CAUTION SINCE THESE PARTS MARKED BY ARE CRITICAL FOR SAFETY, USE ONES DESCRIBED IN PARTS LIST ONLY



ATTENTION: DES PIÈCES RÉPARÉES PAR UN
DANGEREUSES AU POINT DE VUE SÉCURITÉ
N'UTILISER QUE CELLES DÉCRITES
DANS LA NOMENCLATURE DES PIÈCES

CAUTION: DIGITAL TRANSISTOR



- ◀◀ RECORD COLOR SIGNAL
- ◀◀ RECORD LUMINANCE SIGNAL
- ◀◀ PLAYBACK COLOR SIGNAL
- ◀◀ PLAYBACK LUMINANCE SIGNAL

DET.CHKD BY K.K.MURATA	DESCRIPTION NTSC	FUNCTION SYSCON	PCB NAME VMX209A_1
CHKD BY W.PHON	STANDARD UL/FCC	DWG.NO. 1/2	Rev.No.8 ISSU: A
DESIGN BY C.CHAIPUFI	MODEL M4E2-04B	ORION ELECTRIC CO.LTD SECTION: TEC-HQDTR1/24/2006	
DRAWN BY C.SUPAPHAN		DISTRIBUTE: PUBLICATION: TEC-HQ3 SEC	

NOTE: THE DC VOLTAGE AT EACH PART MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK. OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE. CRITICAL FOR SAFETY, USE ONES DESCRIBED IN PARTS LIST ONLY.

ATTENTION: LES PIÈCES RÉPARÉES PAR UN
DANGEREUSES AN POINT DE VUE SECURITE
N'UTILISER QUE CELLES DECRITES
DANS LA NOMENCLATURE DES PIÈCES

RECORD LUMINANCE SIGNAL (Y)
 RECORD COLOR SIGNAL (C)
 AUDIO SIGNAL (REC)
 AUDIO SIGNAL (PB)
 TUNER VIDEO SIGNAL
 PLAYBACK LUMINANCE SIGNAL (Y)
 PLAYBACK COLOR SIGNAL (C)

DET.CHKD BY K.MURATA	DESCRIPTION NTSC	FUNCTION TUNER	PCB NAME VMX209A
CHKD BY W.PHON	STANDARD UL/FCC	DWG.NO. 135	Rev.No.3 ISSU: A
DESIGNED BY C.CHAIPUIT	MODEL M4E2-04B	ORION ELECTRIC CO.LTD	
DRAWN BY C.SAPAPHAN		SECTION: TEC-HQ DATA1/24/200	
		DISTRIBUTE:	
		PUBLICATION: TEC-HQ SEC	

POWER SCHEMATIC DIAGRAM (SYSCON PCB)

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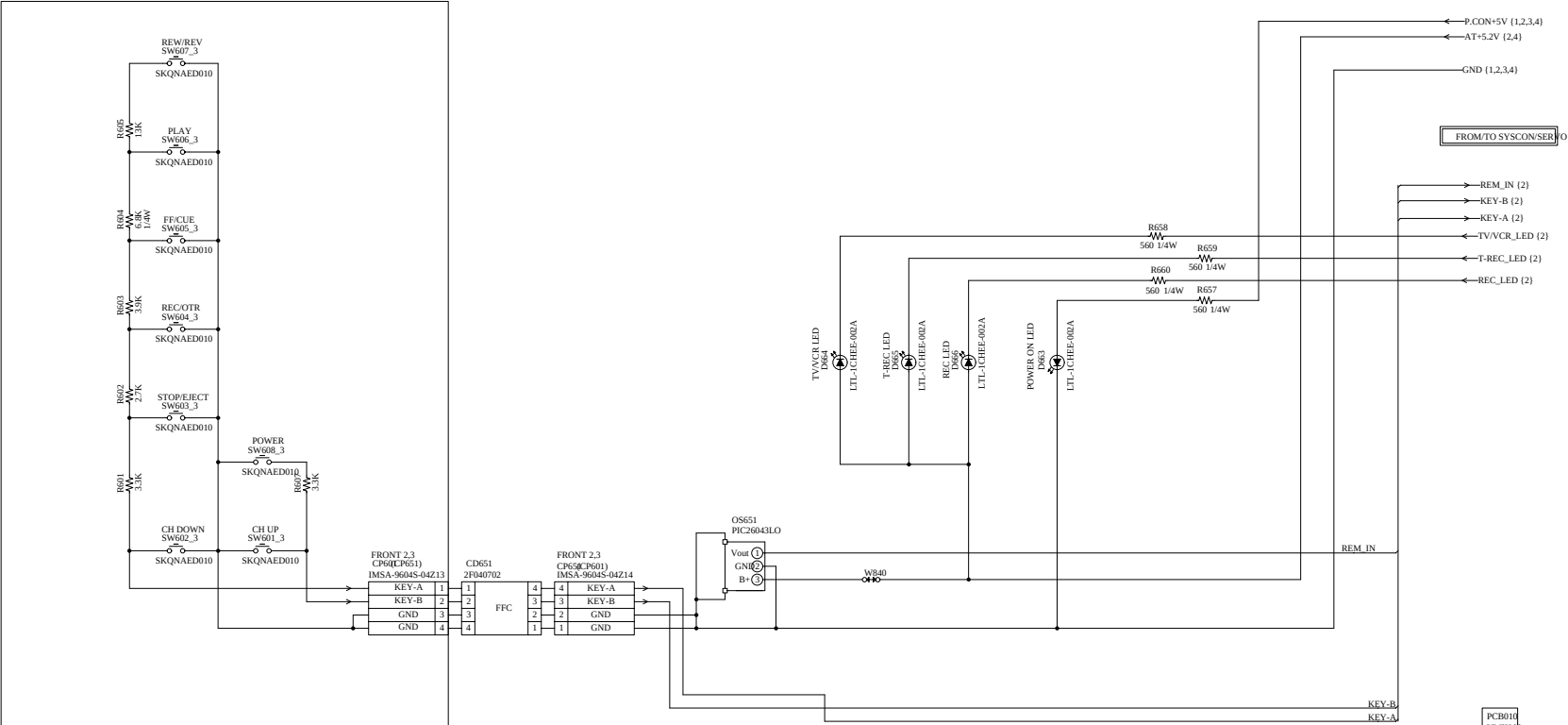
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DET.CHKD BY K.MURATA	DESCRIPTION NTSC	FUNCTION POWER	PCB NAME VMX209A_B
CHKD BY W.PHON	STANDARD UL/FCC	DWG.NO. 145	Rev.No3.0 ISSU: A
DESIGN BY C.CHAIPUJIT	MODEL M4E2-04B	ORION ELECTRIC CO.LTD SECTION: TEC-HQ DATE: 1/24/2000	
DRAWN BY C.SUPAPHAN	DISTRIBUTE: PUBICATION: TEC-HQ1 SEC.		

OPERATION/DISPLAY SCHEMATIC DIAGRAM
(SYSCON PCB)



NOTE: THE DC VOLTAGE AT EACH PART WAS
MEASURED WITH THE DIGITAL TESTER
DURING PLAYBACK.

NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME
OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE

PCB010 VMX20	DET. CHKD BY K. MURATA	DESCRIPTION NTSC	FUNCTION OPERATION/DISPLAY	P.C.B NAME AX-209A_B
	CHKD BY W. PHON	STANDARD UL FCC	DWG. NO. 1/2	Rev. No. 3.0
	DESIGN BY C. CHAIPIIT	MODEL M4E2-04B	ORION ELECTRIC CO., LTD.	ISSU: A
	DRAWN BY C. SUPAPHAN	SECTION TEC-HQ1/24/2000	DISTRIBUTE	
			PUBLICATION: TEC-HQ1 SEC.	