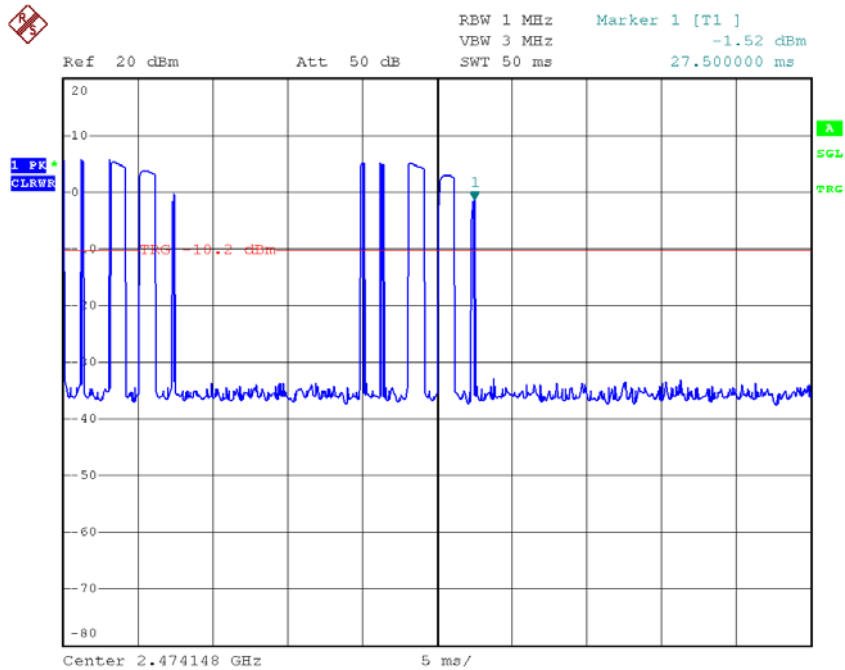
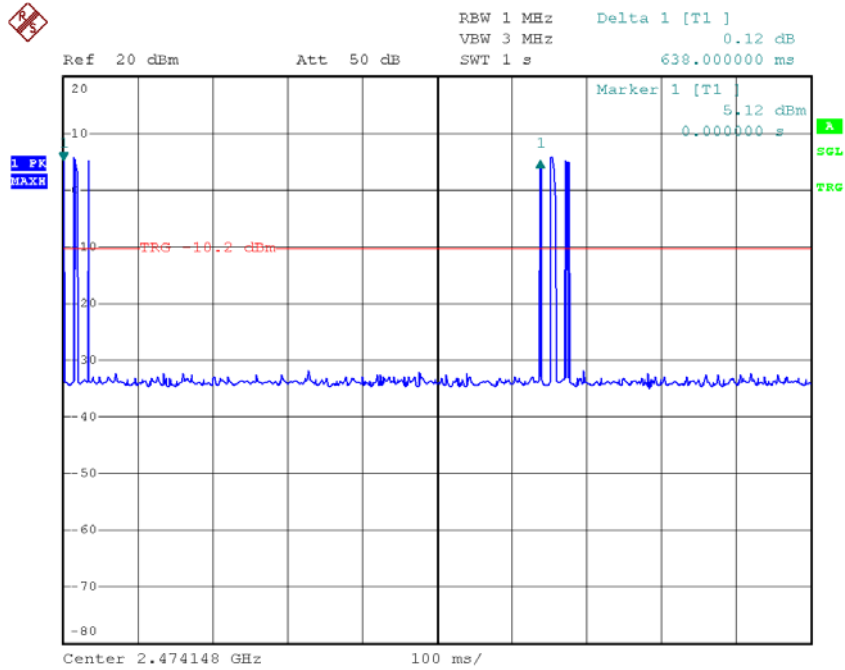


INTERTEK TESTING SERVICES

Report No.: HK12020934-1
Sample No.:1/3



Average Factor Plot – Pursuant to FCC Part 15 Section 15. 249 Requirement (Pass/Fail)

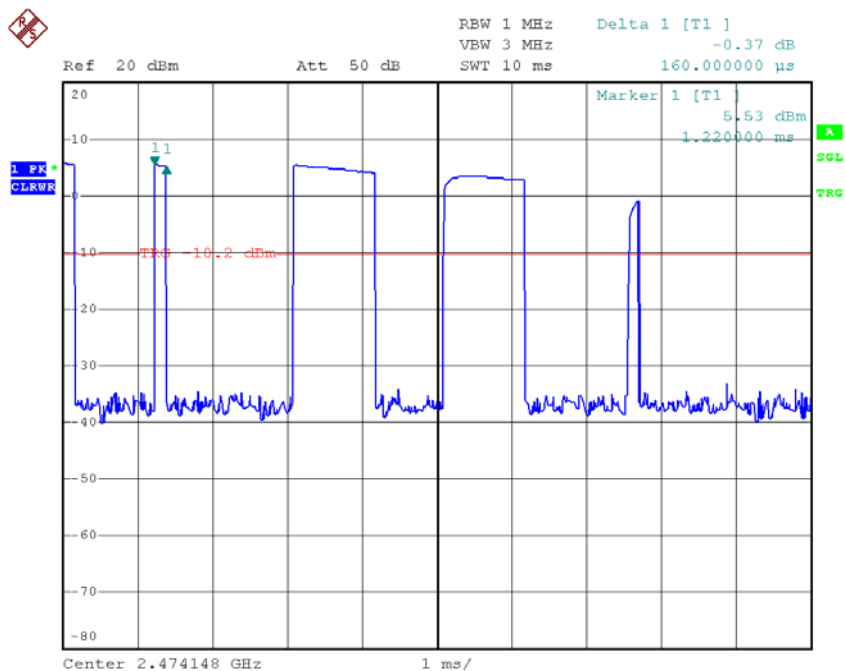
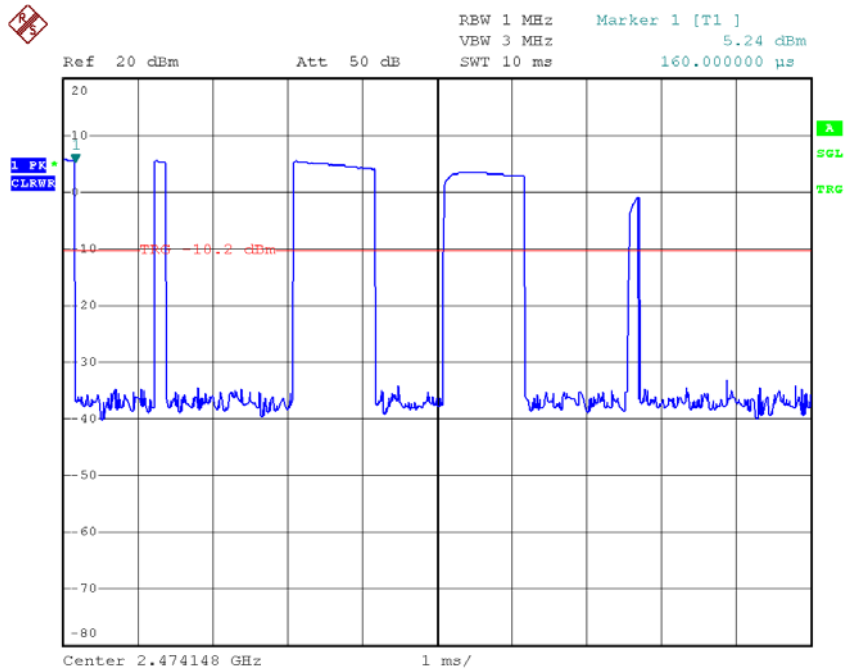
Code: FCC15C_f / Effective Date: 24 Jun 2011

(of)

INTERTEK TESTING SERVICES

Report No.: HK12020934-1

Sample No.:1/3

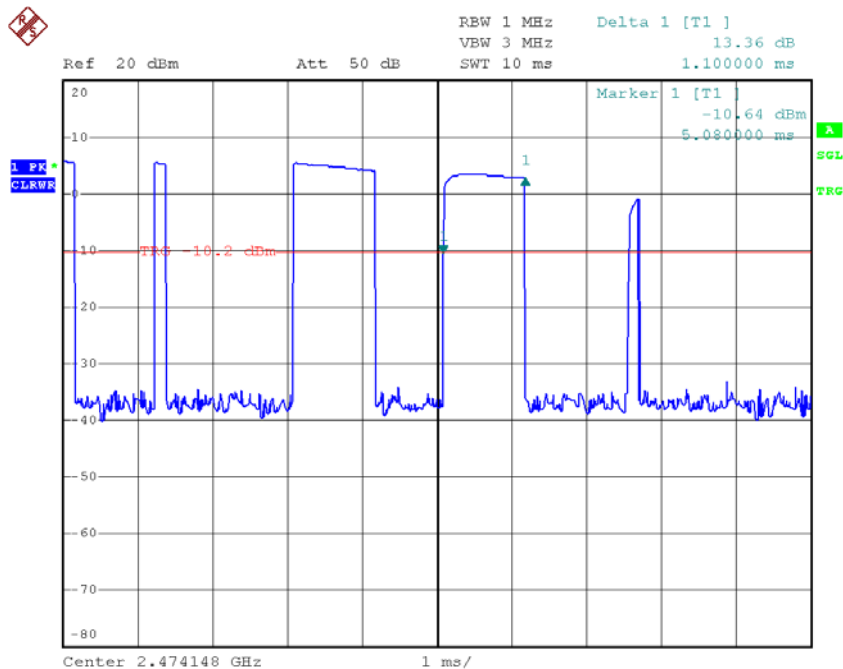
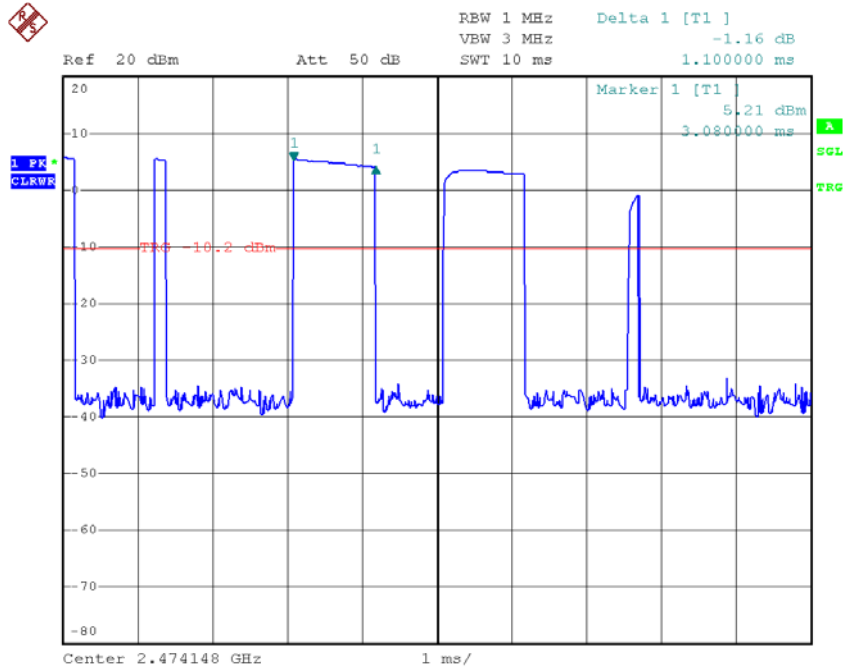


Average Factor Plot – Pursuant to FCC Part 15 Section 15. 249 Requirement (Pass/Fail)

INTERTEK TESTING SERVICES

Report No.: HK12020934-1

Sample No.:1/3



Average Factor Plot – Pursuant to FCC Part 15 Section 15. 249 Requirement (Pass/Fail)

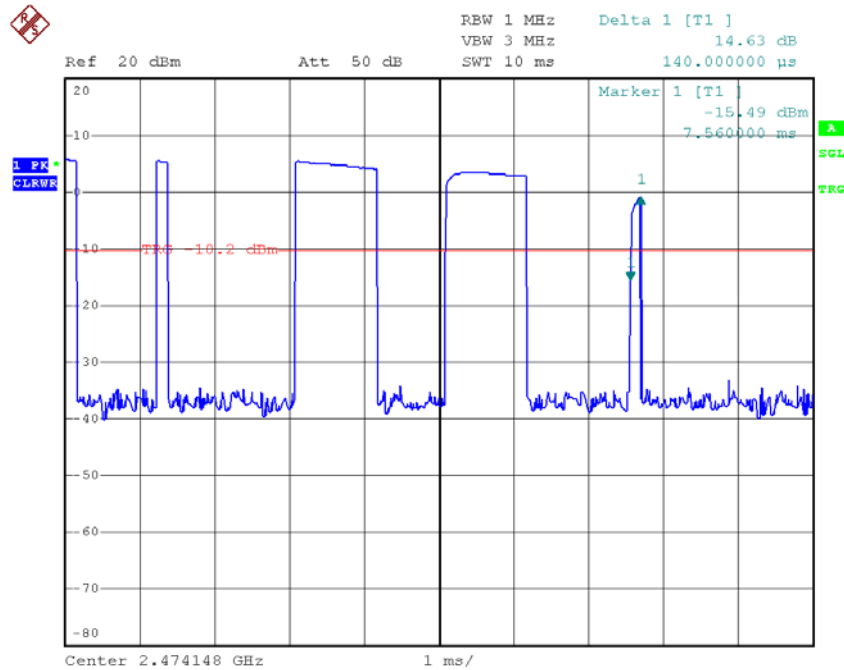
Code: FCC15C_f / Effective Date: 24 Jun 2011

(of)

INTERTEK TESTING SERVICES

Report No.: HK12020934-1

Sample No.:1/3



The duration of one cycle = 100ms

Effective period of the cycle = 1.1ms x 4 + 160us x 4 + 140us x 2

DC = 5.32/100

The averaging factor is found by $20\log 0.0532 = -25.5\text{dB}$.

Average Factor Plot – Pursuant to FCC Part 15 Section 15. 249 Requirement (Pass/Fail)

Code: FCC15C_f / Effective Date: 24 Jun 2011

(of)