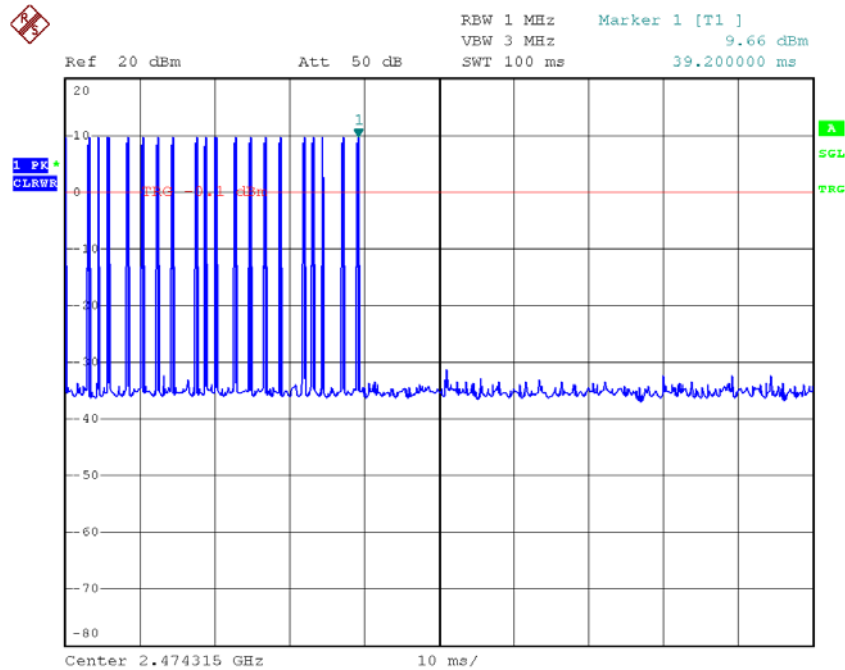
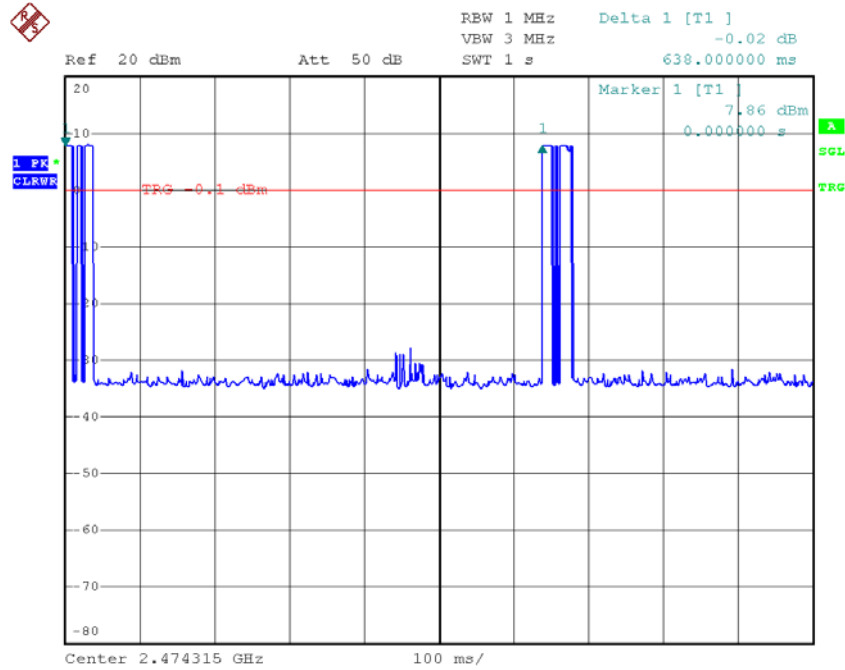


# INTERTEK TESTING SERVICES

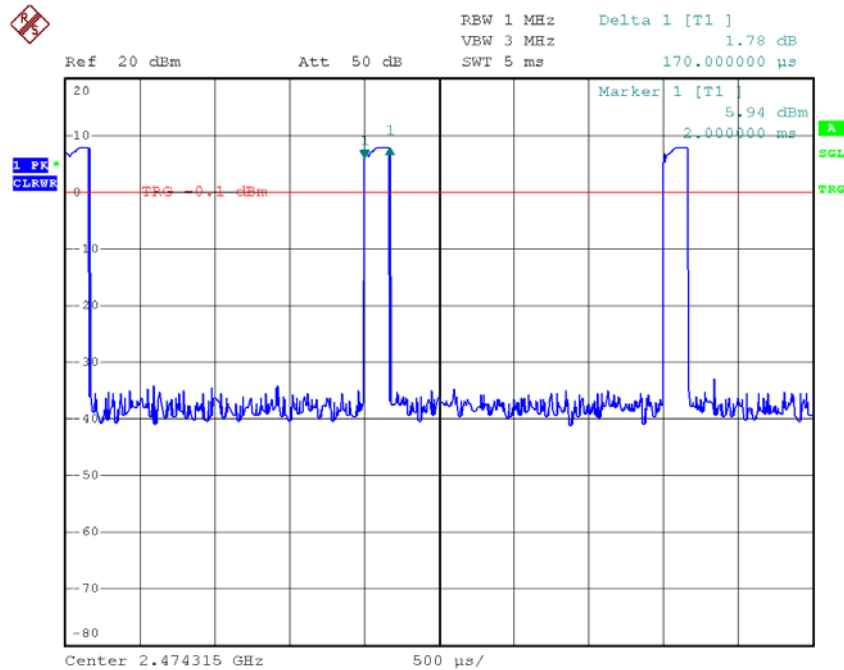
Report No.: HK12050617-1  
Sample No.:1/3



Average Factor Plot – Pursuant to FCC Part 15 Section 15. 249 Requirement (Pass)

# INTERTEK TESTING SERVICES

Report No.: HK12050617-1  
Sample No.:1/3



The duration of one cycle = 100ms

Effective period of the cycle = 170us x 20

DC = 3.4/100

The averaging factor is found by  $20\log 0.034 = -29.4\text{dB}$ .

**Average Factor Plot – Pursuant to FCC Part 15 Section 15. 249 Requirement (Pass)**

Code: FCC15C\_f / Effective Date: 24 Jun 2011

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