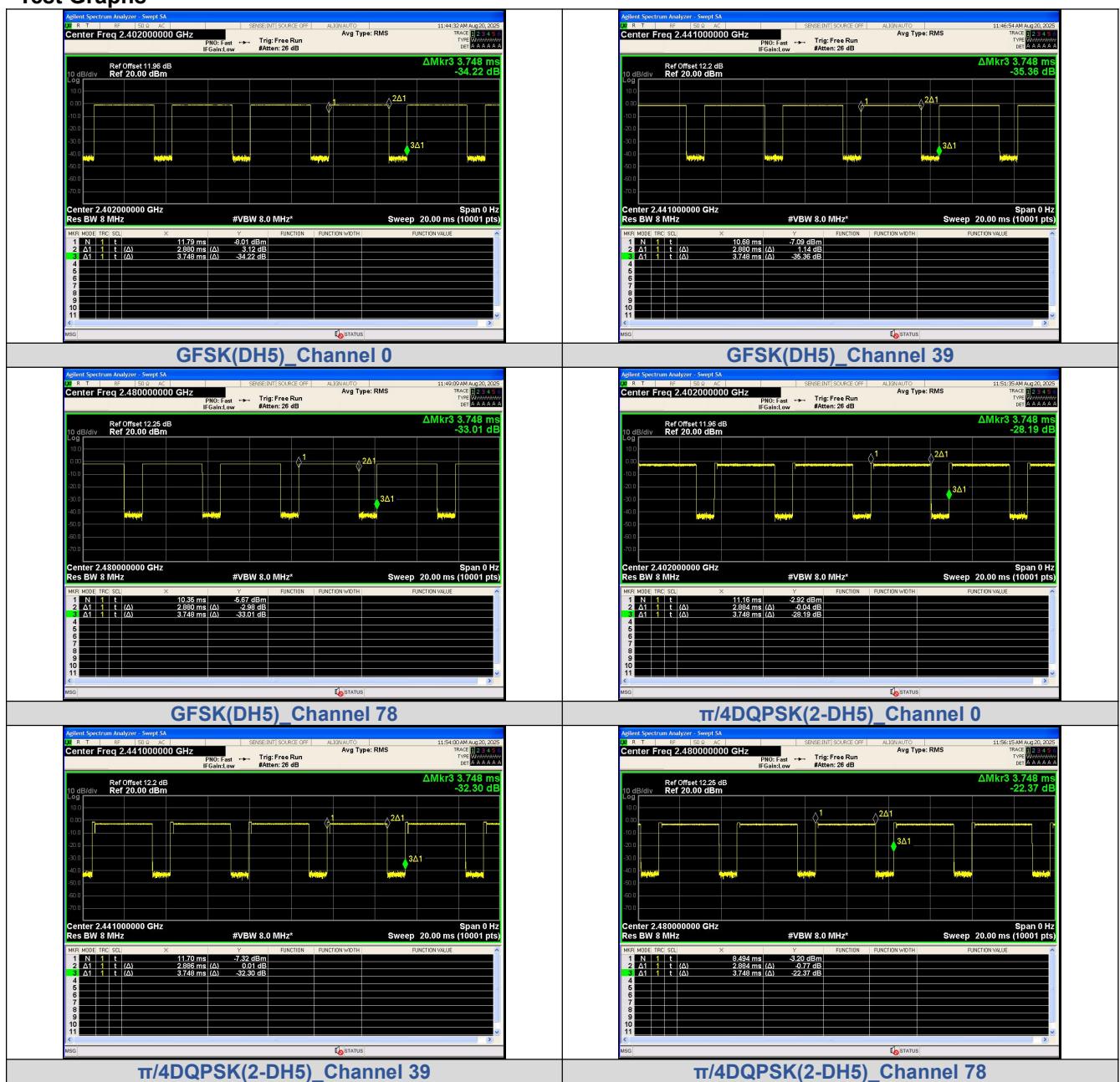


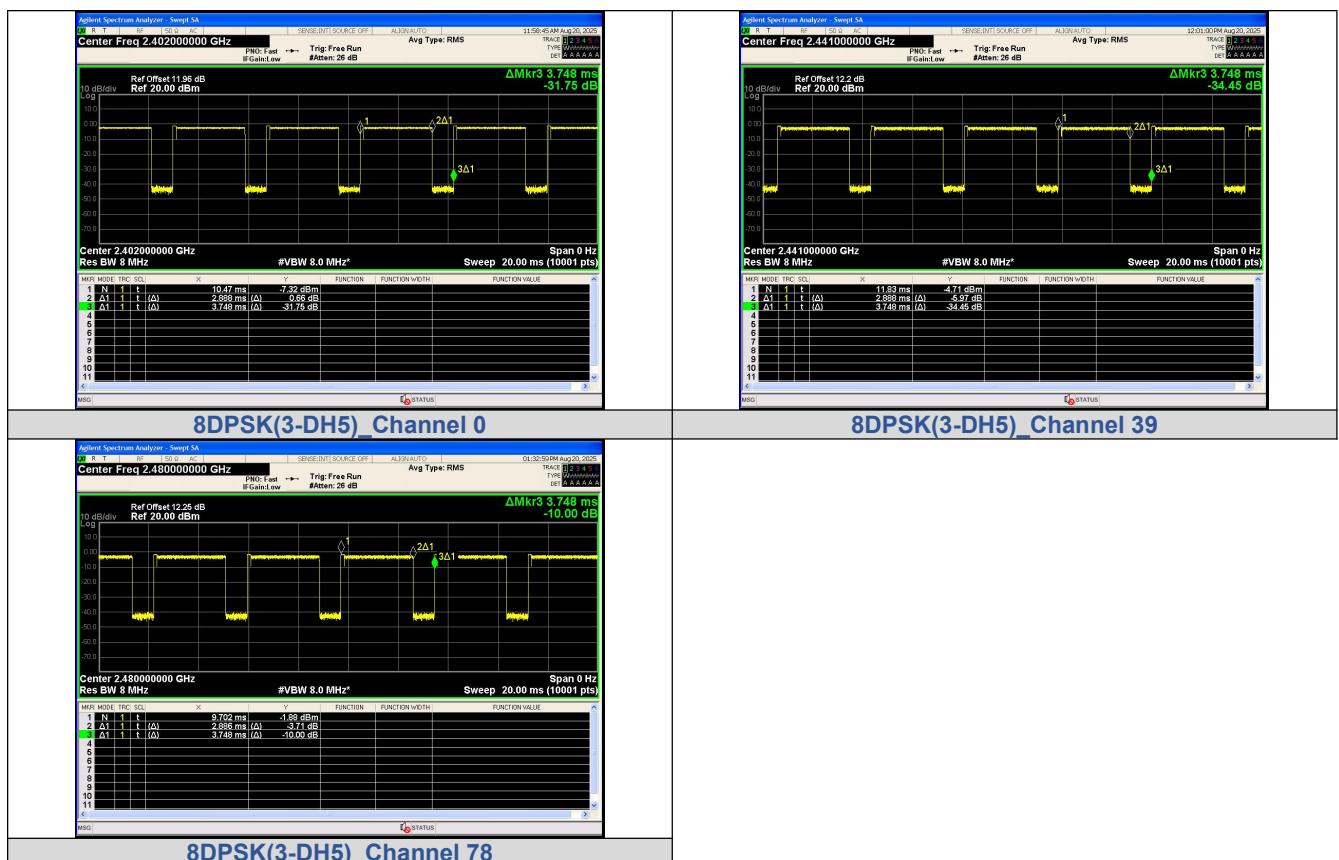
APPENDIX VI. Duty Cycle

Test Result

Modulation	packets	Channel	On Time (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle (linear)	Duty Cycle Factor (dB)	1/T
GFSK	DH5	0	2.880	3.748	76.84	0.7684	1.1441	0.3472
		39	2.880	3.748	76.84	0.7684	1.1441	0.3472
		78	2.880	3.748	76.84	0.7684	1.1441	0.3472
$\pi/4$ DQPSK	2-DH5	0	2.884	3.748	76.95	0.7695	1.1379	0.3467
		39	2.886	3.748	77.00	0.7700	1.1351	0.3465
		78	2.884	3.748	76.95	0.7695	1.1379	0.3467
8DPSK	3-DH5	0	2.888	3.748	77.05	0.7705	1.1323	0.3463
		39	2.888	3.748	77.05	0.7705	1.1323	0.3463
		78	2.886	3.748	77.00	0.7700	1.1351	0.3465

Test Graphs





APPENDIX VII. Dwell Time Test Result

Modulation	Packet	Channel	Pulse Width (ms)	Number of Pulses in 31.6 seconds	Dwell Time (ms)	Limit (ms)	Result
GFSK	DH1	Hopping	0.3744	315	117.94	< 400	PASS
	DH3		1.632	152	248.06		PASS
	DH5		2.880	117	336.96		PASS
π/4DQPSK	2-DH1	Hopping	0.3852	314	120.95	< 400	PASS
	2-DH3		1.632	157	256.22		PASS
	2-DH5		2.896	86	249.06		PASS
8DPSK	3-DH1	Hopping	0.3852	311	119.8	< 400	PASS
	3-DH3		1.632	171	279.07		PASS
	3-DH5		2.880	111	319.68		PASS

Test Graphs

