

MQM744/748 Wi-Fi 6/BT/15.4 Module

– Powered by Qualcomm QCC744/748

Documentation Title	Documentation No	Revision	Classification	Status	Date
MQM744/748 Wi-Fi 6/BT/15.4 Module Datasheet		V1.5	Public	Release	Sep 25, 2025

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1 Product Overview

Powered by Qualcomm high performance 1x1 2.4GHz Wi-Fi 6/BT/15.4 combo all-in-one SoC QCC744/748, MQM744/748 IoT connectivity LGA module portfolio is purposefully designed to pack processing capabilities, Wi-Fi/BT/15.4 combo connectivity, and on-module memory into a single 44-pin LGA form factor with flexible choices of Pin, PCB and RF Connector Antenna+. All variant antenna modules are pin compatible, allowing easy swap among modules. Its 1.27mm (0.05") pitch design enables a seamless conversion from LGA pads to standard 1.27mm (0.05") headers, allowing for field-replaceable header modules if needed.

Unlike other Wi-Fi/BT/15.4 combo modules on the market, MQM744/748 has integrated powerful 32-bit RISC-V processor with FPU and DSP running up to 325MHz, on-chip 484KB SRAM and 128KB ROM, multimedia features such as audio and video codec and interfaces, and 35x multiplexed GPIO rich peripherals. Its all-in-one design and capability contribute to reduced costs and enhanced performance, making it an attractive choice for IoT edge devices requiring single chip solution.

MQM744/748 operates in hostless mode, capable of running both the protocol stack all the way to application level as well as IoT applications without requiring an external MCU. Built on FreeRTOS, Its software SDK will be open-sourced on GitHub coupled with VS Code IDE market extension to facilitate rapid IoT application development. It can also be used as an IoT connectivity transceiver in hosted mode with an external host running all upper protocol stacks and applications.

MQM744/748 includes the following configurations:

Module	Form Factor	USB	Antenna
MQM744-0-50-0P	16 x 16 x 2.2 mm, 1.27 mm pitch, 44-pin, LGA	Disable	Pin Antenna
MQM744-0-50-0B	16 x 21 x 2.2 mm, 1.27 mm pitch, 44-pin, LGA	Disable	PCB Antenna
MQM744-0-50-0U	16 x 16 x 2.2 mm, 1.27 mm pitch, 44-pin, LGA	Disable	RF Connector Antenna+
MQM748-0-50-0P	16 x 16 x 2.2 mm, 1.27 mm pitch, 44-pin, LGA	Enable	Pin Antenna
MQM748-0-50-0B	16 x 21 x 2.2 mm, 1.27 mm pitch, 44-pin, LGA	Enable	PCB Antenna
MQM748-0-50-0U	16 x 16 x 2.2 mm, 1.27 mm pitch, 44-pin, LGA	Enable	RF Connector Antenna+

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MQM744/748 is graphically illustrated below:

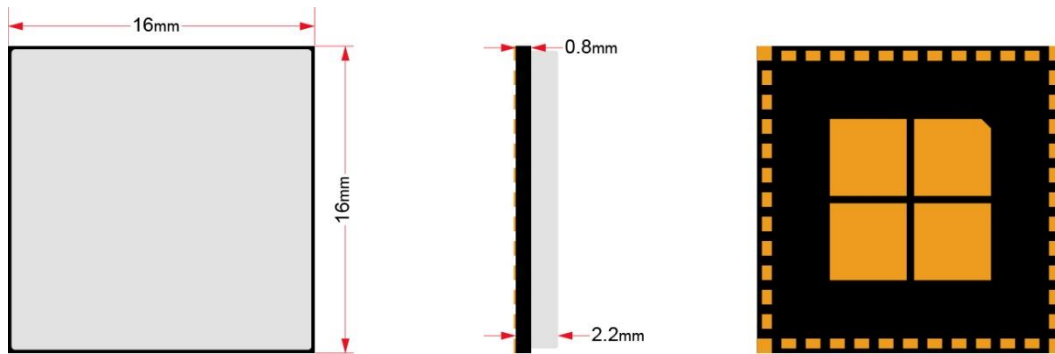


Figure 1: MQM744/748-0-50-0P Module View

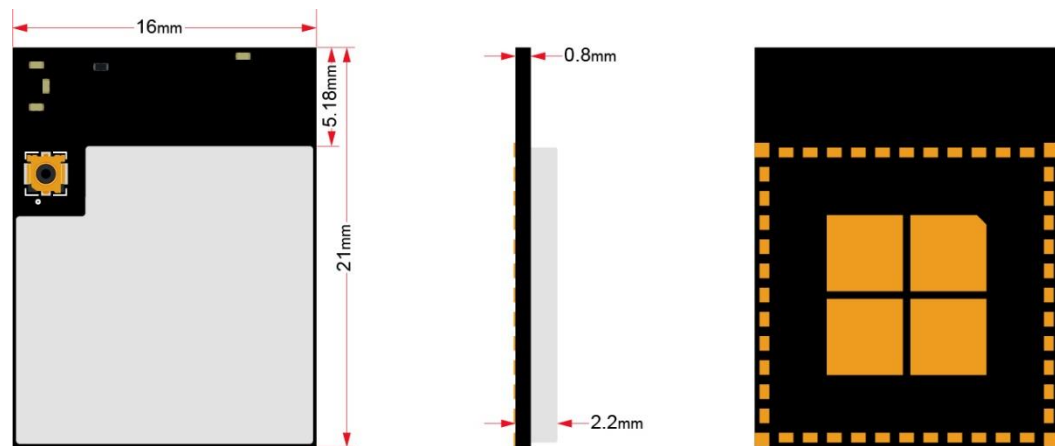


Figure 2: MQM744/748-0-50-0B Module View

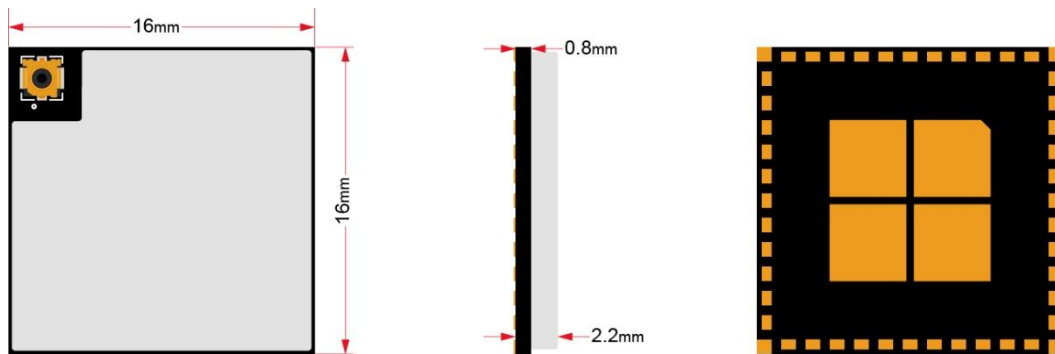


Figure 3: MQM744/748-0-50-0U Module View

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The MQM744/748 specific development kits are also provided to facilitate application software development as shown below:

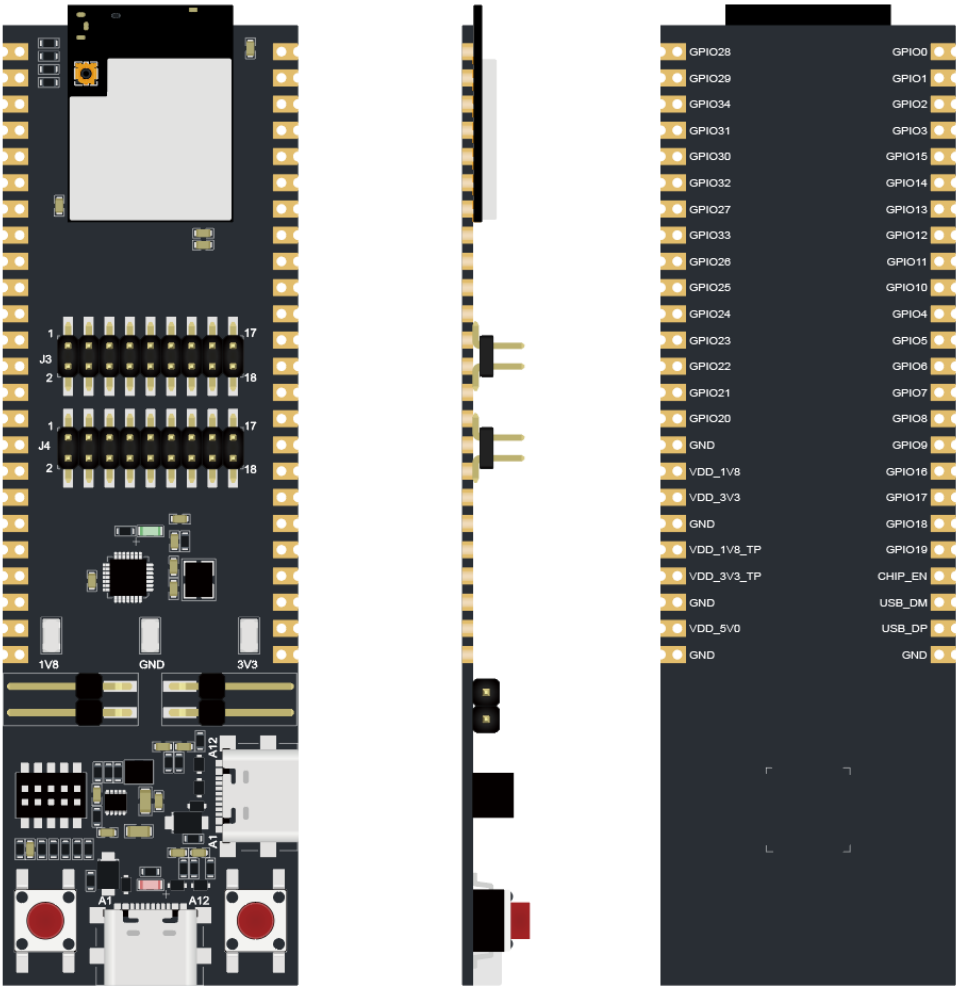


Figure 4: MQM744/748 Module Development Kit

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2 Hardware Specification

This section provides detailed hardware design and specification of MQM744/748. MQM744/748 hardware design has been optimized for small footprint and reduced RBOM cost.

2.1 Block Diagram

MQM744/748 integrates 8MB NOR flash and optional 32.768kHz RTC crystal. The design also supports PCB antenna (MQM744/748-0-50-0B) or simply provides antenna pin (MQM744/748-0-50-0U) to allow customized antenna implementation on motherboard. The block diagram is shown below.

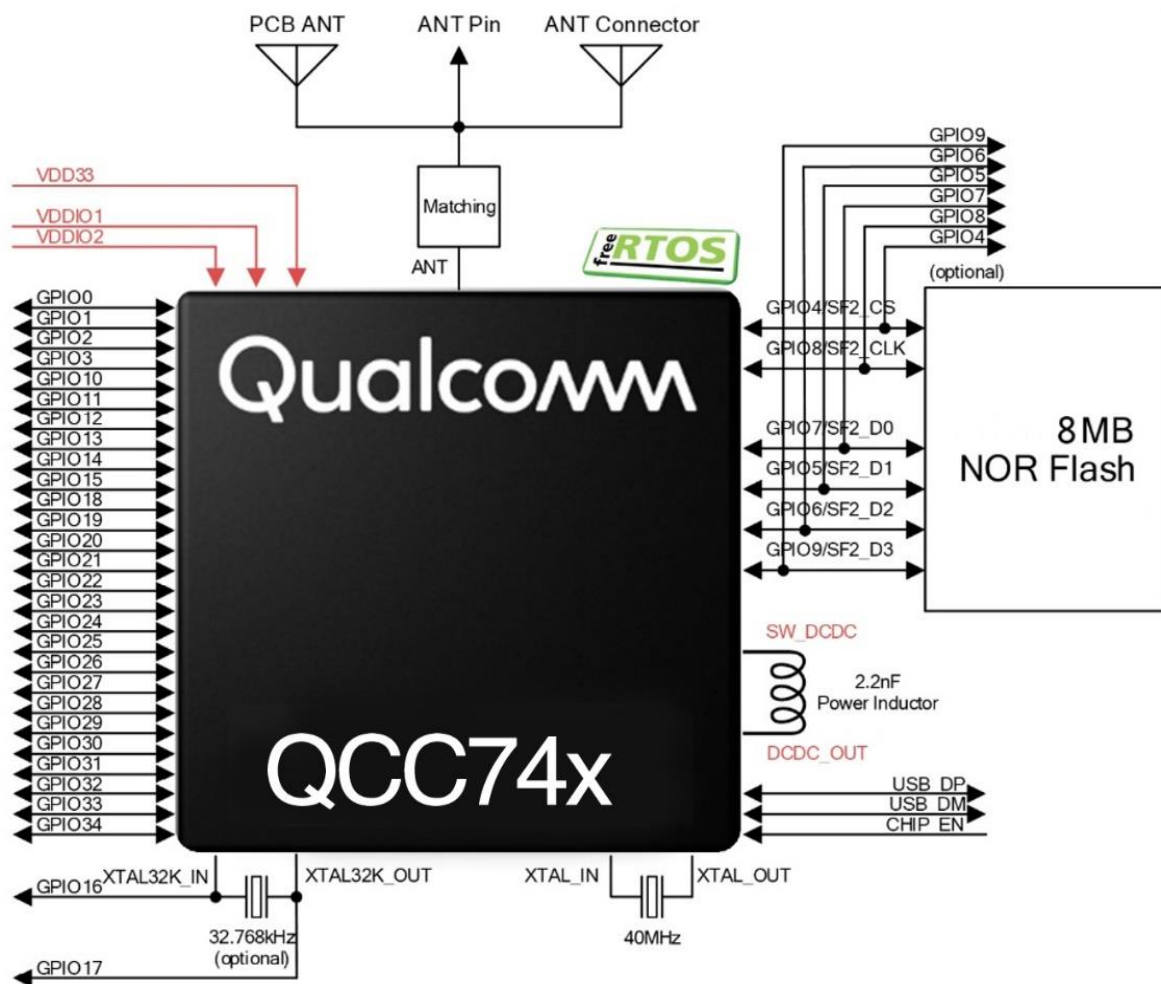


Figure 5: MQM744/748 Module Block Diagram

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2.2 Pinout Description

2.2.1 Pin Map

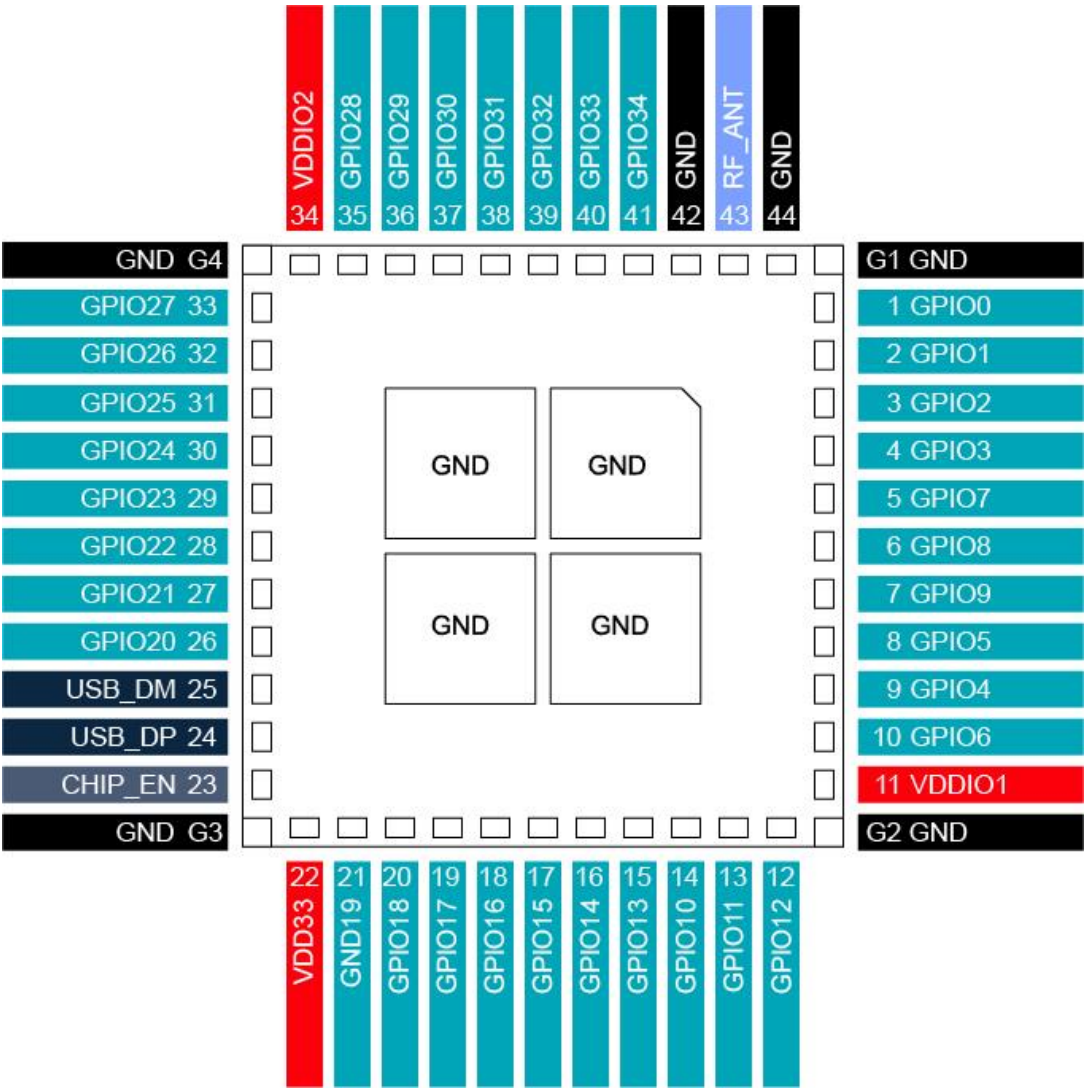


Figure 6: MQM744/748-0-50-0P Module Pin Map

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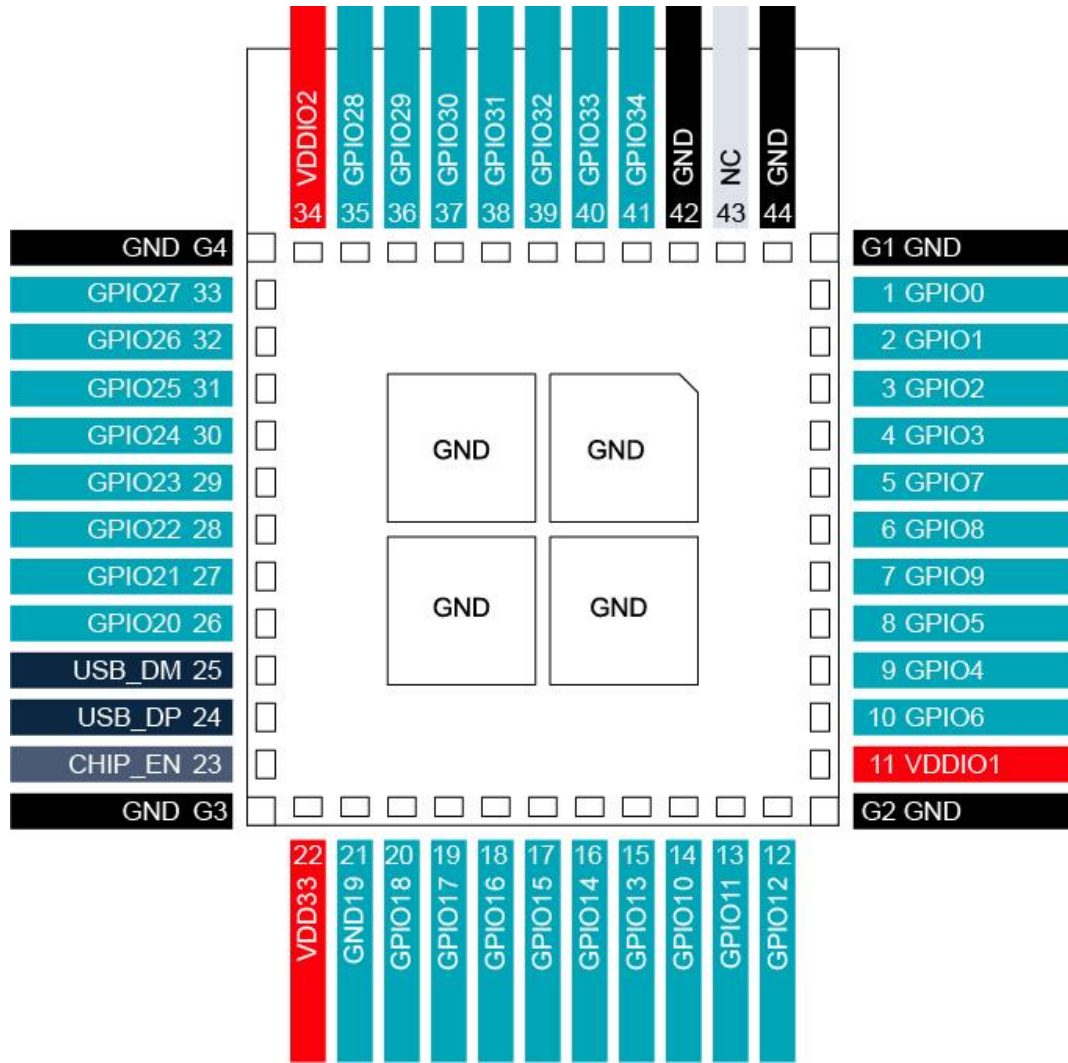


Figure 7: MQM744/748-0-50-0B/U Module Pin Map

2.2.2 Pin Definition

Pin	Pin Name	Type	Power Domain	Description
22	VDD33	PWR	-	Power input (2.97~3.63V)
11	VDDIO1	PWR	-	Host I/O voltage input GPIO0~15
34	VDDIO2	PWR	-	Host I/O voltage input GPIO20~34
G1-G4,42,44	GND	GND	GND	Ground
43	RF_ANT	AI/AO	-	Pin antenna(MQM744/748-0-50-0P)
	NC	-	-	No Connection(MQM744/748-0-50-0B/U)
24	USB_DP	AI/AO	-	Only MQM748 support
25	USB_DM	AI/AO	-	Only MQM748 support

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23	CHIP_EN	DI	-	Chip power on
1	GPIO0	DI/DO	VDDIO1	Generic PIO
2	GPIO1	DI/DO	VDDIO1	Generic PIO
3	GPIO2	DI/DO	VDDIO1	Generic PIO
4	GPIO3	DI/DO	VDDIO1	Generic PIO
9	GPIO4	DI/DO	VDDIO1	Generic PIO
8	GPIO5	DI/DO	VDDIO1	Generic PIO
10	GPIO6	DI/DO	VDDIO1	Generic PIO
5	GPIO7	DI/DO	VDDIO1	Generic PIO
6	GPIO8	DI/DO	VDDIO1	Generic PIO
7	GPIO9	DI/DO	VDDIO1	Generic PIO
14	GPIO10	DI/DO	VDDIO1	Generic PIO
13	GPIO11	DI/DO	VDDIO1	Generic PIO
12	GPIO12	DI/DO	VDDIO1	Generic PIO
15	GPIO13	DI/DO	VDDIO1	Generic PIO
16	GPIO14	DI/DO	VDDIO1	Generic PIO
17	GPIO15	DI/DO	VDDIO1	Generic PIO
18	GPIO16	DI/DO	VDD33	Generic PIO
19	GPIO17	DI/DO	VDD33	Generic PIO
20	GPIO18	DI/DO	VDD33	Generic PIO
21	GPIO19	DI/DO	VDD33	Generic PIO
26	GPIO20	DI/DO	VDDIO2	Generic PIO
27	GPIO21	DI/DO	VDDIO2	Generic PIO
28	GPIO22	DI/DO	VDDIO2	Generic PIO
29	GPIO23	DI/DO	VDDIO2	Generic PIO
30	GPIO24	DI/DO	VDDIO2	Generic PIO
31	GPIO25	DI/DO	VDDIO2	Generic PIO
32	GPIO26	DI/DO	VDDIO2	Generic PIO
33	GPIO27	DI/DO	VDDIO2	Generic PIO
35	GPIO28	DI/DO	VDDIO2	Generic PIO
36	GPIO29	DI/DO	VDDIO2	Generic PIO
37	GPIO30	DI/DO	VDDIO2	Generic PIO
38	GPIO31	DI/DO	VDDIO2	Generic PIO
39	GPIO32	DI/DO	VDDIO2	Generic PIO

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40	GPIO33	DI/DO	VDDIO2	Generic PIO
41	GPIO34	DI/DO	VDDIO2	Generic PIO

2.2.3 I/O Pin Mux Table

GPIO	QSPI	SDIO	SDH	SPI	I2C (0/1)	UART (0/1)	I2S	PWM	CAN	ADC	DAC	EMAC	DVP	DBI	JTAG
GPIO0				CS	SCL	RTS/CTS/TX/RX	BCLK	CH0P/CH0P	TX/RX	CH9			CAM1_VSYNC		TMS
GPIO1				CLK	SDA	RTS/CTS/TX/RX	FS	CH1P/CH0N	TX/RX	CH8			CAM1_HSYNC		TCK
GPIO2				MISO	SCL	RTS/CTS/TX/RX	DI	CH2P/CH1P	TX/RX	CH2					TDO
GPIO3				MOSI	SDA	RTS/CTS/TX/RX	DO	CH3P/CH1N	TX/RX	CH3			CAM1_DAT0		TDI
GPIO10	SCL	DAT2	DAT1	MISO	SCL	RTS/CTS/TX/RX	DI	CH2P/CH1P	TX/RX	CH7			CAM1_DAT1	B_DB2/C_SDA0	TDO
GPIO11	CS	DAT3	DAT0	MOSI	SDA	RTS/CTS/TX/RX	DO	CH3P/CH1N	TX/RX				CAM1_DAT2	B_DB3/C_DCn	TDI
GPIO12	SDA0	CMD	CLK	CS	SCL	RTS/CTS/TX/RX	BCLK	CH0P/CH2P	TX/RX	CH6			CAM1_DAT3	B_DB4/C_SCL	TMS
GPIO13	SDA1	CLK	CMD	CLK	SDA	RTS/CTS/TX/RX	FS	CH1P/CH2N	TX/RX	CH5			CAM1_CLK	B_DB5/C_CSn	TCK
GPIO14	SDA2	DAT0	DAT3	MISO	SCL	RTS/CTS/TX/RX	DI	CH2P/CH3P	TX/RX	CH4	A_PWM_P		CAM1_DAT4	B_DB6/C_SDA0	TDO
GPIO15	SDA3	DAT1	DAT2	MOSI	SDA	RTS/CTS/TX/RX	DO	CH3P/CH3N	TX/RX		A_PWM_N		CAM1_DAT5	B_DB7/C_DCn	TDI
GPIO16				CS	SCL	RTS/CTS/TX/RX	BCLK	CH0P/CH0P	TX/RX				CAM1_DAT6		TMS
GPIO17				CLK	SDA	RTS/CTS/TX/RX	FS	CH1P/CH0N	TX/RX				CAM1_DAT7		TCK
GPIO18				MISO	SCL	RTS/CTS/TX/RX	DI	CH2P/CH1P	TX/RX						TDO
GPIO19				MOSI	SDA	RTS/CTS/TX/RX	DO	CH3P/CH1N	TX/RX	CH1					TDI
GPIO20				CS	SCL	RTS/CTS/TX/RX	BCLK	CH0P/CH2P	TX/RX	CH0/ACH0					TMS
GPIO21				CLK	SDA	RTS/CTS/TX/RX	FS	CH1P/CH2N	TX/RX	ACH1					TCK
GPIO22				MISO	SCL	RTS/CTS/TX/RX	DI	CH2P/CH3P	TX/RX	ACH2	A_PWM_P				TDO
GPIO23				MOSI	SDA	RTS/CTS/TX/RX	DO	CH3P/CH3N	TX/RX	ACH3	A_PWN_N				TDI
GPIO24				CS	SCL	RTS/CTS/TX/RX	BCLK	CH0P/CH0P	TX/RX				CAM0_DAT0		TMS
GPIO25				CLK	SDA	RTS/CTS/TX/RX	FS	CH1P/CH0N	TX/RX			REF_CLK	CAM0_DAT1		TCK
GPIO26				MISO	SCL	RTS/CTS/TX/RX	DI	CH2P/CH1P	TX/RX			TXD[0]	CAM0_DAT2		TDO
GPIO27				MOSI	SDA	RTS/CTS/TX/RX	DO	CH3P/CH1N	TX/RX	CH10/ACH 4	A_PWN_N	TXD[1]	CAM0_DAT3		TDI
GPIO28				CS	SCL	RTS/CTS/TX/RX	BCLK	CH0P/CH2P	TX/RX	CH11/ACH 5	A_PWM_P	RXD[0]	CAM0_HSYNC		TMS
GPIO29				CLK	SDA	RTS/CTS/TX/RX	FS	CH1P/CH2N	TX/RX	ACH6		RXD[1]	CAM0_VSYNC		TCK
GPIO30				MISO	SCL	RTS/CTS/TX/RX	DI	CH2P/CH3P	TX/RX	ACH7		RXERR	CAM0_CLK		TDO
GPIO31				MOSI	SDA	RTS/CTS/TX/RX	DO	CH3P/CH3N	TX/RX			TX_EN	CAM0_DAT4		TDI
GPIO32				CS	SCL	RTS/CTS/TX/RX	BCLK	CH0P/CH0P	TX/RX			RX_DV	CAM0_DAT5		TMS
GPIO33				CLK	SDA	RTS/CTS/TX/RX	FS	CH1P/CH0N	TX/RX			MDC	CAM0_DAT6		TCK
GPIO34				MISO	SCL	RTS/CTS/TX/RX	DI	CH2P/CH1P	TX/RX			MDIO	CAM0_DAT7		TDO

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2.3 Computing Subsystem

MQM744/748 has Qualcomm QCC744/748 at his core which integrates 32-bit RISC-V @325MHz, 128KB ROM and 484KB SRAM as well as on-module NOR flash with size up to 8MB. The code can be executed from flash via Execution-in-Place (XiP).

Qualcomm	pSRAM Built-in	Flash Built-in	Grade	Package
QCC744/748	4MB	X	-40~85°C	QFN-56, 7 x 7 x 0.9 mm, 0.4 mm pitch

The computing subsystem software architecture is shown below:

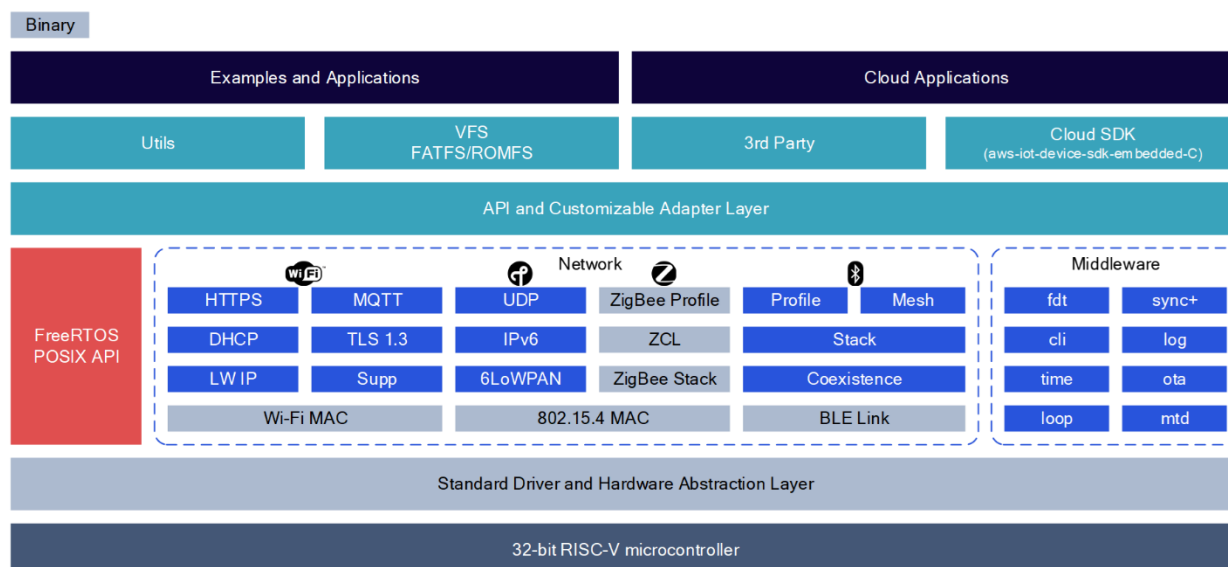


Figure 8: Computing Subsystem Software Architecture

Software SDK will be open sourced at Codelinaro with blocks in dot line will be offered in binary format inside software SDK package.

Codelinaro Download Link: <https://git.codelinaro.org/clo/qcc7xx/QCCSDK-QCC744>

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3 Electrical Characteristics

3.1 Absolute Maximum Ratings

The absolute maximum ratings provided in this section reflect the stress levels that, if exceeded, may cause permanent damage to the device. No functionality is guaranteed outside the operating specifications. Functionality and reliability are only guaranteed within the operating specifications.

Pin	Parameter	Min	Max	Unit
VDD33	Power input voltage	-0.3	3.63	V
VDDIO1, VDDIO 2	I/O port voltage	-0.3	3.63	V
ESD Protection (HBM)	-	-	2,000	V
Storage temperature	-	-40	135	°C

3.2 Recommended Operating Conditions

Pin	Parameter	Min	TYP	Max	Unit
VDD33	Power input voltage	2.97	3.3	3.63	V
VDDIO1, VDDIO 2 (3.3V)	I/O port voltage	2.97	3.3	3.63	V
VDDIO1, VDDIO 2 (1.8V)	I/O port voltage	1.62	1.8	1.98	V
Operating temperature	-	-40	-	85	°C
Storage temperature	-	-40	-	135	°C

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4 Radio Performance

The radio performance for Wi-Fi, Bluetooth and 802.15.4 is shown in the table below:

4.1 802.11b/g/n/ax (Wi-Fi 6)

Standard	PHY	Modulation	MCS Index	Data Rate (Mbps)		Max Tx Power (dBm)		Rx Sensitivity (dBm)	
				HE20	HE40	HE20	HE40	HE20	HE40
802.11b	CCK (DSSS)	DBPSK	-	1		21		-99.0	
	CCK (DSSS)	DQPSK	-	2		21		-96.5	
	CCK (DSSS)	B/DQPSK	-	5.5		21		-94.5	
	CCK (DSSS)	Q/DQPSK	-	11		21		-90.0	
802.11g	OFDM	BPSK	-	6		20		-93.0	
	OFDM	BPSK	-	9		20		-92.5	
	OFDM	QPSK	-	12		20		-91.5	
	OFDM	QPSK	-	18		20		-89.0	
	OFDM	16QAM	-	24		19		-86.5	
	OFDM	16QAM	-	36		19		-83.0	
	OFDM	64QAM	-	48		18		-89.0	
	OFDM	64QAM	-	54		18		-77.5	
802.11n	OFDM	BPSK	MCS0	7.2	15	19	18	-93.0	-89.5
	OFDM	QPSK	MCS1	14.4	30	19	18	-91.0	-88.0
	OFDM	QPSK	MCS2	21.7	45	19	18	-89.0	-86.0
	OFDM	16QAM	MCS3	28.9	60	19	18	-86.5	-83.5
	OFDM	16QAM	MCS4	43.3	90	19	18	-83.0	-80.5
	OFDM	64QAM	MCS5	57.8	120	18	17	-79.0	-76.5
	OFDM	64QAM	MCS6	65	135	18	17	-77.5	-75.0
	OFDM	64QAM	MCS7	72.2	150	18	17	-76.0	-73.0
802.11ax	OFDMA	BPSK	MCS0	8.6	17.2	19	19	-93.0	-89.5
	OFDMA	QPSK	MCS1	17.2	34.4	19	19	-91.0	-88.0

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	OFDMA	QPSK	MCS2	25.8	51.6	19	19	-89.0	-86.5
	OFDMA	16QAM	MCS3	34.4	68.8	19	19	-86.0	-83.5
	OFDMA	16QAM	MCS4	51.6	103.2	19	19	-83.0	-80.0
	OFDMA	64QAM	MCS5	68.8	137.6	18	16	-79.0	-76.5
	OFDMA	64QAM	MCS6	77.4	154.9	18	16	-77.5	-75.0
	OFDMA	64QAM	MCS7	86	172.1	18	16	-75.5	-73.0
	OFDMA	256QAM	MCS8	103.2	206.5	17	16	-72.0	-69.0
	OFDMA	256QAM	MCS9	114.7	229.4	17	16	-70.0	-67.0

4.2 Bluetooth

Standard	PHY	Modulation	Data Rate	Max Tx Power (dBm)	Rx Sensitivity (dBm)
BT Classic	BR	GFSK	1Mbps	+10	-94
	EDR	$\pi/4$ DQPSK	2Mbps	+10	-96
	EDR	8DPSK	3Mbps	+8	-90
BLE	LE 2M	GFSK	2Mbps	+10	-97
	LE 1M	GFSK	1Mbps	+10	-99
	LE Coded (S=2)	GFSK	500Kbps	+10	-102
	LE Coded (S=8)	GFSK	125Kbps	+10	-105

4.3 802.15.4

Standard	Modulation	Data Rate	Max Tx Power (dBm)	Rx Sensitivity (dBm)
802.15.4	DSSS O-QPSK MPSK GFSK BPSK	250Kbps	+20	TBD

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5 Power Consumption

MQM744/748 can operate in four power states as shown below to maximize power saving:

- Active – Continuous transmitting or receiving on connection to AP
- Idle – No active transmitting or receiving. CPU is in idle state.
- Deep Sleep – Wi-Fi/USB/CPU/Peripherals power off. This is DTIM power saving mode
- Hibernation – No connection to AP.
- Shut down – the chip is completed shut down

The power consumption for each state is listed in the table below:

Operation Mode	State	Typical	Unit	Comments
Active	MCU Active & Rx Active	38	mA	MCU 160MHz, XCLK 40MHz, BCLK 80MHz, Flash 40MHz
	Tx Active	326	mA	MCS0, HT20 Tx Power = +19 dBm
Power Saving	Deep Sleep	64	μA	Wi-Fi/USB/CPU/Peripherals Power Off. This is DTIM PS mode
	Hibernation	2.1	μA	4KB RAM retained
	Shut Down	0.15	μA	
DTIM	DTIM 1	476	μA	Xtal32k
		524	μA	RC 32k
	DTIM 3	179	μA	Xtal32k
		227	μA	RC 32k
	DTIM 5	119	μA	Xtal32k
		167	μA	RC 32k
	DTIM 10	75	μA	Xtal32k
		123	μA	RC 32k

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6.2 PCB Antenna

6.2.1 Dimension

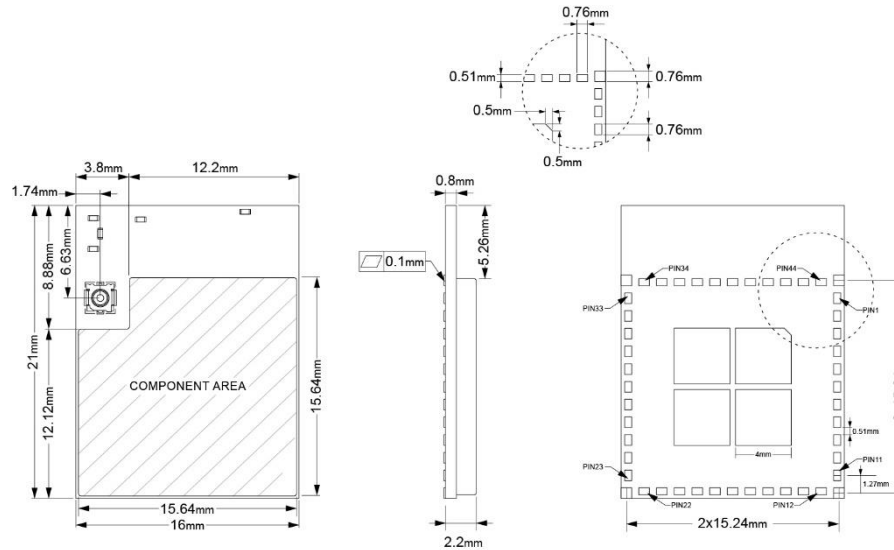


Figure 11: MQM744/748-0-50-0B Module Dimension

6.2.2 Recommended PCB Landing Pattern

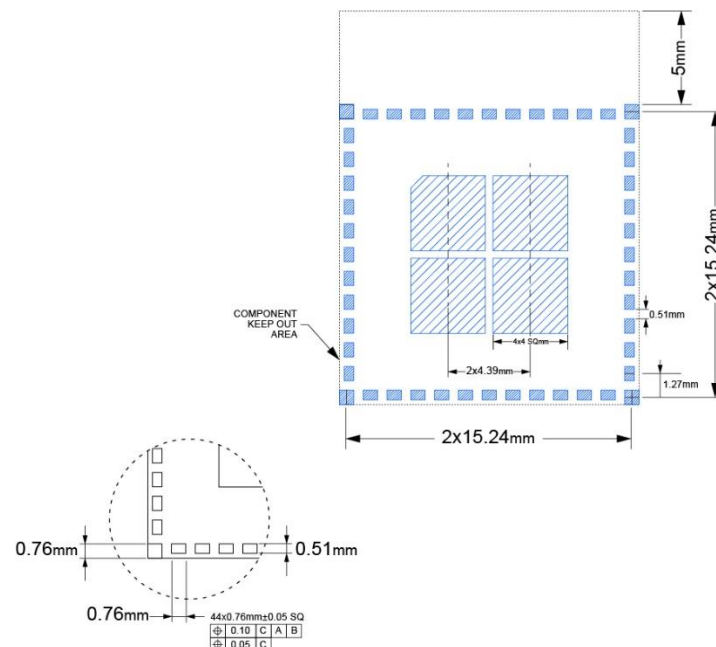


Figure 12: MQM744/748-0-50-0B Module PCB Landing Pattern

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6.3 Connector Antenna

6.3.1 Dimension

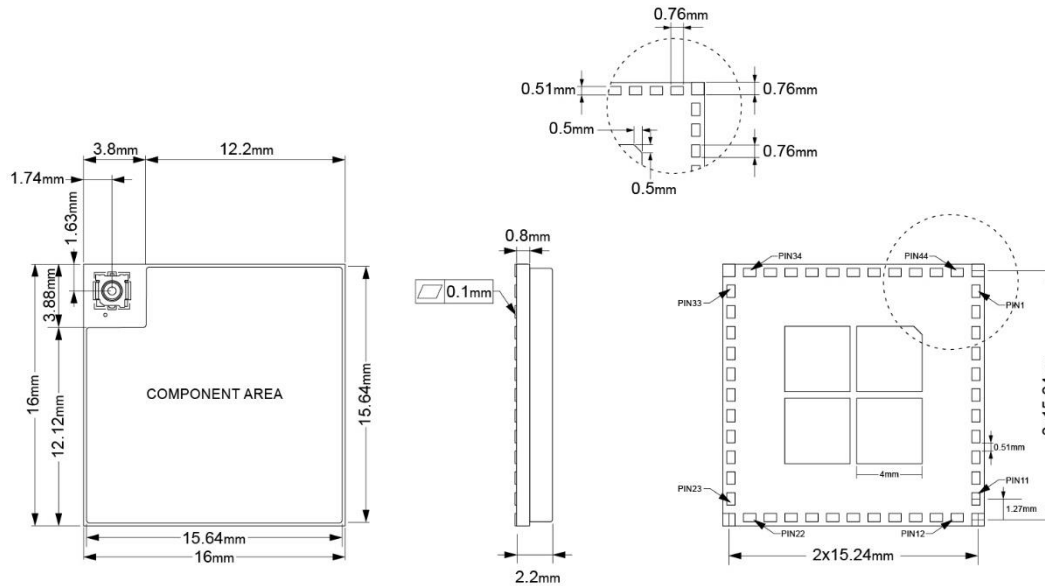


Figure 13: MQM744/748-0-50-0U Module Dimension

6.3.2 Recommended PCB Landing Pattern

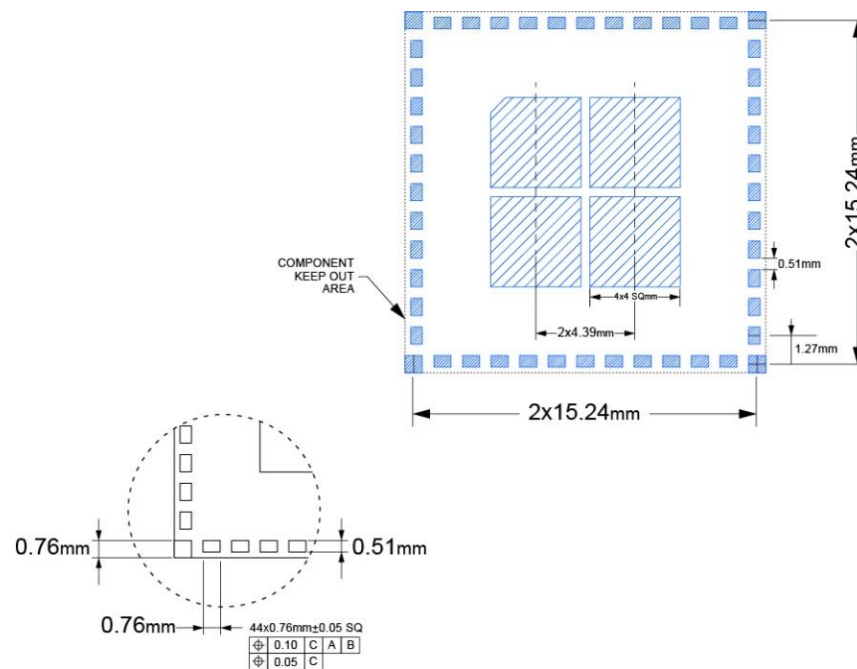


Figure 14: MQM744/748-0-50-0U Module PCB Landing Pattern

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7 Manufacturing Recommendation

7.1 Power Layout Guideline

MQM744/748 is powered by either 3V battery or DC 3.3V. Power pin connection capacitor is as close as possible to chip and pin. Decoupling the power supply from the chip using a capacitor. Use capacitors to prevent noise from coupling back to the power plane.

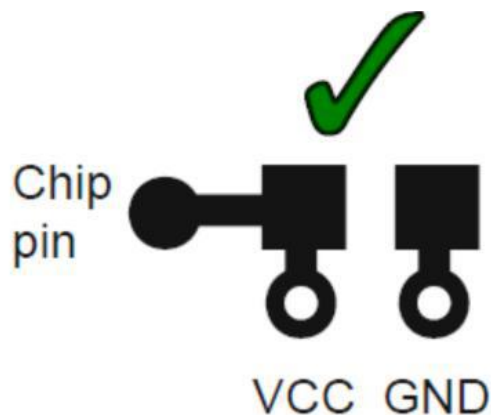


Figure 15: Power Layout Guideline

7.2 Soldering Recommendations

MQM744/748 can be SMT on the board following the temperature curve graph:

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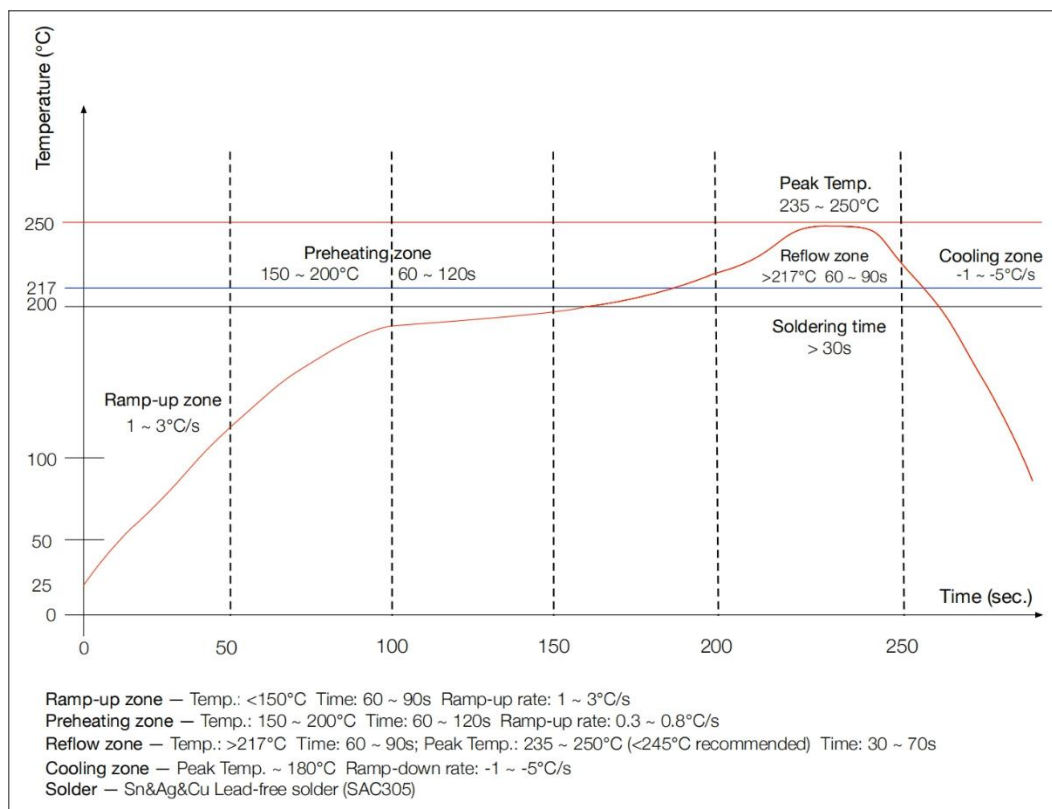


Figure 16: Soldering Guideline

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8 Statement

8.1 Operational use conditions

N/A

8.2 Antenna used

Antenna Type	Brand/ manufacturer	Model No.	Max. Antenna Gain
PCB Antenna	INPAQ TECHNOLOGY(SUZHOU)	RFIQM0744010NB001	2.52dBi
Dipole Antenna	INPAQ TECHNOLOGY(SUZHOU)	RFPCA521010EMABY01	3.37dBi

8.3 Instruction to Host product manufacturer when choosing external connector:

N/A

8.4 Trace antenna designs information

N/A

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Notice to Host Product Manufacturer

Any deviation(s) from the defined parameters of the antenna trace, as described by this instruction, host product manufacturer must notify us that you wish to change the antenna trace design. In this case, a Class II permissive change application is required to be filed by us, or you (host manufacturer) can take responsibility through the change in FCC ID and IC ID (new application) procedure followed by a Class II permissive change application.

FCC&IC regulatory compliance statement

§15.19 Statement

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

- (1) This device may not cause harmful interference, and
- (2) This device must accept any interference received, including interference that may cause undesired operation.

RSS-Gen Statement

This device contains licence-exempt transmitter(s)/receiver(s) that comply with Innovation, Science and Economic Development Canada's licence-exempt RSS(s). Operation is subject to the following two conditions:

- (1) This device may not cause interference.
- (2) This device must accept any interference, including interference that may cause undesired operation of the device.

L'appareil contient des émetteurs/récepteurs exempts de licence qui sont conformes aux CNR exempts de licence d'Innovation, Sciences et Développement économique Canada. L'exploitation est soumise aux deux conditions suivantes :

- (1) l'appareil ne doit pas produire de brouillage,
- (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

§15.21 Information to user

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Warning: changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

RF Exposure compliance statement

This Module complies with FCC radiation exposure limits set forth for an uncontrolled environment. This equipment should be installed and operated with a minimum distance of 20cm between the radiator and your body. This transmitter must not be co-located or operating in conjunction with any other antenna or transmitter.

Labelling Instruction for Host Product Integrator

Please notice that if the FCC and IC identification number is not visible when the module is installed inside another device, then the outside of the device into which the module is installed must also display a label referring to the enclosed module. For FCC, this exterior label should follow "Contains FCC ID: 2BQY7-M7400". In accordance with FCC KDB guidance 784748 Labeling Guidelines.

§ 15.19 Labelling requirements shall be complied on end user device.

Labelling rules for special device, please refer to §2.925, § 15.19(a)(5) and relevant KDB publications. For E-label, please refer to §2.935.

Installation Notice to Host Product Manufacturer

The OEM integrator is responsible for ensuring that the end-user has no manual instruction to remove or install module.

The module is limited to installation in mobile application, a separate approval is required for all other operating configurations, including portable configurations with respect to §2.1093 and difference antenna configurations.

Antenna Change Notice to Host manufacturer

If you desire to increase antenna gain and either change antenna type or use same antenna type certified, a Class II permissive change application is required to be filed by us, or you (host

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manufacturer) can take responsibility through the change in FCC ID&IC ID (new application) procedure followed by a Class II permissive change application.

FCC other Parts, Part 15B Compliance Requirements for Host product manufacturer

This modular transmitter is only FCC authorized for the specific rule parts listed on our grant, host product manufacturer is responsible for compliance to any other FCC rules that apply to the host not covered by the modular transmitter grant of certification.

Host manufacturer in any case shall ensure host product which is installed and operating with the module is in compliant with Part 15B requirements.

Please note that For a Class B or Class A digital device or peripheral, the instructions furnished the user manual of the end-user product shall include statement set out in §15.105 *Information to the user* or such similar statement and place it in a prominent location in the text of host product manual. Original texts as following:

For Class B

Note: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.*
- Increase the separation between the equipment and receiver.*
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.*
- Consult the dealer or an experienced radio/TV technician for help.*

For Class A

Note: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the

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instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

EU Conformity Statement



This product is marked with "CE" and comply therefore with the applicable harmonized European standards listed under the Radio Equipment Directive 2014/53/EU.

RF Exposure Information

This device has been tested and meets applicable limits for Radio Frequency (RF) exposure.

This equipment must be installed and operated in accordance with provided instructions and the antenna(s) used for this transmitter must be installed to provide a separation distance of at least 20 cm from all persons and must not be co-located or operating in conjunction with any other antenna or transmitter.

For EU:

Bluetooth(2402-2480MHz): 9.37dBm

Bluetooth LE(2402-2480MHz): 8.84dBm

2.4G wifi(2412-2472MHz): 18.86dBm

Thread(2400-2483.5MHz): 9.40dBm

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Revision History

Revision	Description	Date
1.0	Initial draft	July 1, 2024
1.1	Update size and pin map	Jan 14, 2025
1.2	Update Module name	Mar 18, 2025
1.3	Update Figure 4: MQM744 Module Development Kit	Jun 24, 2025
1.4	Add statement	Aug 29,2025
1.5	Add MQM748	Sep 25,2025

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