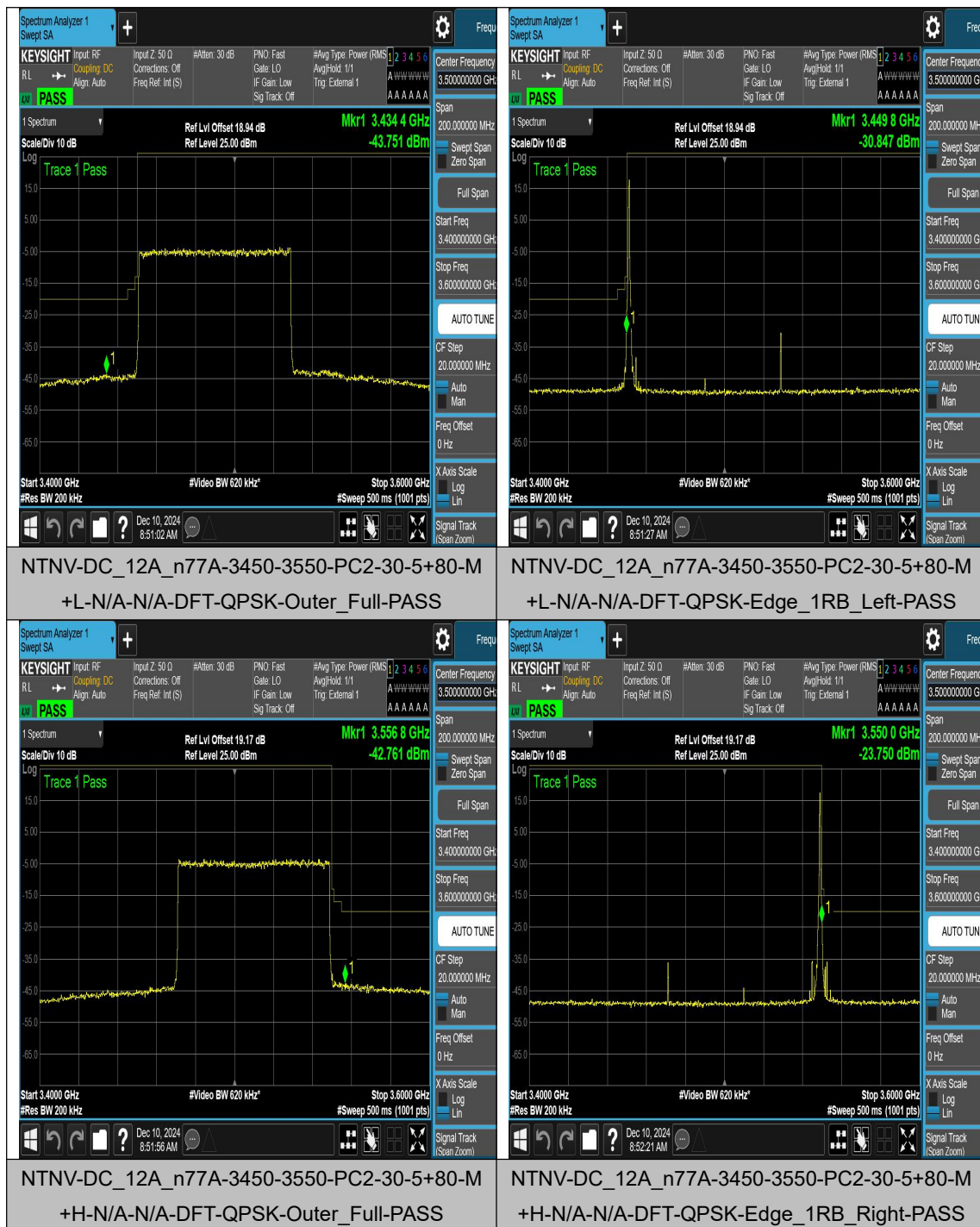
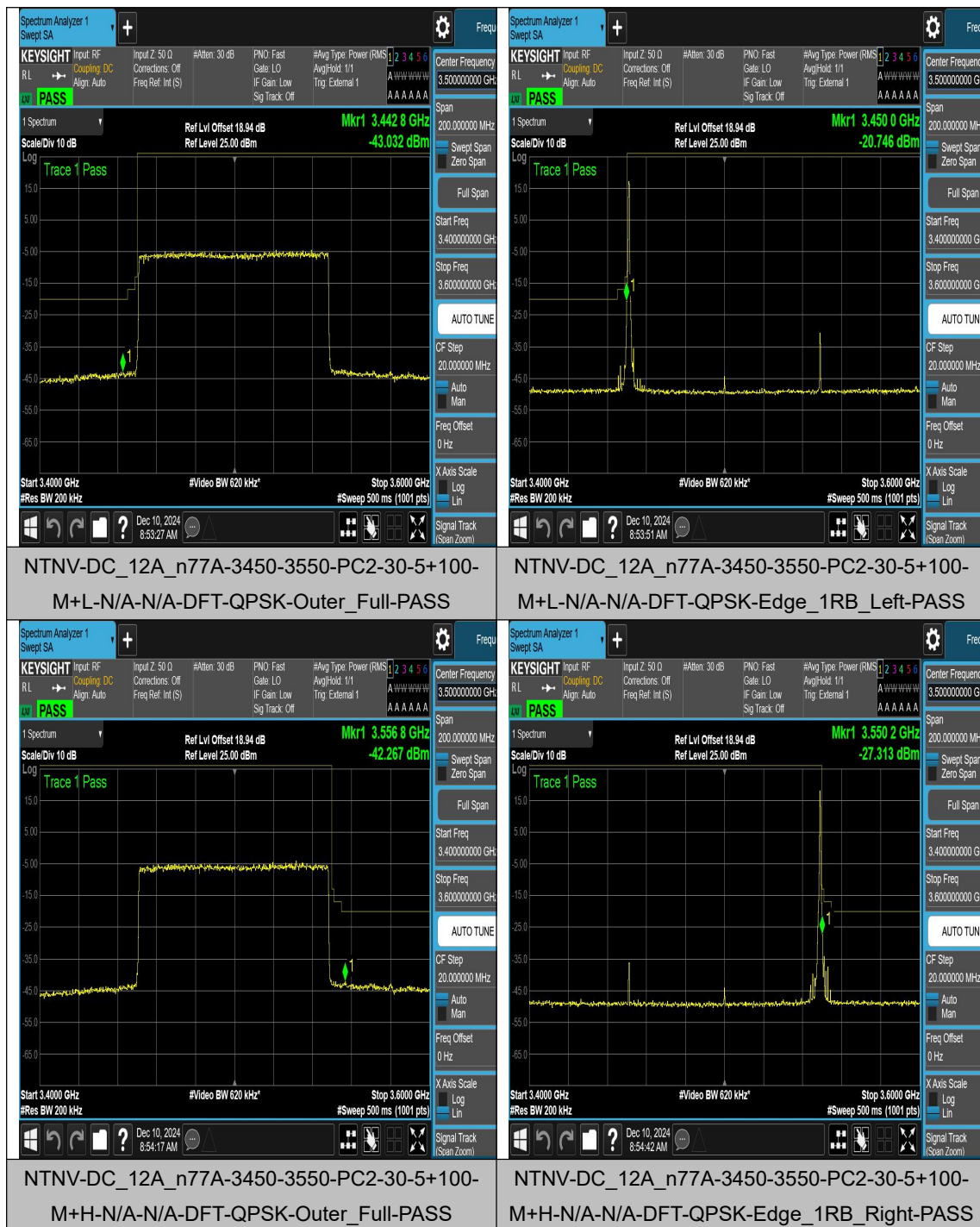






Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.86	-43	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-66.66	-33	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-56.18	-23	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.88	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.72	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.61	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.64	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-67.51	-43	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.65	-33	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-56.13	-23	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-41.00	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.77	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.68	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.73	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.51	-43	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.80	-33	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-56.08	-23	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.93	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.64	-13	PASS

-3450-3550										
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.50	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.88	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-67.01	-43	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-65.40	-33	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-56.24	-23	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.92	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.74	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.63	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.79	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.82	-43	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-65.29	-33	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-56.10	-23	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.89	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.66	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.61	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.56	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.21	-43	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-64.68	-33	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-56.06	-23	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.98	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.65	-13	PASS

DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.40	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.77	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.65	-43	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-66.99	-33	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-56.12	-23	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.91	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.83	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.51	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.74	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.07	-43	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.28	-33	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-56.05	-23	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.95	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.83	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.47	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.69	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.12	-43	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-65.73	-33	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-56.16	-23	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.92	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.41	-13	PASS
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.61	-13	PASS

-3450-3550										
DC_12A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.72	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.95	-43	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	0.15	30	-67.55	-33	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	30	1000	-56.24	-23	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.81	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.80	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.40	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.77	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-67.04	-43	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-67.04	-33	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-56.38	-23	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.95	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.74	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.43	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.73	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.51	-43	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.00	-33	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.97	-23	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.97	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.85	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.59	-13	PASS

DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.69	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-67.18	-43	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	0.15	30	-66.88	-33	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	30	1000	-56.23	-23	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.94	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.58	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.48	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.83	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.94	-43	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-67.29	-33	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-33.08	-23	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.90	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.85	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.48	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.80	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.63	-43	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-65.17	-33	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-56.31	-23	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.89	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.75	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.61	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.75	-13	PASS

-3450-3550										
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-67.04	-43	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	0.15	30	-67.47	-33	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	30	1000	-55.88	-23	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.95	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.73	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.61	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.68	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.65	-43	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-68.02	-33	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.82	-23	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.92	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.71	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.58	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.77	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.61	-43	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-68.00	-33	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.97	-23	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.87	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.70	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.41	-13	PASS
DC_12A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.87	-13	PASS

DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-67.09	-43	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-65.38	-33	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-56.10	-23	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.73	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.74	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.53	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.80	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.68	-43	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-67.76	-33	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-33.31	-23	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.91	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.81	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.61	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.63	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-67.05	-43	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-65.72	-33	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-56.17	-23	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.97	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.67	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.53	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.72	-13	PASS
DC_12A_n77A	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.81	-43	PASS

-3450-3550										
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.14	-33	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-56.27	-23	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.97	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.87	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.41	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.78	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.28	-43	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-66.73	-33	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.63	-23	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.90	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.83	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.66	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.72	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.95	-43	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-67.64	-33	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.69	-23	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.93	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.76	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.57	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.73	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.56	-43	PASS

DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-66.87	-33	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-56.14	-23	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.84	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.75	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.61	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.93	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.77	-43	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.45	-33	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-56.34	-23	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.91	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.83	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.56	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.49	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.81	-43	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-67.84	-33	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-56.13	-23	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.90	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.82	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.39	-13	PASS
DC_12A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.58	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.92	-43	PASS
DC_12A_n77A	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-67.02	-33	PASS

-3450-3550										
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-55.75	-23	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.92	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.75	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.52	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.94	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.93	-43	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-67.11	-33	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-56.14	-23	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-41.02	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.83	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.59	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.66	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.18	-43	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.58	-33	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-56.01	-23	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.93	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.75	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.49	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.69	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-67.43	-43	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.01	-33	PASS

DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-55.90	-23	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.93	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.83	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.39	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.72	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.18	-43	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-65.90	-33	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-56.34	-23	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.95	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.74	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.52	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.75	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.79	-43	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-66.79	-33	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-56.40	-23	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.87	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.78	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.49	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.78	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.11	-43	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-64.91	-33	PASS
DC_12A_n77A	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-56.02	-23	PASS

-3450-3550										
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.97	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.70	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.35	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.69	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.54	-43	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-66.47	-33	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-51.27	-23	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.94	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.83	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.44	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.76	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.32	-43	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-68.71	-33	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-53.62	-23	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.94	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.49	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.52	-13	PASS
DC_12A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.70	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.88	-43	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-67.88	-33	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-56.20	-23	PASS

DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.94	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.74	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.51	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.83	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.92	-43	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.28	-33	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.75	-23	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.85	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.76	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.62	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.66	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.71	-43	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-66.16	-33	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-56.17	-23	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.96	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.71	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.56	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.58	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.32	-43	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.42	-33	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-56.22	-23	PASS
DC_12A_n77A	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.87	-13	PASS

-3450-3550										
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.66	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.65	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.68	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.91	-43	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-66.84	-33	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-56.15	-23	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.88	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.80	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.36	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.70	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.40	-43	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-65.21	-33	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-56.08	-23	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.95	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.67	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.41	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.85	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-67.16	-43	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-67.13	-33	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-55.87	-23	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.86	-13	PASS

DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.69	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.45	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.58	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.65	-43	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-68.57	-33	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-56.11	-23	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.88	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.80	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.60	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.53	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-67.07	-43	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-66.52	-33	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.99	-23	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.87	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.80	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.44	-13	PASS
DC_12A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.65	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.50	-43	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.15	30	-67.58	-33	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-55.97	-23	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.86	-13	PASS
DC_12A_n77A	30	5+10	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.62	-13	PASS

-3450-3550		0								
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.58	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.62	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-67.29	-43	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.95	-33	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-56.29	-23	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.92	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.74	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.50	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.86	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.07	-43	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.41	-33	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-50.90	-23	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.78	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.82	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.53	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.43	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.02	-43	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-65.11	-33	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-56.15	-23	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.91	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.74	-13	PASS

DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.64	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.52	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-67.59	-43	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-68.37	-33	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-56.20	-23	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.85	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.71	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.31	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.86	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.76	-43	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-67.87	-33	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-56.30	-23	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.93	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.86	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.58	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.80	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.08	-43	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-68.13	-33	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-56.33	-23	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.89	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.67	-13	PASS
DC_12A_n77A	30	5+10	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.52	-13	PASS

-3450-3550		0								
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.74	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.42	-43	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.12	-33	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-56.11	-23	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.79	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.68	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.61	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.65	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.31	-43	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-65.67	-33	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-56.17	-23	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.91	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.65	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.54	-13	PASS
DC_12A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.72	-13	PASS

Test Graphs

