

Appendix for DC_12A_n77A(3450-3550MHz)

Product Name: Rugged handheld

Model No: UA60

Appendix A: Average Power Output Data for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_12A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	24.31	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	24.08	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	24.10	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	23.89	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	24.08	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	23.88	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	24.08	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	24.10	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	22.79	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	22.84	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	22.85	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	23.03	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	22.74	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+M	Inner_Full	22.70	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	22.99	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	22.94	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	21.94	PC2	PASS
DC_12A_n77A-3	30	5+20	DFT-QPSK	M+H	Inner_Full	22.20	PC2	PASS

450-3550								
DC_12A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.27	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	21.88	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	22.34	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+H	Inner_Full	22.01	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	22.21	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	21.93	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	22.94	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+L	Inner_Full	22.96	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	23.14	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	22.99	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	23.07	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+L	Inner_Full	22.95	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	23.08	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	22.91	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	22.62	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+M	Inner_Full	22.70	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	22.74	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	22.39	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	22.54	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+M	Inner_Full	22.86	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	22.97	PC2	PASS

DC_12A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	22.49	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	22.06	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+H	Inner_Full	21.91	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	22.13	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	21.85	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	21.94	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+H	Inner_Full	21.89	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	22.09	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	21.87	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	23.20	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	22.99	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	23.24	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	23.26	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	23.22	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+L	Inner_Full	23.02	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	23.23	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	22.99	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	23.06	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	22.96	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	23.07	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	22.96	PC2	PASS
DC_12A_n77A-3	30	5+40	DFT-16QAM	M+M	Outer_Full	22.97	PC2	PASS

450-3550								
DC_12A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+M	Inner_Full	22.98	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	22.96	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	22.98	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	22.25	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	22.24	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	22.24	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	22.24	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	22.25	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+H	Inner_Full	22.26	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	22.26	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	22.26	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	22.69	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	22.80	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	22.79	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	22.91	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	22.79	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+L	Inner_Full	22.79	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	22.79	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	22.89	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	22.58	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+M	Inner_Full	22.48	PC2	PASS

DC_12A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	22.59	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	22.47	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	22.48	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+M	Inner_Full	22.48	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	22.59	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	22.48	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	21.99	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	22.00	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	22.00	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	22.00	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	21.90	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+H	Inner_Full	21.91	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	21.90	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	21.91	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	22.63	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	22.84	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	22.62	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	22.62	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	22.82	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+L	Inner_Full	22.71	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	22.64	PC2	PASS
DC_12A_n77A-3	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	22.84	PC2	PASS

450-3550								
DC_12A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	22.36	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	22.36	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	22.34	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	22.24	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	22.25	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+M	Inner_Full	22.14	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	22.35	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	22.15	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	21.82	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	21.82	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	21.94	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	21.94	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	21.83	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+H	Inner_Full	21.94	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	21.83	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	21.83	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	23.59	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	24.67	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	23.45	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	22.86	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	22.61	PC2	PASS

DC_12A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+L	Inner_Full	23.70	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	22.61	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	21.98	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	23.68	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	24.72	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	23.62	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	22.75	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	22.58	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+M	Inner_Full	23.91	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	22.59	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	21.90	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	23.70	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	24.81	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	23.40	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	22.89	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	22.66	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+H	Inner_Full	23.94	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	22.69	PC2	PASS
DC_12A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	21.62	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

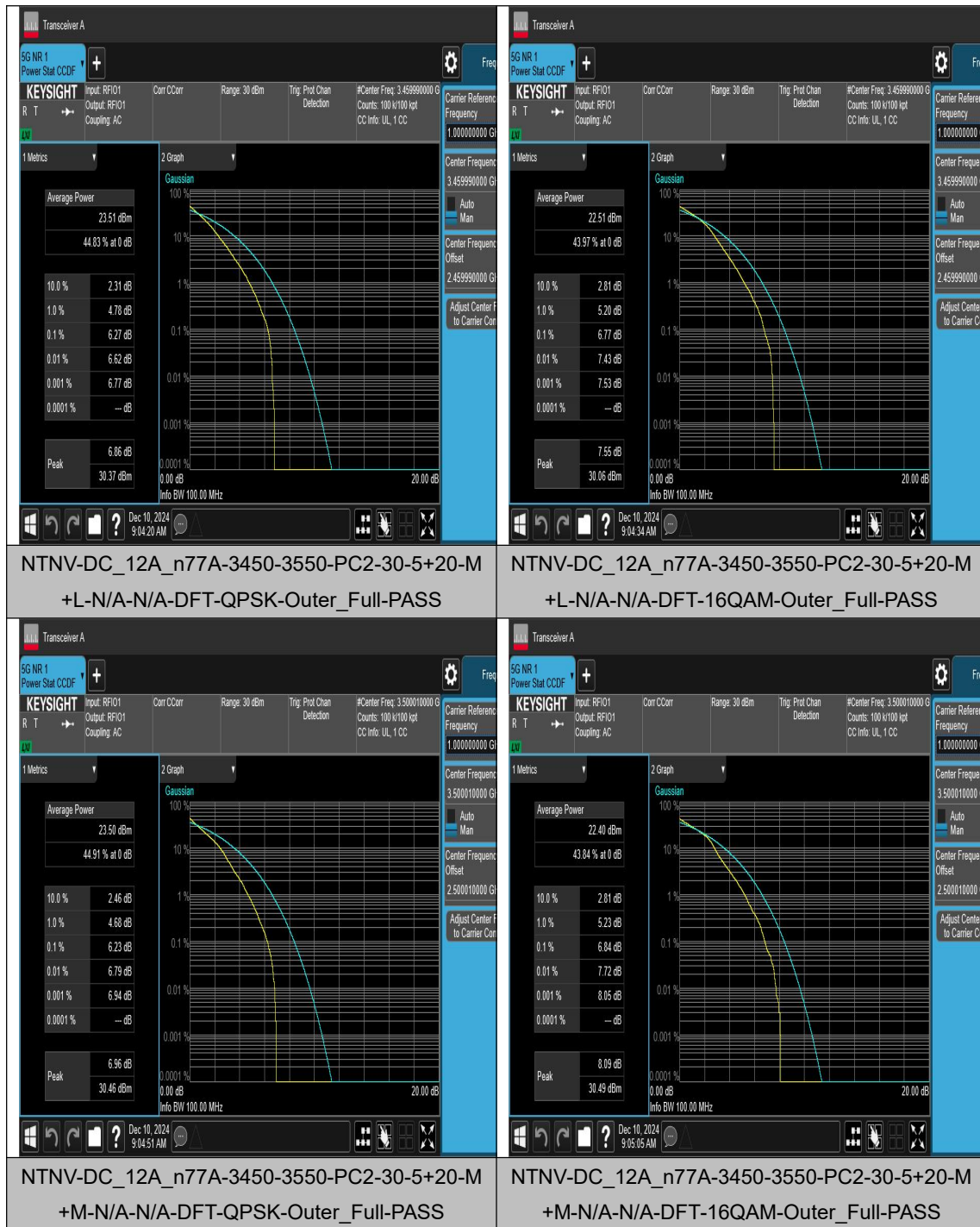
Test Result

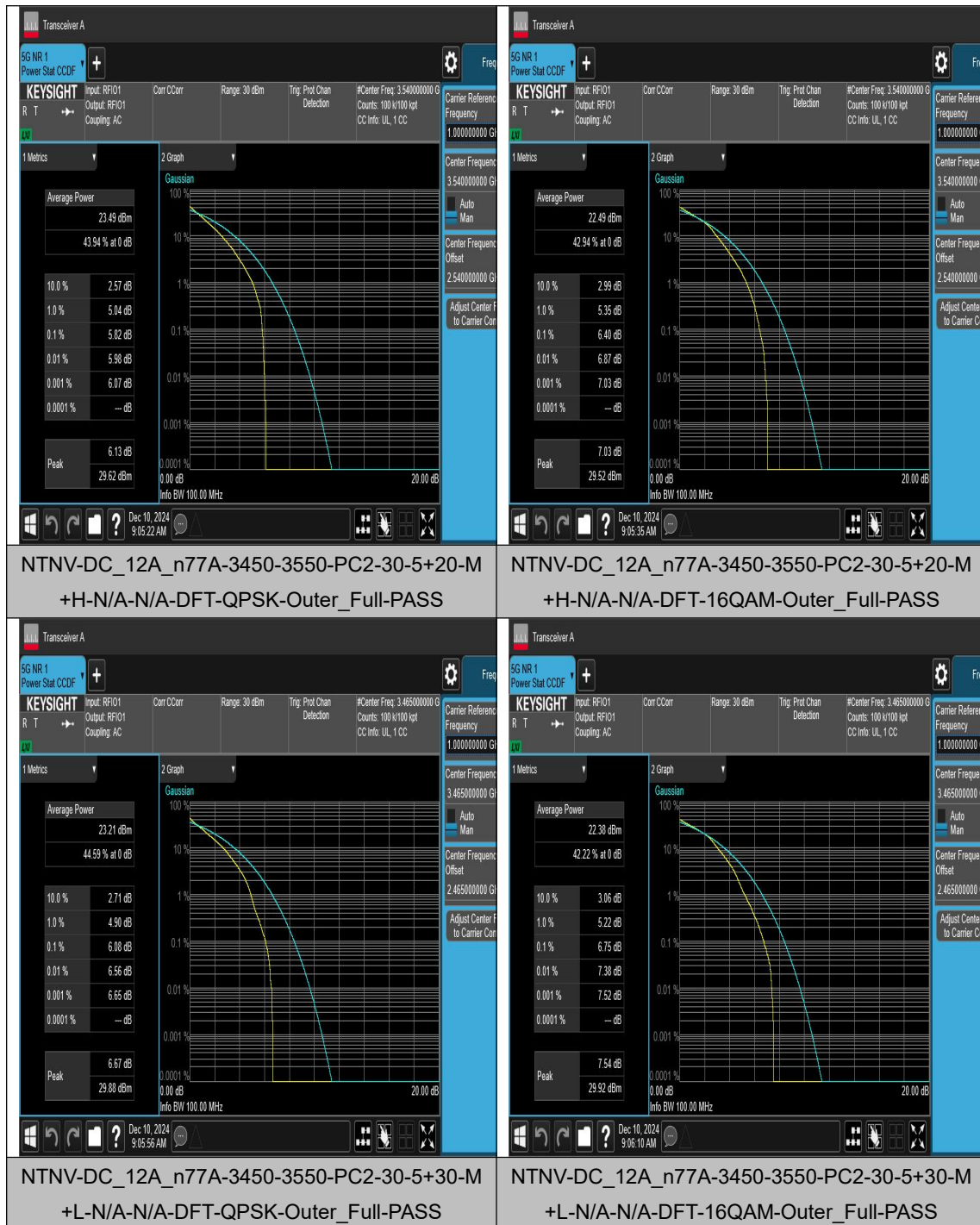
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_12A_n 77A-3450-3 550	30	5+20	DFT-QPSK	M+L	Outer_Full	6.27	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+20	DFT-16QAM	M+L	Outer_Full	6.77	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+20	DFT-QPSK	M+M	Outer_Full	6.23	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+20	DFT-16QAM	M+M	Outer_Full	6.84	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+20	DFT-QPSK	M+H	Outer_Full	5.82	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+20	DFT-16QAM	M+H	Outer_Full	6.40	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+30	DFT-QPSK	M+L	Outer_Full	6.08	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+30	DFT-16QAM	M+L	Outer_Full	6.75	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+30	DFT-QPSK	M+M	Outer_Full	6.19	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+30	DFT-16QAM	M+M	Outer_Full	6.93	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+30	DFT-QPSK	M+H	Outer_Full	6.04	≤13	PASS
DC_12A_n 77A-3450-3	30	5+30	DFT-16QAM	M+H	Outer_Full	6.73	≤13	PASS

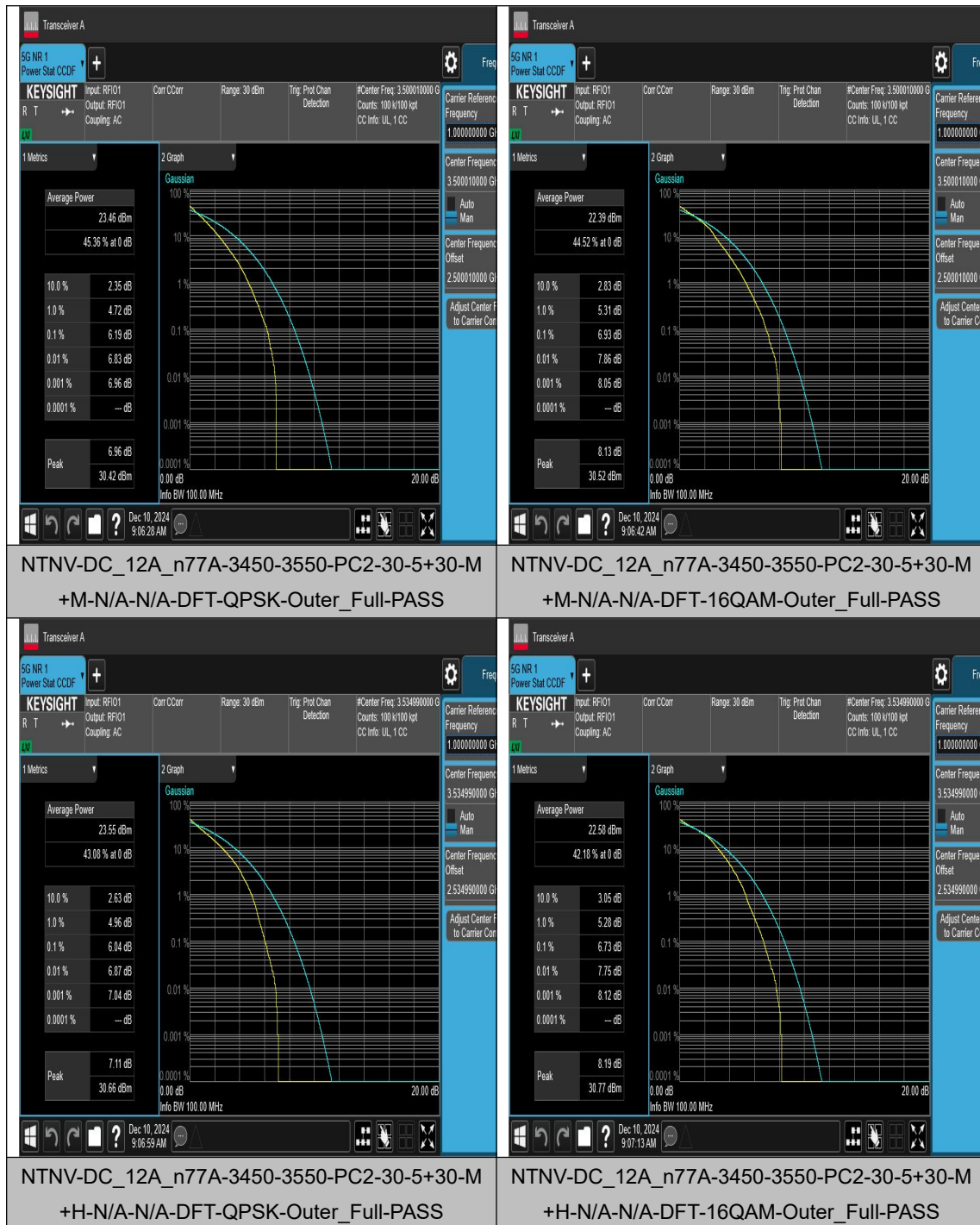
550								
DC_12A_n 77A-3450-3 550	30	5+40	DFT-QPSK	M+L	Outer_Full	6.48	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+40	DFT-16QAM	M+L	Outer_Full	6.80	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+40	DFT-QPSK	M+M	Outer_Full	6.64	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+40	DFT-16QAM	M+M	Outer_Full	6.89	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+40	DFT-QPSK	M+H	Outer_Full	5.77	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+40	DFT-16QAM	M+H	Outer_Full	6.56	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+60	DFT-QPSK	M+L	Outer_Full	6.15	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+60	DFT-16QAM	M+L	Outer_Full	6.75	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+60	DFT-QPSK	M+M	Outer_Full	5.74	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+60	DFT-16QAM	M+M	Outer_Full	6.59	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+60	DFT-QPSK	M+H	Outer_Full	5.94	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+60	DFT-16QAM	M+H	Outer_Full	6.79	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+80	DFT-QPSK	M+L	Outer_Full	6.03	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+80	DFT-16QAM	M+L	Outer_Full	6.81	≤13	PASS

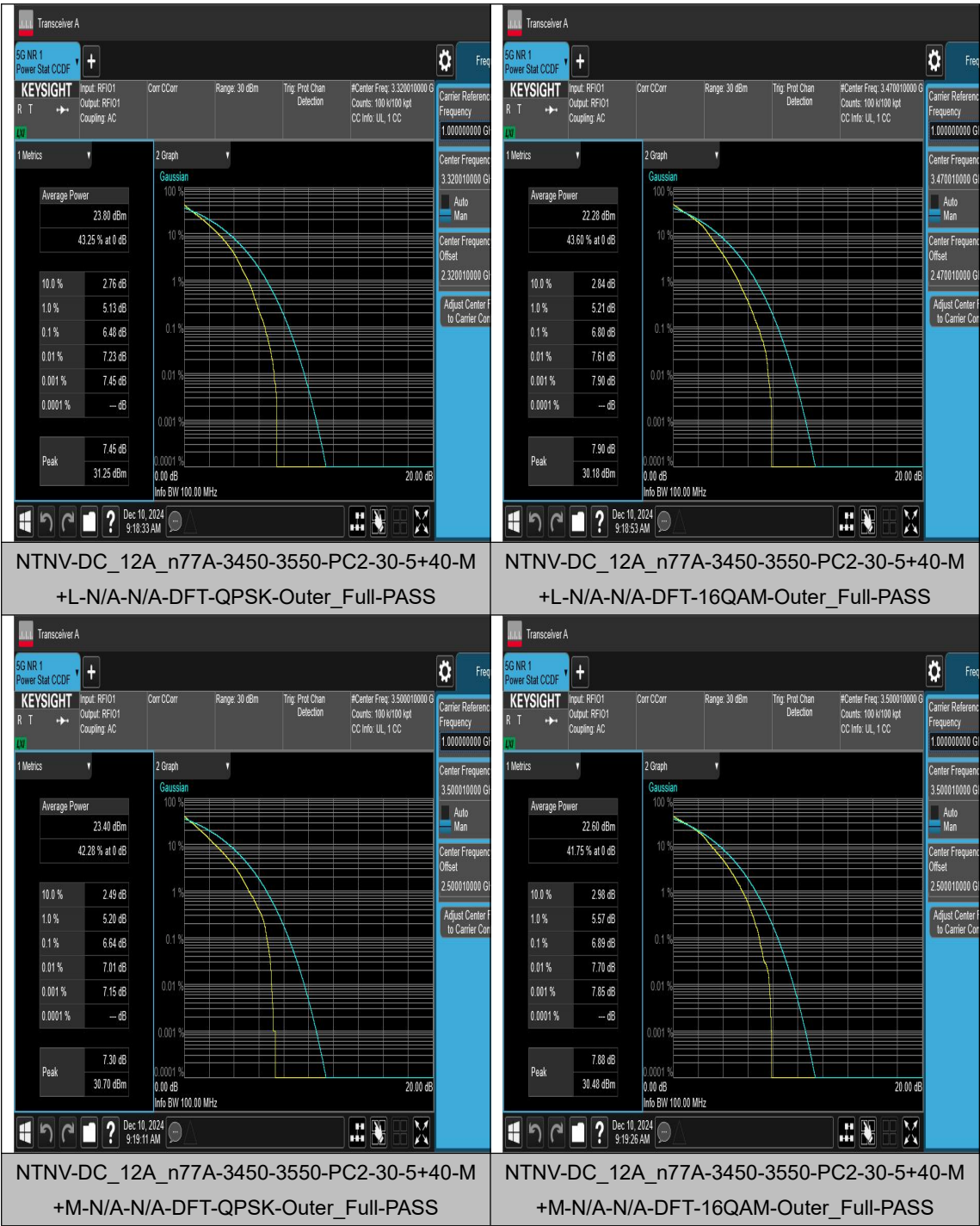
DC_12A_n 77A-3450-3 550	30	5+80	DFT-QPSK	M+M	Outer_Full	6.00	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+80	DFT-16QAM	M+M	Outer_Full	6.87	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+80	DFT-QPSK	M+H	Outer_Full	5.68	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+80	DFT-16QAM	M+H	Outer_Full	6.53	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+100	DFT-QPSK	M+L	Outer_Full	5.86	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+100	DFT-16QAM	M+L	Outer_Full	6.64	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+100	DFT-QPSK	M+M	Outer_Full	5.82	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+100	DFT-16QAM	M+M	Outer_Full	6.63	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+100	DFT-QPSK	M+H	Outer_Full	5.89	≤13	PASS
DC_12A_n 77A-3450-3 550	30	5+100	DFT-16QAM	M+H	Outer_Full	6.63	≤13	PASS

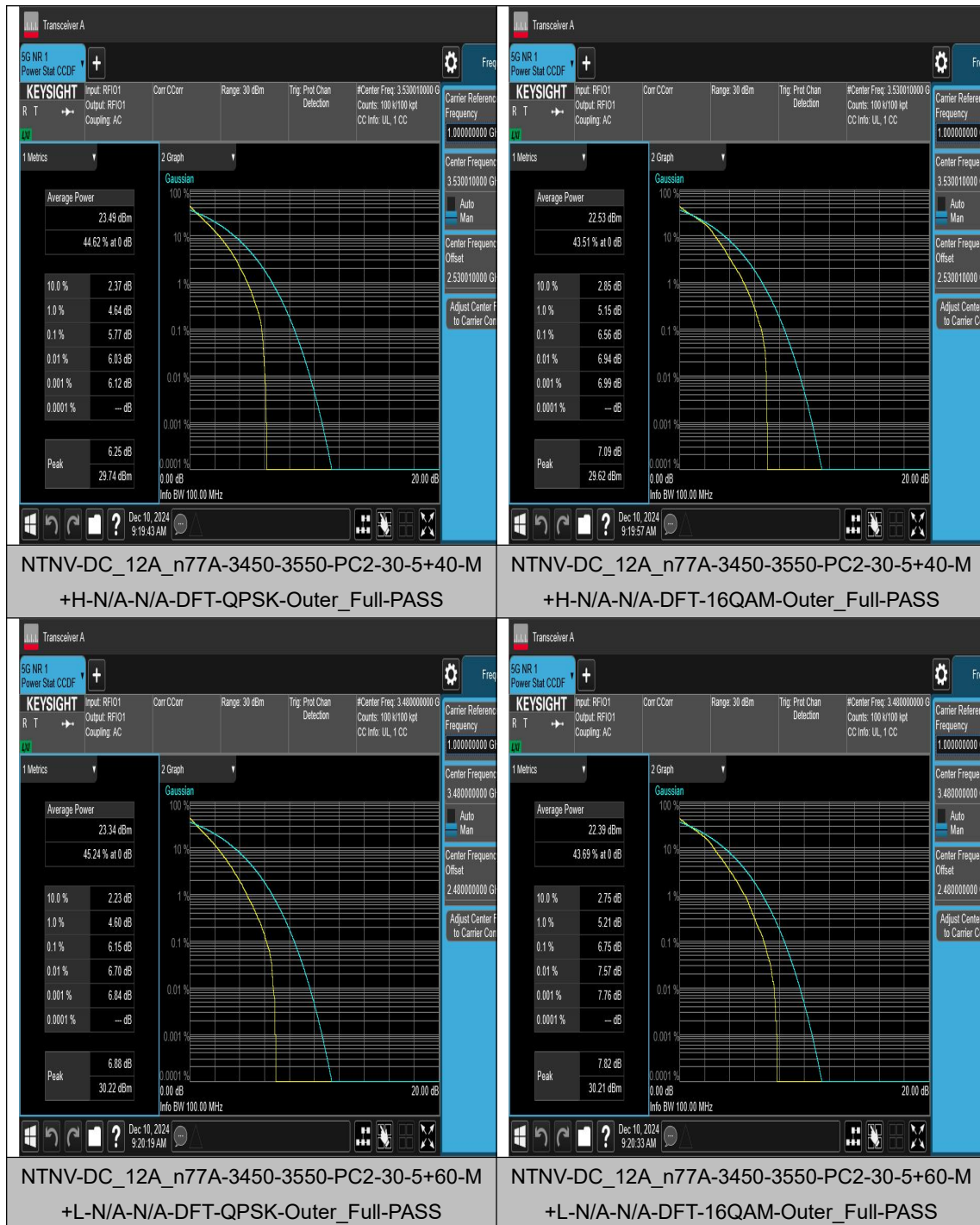
Test Graphs

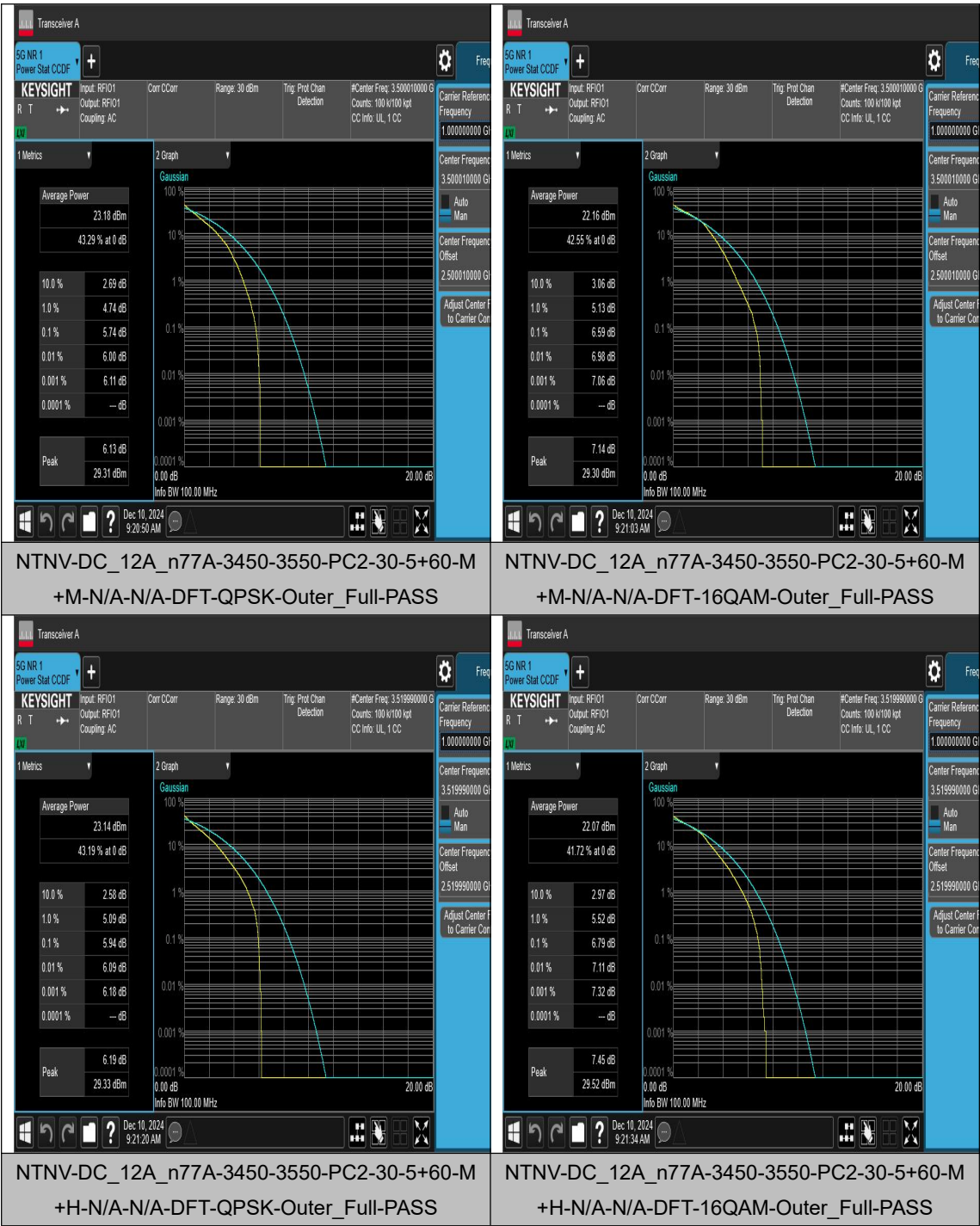


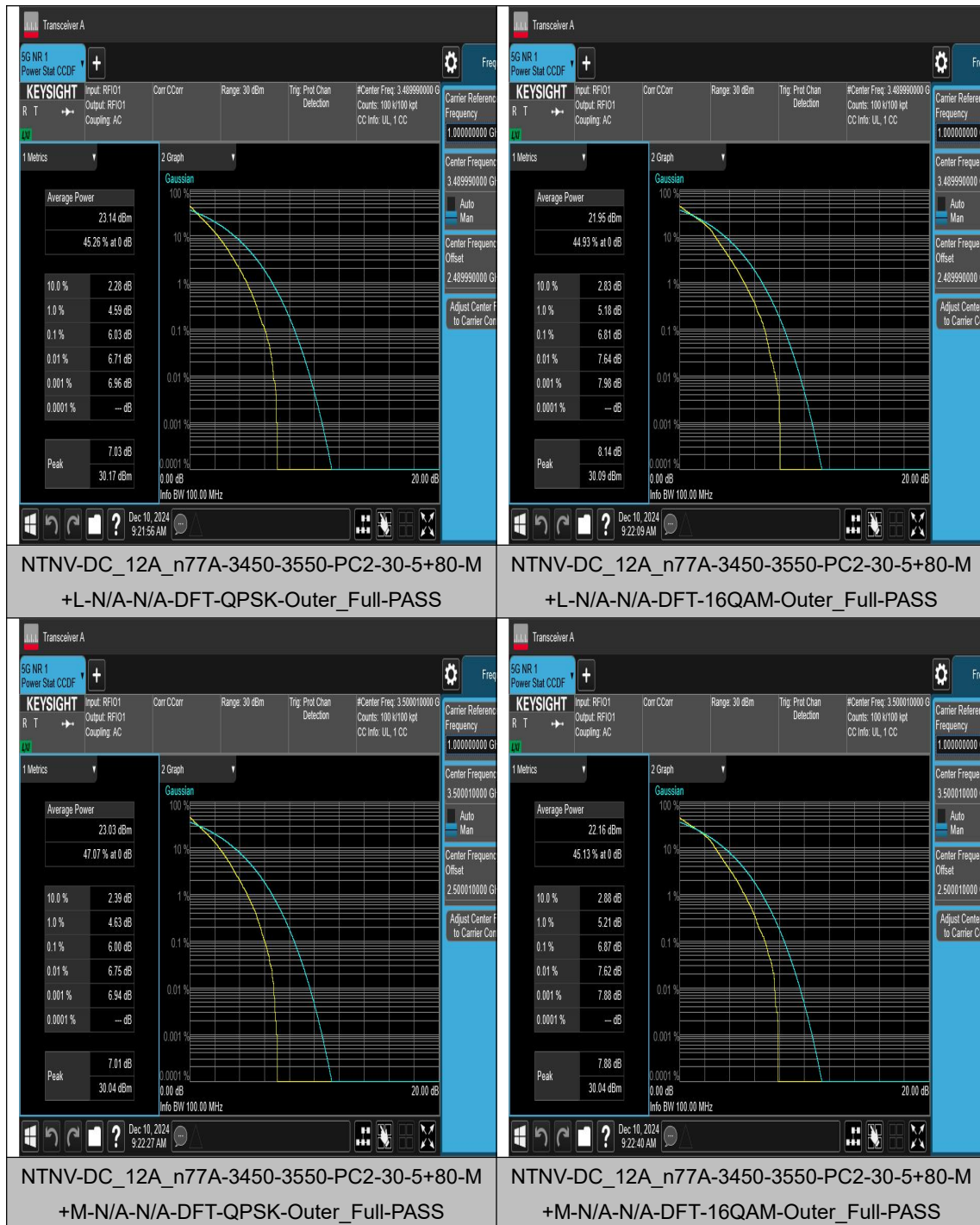


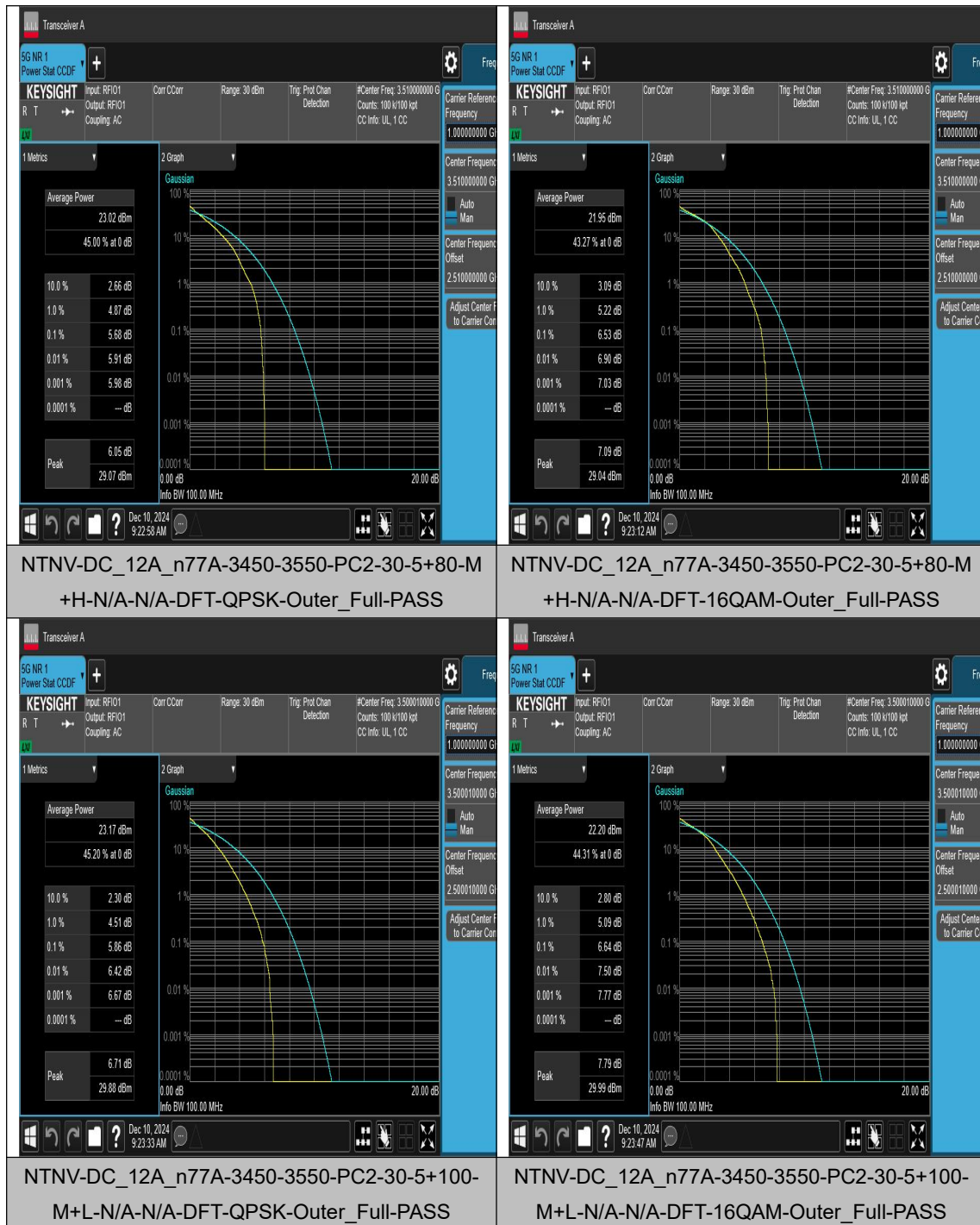


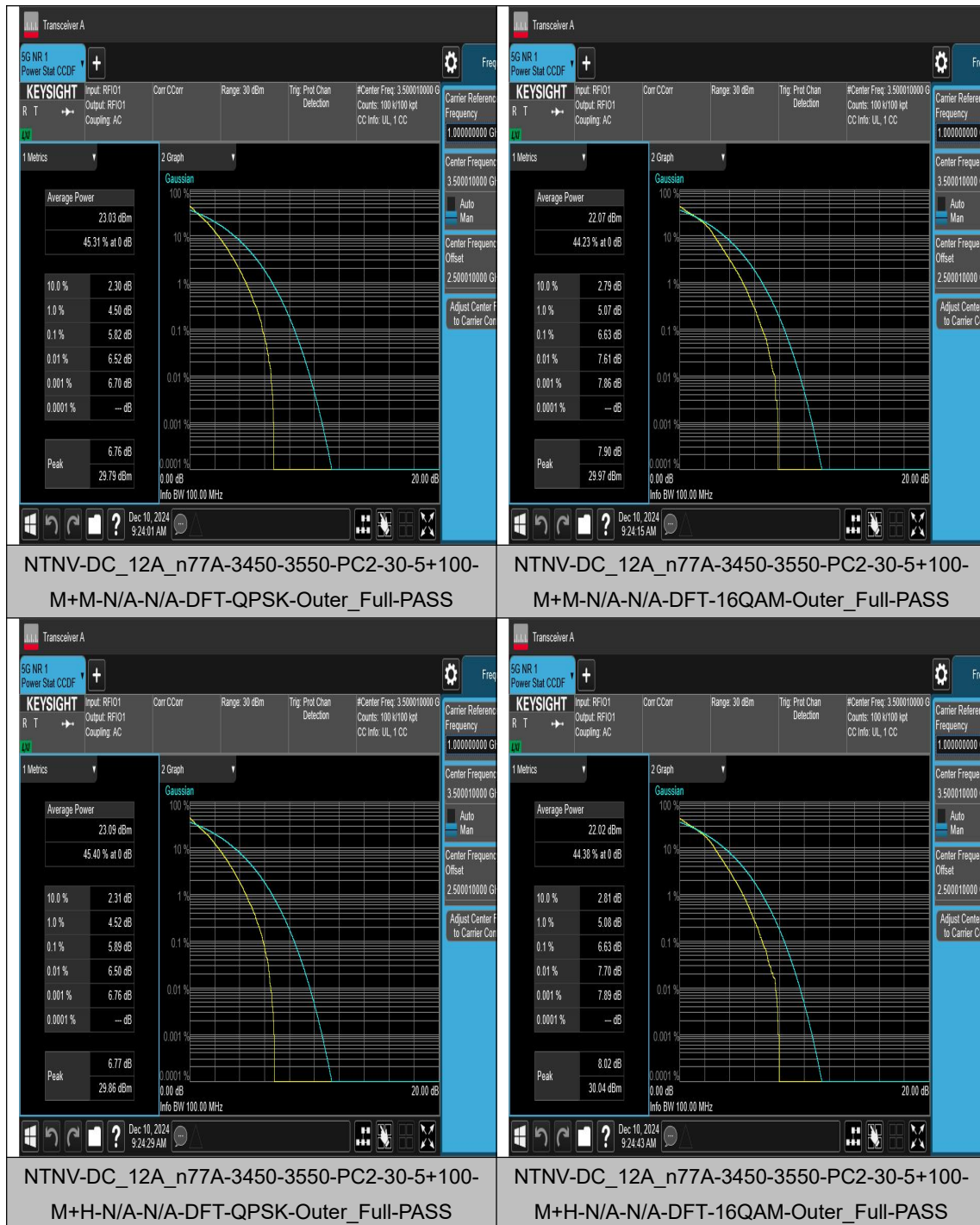












Appendix C: 26dB Bandwidth and Occupied Bandwidth for

NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_12A_n77A-3450-3 550	30	5+20	DFT-QPSK	M+L	Outer_Full	17.855	19.48	PASS
DC_12A_n77A-3450-3 550	30	5+20	DFT-16QAM	M+L	Outer_Full	17.923	19.21	PASS
DC_12A_n77A-3450-3 550	30	5+20	DFT-QPSK	M+M	Outer_Full	17.904	19.24	PASS
DC_12A_n77A-3450-3 550	30	5+20	DFT-16QAM	M+M	Outer_Full	17.882	19.11	PASS
DC_12A_n77A-3450-3 550	30	5+20	DFT-QPSK	M+H	Outer_Full	17.921	19.42	PASS
DC_12A_n77A-3450-3 550	30	5+20	DFT-16QAM	M+H	Outer_Full	17.863	19.42	PASS
DC_12A_n77A-3450-3 550	30	5+30	DFT-QPSK	M+L	Outer_Full	26.770	29.15	PASS
DC_12A_n77A-3450-3 550	30	5+30	DFT-16QAM	M+L	Outer_Full	26.797	28.78	PASS
DC_12A_n77A-3450-3 550	30	5+30	DFT-QPSK	M+M	Outer_Full	26.838	28.72	PASS
DC_12A_n77A-3450-3 550	30	5+30	DFT-16QAM	M+M	Outer_Full	26.793	28.60	PASS
DC_12A_n77A-3450-3 550	30	5+30	DFT-QPSK	M+H	Outer_Full	26.834	28.72	PASS
DC_12A_n77A-3450-3 550	30	5+30	DFT-16QAM	M+H	Outer_Full	26.696	28.48	PASS
DC_12A_n77A-3450-3 550	30	5+40	DFT-QPSK	M+L	Outer_Full	35.704	37.79	PASS
DC_12A_n77A-3450-3 550	30	5+40	DFT-16QAM	M+L	Outer_Full	35.686	37.84	PASS
DC_12A_n77A-3450-3 550	30	5+40	DFT-QPSK	M+M	Outer_Full	35.663	37.91	PASS
DC_12A_n77A-3450-3 550	30	5+40	DFT-16QAM	M+M	Outer_Full	35.629	37.83	PASS
DC_12A_n77A-3450-3 550	30	5+40	DFT-QPSK	M+H	Outer_Full	35.747	37.82	PASS

DC_12A_n77A-3450-3 550	30	5+40	DFT-16QAM	M+H	Outer_Full	35.683	37.72	PASS
DC_12A_n77A-3450-3 550	30	5+60	DFT-QPSK	M+L	Outer_Full	57.875	60.44	PASS
DC_12A_n77A-3450-3 550	30	5+60	DFT-16QAM	M+L	Outer_Full	57.777	60.28	PASS
DC_12A_n77A-3450-3 550	30	5+60	DFT-QPSK	M+M	Outer_Full	57.895	60.56	PASS
DC_12A_n77A-3450-3 550	30	5+60	DFT-16QAM	M+M	Outer_Full	57.902	60.50	PASS
DC_12A_n77A-3450-3 550	30	5+60	DFT-QPSK	M+H	Outer_Full	57.755	60.47	PASS
DC_12A_n77A-3450-3 550	30	5+60	DFT-16QAM	M+H	Outer_Full	57.852	60.14	PASS
DC_12A_n77A-3450-3 550	30	5+80	DFT-QPSK	M+L	Outer_Full	77.227	81.20	PASS
DC_12A_n77A-3450-3 550	30	5+80	DFT-16QAM	M+L	Outer_Full	76.928	80.05	PASS
DC_12A_n77A-3450-3 550	30	5+80	DFT-QPSK	M+M	Outer_Full	77.038	80.28	PASS
DC_12A_n77A-3450-3 550	30	5+80	DFT-16QAM	M+M	Outer_Full	77.068	80.93	PASS
DC_12A_n77A-3450-3 550	30	5+80	DFT-QPSK	M+H	Outer_Full	76.933	80.13	PASS
DC_12A_n77A-3450-3 550	30	5+80	DFT-16QAM	M+H	Outer_Full	77.197	80.46	PASS
DC_12A_n77A-3450-3 550	30	5+100	DFT-QPSK	M+L	Outer_Full	96.600	101.3	PASS
DC_12A_n77A-3450-3 550	30	5+100	DFT-16QAM	M+L	Outer_Full	96.212	99.89	PASS
DC_12A_n77A-3450-3 550	30	5+100	DFT-QPSK	M+M	Outer_Full	96.547	100.8	PASS
DC_12A_n77A-3450-3 550	30	5+100	DFT-16QAM	M+M	Outer_Full	96.188	99.87	PASS
DC_12A_n77A-3450-3 550	30	5+100	DFT-QPSK	M+H	Outer_Full	96.496	99.92	PASS
DC_12A_n77A-3450-3 550	30	5+100	DFT-16QAM	M+H	Outer_Full	96.172	99.86	PASS

Test Graphs

