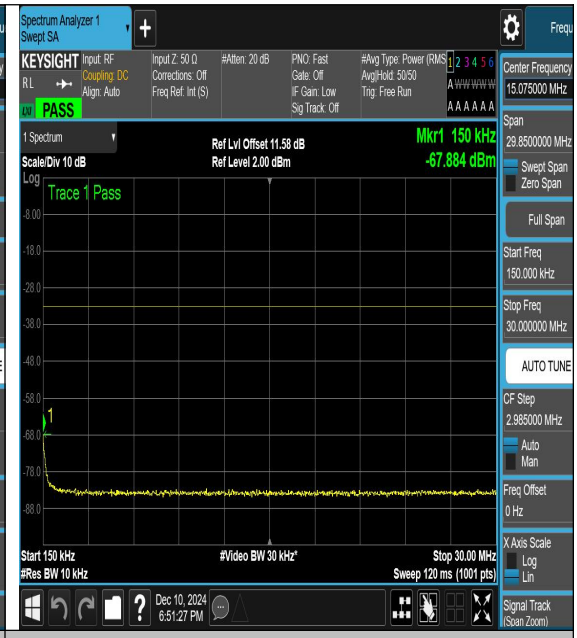
Test Graphs

NTNV-DC_5A_n78A-3700-3800-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Outer_Full-0.009-0.15-PASS



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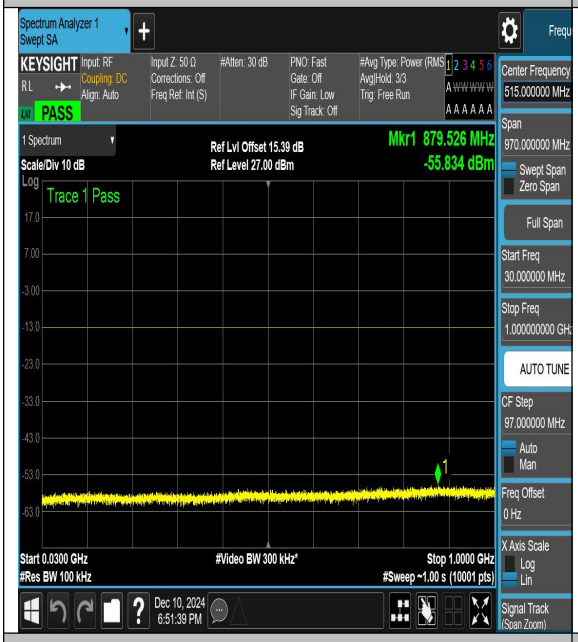

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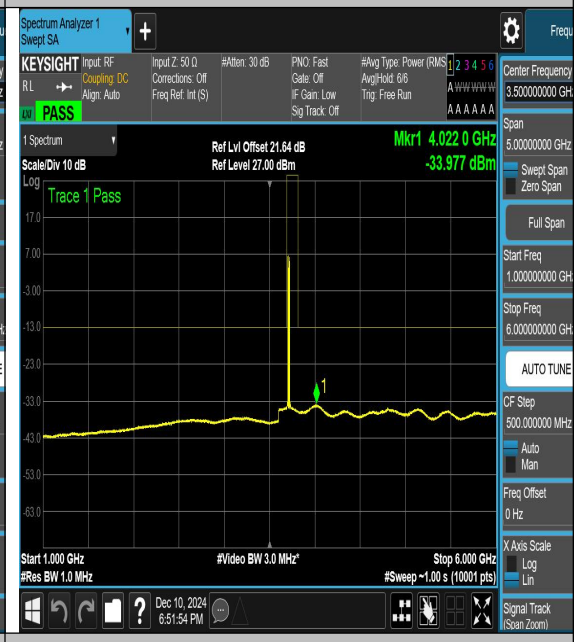

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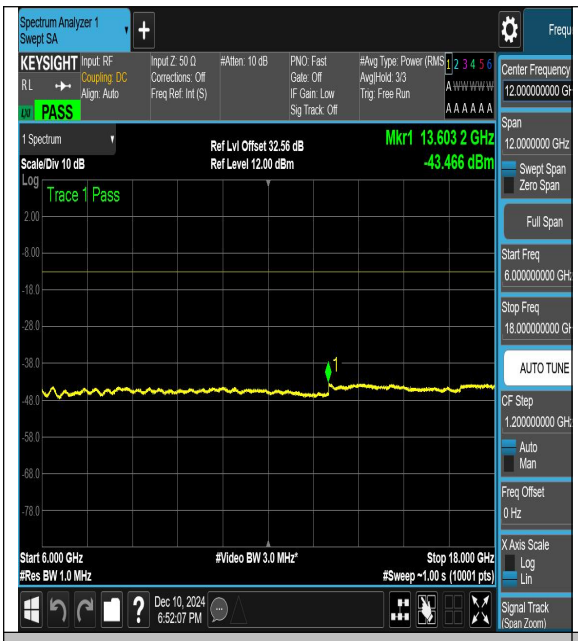

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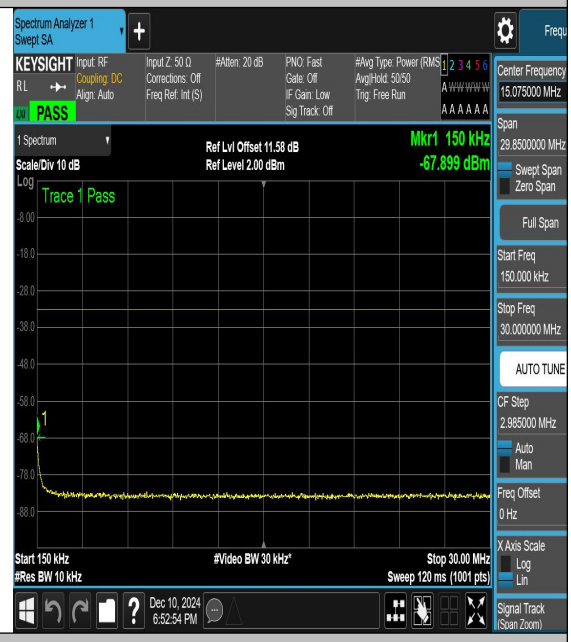
NTNV-DC_5A_n78A-3700-3800-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Outer_Full-18000-40000-PASS

NTNV-DC_5A_n78A-3700-3800-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Edge_1RB_Left-0.009-0.15-PASS



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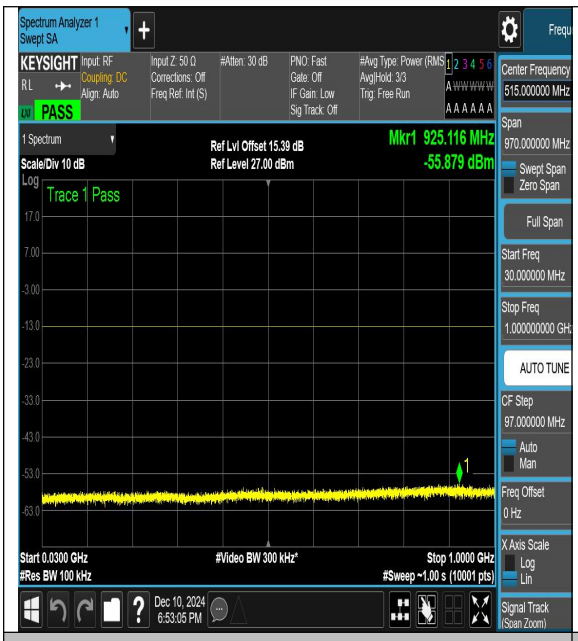

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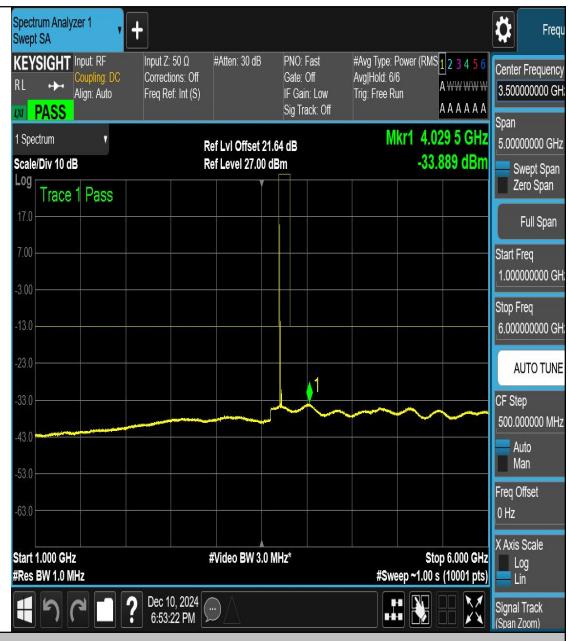

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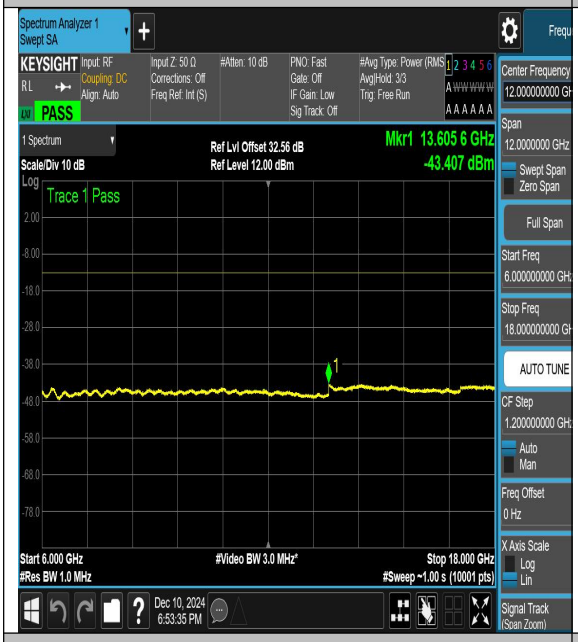

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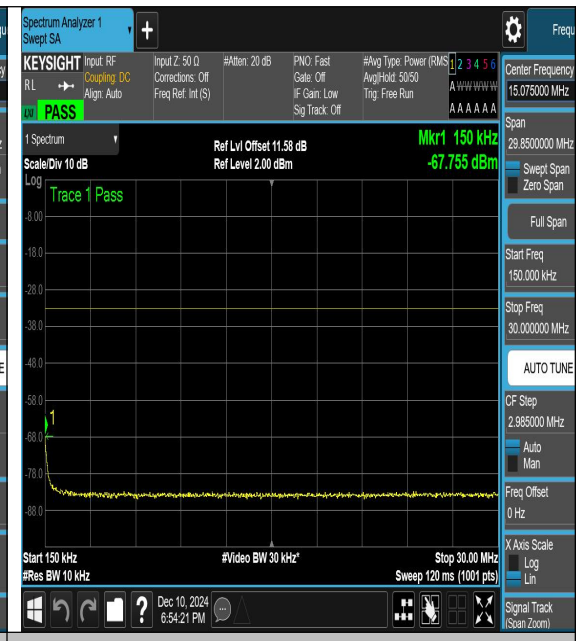
NTNV-DC_5A_n78A-3700-3800-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Edge_1RB_Left-18000-40000-PASS

NTNV-DC_5A_n78A-3700-3800-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Edge_1RB_Right-0.009-0.15-PASS



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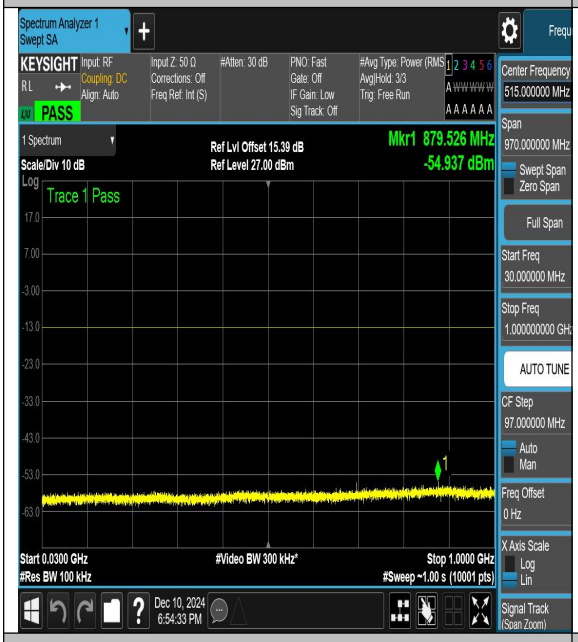

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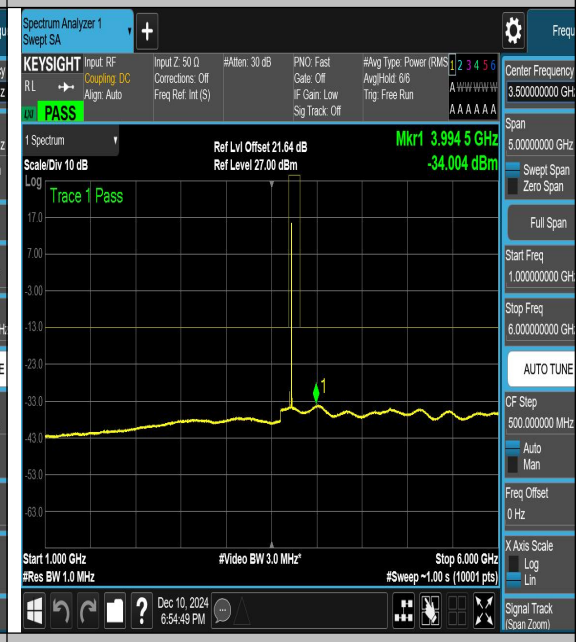

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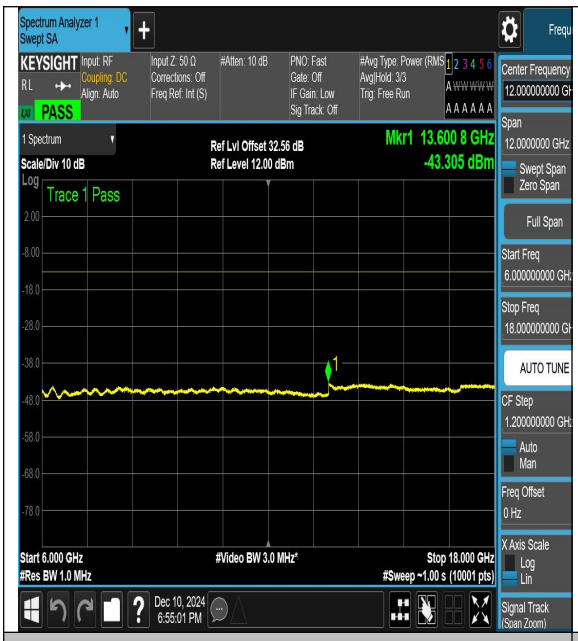

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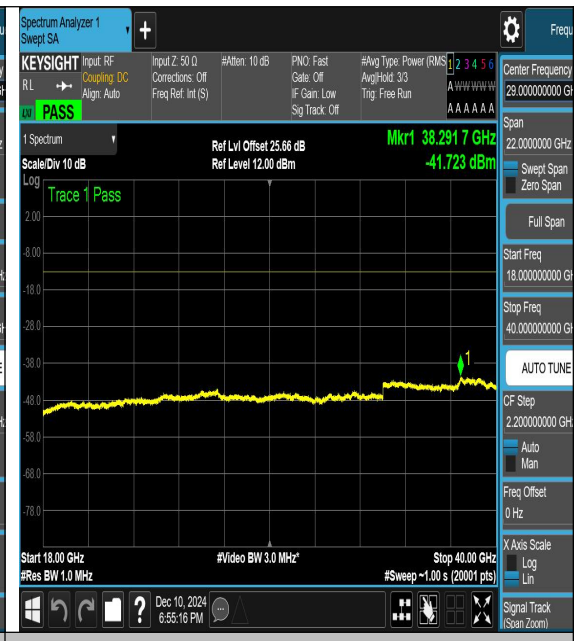

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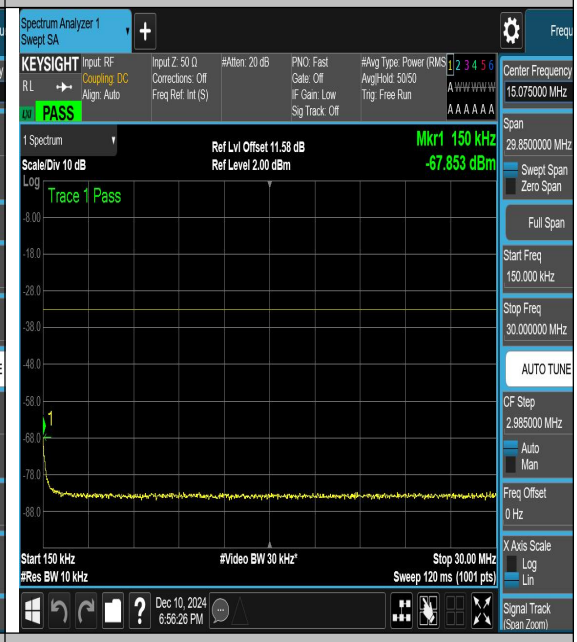
NTNV-DC_5A_n78A-3700-3800-PC2-30-5+20-M+M-N/A-N/A-DFT-QPSK-Outer_Full-0.009-0.15-PASS



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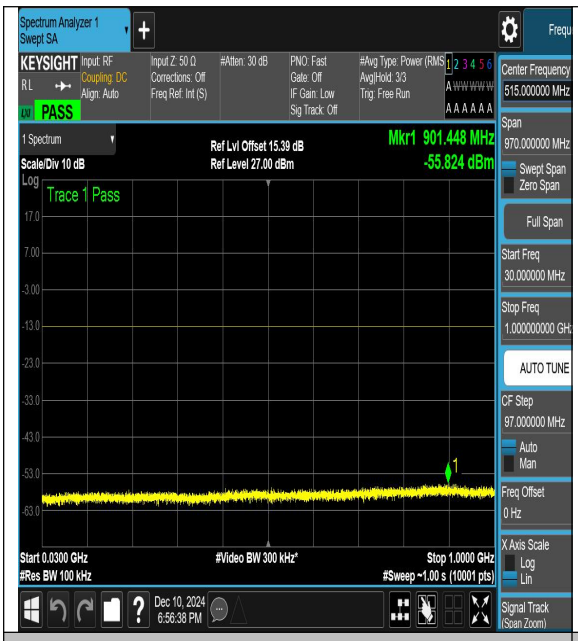

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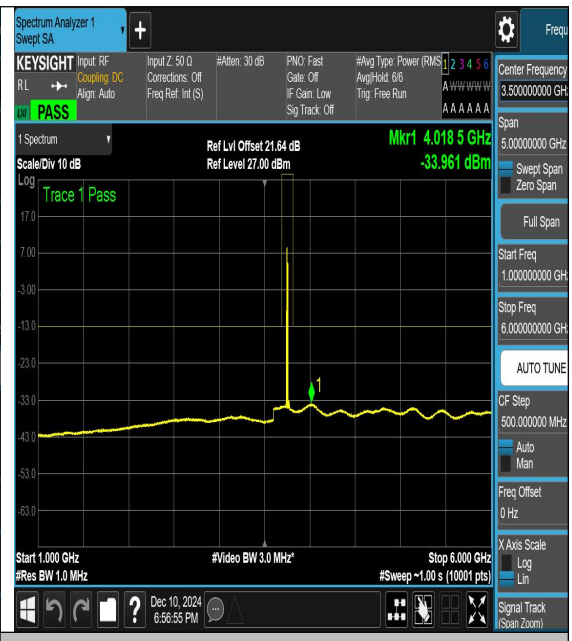

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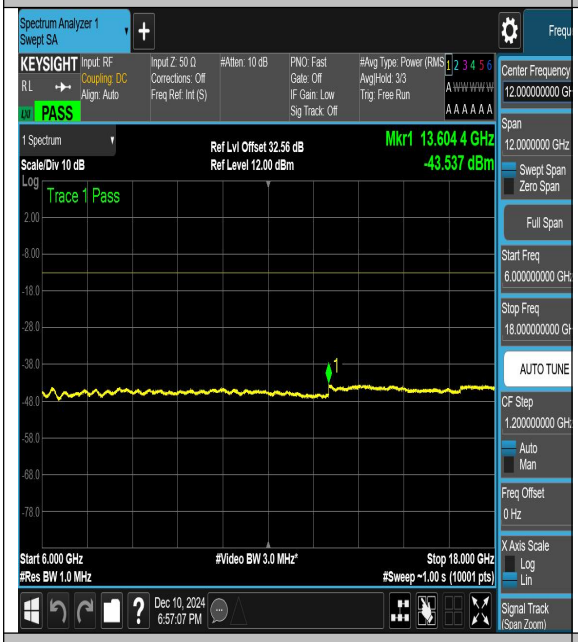

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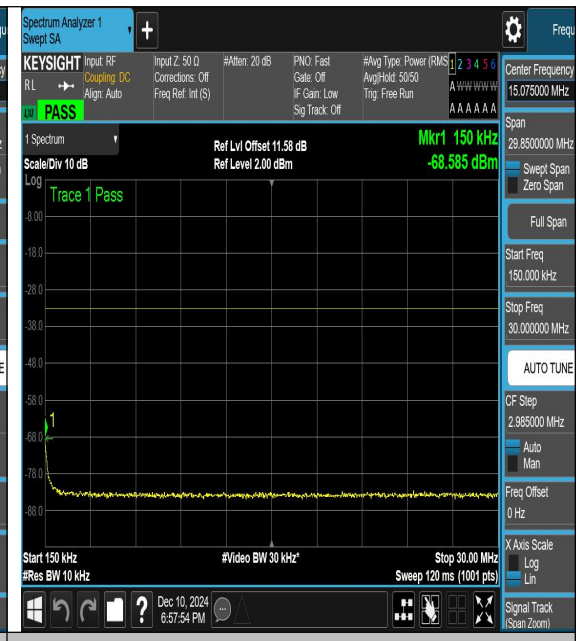
NTNV-DC_5A_n78A-3700-3800-PC2-30-5+20-M+M-N/A-N/A-DFT-QPSK-Edge_1RB_Left-0.009-0.15-PASS



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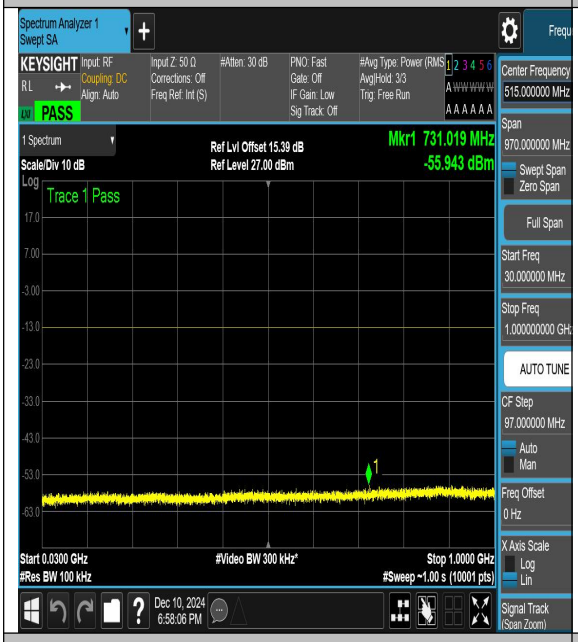

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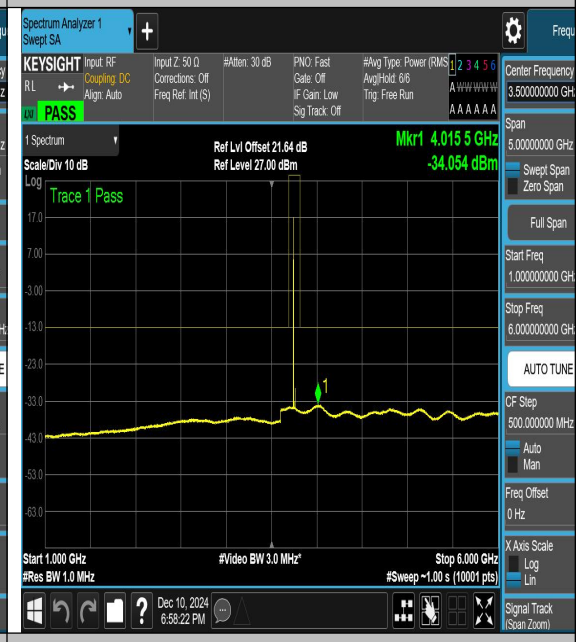

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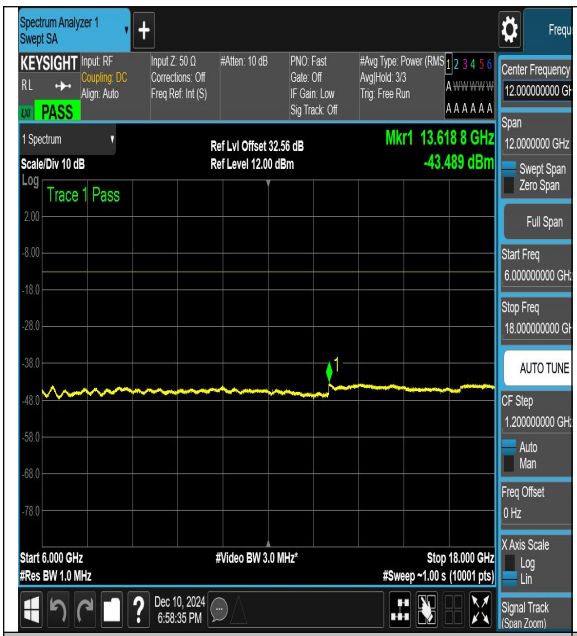

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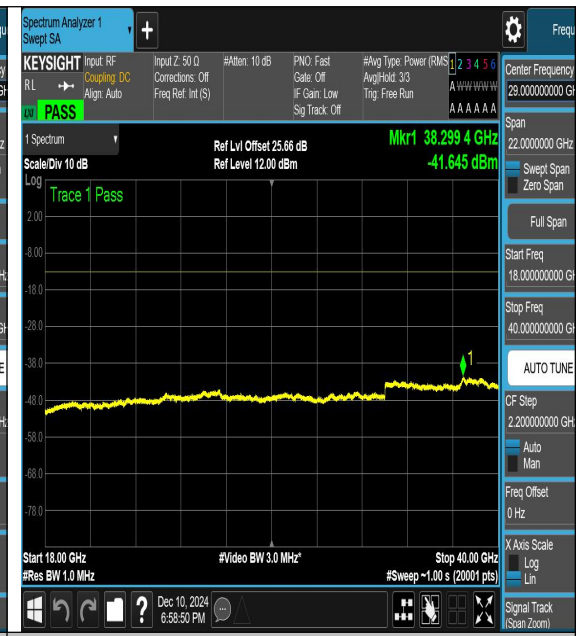
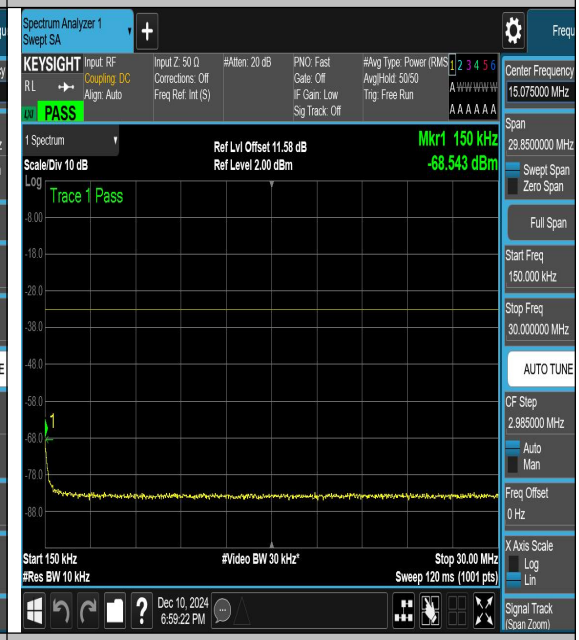


NTNV-DC_5A_n78A-3700-3800-PC2-30-5+20-M+M-N/A-N/A-DFT-QPSK-Edge_1RB_Left-18000-40000-PASS

NTNV-DC_5A_n78A-3700-3800-PC2-30-5+20-M+M-N/A-N/A-DFT-QPSK-Edge_1RB_Right-0.009-0.15-PASS



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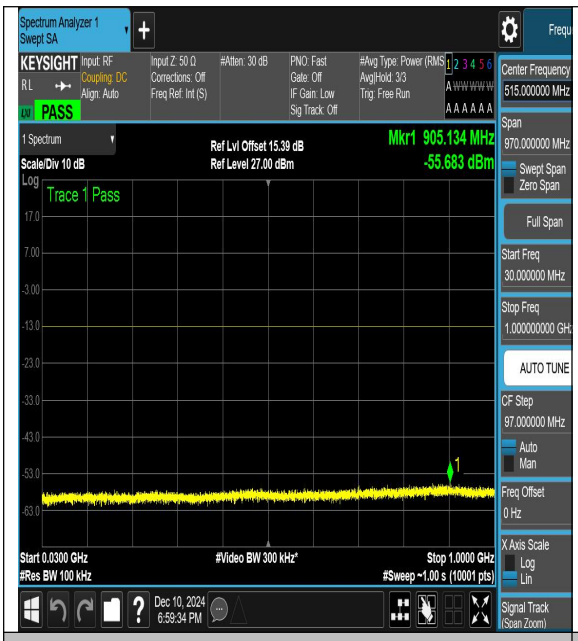

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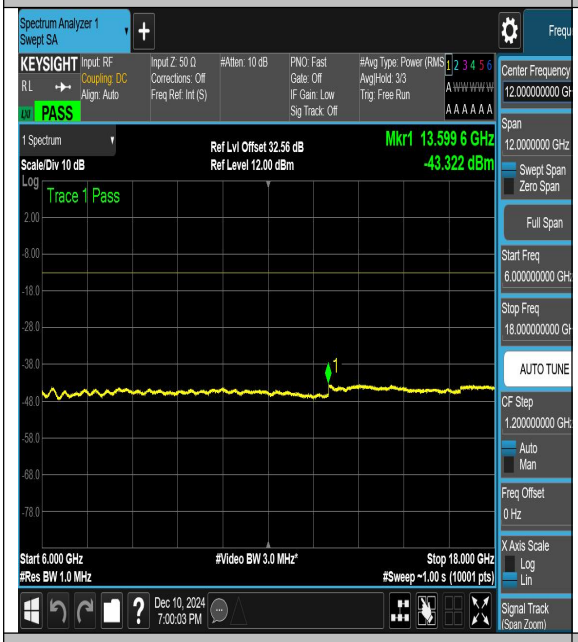

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NTNV-DC_5A_n78A-3700-3800-PC2-30-5+20-M+M-N/A-N/A-DFT-QPSK-Edge_1RB_Right-18000-40000-PASS