

Test Graphs



















Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-58.42	-43	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-67.88	-33	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-55.83	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.98	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	18000	-43.47	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	18000	40000	-41.60	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-58.45	-43	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-67.90	-33	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.88	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.89	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-43.41	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-41.70	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-58.58	-43	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.75	-33	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.94	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-34.00	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-43.30	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-41.72	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-58.40	-43	PASS

3700-3800										
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.85	-33	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-55.82	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.96	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	18000	-43.54	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	18000	40000	-41.75	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-58.11	-43	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-68.59	-33	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.94	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-34.05	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-43.49	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-41.65	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-58.14	-43	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-68.54	-33	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.68	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.95	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-43.32	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-41.65	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-57.66	-43	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-67.97	-33	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-55.54	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.91	-13	PASS

DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	18000	-43.28	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	18000	40000	-41.72	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-57.98	-43	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-68.25	-33	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.46	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-34.06	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-43.35	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-41.52	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-57.83	-43	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-67.56	-33	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.60	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-34.12	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-43.17	-13	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-41.57	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-58.32	-43	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	0.15	30	-68.76	-33	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	30	1000	-55.20	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.96	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	6000	18000	-43.43	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	18000	40000	-41.71	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-58.81	-43	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-68.43	-33	PASS

3700-3800										
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.60	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-34.04	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-43.12	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-41.41	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-58.21	-43	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-68.58	-33	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.57	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-34.03	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-43.41	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-41.70	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-58.61	-43	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Outer_Full	0.15	30	-68.54	-33	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Outer_Full	30	1000	-55.97	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Outer_Full	1000	6000	-34.10	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Outer_Full	6000	18000	-43.22	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Outer_Full	18000	40000	-41.68	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-58.32	-43	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-68.23	-33	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.74	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-33.90	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-43.46	-13	PASS

DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-41.60	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-58.54	-43	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-68.75	-33	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.11	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-34.04	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-43.31	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-41.55	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-58.38	-43	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	0.15	30	-68.13	-33	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	30	1000	-55.90	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.94	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	6000	18000	-43.38	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	18000	40000	-41.41	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-57.94	-43	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-68.30	-33	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.60	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-34.04	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-43.38	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-41.55	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-58.05	-43	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-68.12	-33	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.94	-13	PASS

3700-3800										
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-34.04	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-43.35	-13	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-41.61	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-59.02	-43	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-68.69	-33	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-55.80	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	6000	-34.03	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	18000	-43.32	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	18000	40000	-41.55	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-58.23	-43	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-68.44	-33	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.06	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-34.03	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-43.30	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-41.56	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-59.01	-43	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-68.24	-33	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.43	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-34.03	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-43.30	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-41.62	-13	PASS

DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-59.31	-43	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-68.47	-33	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-55.74	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	6000	-34.04	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	18000	-43.16	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	18000	40000	-41.13	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-58.53	-43	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-68.32	-33	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.86	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-34.04	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-43.22	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-41.71	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-57.78	-43	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-67.99	-33	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.76	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.87	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-43.42	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-41.42	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-58.28	-43	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-68.17	-33	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-54.30	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	6000	-33.99	-13	PASS

3700-3800										
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	18000	-43.32	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	18000	40000	-41.39	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-57.89	-43	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-68.10	-33	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.33	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.99	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-43.43	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-41.67	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-58.10	-43	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-68.97	-33	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-33.23	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.94	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-43.46	-13	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-41.51	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-58.61	-43	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-67.27	-33	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-55.84	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	6000	-34.04	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	18000	-43.29	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	18000	40000	-41.69	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-58.35	-43	PASS

DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-68.12	-33	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.44	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-34.04	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-43.38	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-41.33	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-58.22	-43	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-68.32	-33	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.76	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-34.01	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-43.29	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-41.71	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-58.36	-43	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.94	-33	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-55.80	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.99	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	18000	-43.26	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	18000	40000	-41.72	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-59.09	-43	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-68.10	-33	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.59	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-34.04	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-43.24	-13	PASS

3700-3800										
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-41.57	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-58.57	-43	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-67.82	-33	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.98	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.96	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-43.16	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-41.45	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-58.47	-43	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-67.81	-33	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-55.93	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	6000	-34.04	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	18000	-43.45	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	18000	40000	-41.54	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-58.99	-43	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-68.21	-33	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.61	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-34.01	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-43.31	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-41.62	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-58.45	-43	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-67.99	-33	PASS

DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.58	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-34.02	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-43.48	-13	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-41.69	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-58.11	-43	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-68.56	-33	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-55.81	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	6000	-34.06	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	18000	-43.33	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	18000	40000	-41.62	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-58.34	-43	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-68.48	-33	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.30	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.94	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-43.45	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-41.58	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-57.37	-43	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.57	-33	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.70	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-33.98	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-43.47	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-41.66	-13	PASS

3700-3800										
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-58.83	-43	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-68.02	-33	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-43.41	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.93	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	18000	-43.39	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	18000	40000	-41.70	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-58.63	-43	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-68.49	-33	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.71	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-34.06	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-43.30	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-41.62	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-58.74	-43	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-68.24	-33	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.75	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.95	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-43.37	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-41.29	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-58.01	-43	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-67.97	-33	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-55.90	-13	PASS

DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	6000	-34.09	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	18000	-43.21	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	18000	40000	-41.62	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-58.18	-43	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.80	-33	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.82	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-34.07	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-43.26	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-41.66	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-58.31	-43	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-68.36	-33	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.81	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-34.07	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-43.29	-13	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-41.66	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-58.21	-43	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	0.15	30	-67.97	-33	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	30	1000	-55.44	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	1000	6000	-34.06	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	6000	18000	-43.44	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	18000	40000	-41.73	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-58.70	-43	PASS

3700-3800										
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-68.13	-33	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.44	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-34.03	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-43.17	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-41.58	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-58.50	-43	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.90	-33	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-56.02	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-34.05	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-43.31	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-41.52	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-57.98	-43	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	0.15	30	-68.39	-33	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	30	1000	-55.89	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	1000	6000	-33.97	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	6000	18000	-43.31	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	18000	40000	-41.63	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-58.37	-43	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-68.05	-33	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.41	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-34.00	-13	PASS

DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-43.25	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-41.59	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-58.32	-43	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-68.15	-33	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.68	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-33.91	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-43.22	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-41.64	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-58.01	-43	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	0.15	30	-67.34	-33	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	30	1000	-55.92	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	1000	6000	-34.06	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	6000	18000	-43.03	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	18000	40000	-41.75	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-58.14	-43	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-68.02	-33	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.85	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-34.07	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-43.40	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-41.82	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-57.97	-43	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-67.79	-33	PASS

3700-3800										
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.87	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-33.93	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-43.38	-13	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-41.32	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-58.01	-43	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-68.16	-33	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-55.90	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	6000	-33.98	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	18000	-43.40	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	18000	40000	-41.57	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-57.89	-43	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-67.78	-33	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.23	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-34.00	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-43.44	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-41.46	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-58.32	-43	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-68.18	-33	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.91	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-34.11	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-43.46	-13	PASS

DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-41.37	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-58.91	-43	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-68.55	-33	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-56.03	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	6000	-34.18	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	18000	-43.47	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	18000	40000	-41.51	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-58.75	-43	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-68.46	-33	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.80	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-34.11	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-43.29	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-41.38	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-58.44	-43	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-68.23	-33	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.29	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-34.05	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-43.40	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-41.92	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-58.11	-43	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-68.25	-33	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-55.97	-13	PASS

3700-3800										
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	6000	-34.14	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	18000	-43.38	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	18000	40000	-41.48	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-58.15	-43	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.79	-33	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.55	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-34.14	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-43.28	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-41.84	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-58.47	-43	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-68.33	-33	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.78	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-34.16	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-43.49	-13	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-41.69	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-58.37	-43	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-68.58	-33	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-55.07	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	6000	-34.19	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	18000	-43.20	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	18000	40000	-41.59	-13	PASS

DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-58.83	-43	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-68.69	-33	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.67	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-33.94	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-43.30	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-41.67	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-58.07	-43	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-68.33	-33	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.98	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-34.17	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-43.42	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-41.56	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-58.37	-43	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-68.02	-33	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-55.84	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	6000	-34.20	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	18000	-43.27	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	18000	40000	-41.73	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-58.57	-43	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-68.55	-33	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.38	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-34.10	-13	PASS

3700-3800										
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-43.34	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-41.69	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-58.48	-43	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-67.79	-33	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.87	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-34.06	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-43.35	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-41.69	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-58.37	-43	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-67.86	-33	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-55.80	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	6000	-34.04	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	18000	-43.44	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	18000	40000	-41.81	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-58.13	-43	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-68.31	-33	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-46.80	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-34.10	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-43.41	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-41.59	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-58.19	-43	PASS

DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-68.49	-33	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-56.10	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-34.12	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-43.36	-13	PASS
DC_5A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-41.71	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-58.20	-43	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.15	30	-68.47	-33	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-55.63	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	1000	6000	-34.11	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	6000	18000	-43.36	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	18000	40000	-41.71	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-58.38	-43	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-68.45	-33	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.84	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-34.10	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-43.53	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-41.76	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-57.88	-43	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-68.41	-33	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-56.09	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-34.23	-13	PASS
DC_5A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-43.32	-13	PASS

3700-3800		0								
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-41.50	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-57.95	-43	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.94	-33	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-55.65	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	6000	-34.02	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	18000	-43.40	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	18000	40000	-41.59	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-58.74	-43	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-68.38	-33	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-56.06	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-34.08	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-43.41	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-41.60	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-57.25	-43	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-68.45	-33	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.89	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-34.15	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-43.44	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-41.71	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-58.56	-43	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-67.64	-33	PASS

DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-55.72	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	6000	-34.07	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	18000	-43.46	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	18000	40000	-41.57	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-58.36	-43	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-68.60	-33	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.55	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-33.99	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-43.43	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-41.71	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-58.69	-43	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-67.65	-33	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.52	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-34.01	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-43.43	-13	PASS
DC_5A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-41.41	-13	PASS