

Appendix for DC_5A_n78A(3700-3800MHz)

Product Name: Rugged handheld

Model No: UA60

Appendix A: Average Power Output Data for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_5A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	24.17	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Inner_Full	25.19	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.91	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	24.38	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	23.06	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Inner_Full	24.18	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	22.90	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	23.32	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	24.87	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Inner_Full	25.74	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	24.90	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	24.90	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	23.82	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Inner_Full	25.01	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	23.60	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	23.82	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	24.60	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Inner_Full	25.61	PC2	PASS

DC_5A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	24.58	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	24.67	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	23.77	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Inner_Full	24.74	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	23.94	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	23.81	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	24.11	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-QPSK	M+L	Inner_Full	25.19	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	24.21	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	24.33	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-16QAM	M+L	Outer_Full	23.09	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-16QAM	M+L	Inner_Full	24.19	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	23.43	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	23.33	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-QPSK	M+M	Outer_Full	24.87	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-QPSK	M+M	Inner_Full	25.83	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	24.46	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	24.99	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-16QAM	M+M	Outer_Full	24.04	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-16QAM	M+M	Inner_Full	24.71	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	23.68	PC2	PASS
DC_5A_n78A-37	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	24.06	PC2	PASS

00-3800								
DC_5A_n78A-37 00-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	24.58	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-QPSK	M+H	Inner_Full	25.56	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	24.78	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	24.62	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-16QAM	M+H	Outer_Full	23.79	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-16QAM	M+H	Inner_Full	24.76	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	23.86	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	23.48	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	24.16	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Inner_Full	25.38	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	24.18	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	24.44	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	23.37	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Inner_Full	24.25	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	23.44	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	23.61	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	25.12	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Inner_Full	25.99	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	24.84	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	25.29	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	24.02	PC2	PASS

DC_5A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Inner_Full	25.06	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	23.85	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	23.91	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	24.92	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Inner_Full	25.63	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	24.69	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	24.61	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	23.70	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Inner_Full	24.79	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	23.61	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	23.61	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	23.86	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Inner_Full	24.83	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	23.79	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	24.04	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	22.94	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Inner_Full	24.20	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	22.86	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	23.18	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	24.75	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Inner_Full	25.81	PC2	PASS
DC_5A_n78A-37	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	24.38	PC2	PASS

00-3800								
DC_5A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	24.67	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	23.75	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Inner_Full	24.63	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	23.62	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	23.82	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	24.34	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Inner_Full	25.65	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	24.53	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	24.55	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	23.61	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Inner_Full	24.67	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	23.55	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	23.44	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	23.97	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Inner_Full	25.23	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	23.68	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	24.19	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	22.95	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Inner_Full	24.04	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	23.12	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	23.45	PC2	PASS

DC_5A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	24.52	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Inner_Full	25.55	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	23.99	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	24.53	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	23.59	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Inner_Full	24.57	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	23.31	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	23.56	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	24.31	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Inner_Full	25.47	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	24.25	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	24.25	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	23.62	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Inner_Full	24.39	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	23.24	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	23.23	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	24.34	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Inner_Full	25.40	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	23.97	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	24.16	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	23.35	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Inner_Full	24.24	PC2	PASS

00-3800								
DC_5A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	23.03	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	23.24	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	24.57	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Inner_Full	25.86	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	24.13	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	24.61	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	23.59	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Inner_Full	24.53	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	23.17	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	23.74	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	24.27	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Inner_Full	25.34	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	24.43	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	24.42	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	23.36	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Inner_Full	24.34	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	23.40	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	23.23	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	24.60	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Inner_Full	25.74	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	24.25	PC2	PASS

DC_5A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	24.60	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	23.38	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Inner_Full	24.48	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	23.26	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	23.97	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	24.48	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Inner_Full	25.54	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.95	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	24.69	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	23.76	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Inner_Full	24.52	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	22.96	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	23.79	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	24.49	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Inner_Full	25.66	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	24.10	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	24.36	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	23.50	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Inner_Full	24.85	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	23.14	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	23.66	PC2	PASS
DC_5A_n78A-37	30	5+90	DFT-QPSK	M+L	Outer_Full	24.47	PC2	PASS

00-3800								
DC_5A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Inner_Full	25.83	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	24.20	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	24.56	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	23.76	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Inner_Full	24.83	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	23.06	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	23.62	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	24.45	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Inner_Full	25.63	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	24.07	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	24.56	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	23.57	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Inner_Full	24.53	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	23.03	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	23.52	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	24.47	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Inner_Full	25.84	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	24.21	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	24.51	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	23.56	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Inner_Full	24.87	PC2	PASS

DC_5A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	23.19	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	23.52	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	24.47	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Inner_Full	25.57	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	23.79	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	24.25	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Outer_Full	23.69	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Inner_Full	24.86	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	23.04	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	23.52	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Outer_Full	24.46	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Inner_Full	26.03	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	23.79	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	24.26	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Outer_Full	23.47	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Inner_Full	24.53	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	23.03	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	23.50	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	24.47	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Inner_Full	25.66	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	24.02	PC2	PASS
DC_5A_n78A-37	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	24.24	PC2	PASS

00-3800								
DC_5A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	23.49	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Inner_Full	24.56	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	23.24	PC2	PASS
DC_5A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	23.51	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

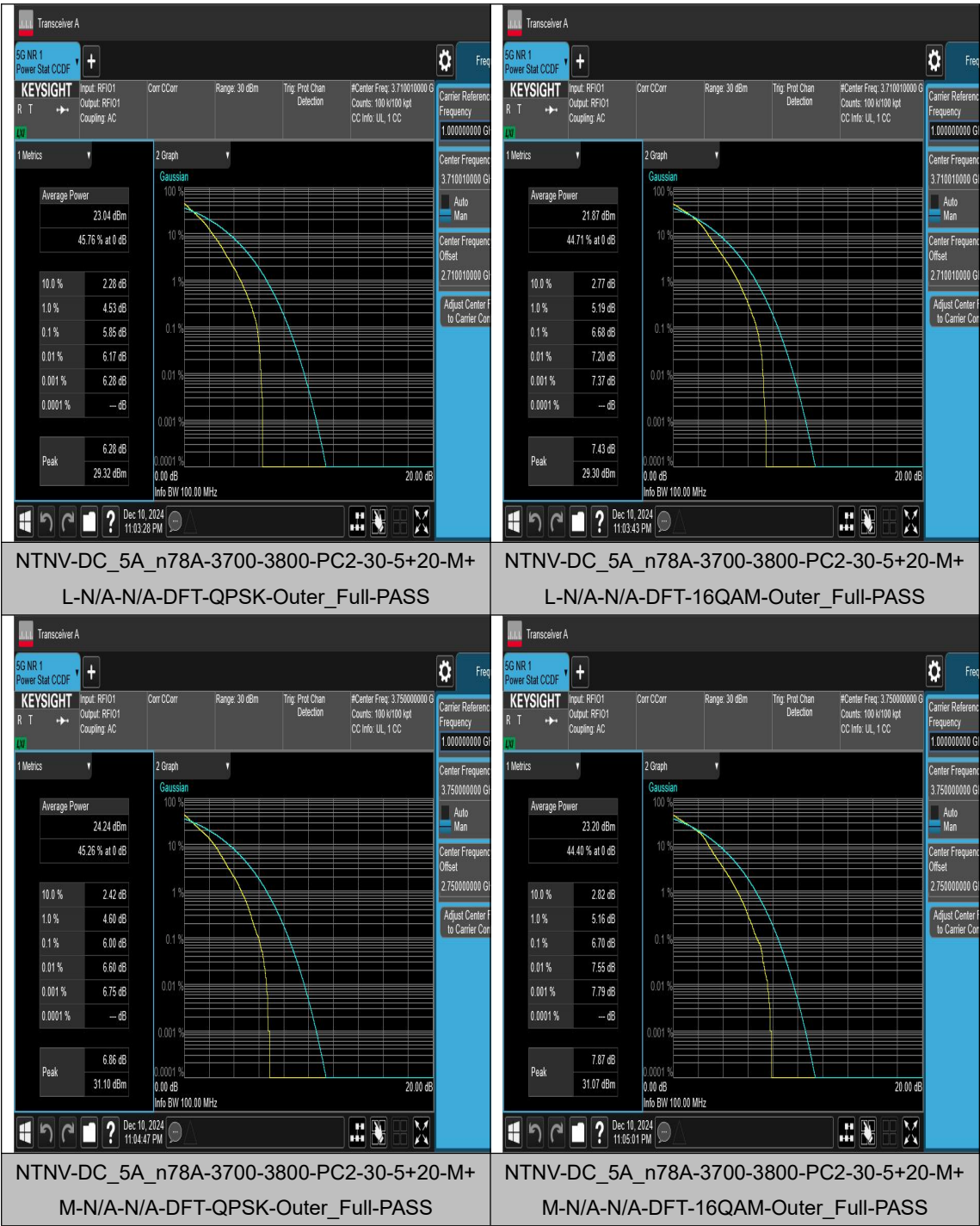
Test Result

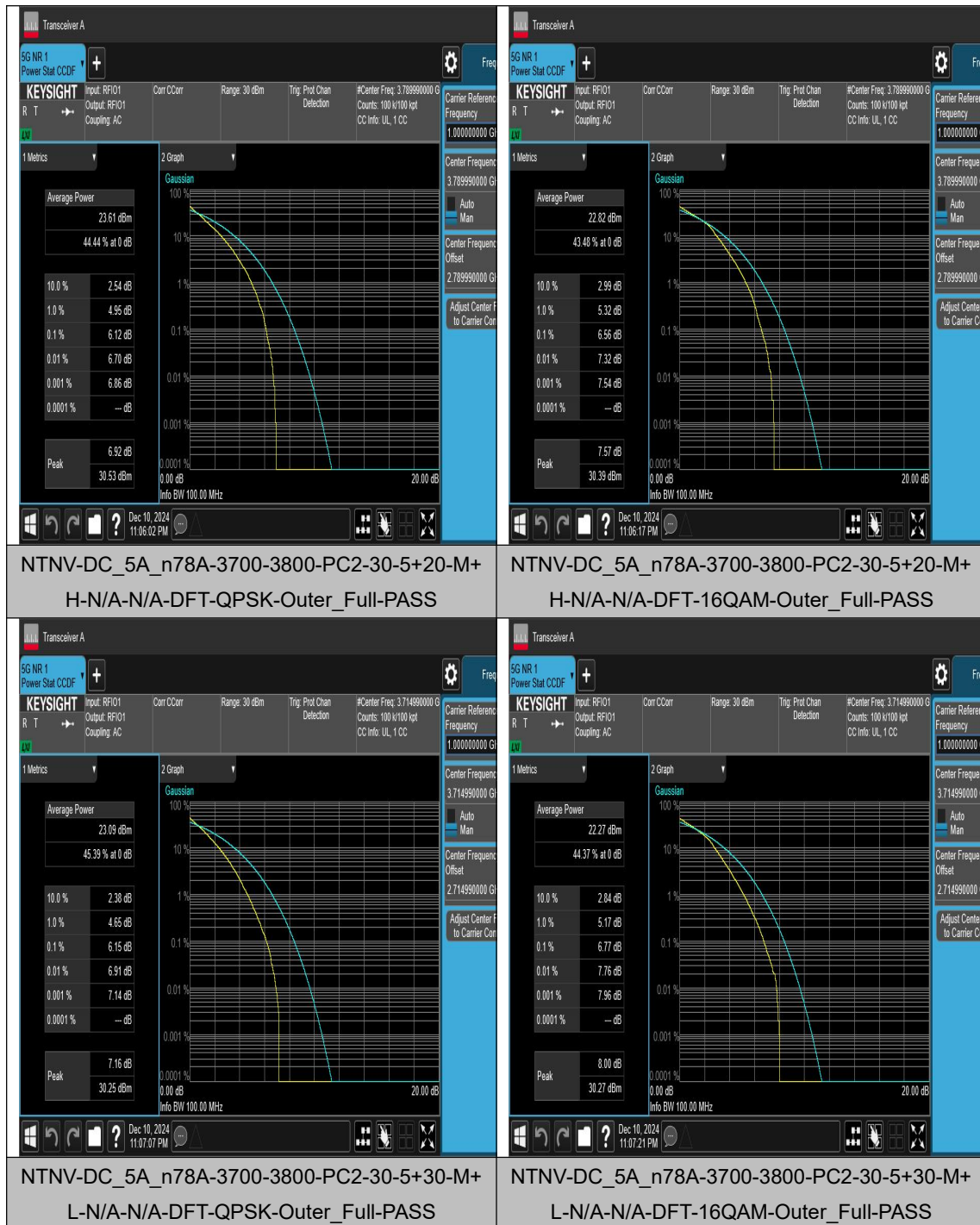
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_5A_n7 8A-3700-38 00	30	5+20	DFT-QPSK	M+L	Outer_Full	5.85	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+20	DFT-QPSK	M+M	Outer_Full	6.00	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+20	DFT-QPSK	M+H	Outer_Full	6.12	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+30	DFT-QPSK	M+L	Outer_Full	6.15	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+30	DFT-QPSK	M+M	Outer_Full	5.98	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+30	DFT-QPSK	M+H	Outer_Full	5.99	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+40	DFT-QPSK	M+L	Outer_Full	6.09	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+40	DFT-QPSK	M+M	Outer_Full	6.07	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+40	DFT-QPSK	M+H	Outer_Full	6.54	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+50	DFT-QPSK	M+L	Outer_Full	5.74	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+50	DFT-QPSK	M+M	Outer_Full	5.98	≤13	PASS
DC_5A_n7 8A-3700-38	30	5+50	DFT-QPSK	M+H	Outer_Full	6.01	≤13	PASS

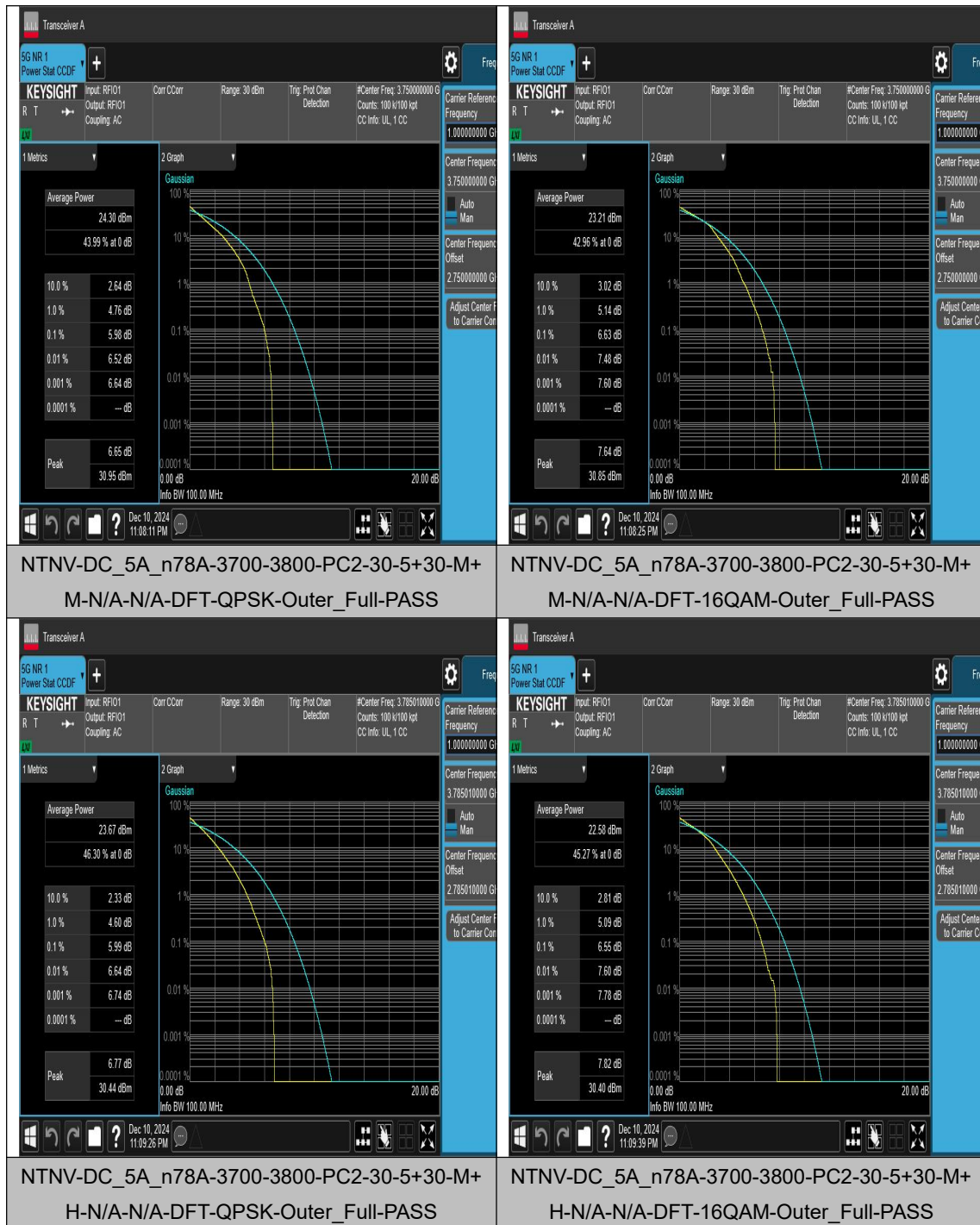
00								
DC_5A_n7 8A-3700-38 00	30	5+60	DFT-QPSK	M+L	Outer_Full	5.81	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+60	DFT-QPSK	M+M	Outer_Full	6.54	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+60	DFT-QPSK	M+H	Outer_Full	6.09	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+70	DFT-QPSK	M+L	Outer_Full	6.05	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+70	DFT-QPSK	M+M	Outer_Full	-1000.0 0	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+70	DFT-QPSK	M+H	Outer_Full	-1000.0 0	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+80	DFT-QPSK	M+L	Outer_Full	-1000.0 0	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+80	DFT-QPSK	M+M	Outer_Full	-1000.0 0	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+80	DFT-QPSK	M+H	Outer_Full	-1000.0 0	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+90	DFT-QPSK	M+L	Outer_Full	-1000.0 0	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+90	DFT-QPSK	M+M	Outer_Full	-1000.0 0	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+90	DFT-QPSK	M+H	Outer_Full	-1000.0 0	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+100	DFT-QPSK	M+L	Outer_Full	-1000.0 0	≤13	PASS
DC_5A_n7 8A-3700-38 00	30	5+100	DFT-QPSK	M+M	Outer_Full	-1000.0 0	≤13	PASS

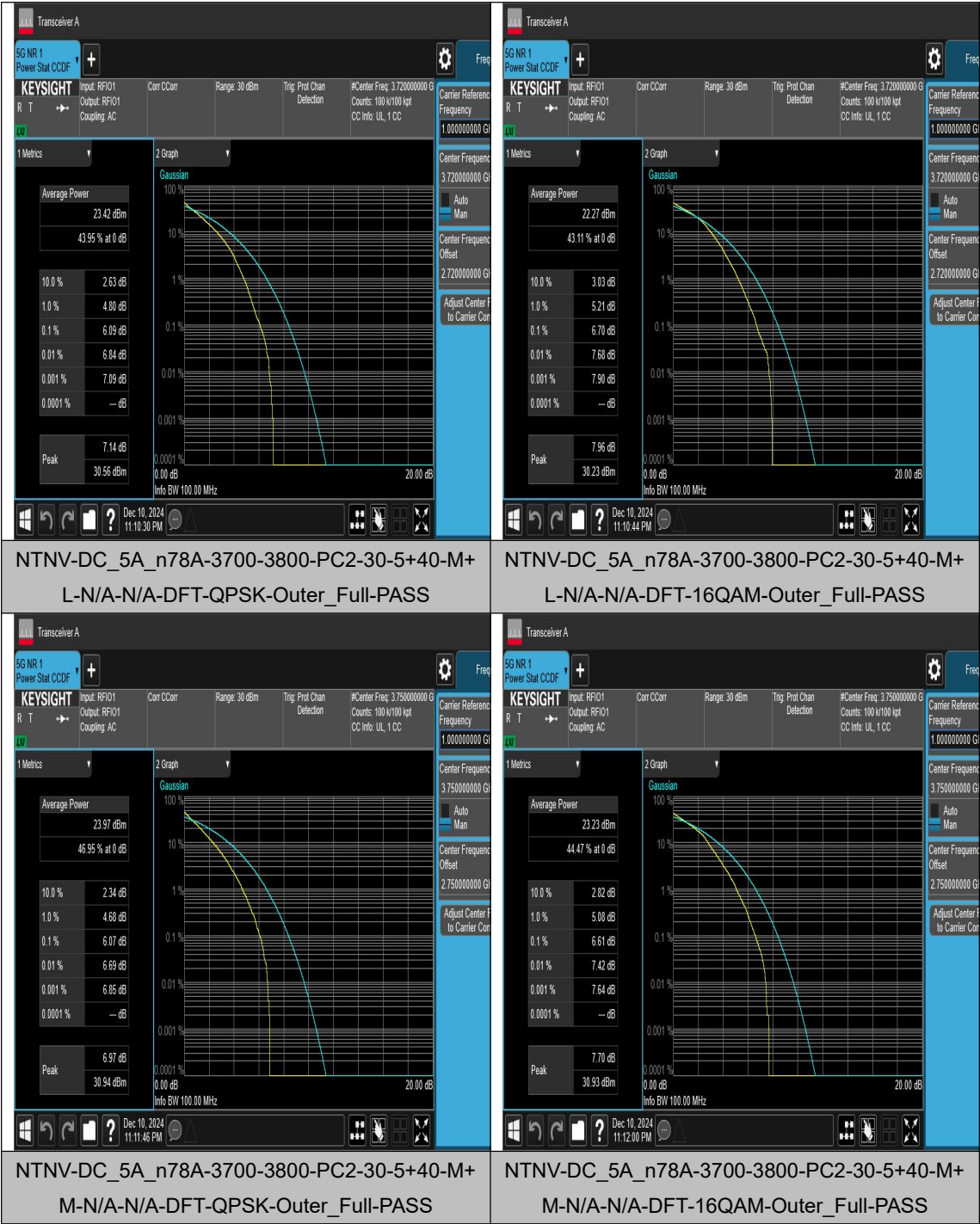
DC_5A_n7 8A-3700-38 00	30	5+100	DFT-QPSK	M+H	Outer_Full	-1000.0 0	≤13	PASS
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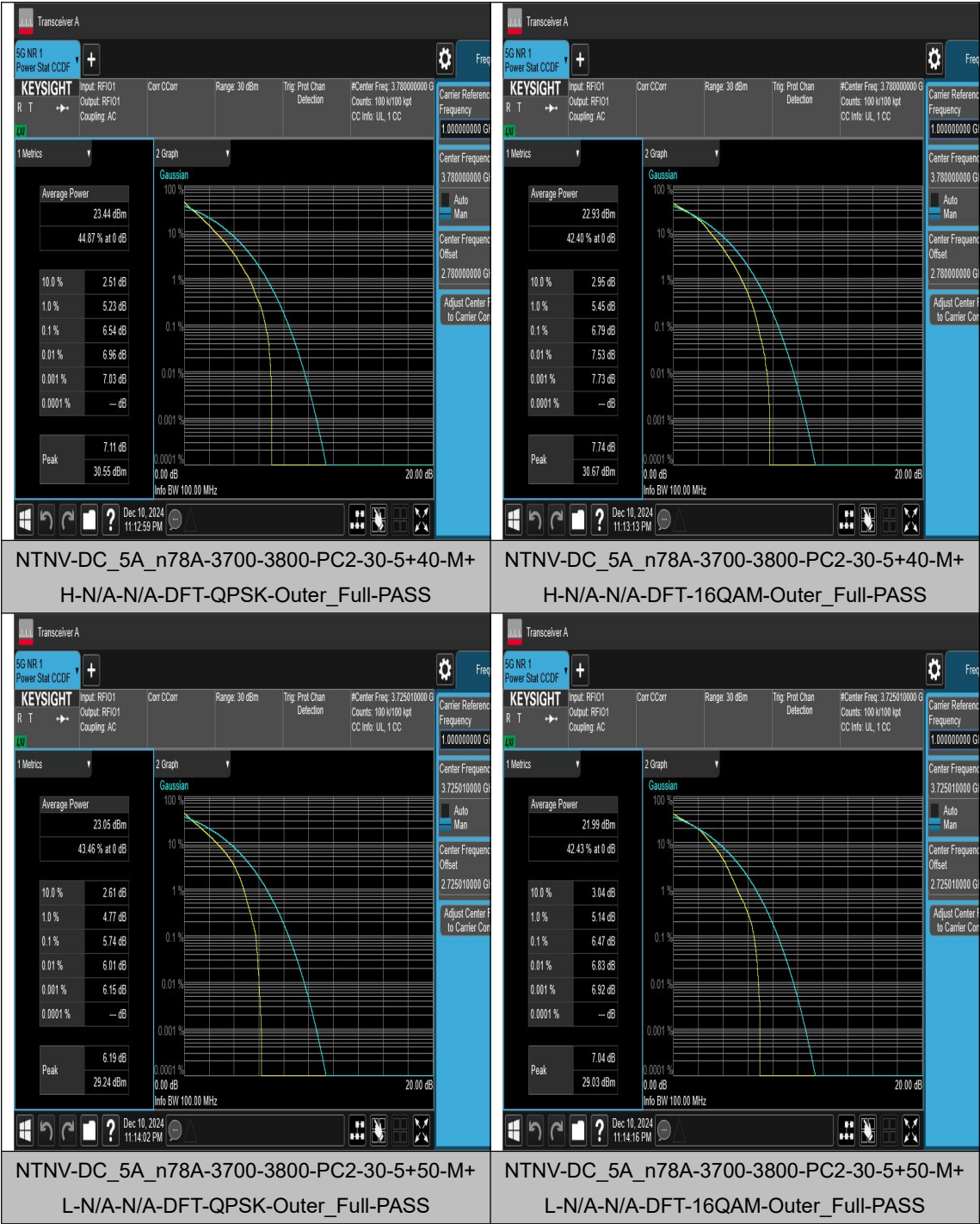
Test Graphs

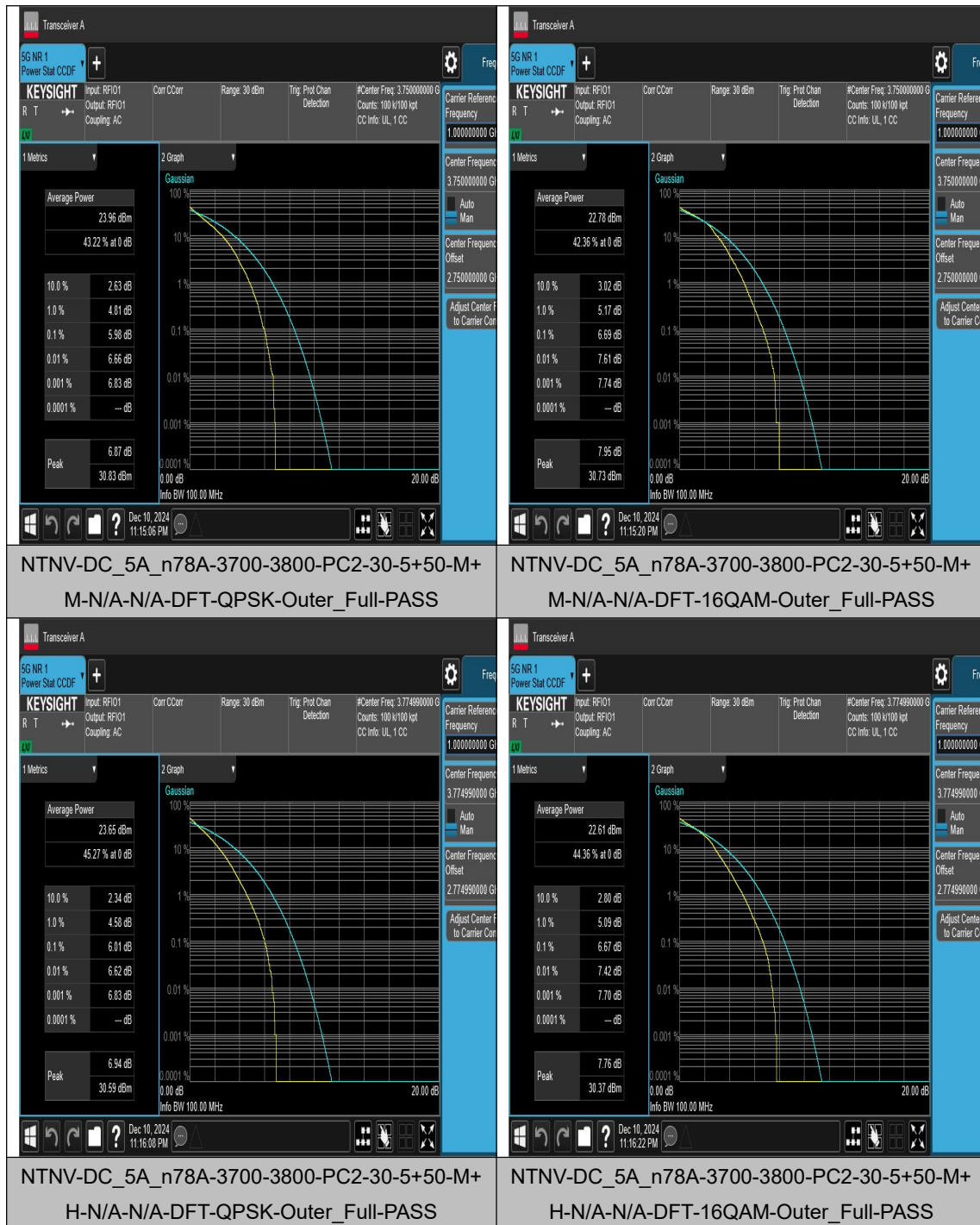


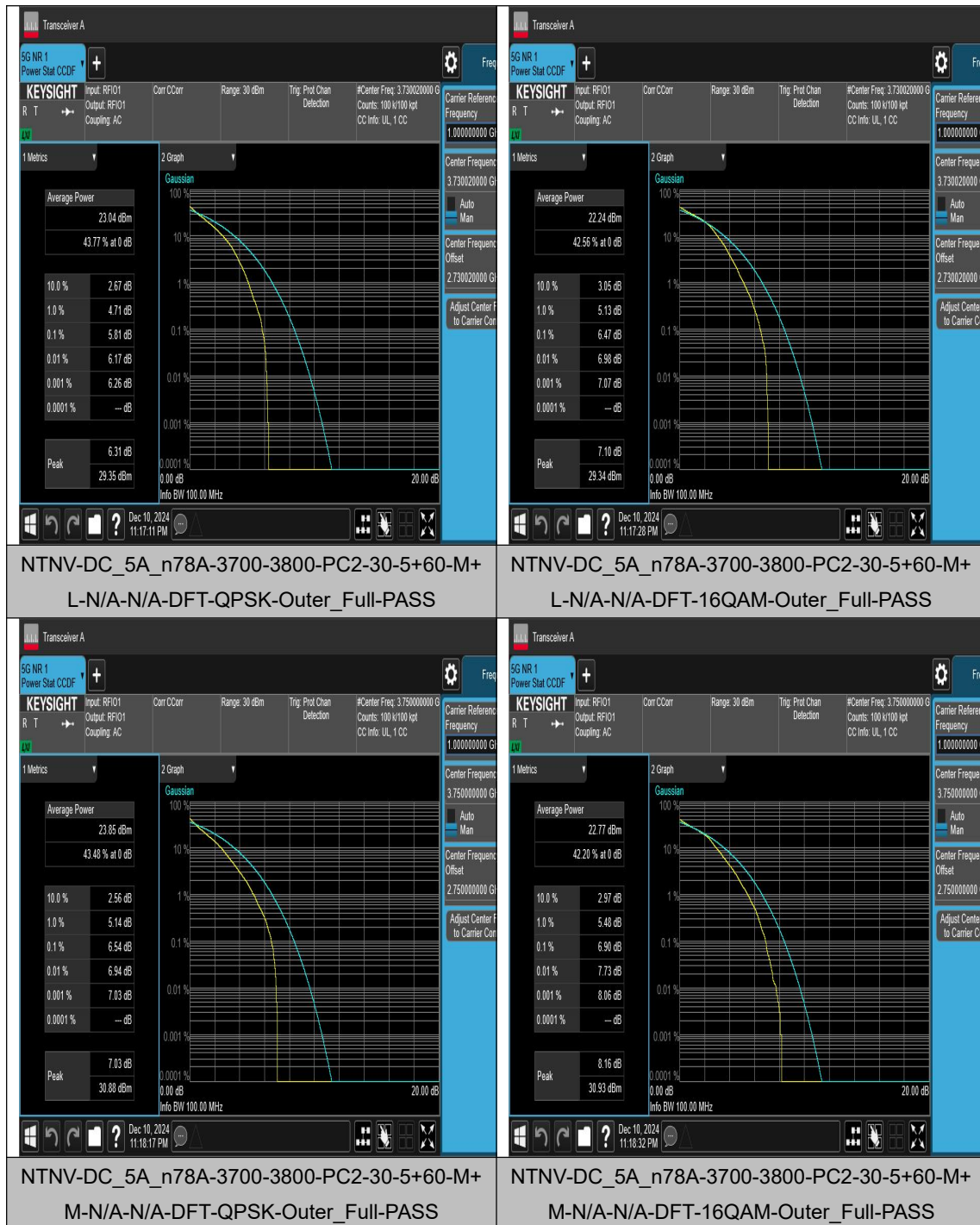


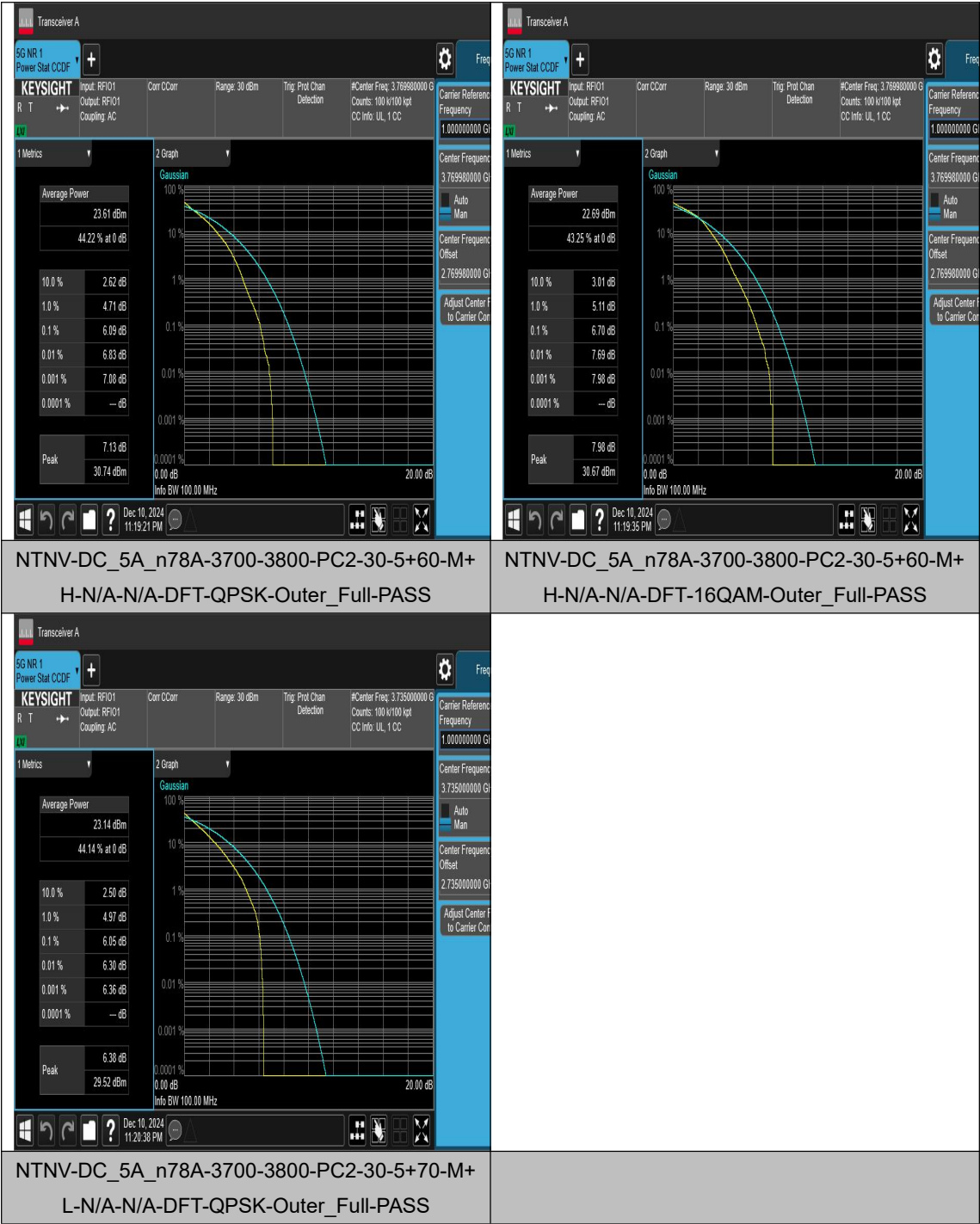












Appendix C: 26dB Bandwidth and Occupied Bandwidth for

NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	17.922	19.53	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	17.873	19.01	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	17.841	19.30	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	17.821	19.30	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	17.898	19.49	PASS
DC_5A_n78A-3700-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	17.827	19.46	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+L	Outer_Full	26.832	28.59	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-16QAM	M+L	Outer_Full	26.734	28.39	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+M	Outer_Full	26.778	28.58	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-16QAM	M+M	Outer_Full	26.854	28.72	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-QPSK	M+H	Outer_Full	26.772	28.58	PASS
DC_5A_n78A-3700-3800	30	5+30	DFT-16QAM	M+H	Outer_Full	26.776	28.45	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	35.796	37.48	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	35.737	37.78	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	35.744	37.81	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	35.694	37.64	PASS
DC_5A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	35.771	37.86	PASS

DC_5A_n78A-3700-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	35.727	38.02	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	45.676	47.98	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	45.822	48.03	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	45.739	48.11	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	45.667	48.79	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	45.782	47.95	PASS
DC_5A_n78A-3700-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	45.614	49.08	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	57.812	60.28	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	57.889	60.45	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	57.788	60.44	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	57.794	60.22	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	57.859	60.36	PASS
DC_5A_n78A-3700-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	57.805	60.54	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	64.277	67.00	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	64.232	67.05	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	64.299	67.09	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	64.218	66.69	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	64.392	67.90	PASS
DC_5A_n78A-3700-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	64.194	67.62	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	77.095	80.17	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	77.036	80.11	PASS
DC_5A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	77.086	80.89	PASS

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DC_5A_n78A-3700-38 00	30	5+80	DFT-16QAM	M+M	Outer_Full	77.053	80.90	PASS
DC_5A_n78A-3700-38 00	30	5+80	DFT-QPSK	M+H	Outer_Full	76.943	80.12	PASS
DC_5A_n78A-3700-38 00	30	5+80	DFT-16QAM	M+H	Outer_Full	76.945	80.20	PASS
DC_5A_n78A-3700-38 00	30	5+90	DFT-QPSK	M+L	Outer_Full	86.681	89.97	PASS
DC_5A_n78A-3700-38 00	30	5+90	DFT-16QAM	M+L	Outer_Full	86.697	90.07	PASS
DC_5A_n78A-3700-38 00	30	5+90	DFT-QPSK	M+M	Outer_Full	86.671	89.90	PASS
DC_5A_n78A-3700-38 00	30	5+90	DFT-16QAM	M+M	Outer_Full	86.571	90.12	PASS
DC_5A_n78A-3700-38 00	30	5+90	DFT-QPSK	M+H	Outer_Full	86.548	90.00	PASS
DC_5A_n78A-3700-38 00	30	5+90	DFT-16QAM	M+H	Outer_Full	86.624	90.02	PASS
DC_5A_n78A-3700-38 00	30	5+100	DFT-QPSK	M+L	Outer_Full	96.213	99.86	PASS
DC_5A_n78A-3700-38 00	30	5+100	DFT-16QAM	M+L	Outer_Full	96.278	101.5	PASS
DC_5A_n78A-3700-38 00	30	5+100	DFT-QPSK	M+M	Outer_Full	96.309	101.4	PASS
DC_5A_n78A-3700-38 00	30	5+100	DFT-16QAM	M+M	Outer_Full	96.479	99.87	PASS
DC_5A_n78A-3700-38 00	30	5+100	DFT-QPSK	M+H	Outer_Full	96.362	99.94	PASS
DC_5A_n78A-3700-38 00	30	5+100	DFT-16QAM	M+H	Outer_Full	96.319	99.96	PASS

Test Graphs

