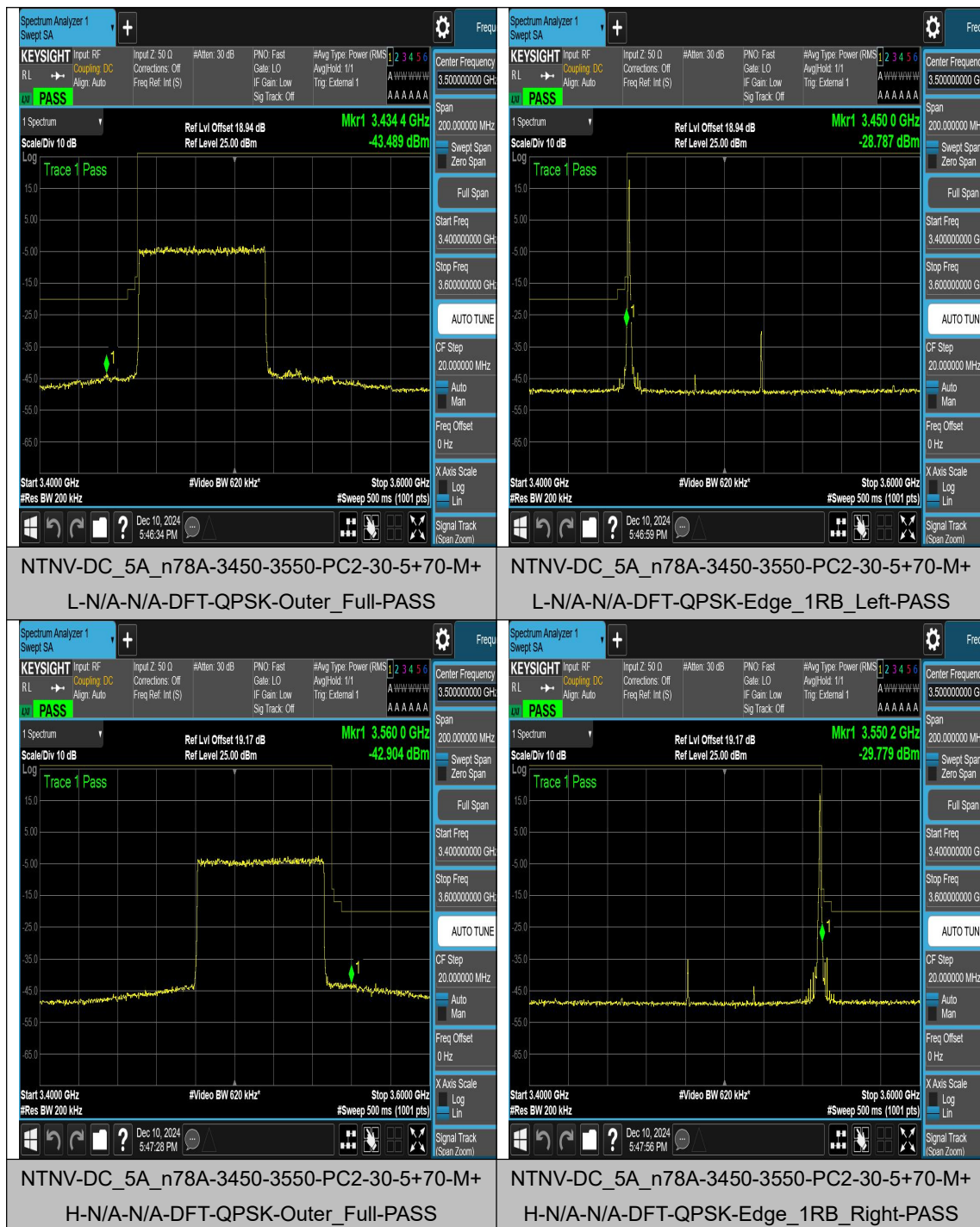
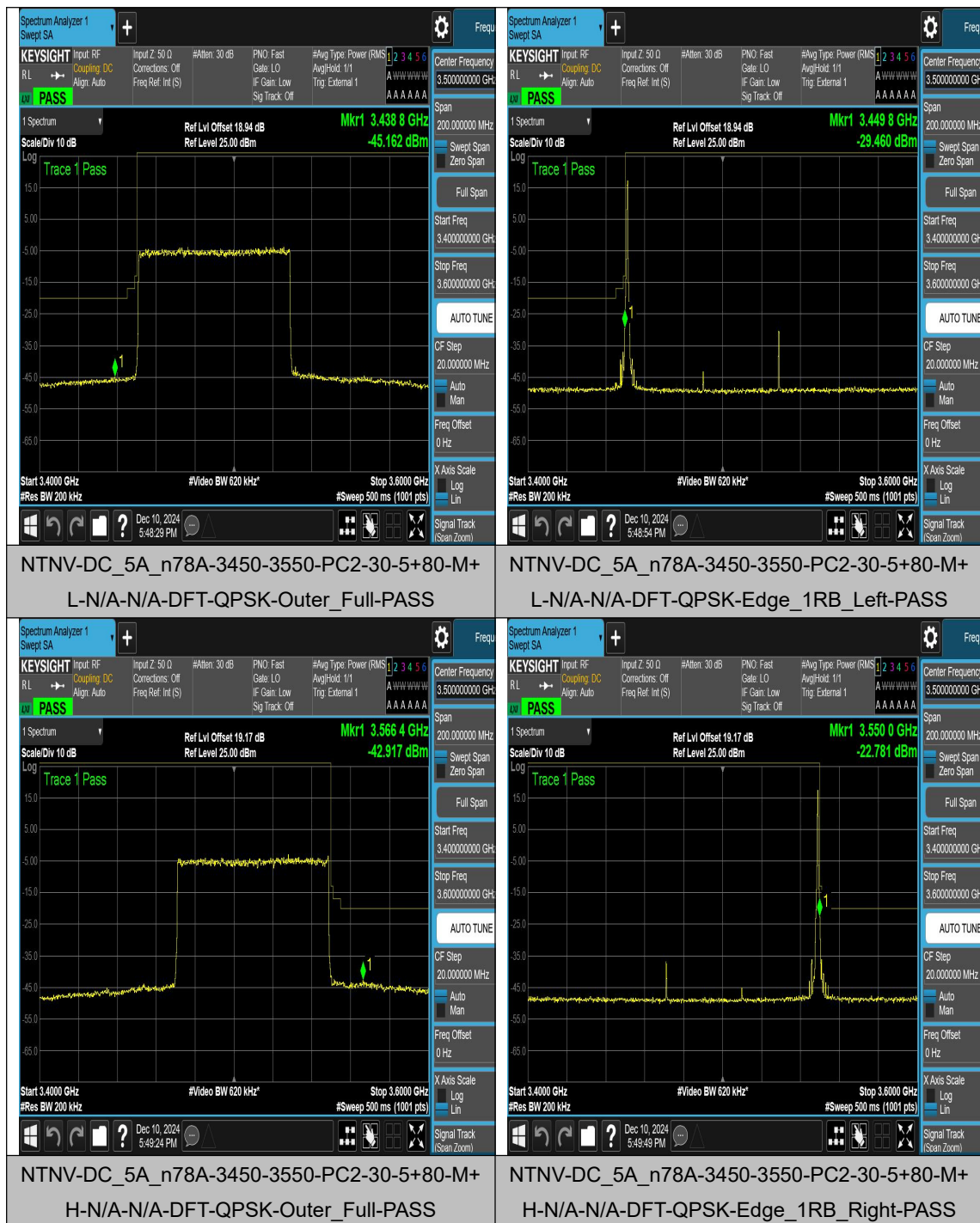
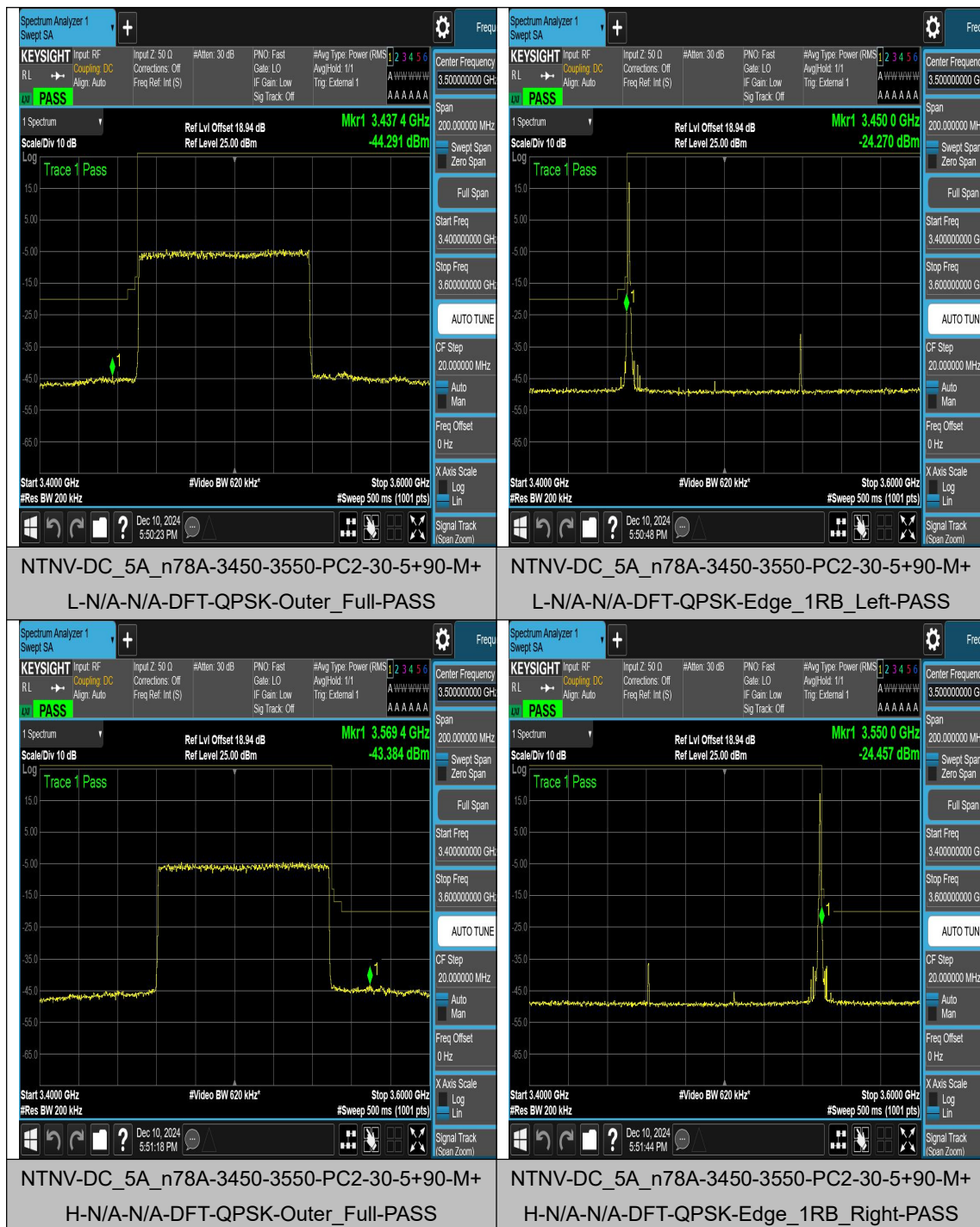
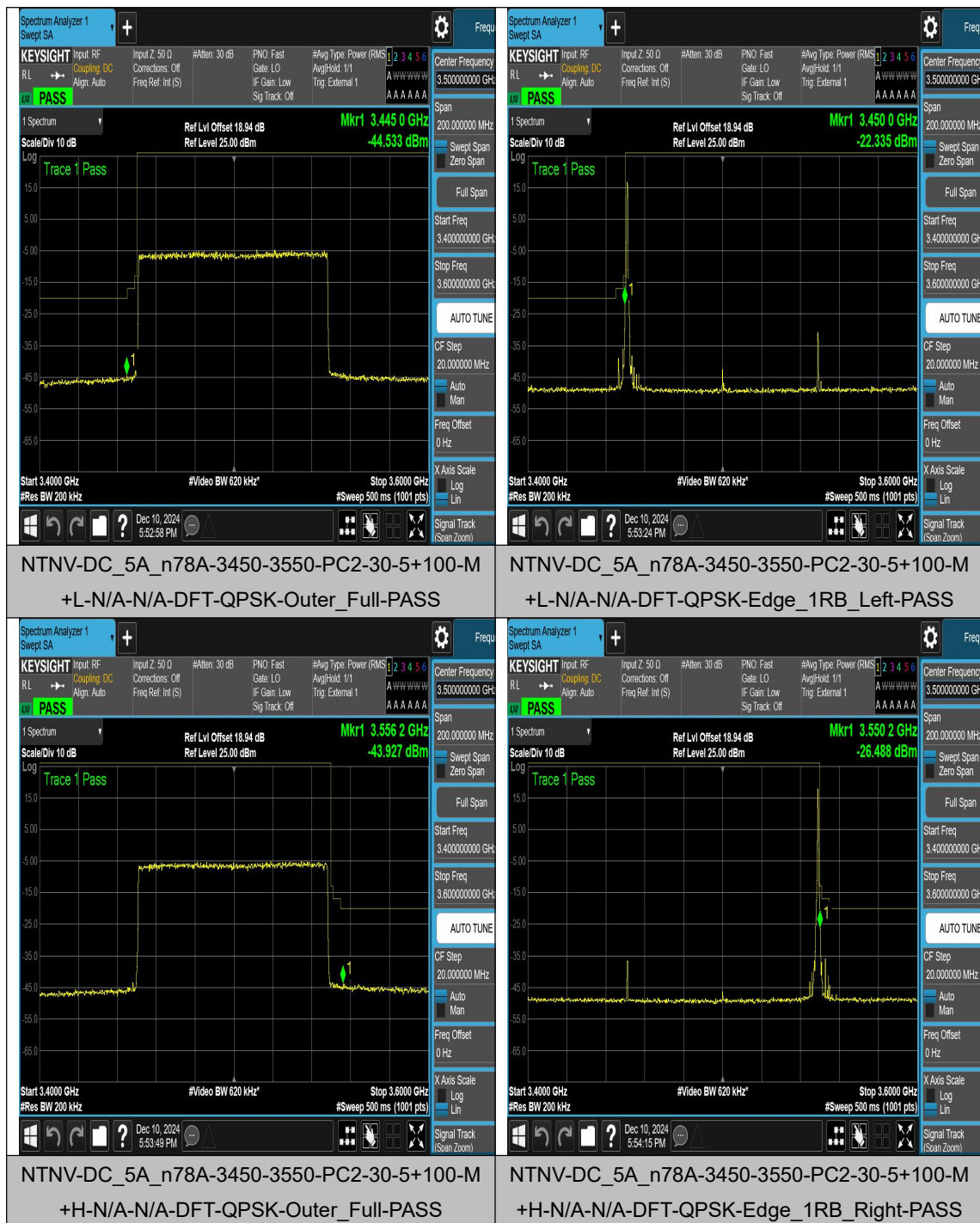










Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-56.82	-43	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-67.19	-33	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-55.43	-23	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.91	-13	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.50	-13	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.46	-13	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.70	-13	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-56.05	-43	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.39	-33	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.27	-23	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.87	-13	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.68	-13	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.42	-13	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.74	-13	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-56.31	-43	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-66.09	-33	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.94	-23	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.82	-13	PASS
DC_5A_n78A- 3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.64	-13	PASS

3450-3550										
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.42	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.59	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-56.49	-43	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-66.90	-33	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-56.05	-23	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.85	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.77	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.35	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.54	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.96	-43	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-67.59	-33	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-56.12	-23	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.81	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.78	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.49	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.68	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-56.57	-43	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-67.74	-33	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-56.14	-23	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.80	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.45	-13	PASS

DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.53	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.60	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-57.06	-43	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-66.64	-33	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-55.98	-23	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.91	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.79	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.43	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.68	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-57.26	-43	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.07	-33	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-44.07	-23	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.87	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.73	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.39	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.59	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-57.20	-43	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-67.12	-33	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-56.14	-23	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.80	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.63	-13	PASS
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.45	-13	PASS

3450-3550										
DC_5A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.59	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-55.89	-43	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	0.15	30	-67.89	-33	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	30	1000	-56.01	-23	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.82	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.63	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.48	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.52	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-55.93	-43	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.39	-33	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-56.05	-23	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.90	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.75	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.37	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.61	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-56.64	-43	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-66.33	-33	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-56.07	-23	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.79	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.67	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.31	-13	PASS

DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.75	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-56.51	-43	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.03	-33	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	30	1000	-55.39	-23	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.69	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.61	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.26	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.51	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-56.08	-43	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-66.09	-33	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.63	-23	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.92	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.67	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.44	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.55	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-57.04	-43	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-67.51	-33	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-49.41	-23	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.68	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.71	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.48	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.75	-13	PASS

3450-3550										
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-55.93	-43	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	0.15	30	-68.31	-33	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	30	1000	-55.83	-23	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.84	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.73	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.34	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.73	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-56.32	-43	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.06	-33	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-56.09	-23	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.94	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.56	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.52	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.73	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-57.34	-43	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-66.68	-33	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-56.17	-23	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.90	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.69	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.43	-13	PASS
DC_5A_n78A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.55	-13	PASS

DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-56.71	-43	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-66.68	-33	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-55.85	-23	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.88	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.76	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.38	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.57	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-56.78	-43	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-67.01	-33	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-56.06	-23	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.79	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.78	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.46	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.65	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-57.21	-43	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.13	-33	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.27	-23	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.88	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.82	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.49	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.64	-13	PASS
DC_5A_n78A-	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-57.18	-43	PASS

3450-3550										
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-68.00	-33	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-55.91	-23	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.92	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.77	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.36	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.55	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-56.44	-43	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-66.91	-33	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.92	-23	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.76	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.64	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.53	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.62	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-57.03	-43	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-66.10	-33	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.52	-23	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.83	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.75	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.38	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.66	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-56.27	-43	PASS

DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-67.40	-33	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-56.06	-23	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.86	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.55	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.49	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.74	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-54.92	-43	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-66.00	-33	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.27	-23	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.77	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.53	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.48	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.76	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-57.03	-43	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-65.30	-33	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-56.08	-23	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.83	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.50	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.49	-13	PASS
DC_5A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.57	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-56.74	-43	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-67.38	-33	PASS

3450-3550										
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-54.66	-23	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.87	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.71	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.44	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.67	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-56.95	-43	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-67.78	-33	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.40	-23	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.84	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.82	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.44	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.49	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-56.43	-43	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.49	-33	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-56.20	-23	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.79	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.72	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.32	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.73	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-56.18	-43	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-66.47	-33	PASS

DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-55.94	-23	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.84	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.72	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.34	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.63	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-57.44	-43	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-67.00	-33	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-56.02	-23	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.80	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.61	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.39	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.57	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-57.37	-43	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-67.90	-33	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-56.18	-23	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.79	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.89	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.50	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.67	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-55.88	-43	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-66.83	-33	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-55.99	-23	PASS

3450-3550										
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.91	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.67	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.30	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.48	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-57.63	-43	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-66.54	-33	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.54	-23	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.83	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.69	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.50	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.68	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-55.29	-43	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-68.33	-33	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.38	-23	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.84	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.72	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.56	-13	PASS
DC_5A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.63	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-56.70	-43	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-66.53	-33	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-55.92	-23	PASS

DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.84	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.66	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.52	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.64	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-56.99	-43	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-67.39	-33	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-49.68	-23	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.88	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.68	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.40	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.56	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-55.83	-43	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.81	-33	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.96	-23	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.94	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.69	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.27	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.70	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-56.46	-43	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.34	-33	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-56.03	-23	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.92	-13	PASS

3450-3550										
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.84	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.49	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.61	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-56.25	-43	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-66.69	-33	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-56.13	-23	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.95	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.87	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.60	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.82	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-56.78	-43	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-67.22	-33	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.95	-23	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.90	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.65	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.33	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.54	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-57.39	-43	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-66.84	-33	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-55.84	-23	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.90	-13	PASS

DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.64	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.25	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.58	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-56.89	-43	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.80	-33	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-56.06	-23	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.86	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.59	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.23	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.32	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-56.23	-43	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-65.34	-33	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-56.08	-23	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.88	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.65	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.53	-13	PASS
DC_5A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.71	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-57.01	-43	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	0.15	30	-68.29	-33	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	30	1000	-55.56	-23	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.95	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.44	-13	PASS

3450-3550										
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.47	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.59	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-57.20	-43	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-67.99	-33	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-56.12	-23	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.91	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.54	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.34	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.50	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-55.71	-43	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.86	-33	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.92	-23	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.88	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.76	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.43	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.53	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-55.45	-43	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.10	-33	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	30	1000	-55.12	-23	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.93	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.60	-13	PASS

DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.42	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.51	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-54.84	-43	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-67.76	-33	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.59	-23	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.85	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.68	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.47	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.67	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-57.27	-43	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-67.90	-33	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-47.32	-23	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.87	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.79	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.44	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.69	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-57.18	-43	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	0.15	30	-66.89	-33	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	30	1000	-44.24	-23	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.80	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.52	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.48	-13	PASS

3450-3550										
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.77	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-56.78	-43	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.50	-33	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.93	-23	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.92	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.68	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.40	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.58	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-56.03	-43	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-66.73	-33	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-49.59	-23	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.86	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.69	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.52	-13	PASS
DC_5A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.50	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-56.13	-43	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-65.62	-33	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-55.92	-23	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.81	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.72	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.16	-13	PASS

DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.65	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-56.58	-43	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.43	-33	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.54	-23	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.80	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.82	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.36	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.54	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-56.07	-43	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.33	-33	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.89	-23	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.86	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.71	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.39	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.65	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-56.38	-43	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.59	-33	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-55.87	-23	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.88	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.73	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.40	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.49	-13	PASS

3450-3550										
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-56.54	-43	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-66.51	-33	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.41	-23	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.86	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.75	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.47	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.66	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-55.69	-43	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-65.50	-33	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-56.00	-23	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.86	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.60	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.28	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.55	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-57.48	-43	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-68.16	-33	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-56.03	-23	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.87	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.54	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.46	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.60	-13	PASS

DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-56.10	-43	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.39	-33	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-56.16	-23	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.86	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.71	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.36	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.63	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-56.15	-43	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-67.34	-33	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.07	-23	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.84	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.65	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.41	-13	PASS
DC_5A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.61	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-55.14	-43	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-66.57	-33	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-55.70	-23	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.87	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.57	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.45	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.47	-13	PASS
DC_5A_n78A-	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-56.69	-43	PASS

3450-3550										
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-67.29	-33	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-56.14	-23	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.85	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.73	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.54	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.78	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-56.42	-43	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.25	-33	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-56.02	-23	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.77	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.73	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.33	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.48	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-56.52	-43	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.58	-33	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-56.14	-23	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.94	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.72	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.39	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.62	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.75	-43	PASS

DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-67.34	-33	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.95	-23	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.88	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.70	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.41	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.50	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-56.79	-43	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-66.27	-33	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.41	-23	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.87	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.73	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.34	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.58	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-56.56	-43	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-67.11	-33	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-56.01	-23	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.96	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.65	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.34	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.25	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-57.68	-43	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-66.20	-33	PASS

3450-3550										
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.45	-23	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.84	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.66	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.43	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.69	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-57.59	-43	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-66.37	-33	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-56.12	-23	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.85	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.50	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.41	-13	PASS
DC_5A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.67	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-56.18	-43	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.15	30	-66.81	-33	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-56.04	-23	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.85	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	3000	6000	-33.62	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	6000	26000	-37.50	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	26000	40000	-41.58	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-57.23	-43	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.45	-33	PASS

DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-42.95	-23	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.87	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-33.65	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-37.42	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.48	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-56.76	-43	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.52	-33	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-56.44	-23	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.93	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-33.63	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-37.36	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.50	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-56.06	-43	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.25	-33	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-55.97	-23	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.91	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	3000	6000	-33.63	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	26000	-37.51	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	26000	40000	-41.64	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-56.80	-43	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-66.39	-33	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23.06	-23	PASS

3450-3550		0								
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.94	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-33.81	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-37.52	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-41.66	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-56.21	-43	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-67.50	-33	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.97	-23	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.91	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-33.65	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-37.41	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-41.70	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-56.72	-43	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-66.87	-33	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-30.43	-23	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.98	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	3000	6000	-33.76	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	26000	-37.54	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.63	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-55.94	-43	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.26	-33	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-56.13	-23	PASS

DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.78	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-33.73	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-37.43	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.48	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-56.94	-43	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-65.80	-33	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-56.03	-23	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.87	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-33.82	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-37.53	-13	PASS
DC_5A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.48	-13	PASS

Test Graphs

