

Appendix for DC_5A_n78A(3450-3550MHz)

Product Name: Rugged handheld

Model No: UA60

Appendix A: Average Power Output Data for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_5A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	23.98	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	25.23	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.66	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	23.70	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	23.05	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	24.10	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	23.03	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	22.99	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	24.12	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	25.13	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	24.28	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	23.71	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	23.11	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Inner_Full	24.34	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	23.34	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	22.79	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	23.50	PC2	PASS
DC_5A_n78A-34	30	5+20	DFT-QPSK	M+H	Inner_Full	24.58	PC2	PASS

50-3550								
DC_5A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	23.87	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	23.36	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	22.59	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Inner_Full	23.71	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	22.85	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	22.19	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	24.18	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-QPSK	M+L	Inner_Full	24.98	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	24.06	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	24.07	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	23.18	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-16QAM	M+L	Inner_Full	24.24	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	22.78	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	23.29	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	24.09	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-QPSK	M+M	Inner_Full	25.12	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	23.96	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	23.76	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	23.10	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-16QAM	M+M	Inner_Full	24.15	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	23.18	PC2	PASS

DC_5A_n78A-34 50-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	22.46	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	23.63	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-QPSK	M+H	Inner_Full	24.87	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	23.68	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	23.44	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	22.61	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-16QAM	M+H	Inner_Full	23.73	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	22.50	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	22.30	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	24.16	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	25.15	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	23.86	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	24.16	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	23.12	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Inner_Full	23.96	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	23.32	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	23.56	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	23.98	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	25.17	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	24.24	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	23.86	PC2	PASS
DC_5A_n78A-34	30	5+40	DFT-16QAM	M+M	Outer_Full	22.96	PC2	PASS

50-3550								
DC_5A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Inner_Full	24.12	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	23.04	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	22.54	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	23.64	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	24.73	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	23.85	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	23.34	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	22.70	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Inner_Full	23.88	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	22.62	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	22.11	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	23.97	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	25.00	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	23.58	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	23.86	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	22.70	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Inner_Full	23.93	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	22.49	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	22.88	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	23.89	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Inner_Full	24.93	PC2	PASS

DC_5A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	24.01	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	23.61	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	22.85	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Inner_Full	23.93	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	22.93	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	22.63	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	23.53	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	24.62	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	23.61	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	23.14	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	22.36	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Inner_Full	23.60	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	22.79	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	22.16	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	23.74	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	24.86	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	23.54	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	23.38	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	22.70	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Inner_Full	23.98	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	22.76	PC2	PASS
DC_5A_n78A-34	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	22.51	PC2	PASS

50-3550								
DC_5A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	23.71	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Inner_Full	24.84	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	23.72	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	23.56	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	22.63	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Inner_Full	23.74	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	22.81	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	22.53	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	23.63	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	24.64	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	23.78	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	22.98	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	22.63	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Inner_Full	23.66	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	22.87	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	21.99	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	23.67	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Inner_Full	24.93	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	23.62	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	23.21	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	22.66	PC2	PASS

DC_5A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Inner_Full	23.74	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	22.55	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	22.34	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	23.69	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Inner_Full	24.89	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	23.78	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	23.44	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	22.78	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Inner_Full	23.65	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	22.94	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	22.32	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	23.49	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Inner_Full	24.50	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	23.77	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	22.89	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	22.45	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Inner_Full	23.65	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	23.05	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	21.90	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	23.53	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	24.88	PC2	PASS
DC_5A_n78A-34	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.45	PC2	PASS

50-3550								
DC_5A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	23.20	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	22.70	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Inner_Full	23.78	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	22.54	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	22.74	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	23.73	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	24.66	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.76	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	23.21	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	22.68	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Inner_Full	23.60	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	22.54	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	22.07	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	23.40	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	24.63	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	23.74	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	22.91	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	22.48	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Inner_Full	23.59	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	22.66	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	21.89	PC2	PASS

DC_5A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	23.69	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	24.62	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	23.24	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	23.02	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	22.67	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Inner_Full	23.73	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	22.39	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	22.15	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	23.66	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	24.63	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	23.62	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	23.03	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	22.64	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Inner_Full	23.61	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	22.35	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	21.95	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	23.43	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	24.50	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	23.56	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	22.89	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	22.61	PC2	PASS
DC_5A_n78A-34	30	5+90	DFT-16QAM	M+H	Inner_Full	23.68	PC2	PASS

50-3550								
DC_5A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	22.50	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	21.99	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	23.42	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	24.63	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	23.16	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	22.72	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	22.53	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Inner_Full	23.71	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	22.46	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	21.85	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	23.42	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	24.63	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	23.25	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	22.61	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	22.53	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Inner_Full	23.62	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	22.45	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	21.95	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	23.63	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	25.26	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	23.14	PC2	PASS

DC_5A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	22.48	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	22.52	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Inner_Full	23.71	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	22.45	PC2	PASS
DC_5A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	21.82	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_5A_n7 8A-3450-35 50	30	5+20	DFT-QPSK	M+L	Outer_Full	6.19	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+20	DFT-16QAM	M+L	Outer_Full	6.72	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+20	DFT-QPSK	M+M	Outer_Full	6.27	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+20	DFT-16QAM	M+M	Outer_Full	6.97	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+20	DFT-QPSK	M+H	Outer_Full	5.88	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+20	DFT-16QAM	M+H	Outer_Full	6.63	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+30	DFT-QPSK	M+L	Outer_Full	6.11	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+30	DFT-16QAM	M+L	Outer_Full	6.72	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+30	DFT-QPSK	M+M	Outer_Full	6.16	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+30	DFT-16QAM	M+M	Outer_Full	6.88	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+30	DFT-QPSK	M+H	Outer_Full	6.21	≤13	PASS
DC_5A_n7 8A-3450-35	30	5+30	DFT-16QAM	M+H	Outer_Full	6.82	≤13	PASS

50								
DC_5A_n7 8A-3450-35 50	30	5+40	DFT-QPSK	M+L	Outer_Full	6.14	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+40	DFT-16QAM	M+L	Outer_Full	6.84	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+40	DFT-QPSK	M+M	Outer_Full	6.68	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+40	DFT-16QAM	M+M	Outer_Full	7.03	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+40	DFT-QPSK	M+H	Outer_Full	5.85	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+40	DFT-16QAM	M+H	Outer_Full	6.65	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+50	DFT-QPSK	M+L	Outer_Full	6.20	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+50	DFT-16QAM	M+L	Outer_Full	6.72	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+50	DFT-QPSK	M+M	Outer_Full	5.78	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+50	DFT-16QAM	M+M	Outer_Full	6.57	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+50	DFT-QPSK	M+H	Outer_Full	5.89	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+50	DFT-16QAM	M+H	Outer_Full	6.55	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+60	DFT-QPSK	M+L	Outer_Full	5.71	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+60	DFT-16QAM	M+L	Outer_Full	6.45	≤13	PASS

DC_5A_n7 8A-3450-35 50	30	5+60	DFT-QPSK	M+M	Outer_Full	6.16	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+60	DFT-16QAM	M+M	Outer_Full	6.86	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+60	DFT-QPSK	M+H	Outer_Full	5.83	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+60	DFT-16QAM	M+H	Outer_Full	6.67	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+70	DFT-QPSK	M+L	Outer_Full	6.08	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+70	DFT-16QAM	M+L	Outer_Full	6.68	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+70	DFT-QPSK	M+M	Outer_Full	5.92	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+70	DFT-16QAM	M+M	Outer_Full	6.56	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+70	DFT-QPSK	M+H	Outer_Full	6.01	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+70	DFT-16QAM	M+H	Outer_Full	6.64	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+80	DFT-QPSK	M+L	Outer_Full	5.70	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+80	DFT-16QAM	M+L	Outer_Full	6.42	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+80	DFT-QPSK	M+M	Outer_Full	5.61	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+80	DFT-16QAM	M+M	Outer_Full	6.49	≤13	PASS
DC_5A_n7	30	5+80	DFT-QPSK	M+H	Outer_Full	5.73	≤13	PASS

8A-3450-35 50								
DC_5A_n7 8A-3450-35 50	30	5+80	DFT-16QAM	M+H	Outer_Full	6.46	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+90	DFT-QPSK	M+L	Outer_Full	5.87	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+90	DFT-16QAM	M+L	Outer_Full	6.70	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+90	DFT-QPSK	M+M	Outer_Full	5.52	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+90	DFT-16QAM	M+M	Outer_Full	6.47	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+90	DFT-QPSK	M+H	Outer_Full	5.73	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+90	DFT-16QAM	M+H	Outer_Full	6.44	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+100	DFT-QPSK	M+L	Outer_Full	5.62	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+100	DFT-16QAM	M+L	Outer_Full	6.35	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+100	DFT-QPSK	M+M	Outer_Full	5.53	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+100	DFT-16QAM	M+M	Outer_Full	6.35	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+100	DFT-QPSK	M+H	Outer_Full	5.59	≤13	PASS
DC_5A_n7 8A-3450-35 50	30	5+100	DFT-16QAM	M+H	Outer_Full	6.39	≤13	PASS

Test Graphs























