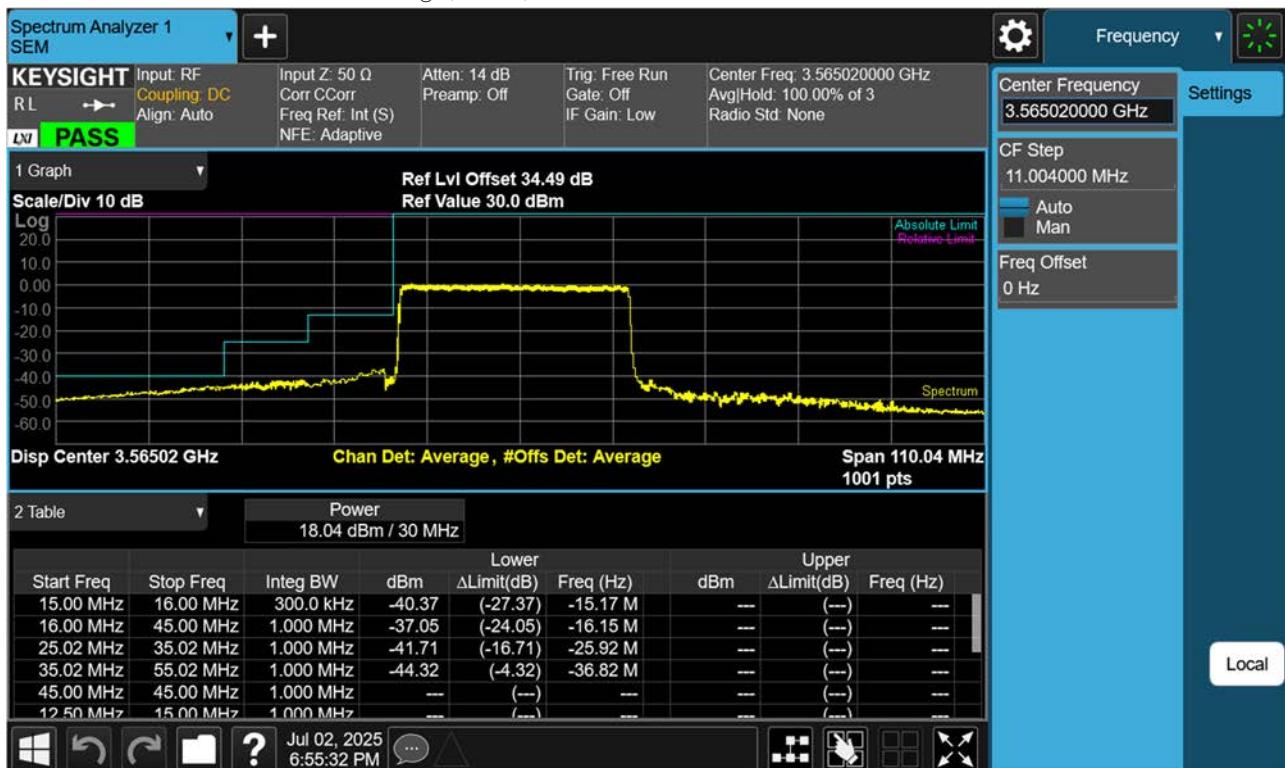
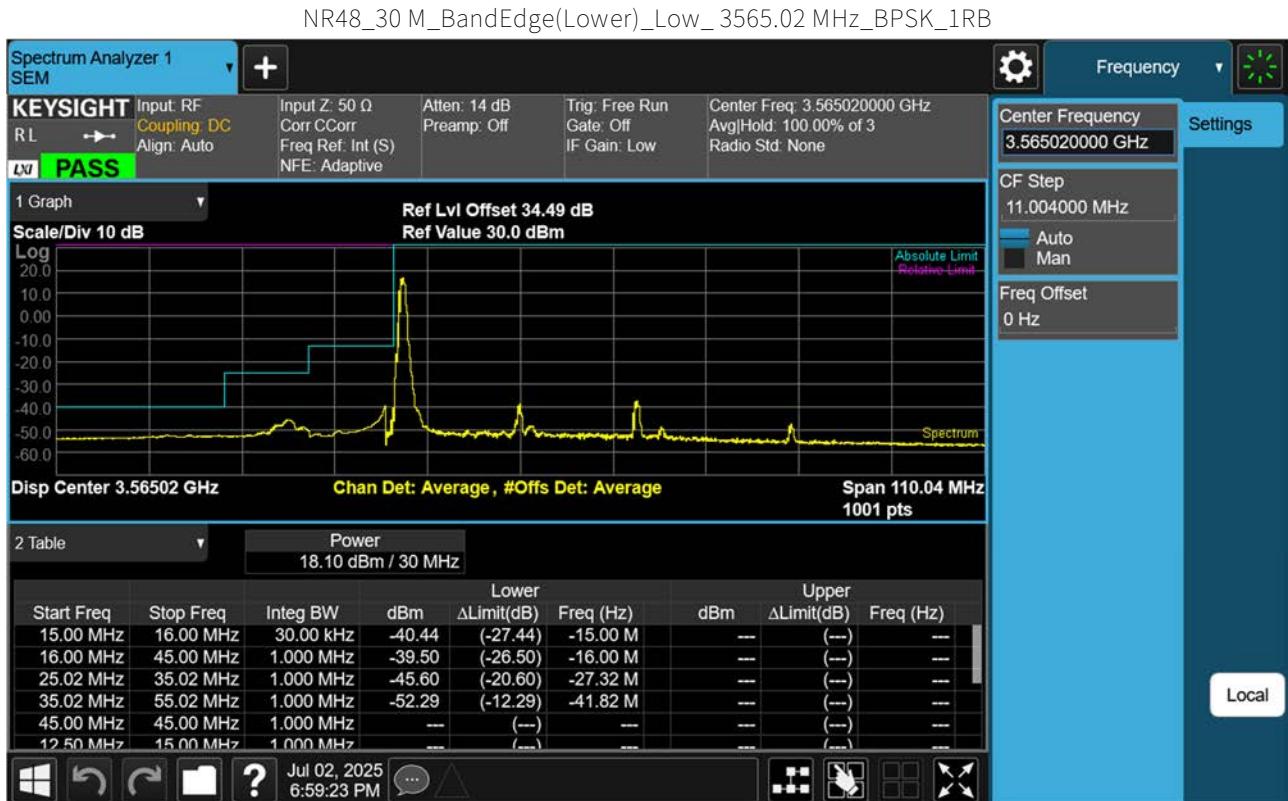
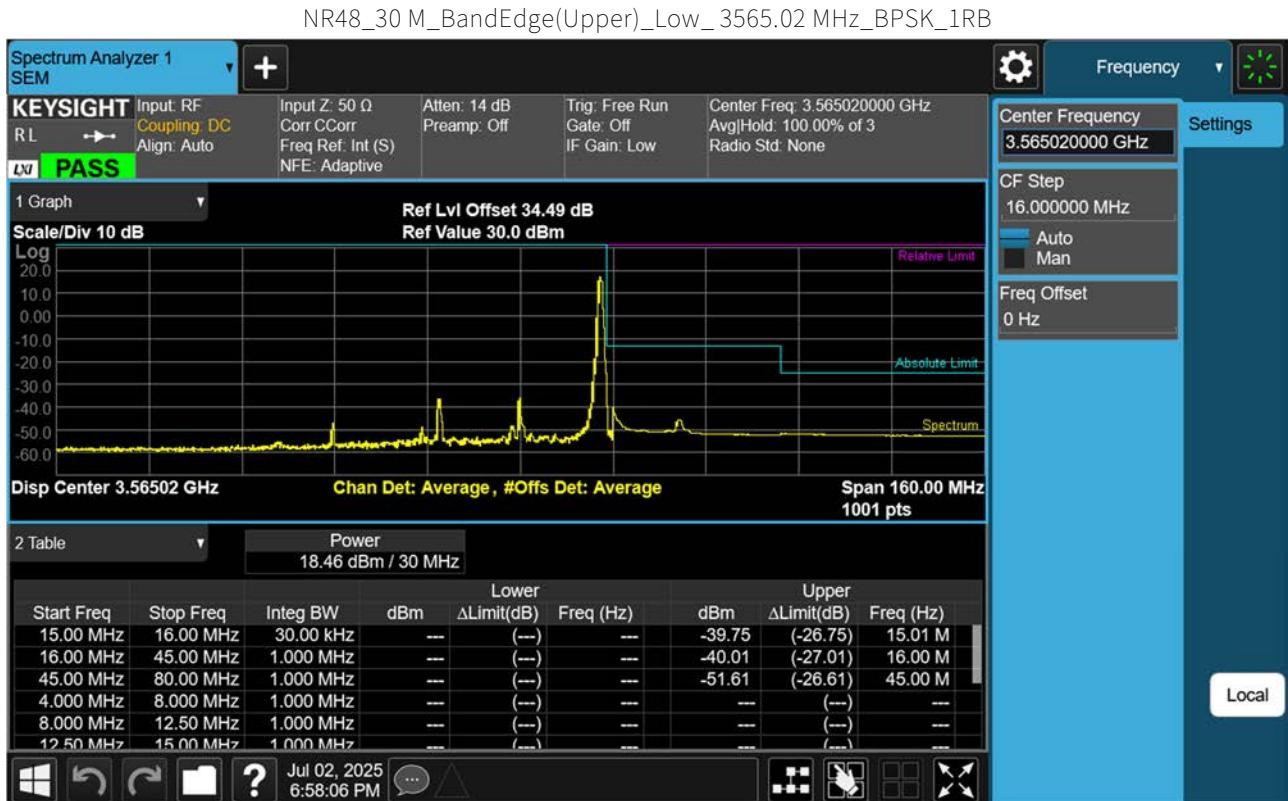


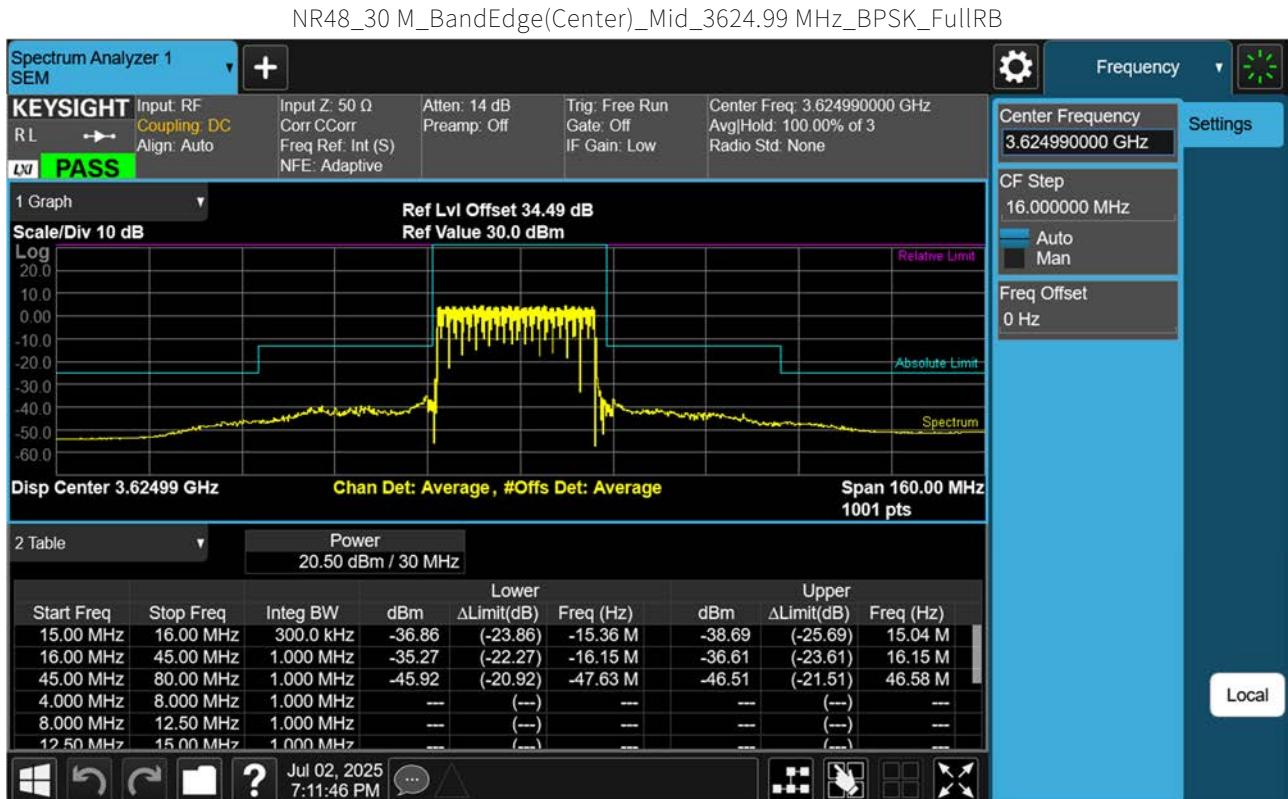
NR48\_30 M BandEdge(Lower)\_Low\_3565.02 MHz\_BPSK\_FullRB\_Below 3530MHz

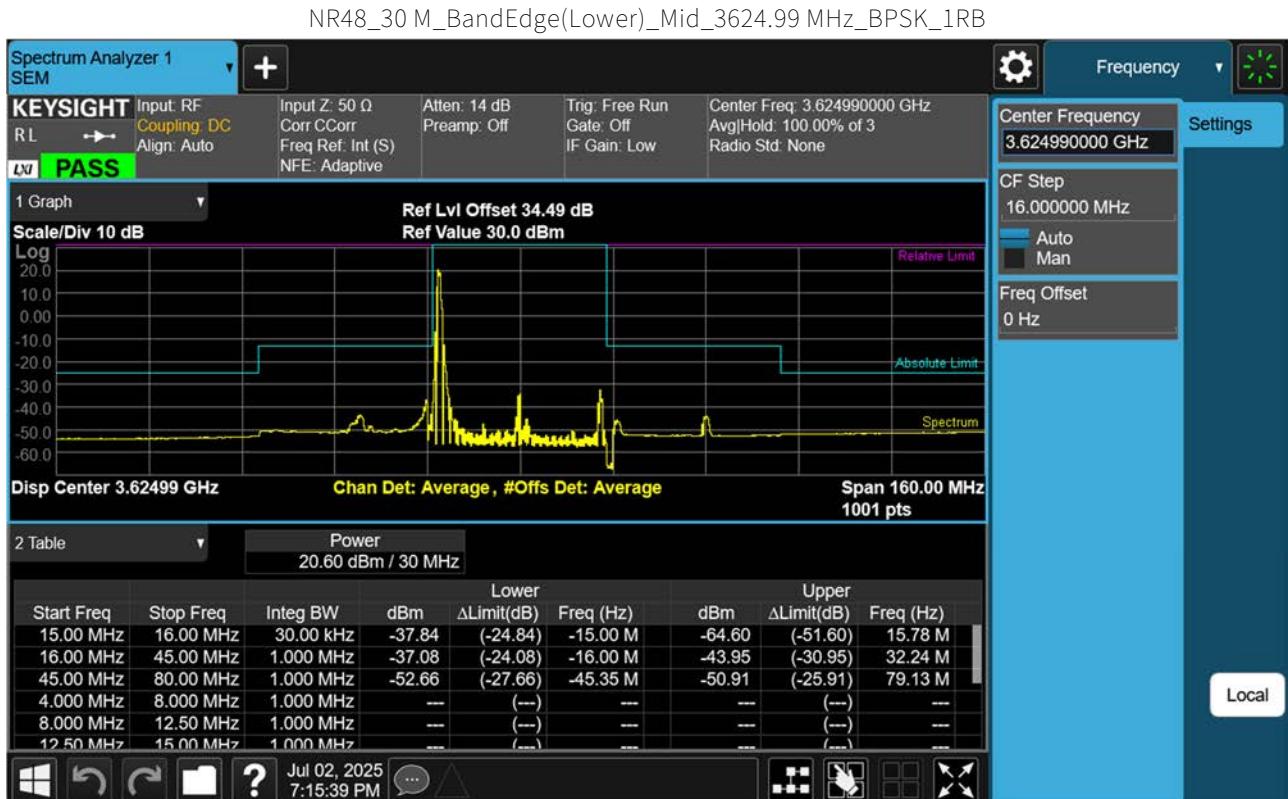


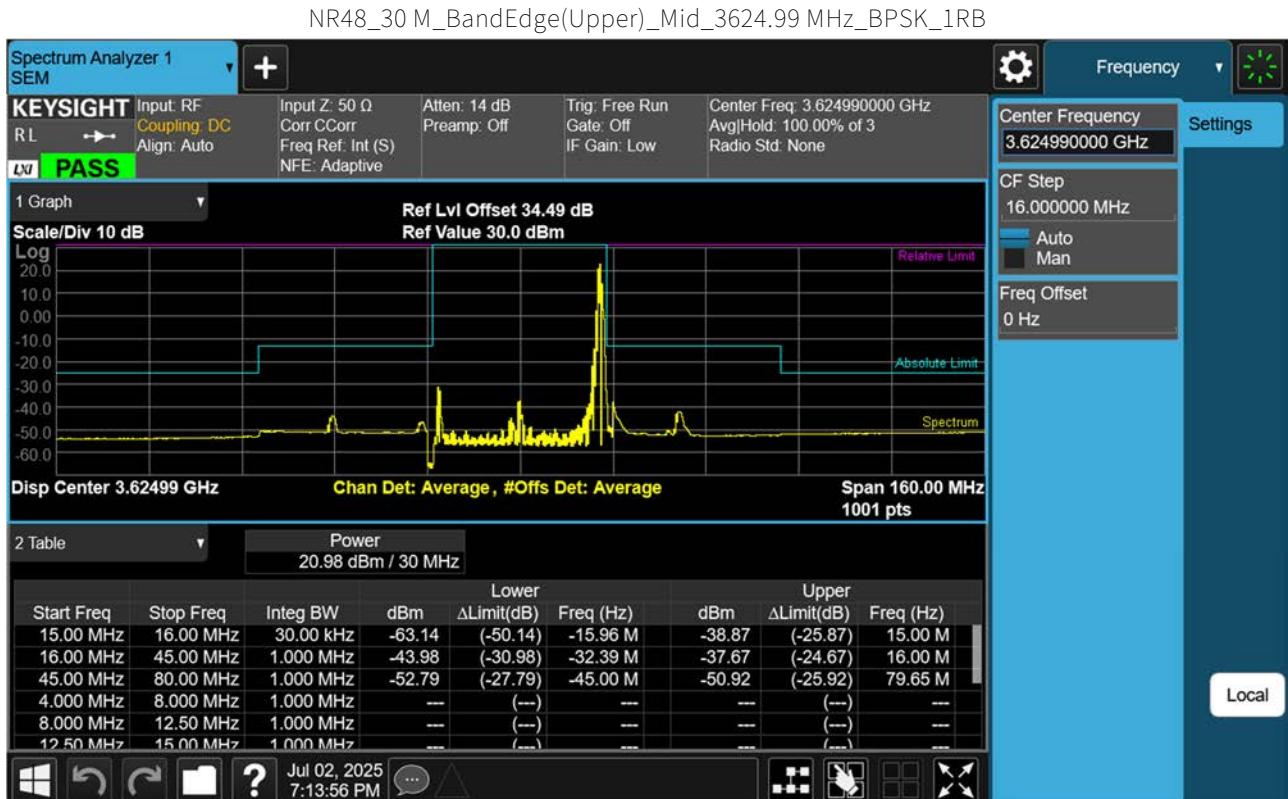


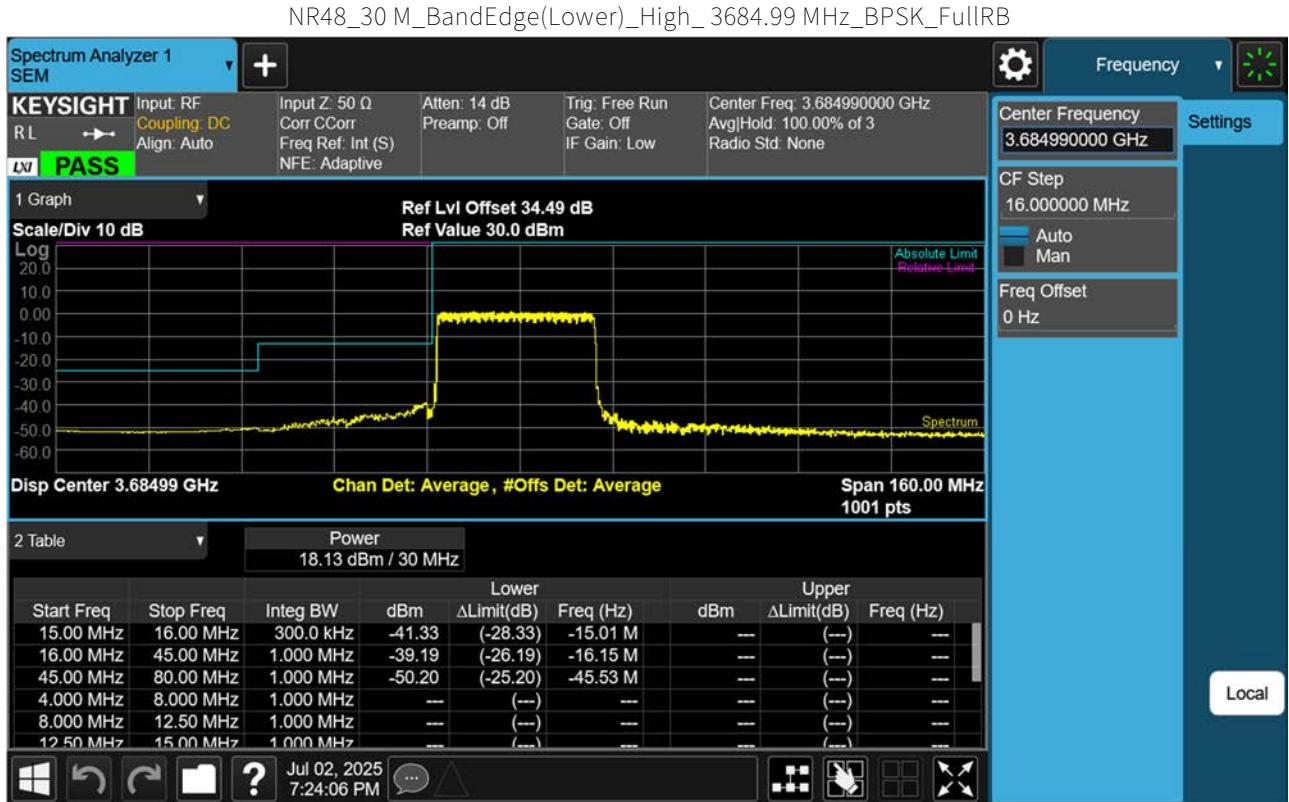




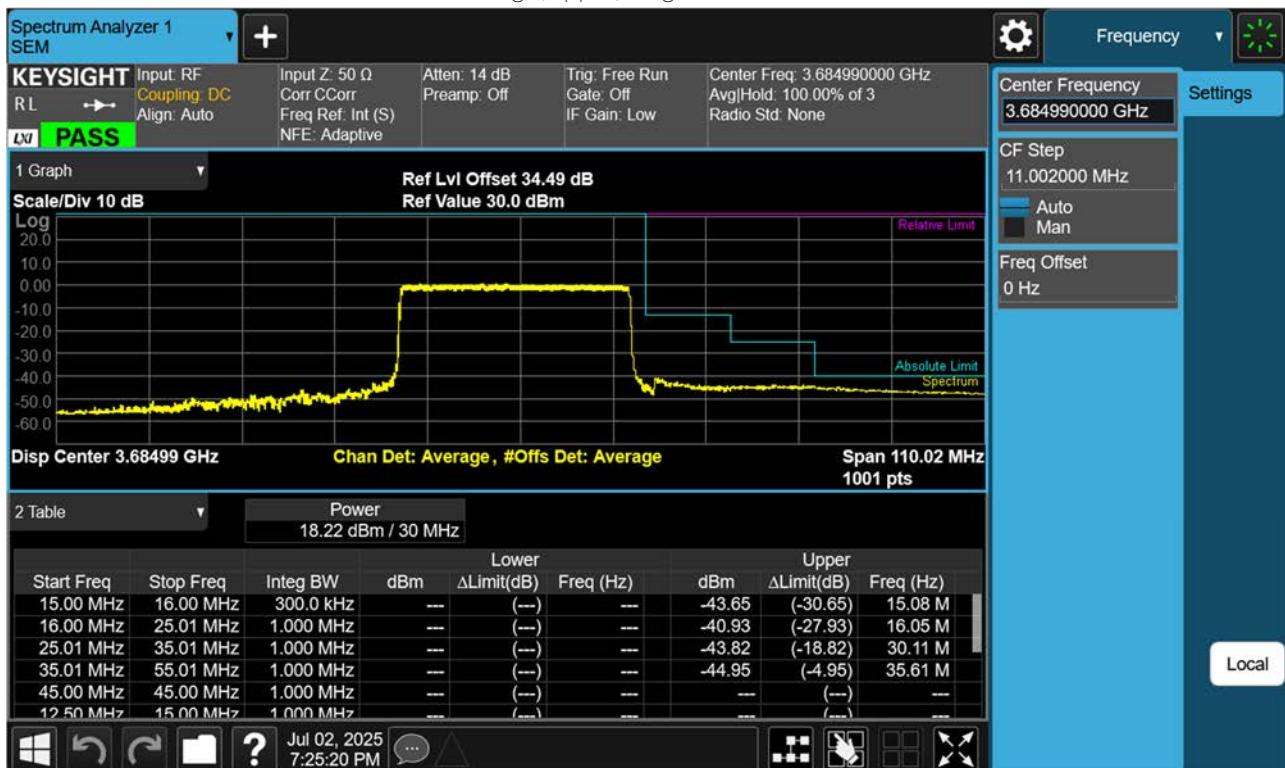


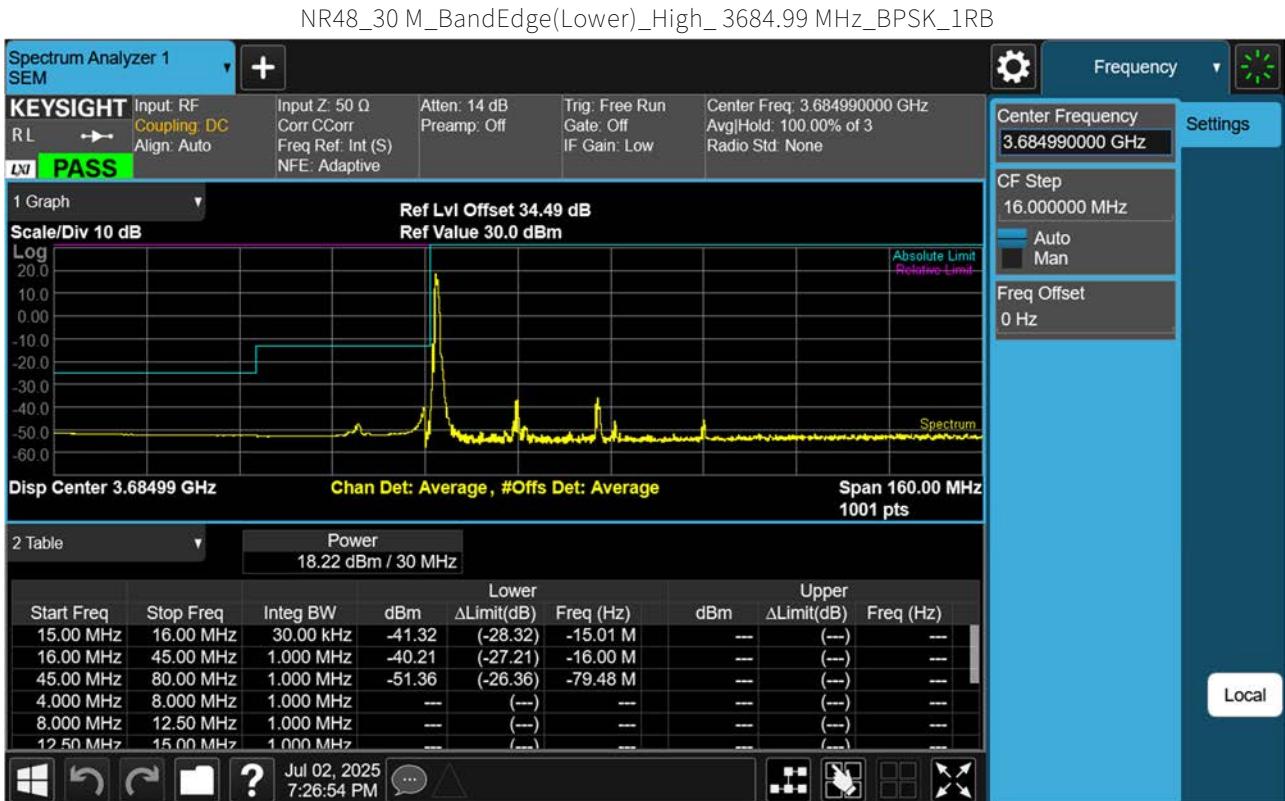


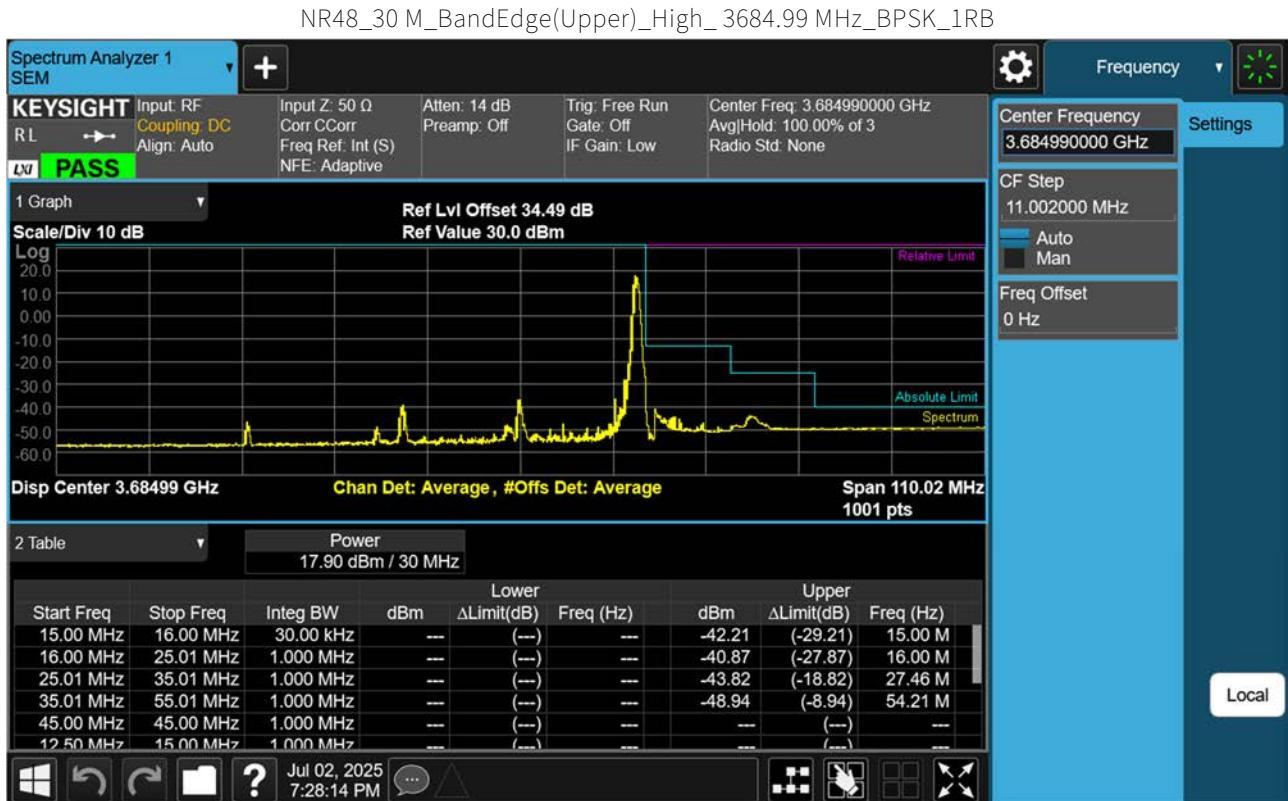




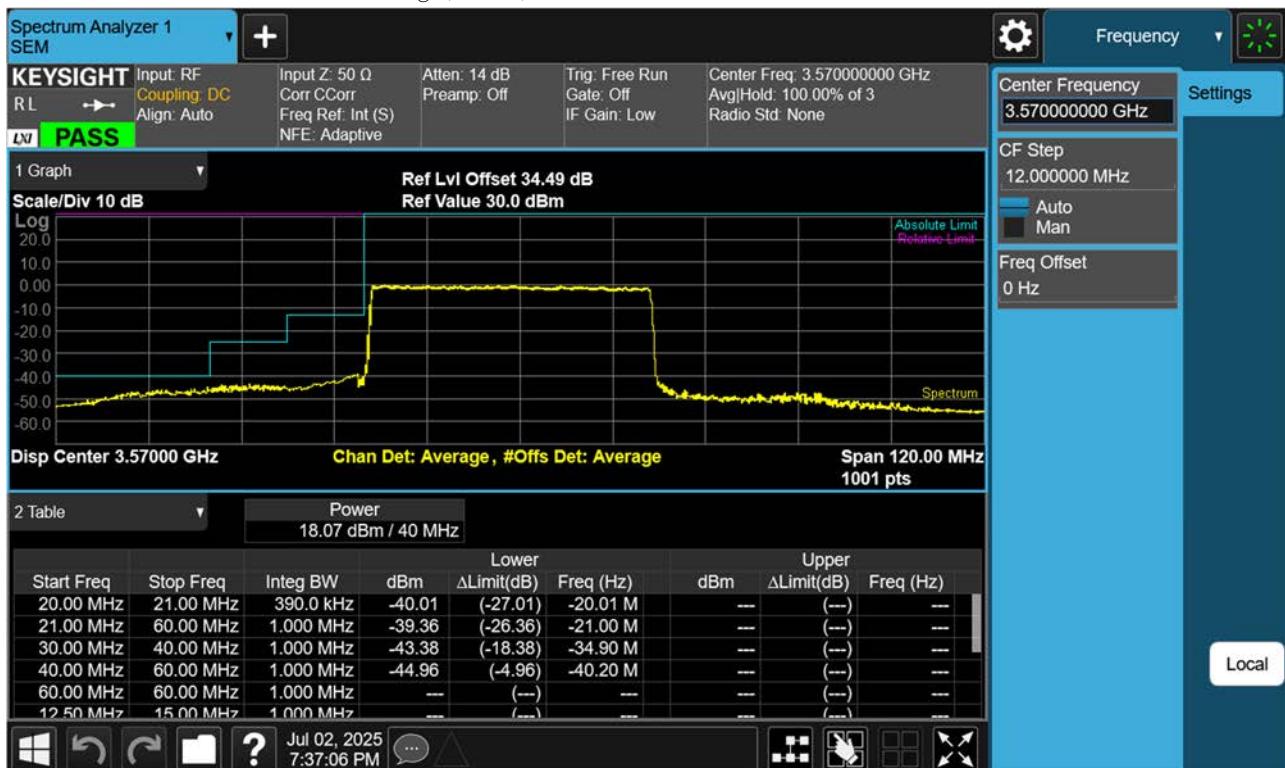
NR48\_30 M\_BandEdge(Upper)\_High\_3684.99 MHz\_BPSK\_FullRB

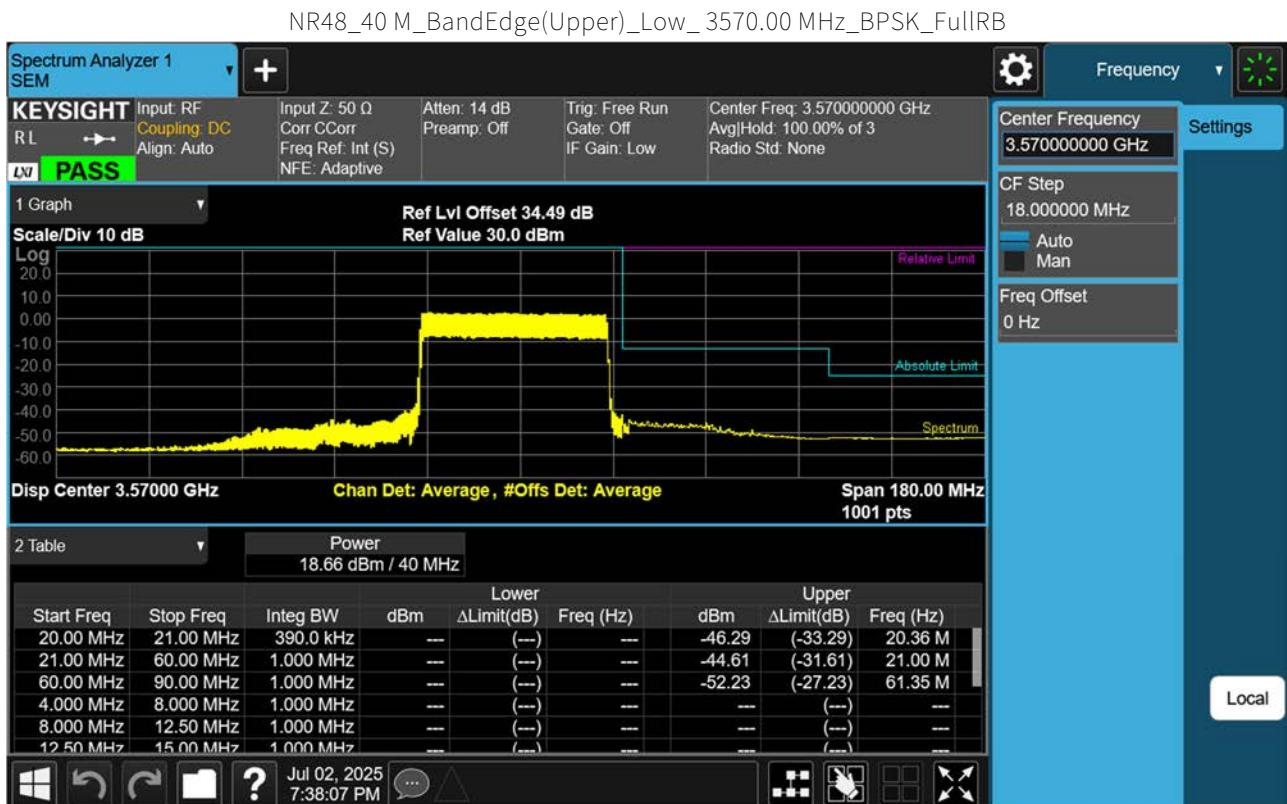


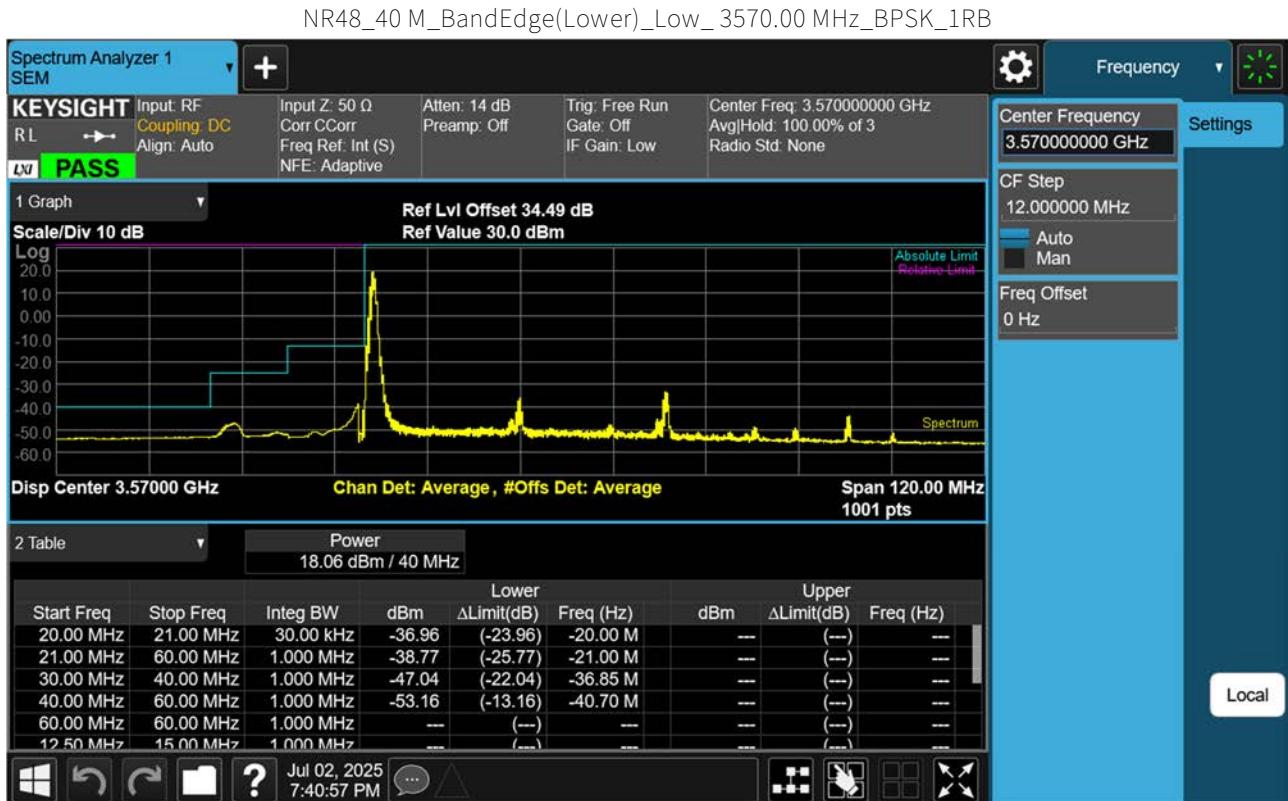


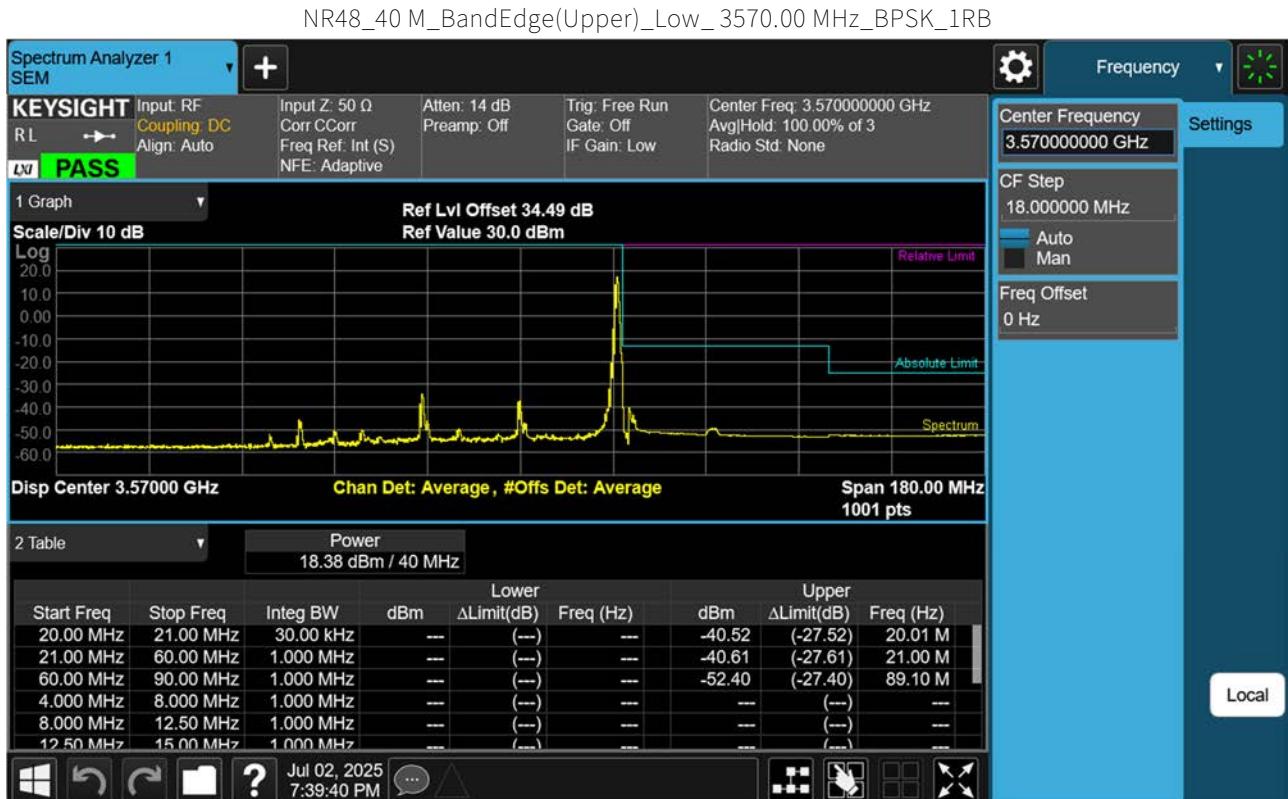


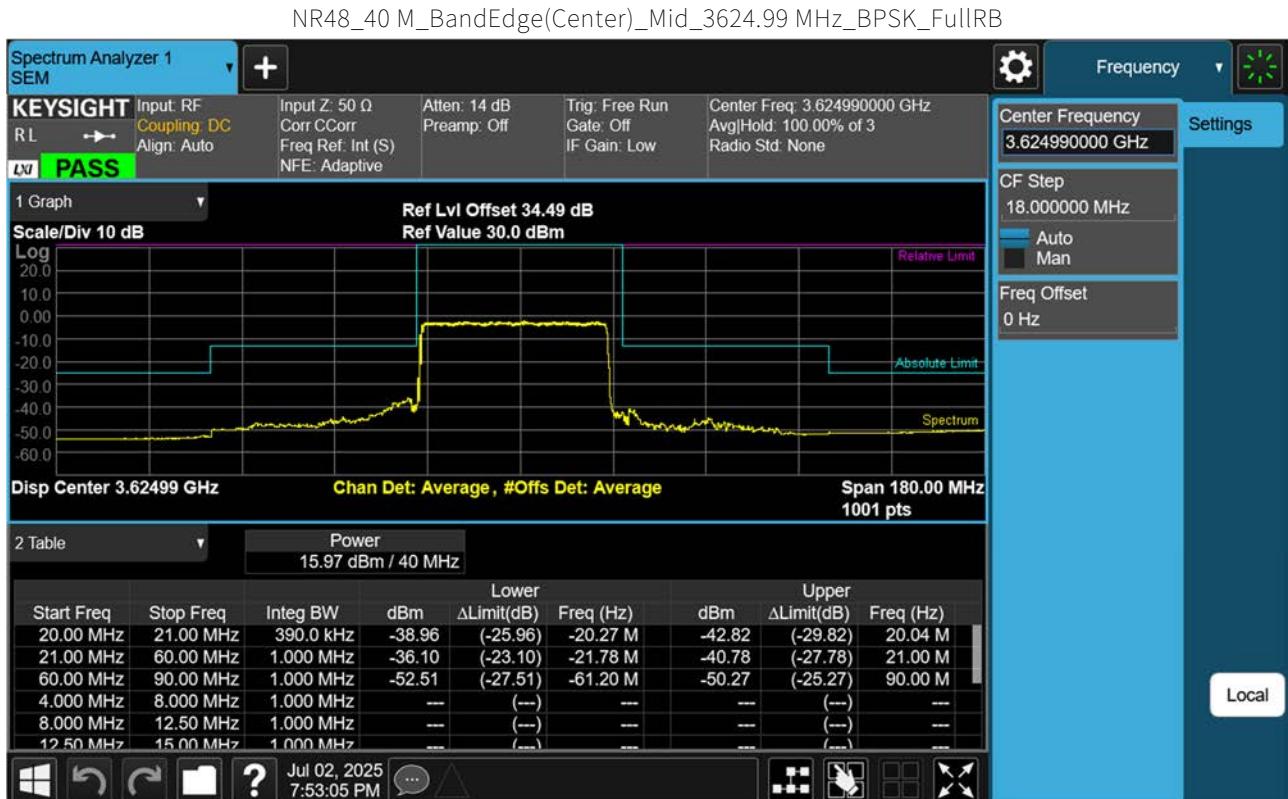
NR48\_40 M BandEdge(Lower)\_Low\_ 3570.00 MHz\_BPSK\_FullRB\_Below 3530MHz

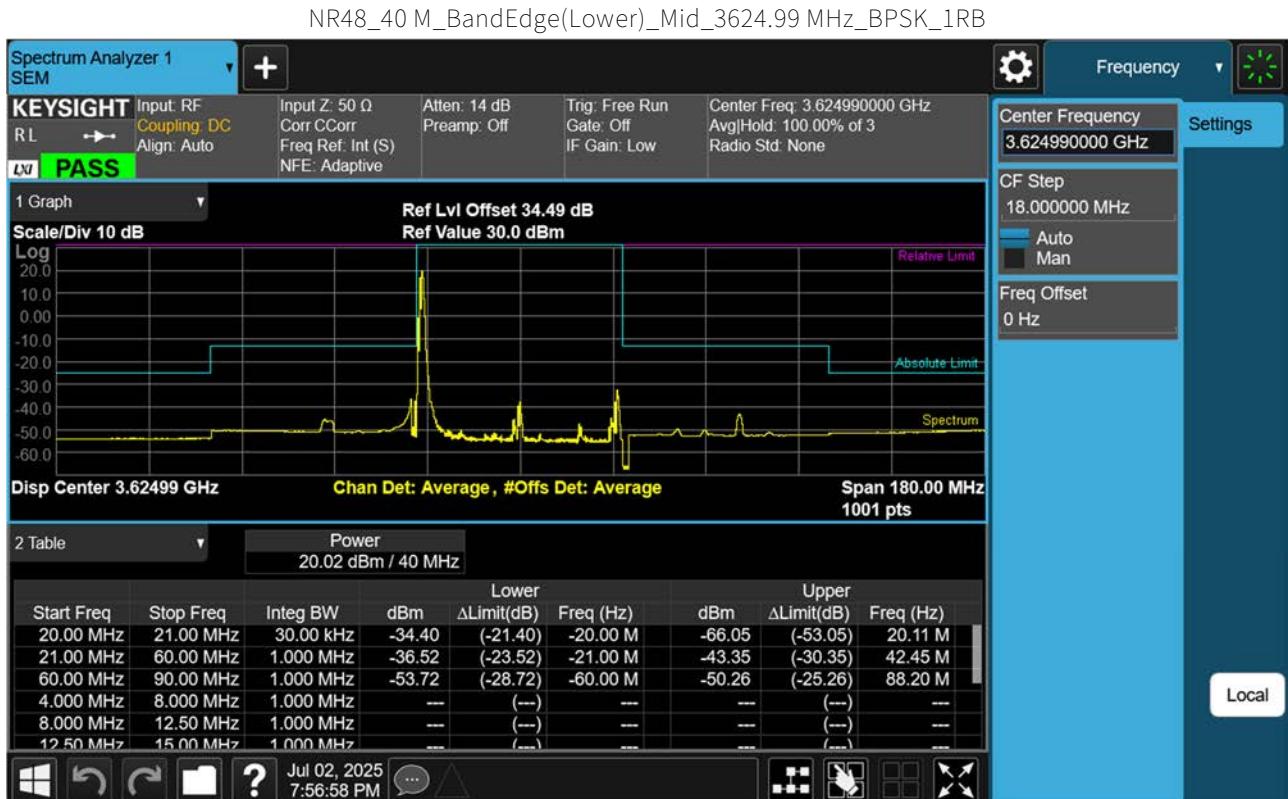


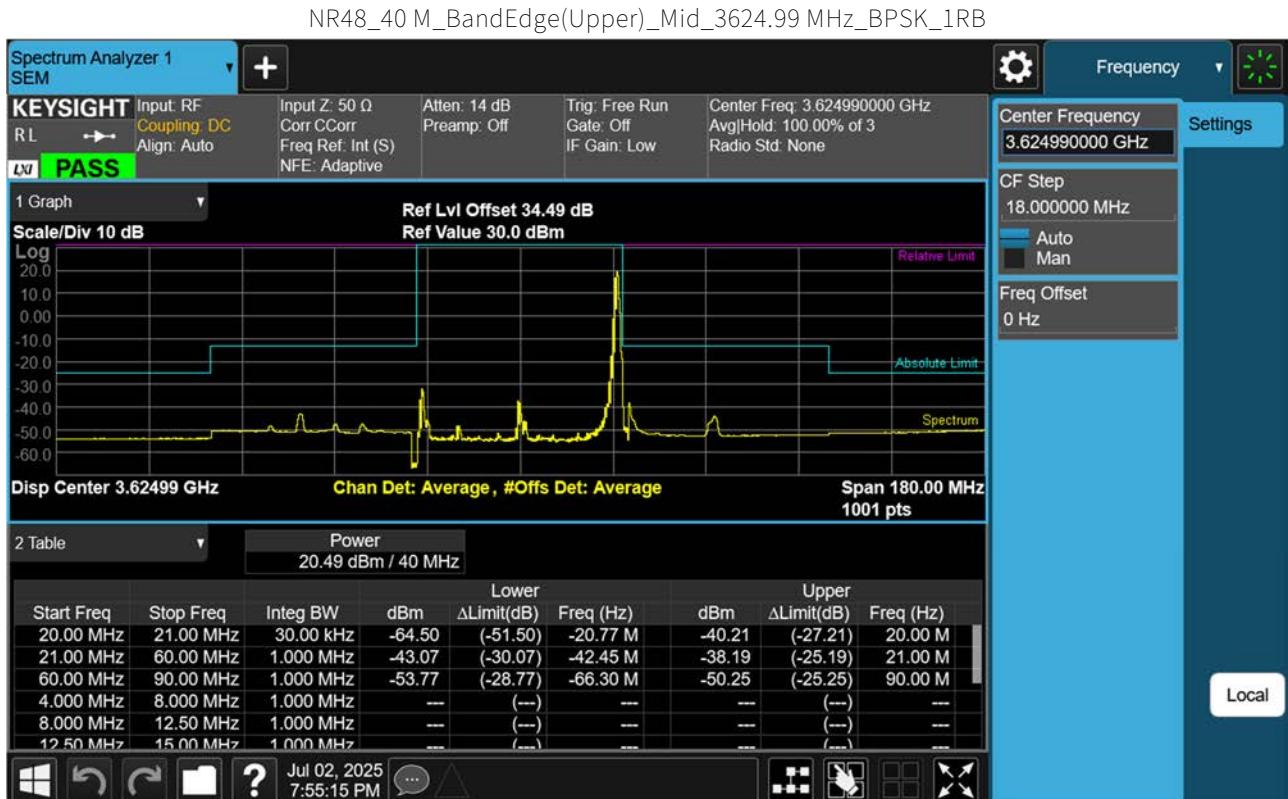


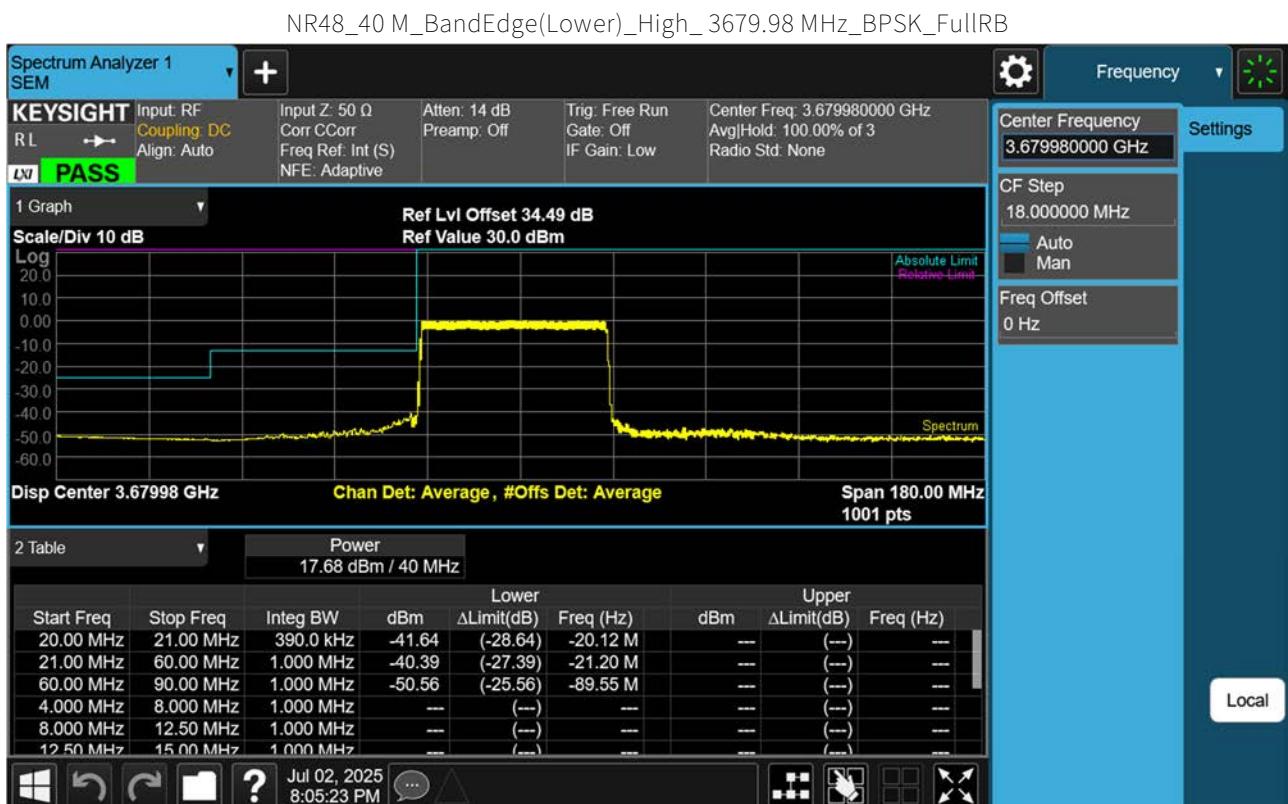






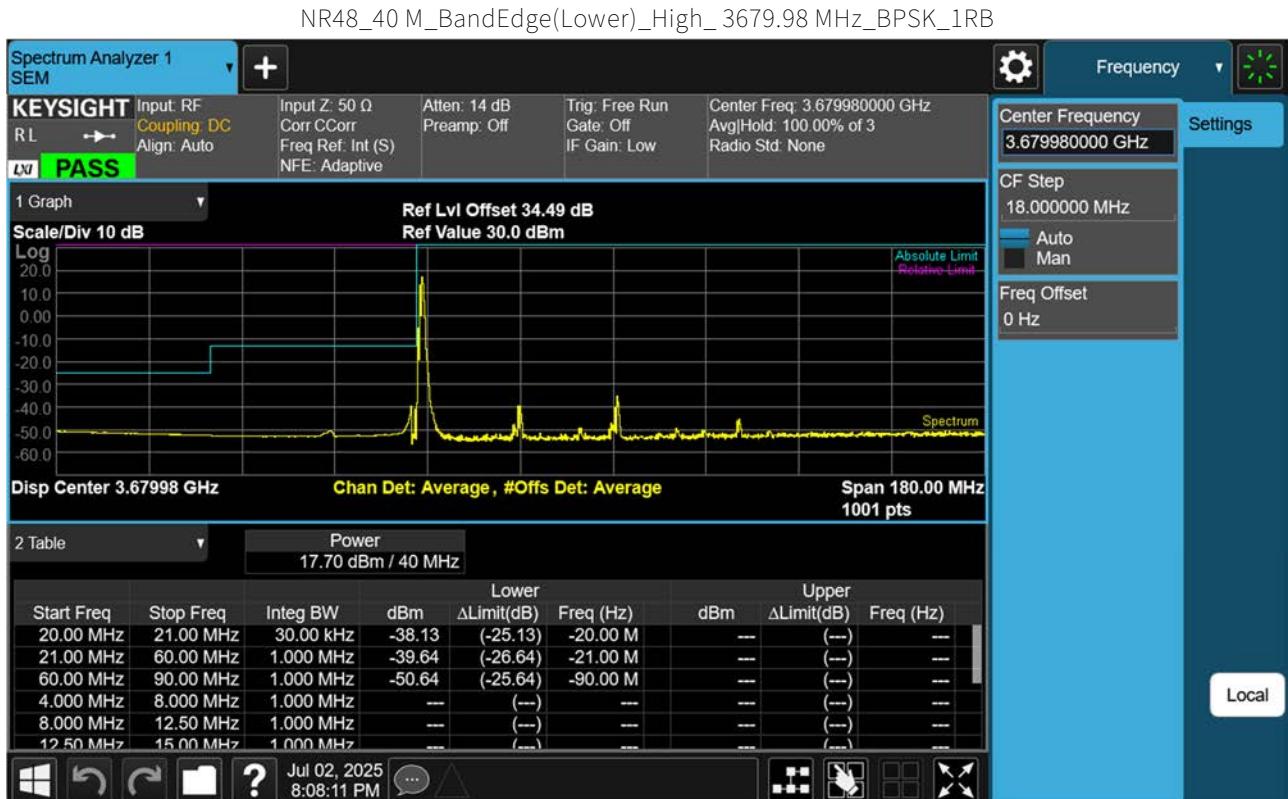


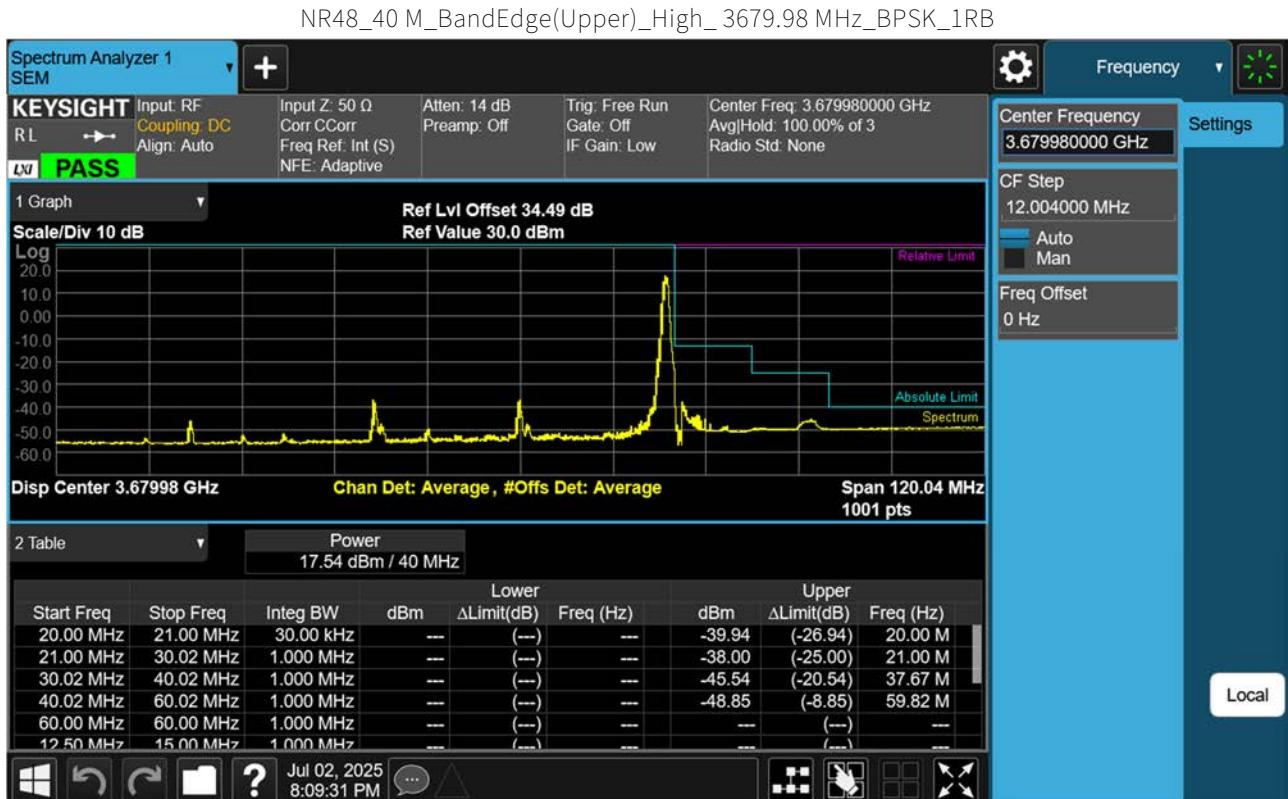




NR48\_40 M\_BandEdge(Upper)\_High\_ 3679.98 MHz\_BPSK\_FullRB\_Above 3720MHz

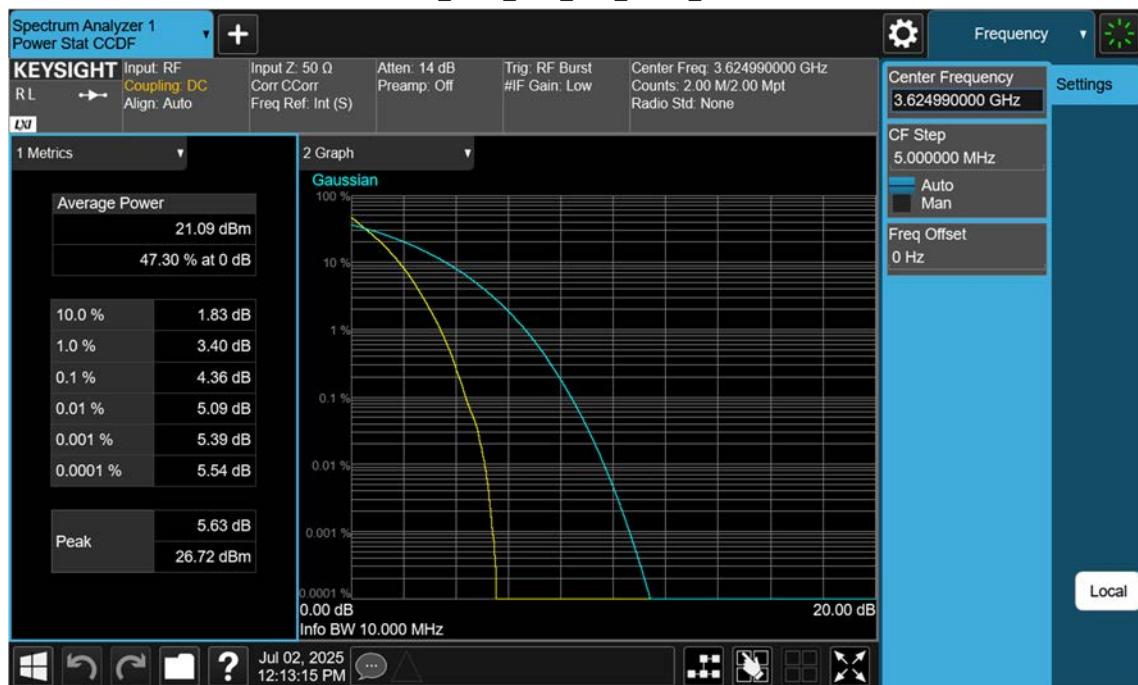




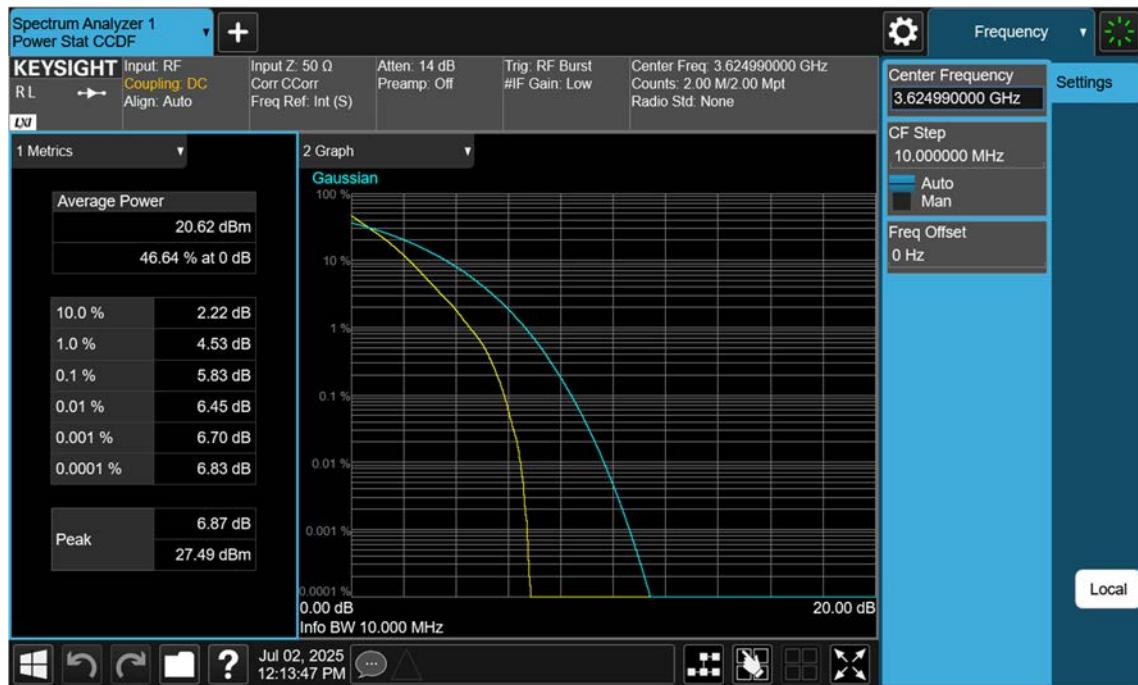


## 12. TEST PLOTS (MIMO2)

## NR48\_10 M\_PAR\_Mid\_BPSK\_FullRB



## NR48\_10 M\_PAR\_Mid\_QPSK\_FullRB



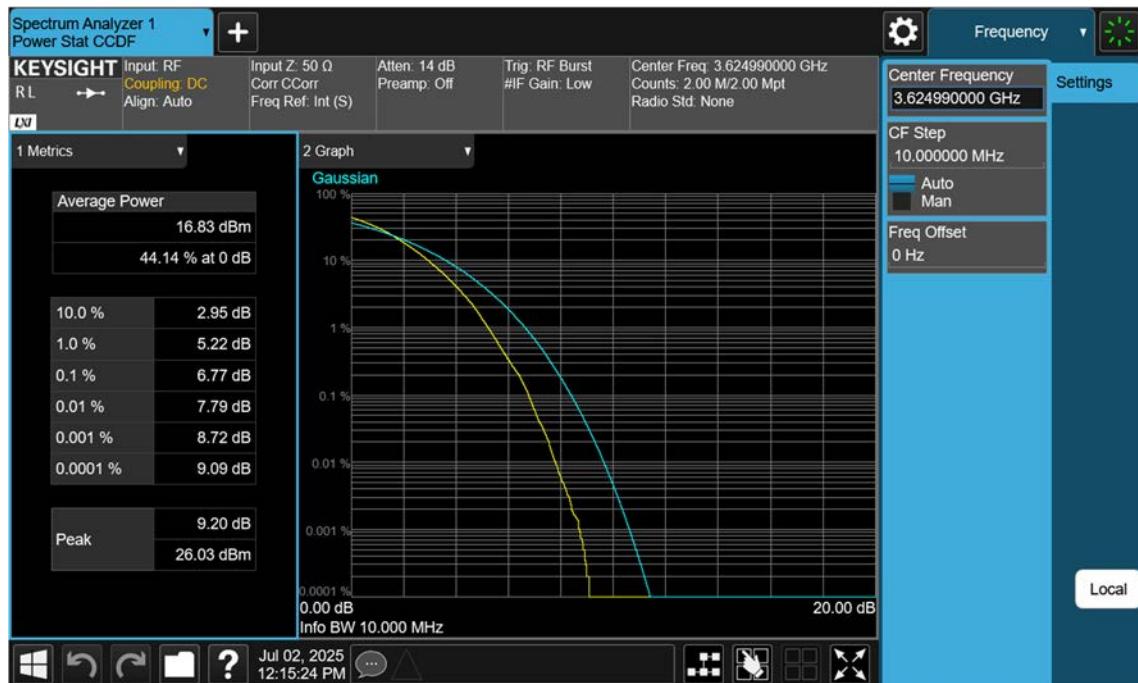
## NR48\_10 M\_PAR\_Mid\_16QAM\_FullRB



## NR48\_10 M\_PAR\_Mid\_64QAM\_FullRB



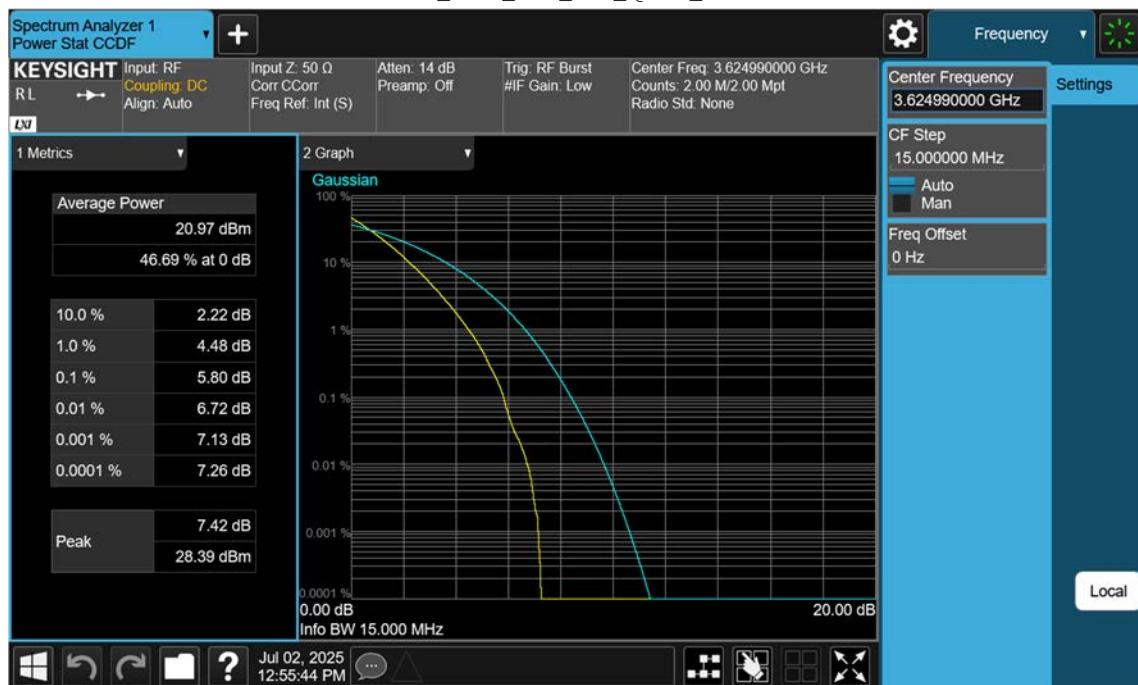
## NR48\_10 M\_PAR\_Mid\_256QAM\_FullRB



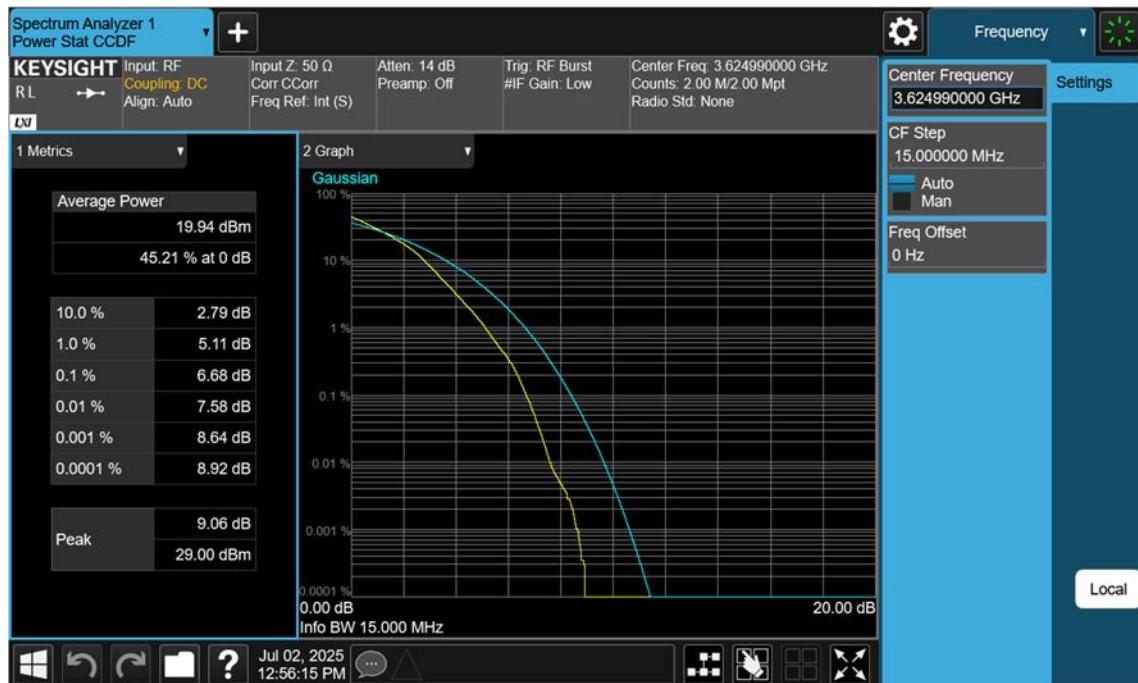
## NR48\_15 M\_PAR\_Mid\_BPSK\_FullRB



## NR48\_15 M\_PAR\_Mid\_QPSK\_FullRB



NR48\_15 M\_PAR\_Mid\_16QAM\_FullRB



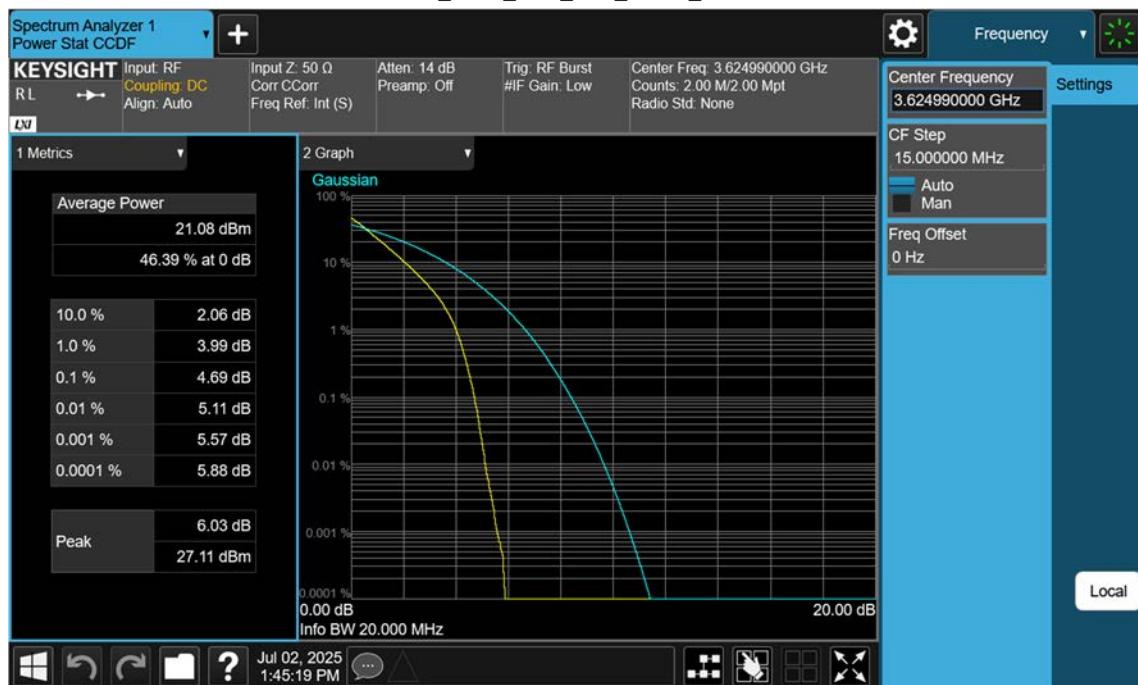
NR48\_15 M\_PAR\_Mid\_64QAM\_FullRB



## NR48\_15 M\_PAR\_Mid\_256QAM\_FullRB



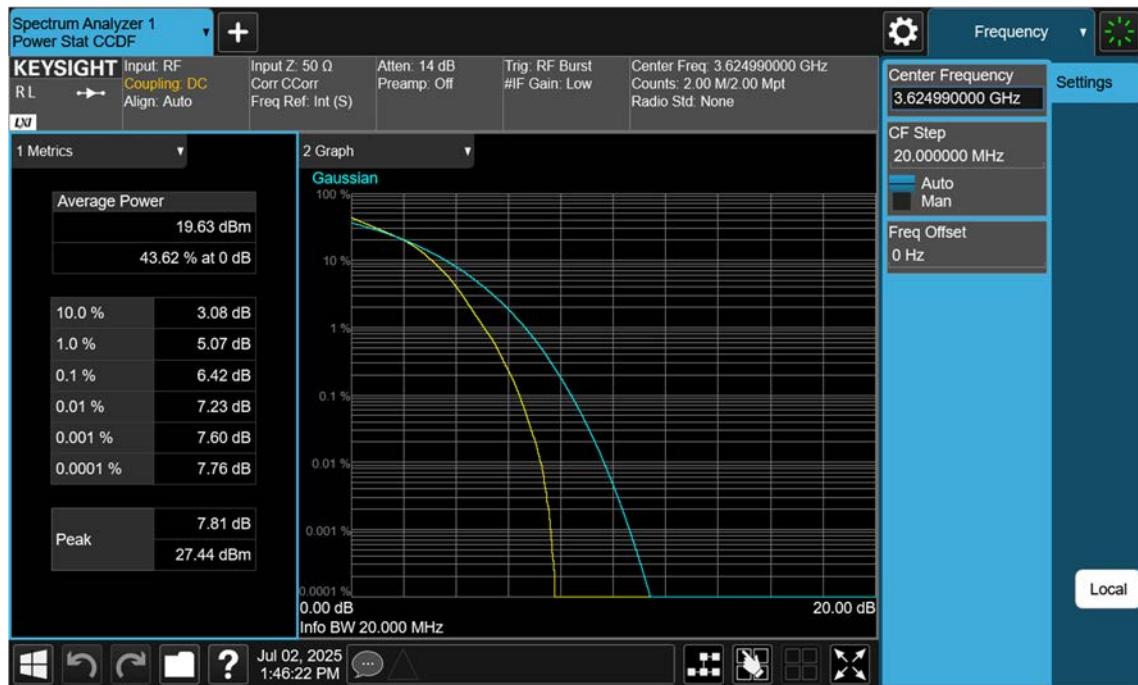
## NR48\_20 M\_PAR\_Mid\_BPSK\_FullRB



## NR48\_20 M\_PAR\_Mid\_QPSK\_FullRB



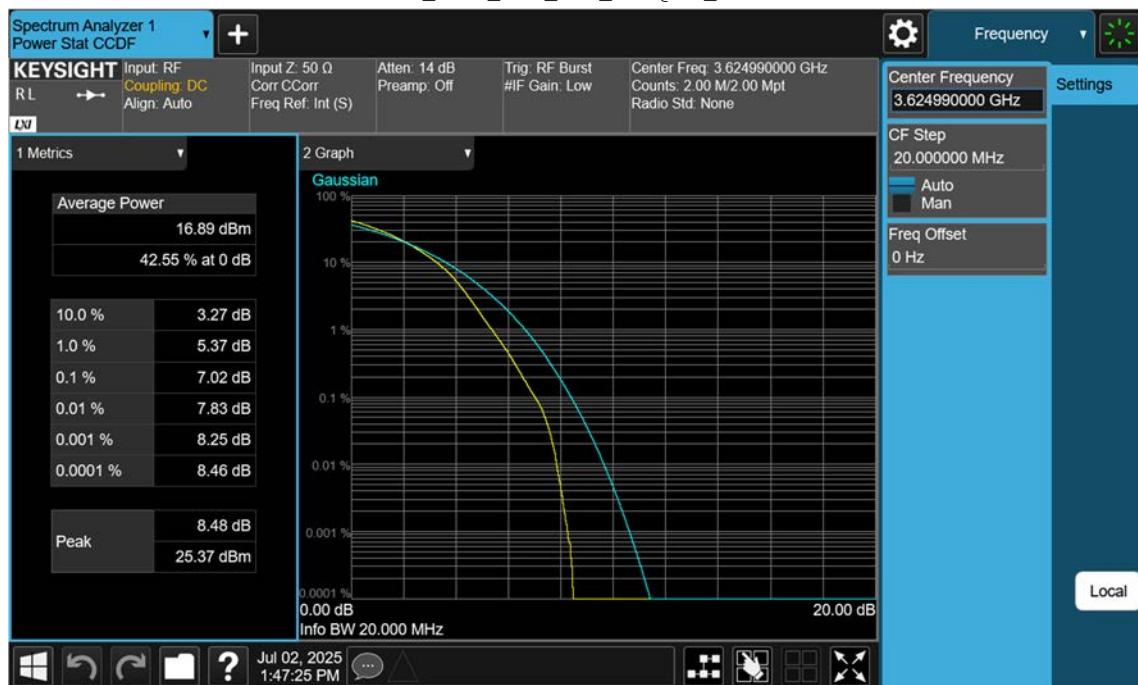
## NR48\_20 M\_PAR\_Mid\_16QAM\_FullRB



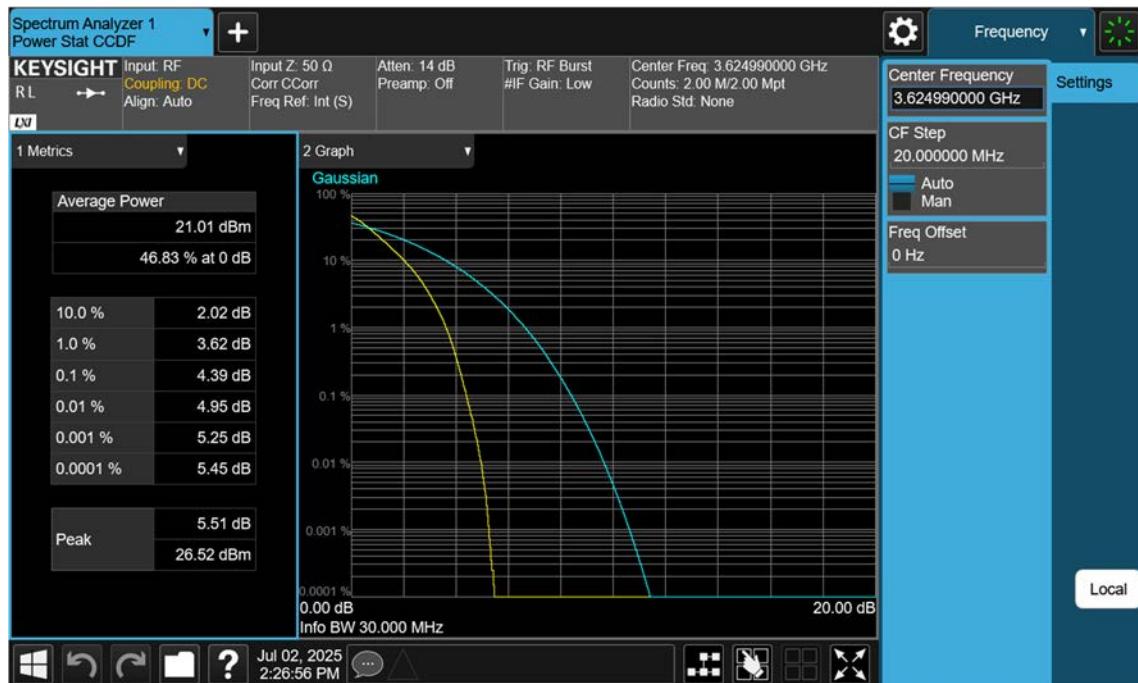
## NR48\_20 M\_PAR\_Mid\_64QAM\_FullRB



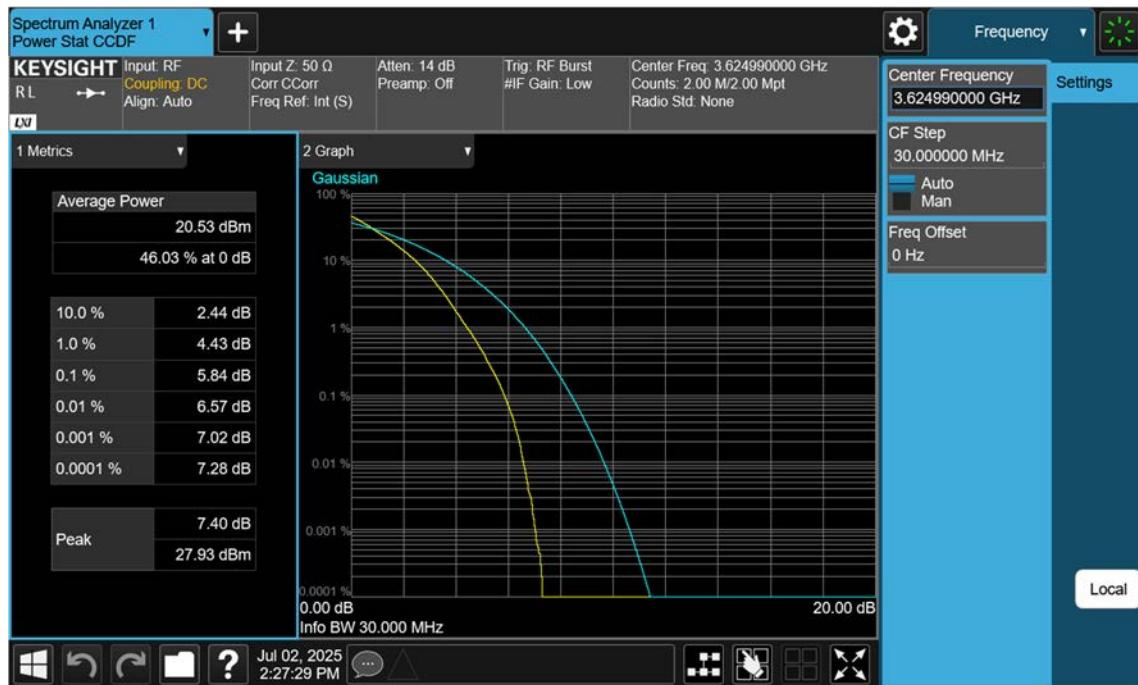
NR48\_20 M\_PAR\_Mid\_256QAM\_FullRB



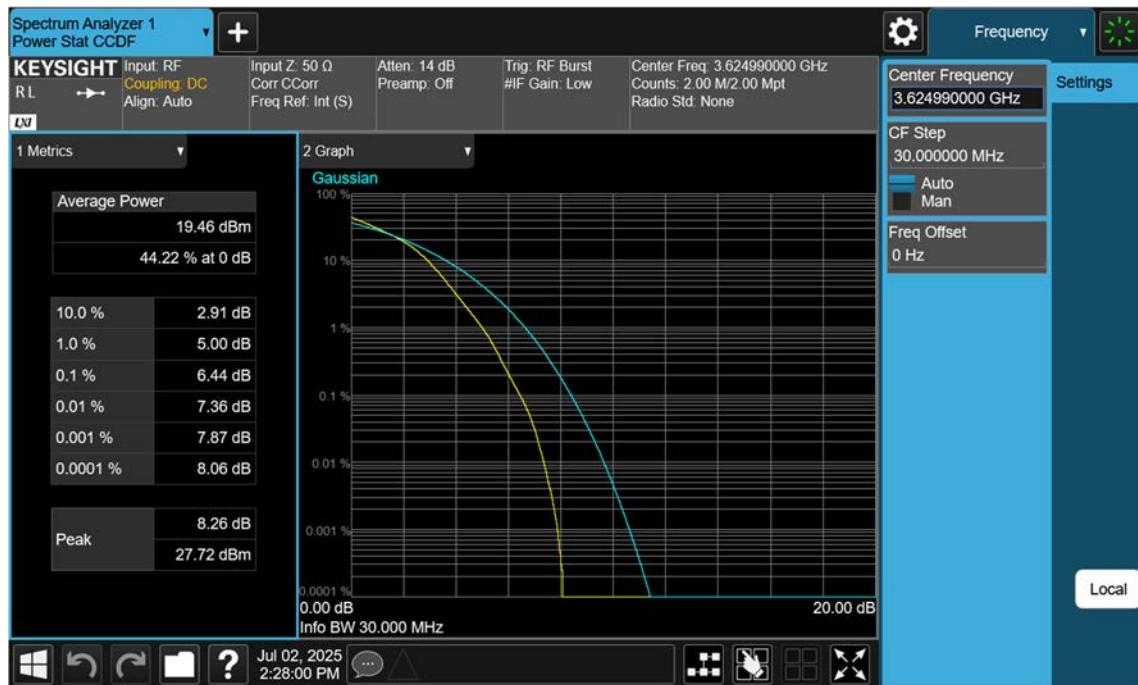
## NR48\_30 M\_PAR\_Mid\_BPSK\_FullRB



## NR48\_30 M\_PAR\_Mid\_QPSK\_FullRB



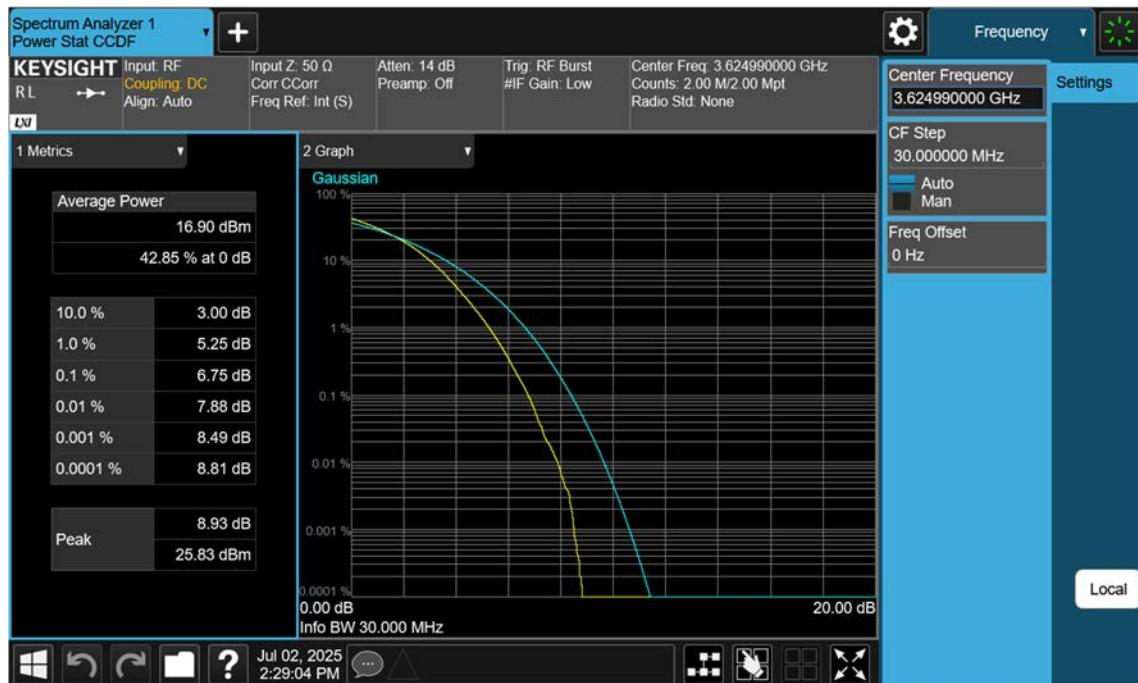
## NR48\_30 M\_PAR\_Mid\_16QAM\_FullRB



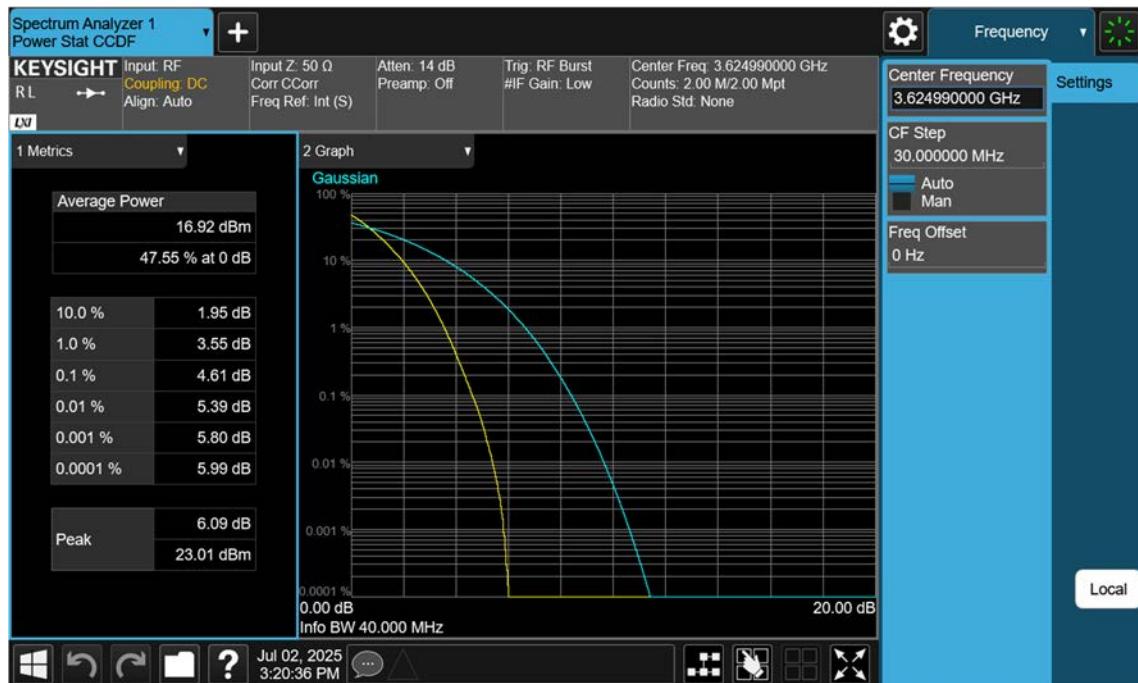
NR48\_30 M\_PAR\_Mid\_64QAM\_FullRB



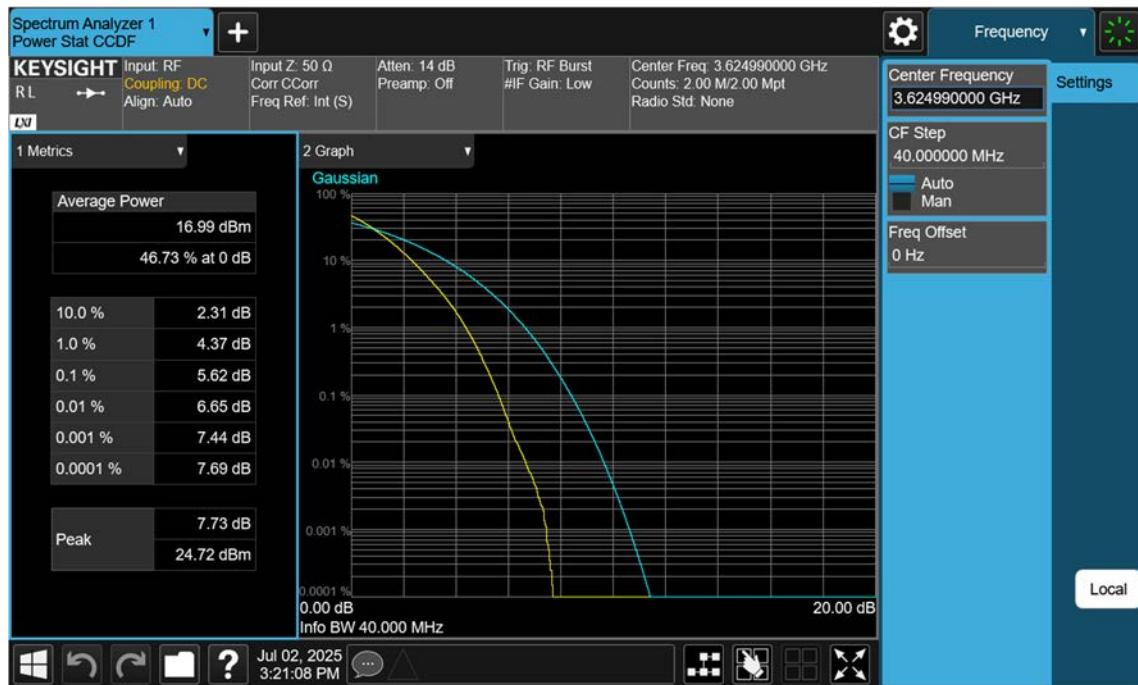
NR48\_30 M\_PAR\_Mid\_256QAM\_FullRB



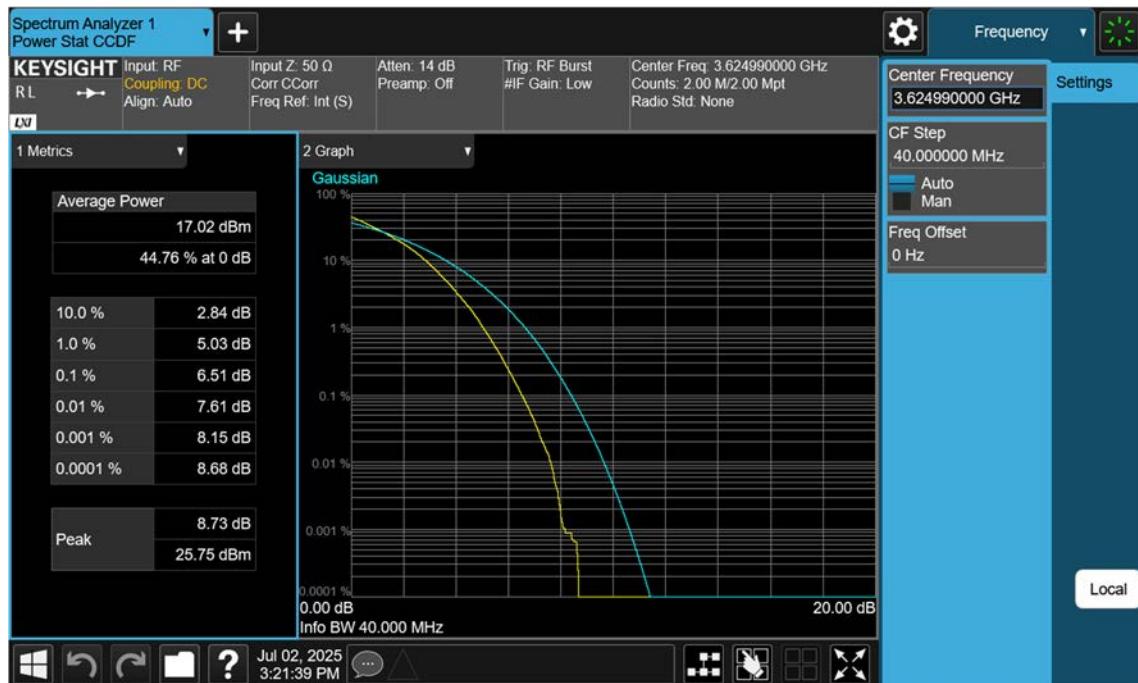
## NR48\_40 M\_PAR\_Mid\_BPSK\_FullRB



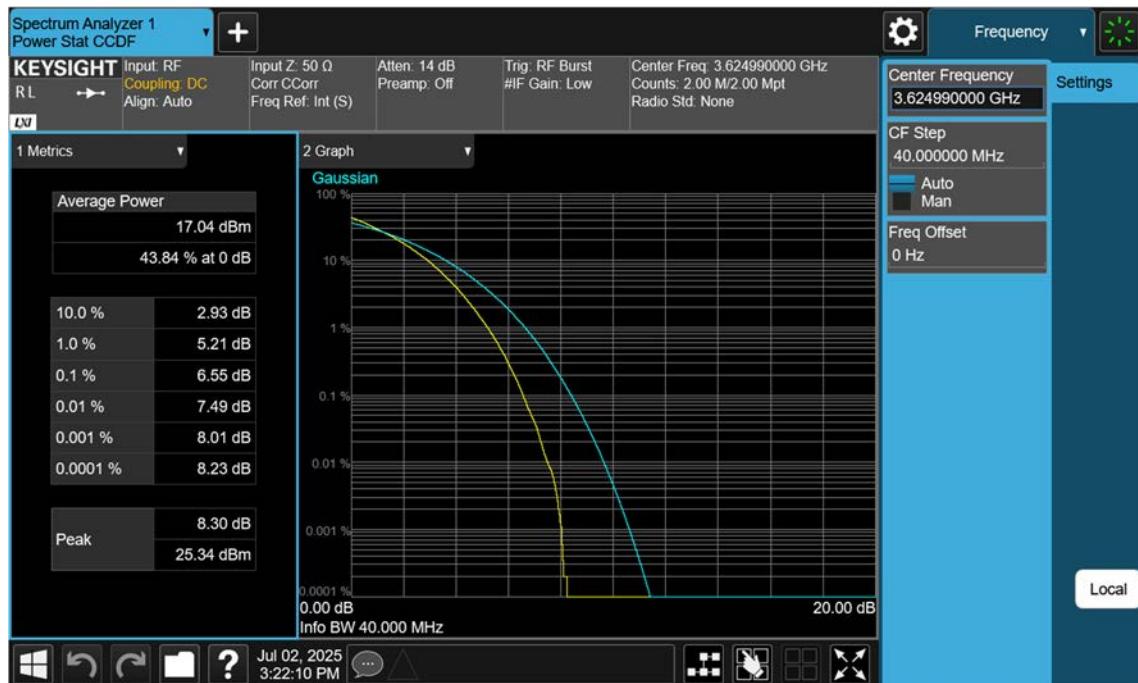
## NR48\_40 M\_PAR\_Mid\_QPSK\_FullRB



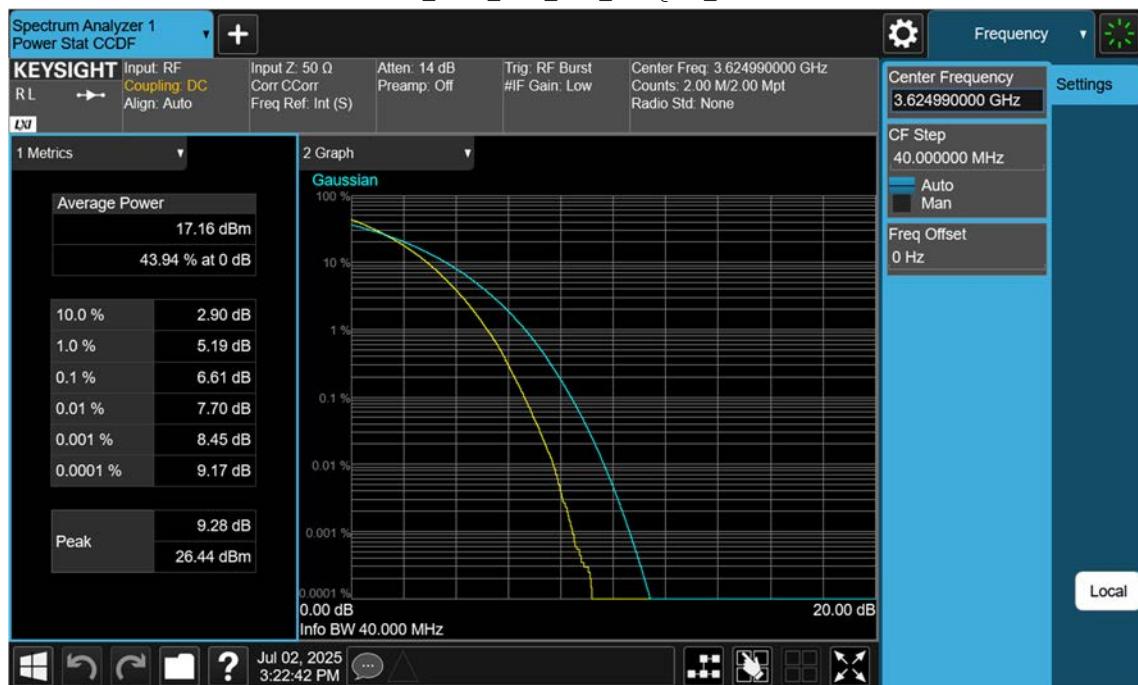
## NR48\_40 M\_PAR\_Mid\_16QAM\_FullRB



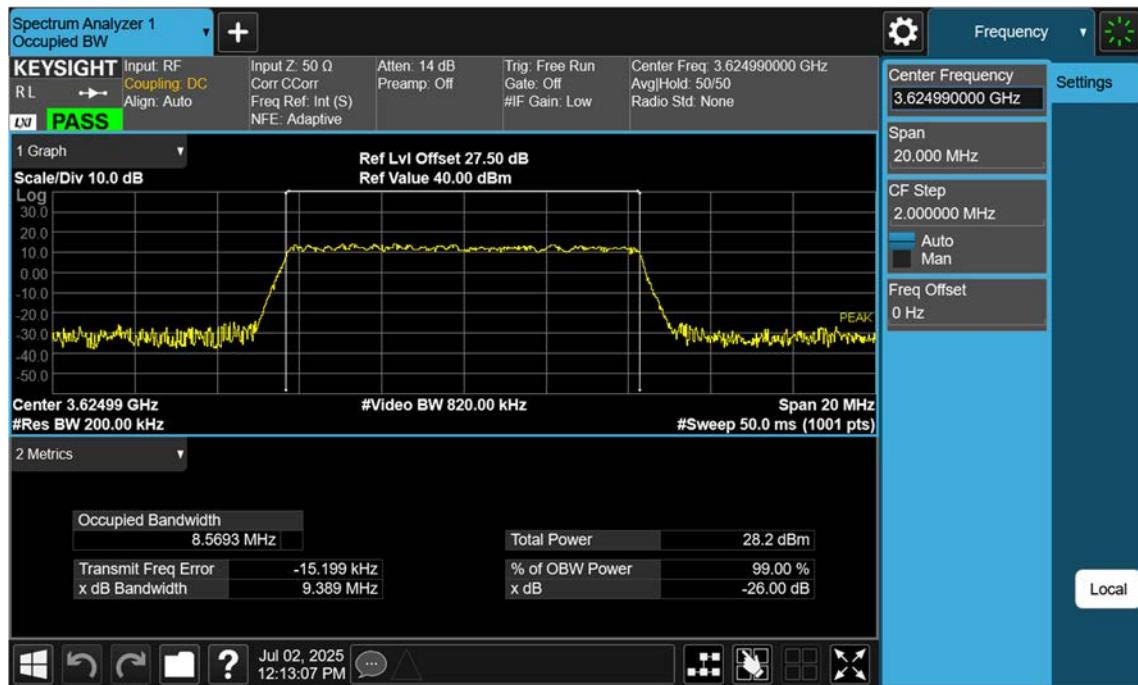
NR48\_40 M\_PAR\_Mid\_64QAM\_FullRB



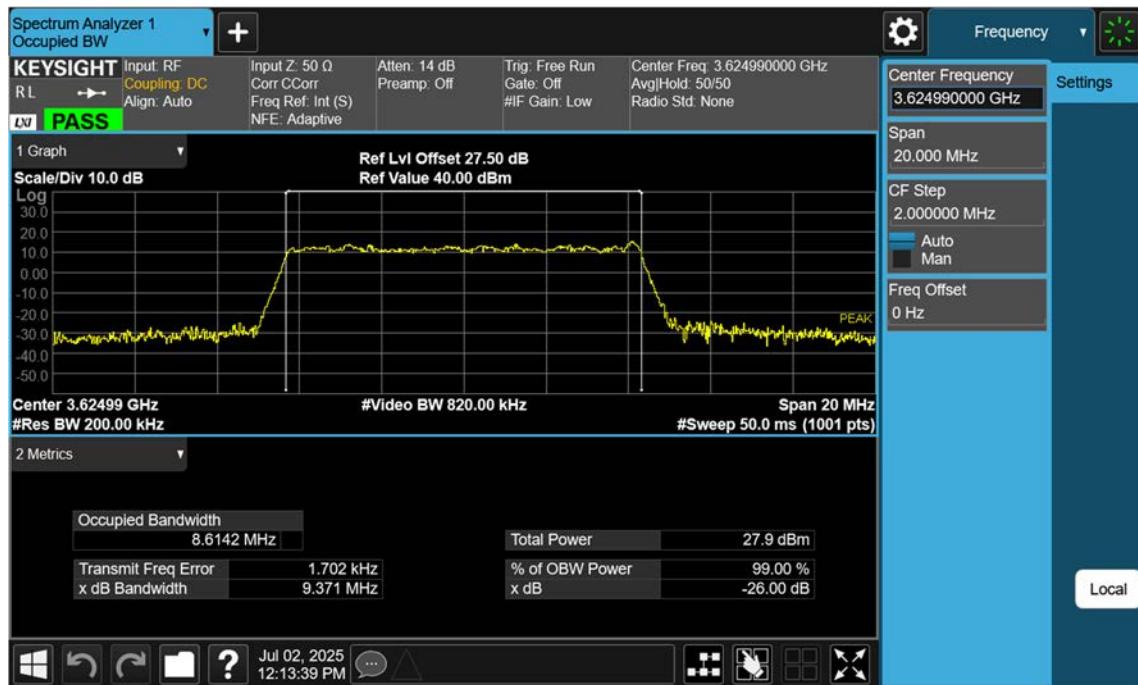
NR48\_40 M\_PAR\_Mid\_256QAM\_FullRB



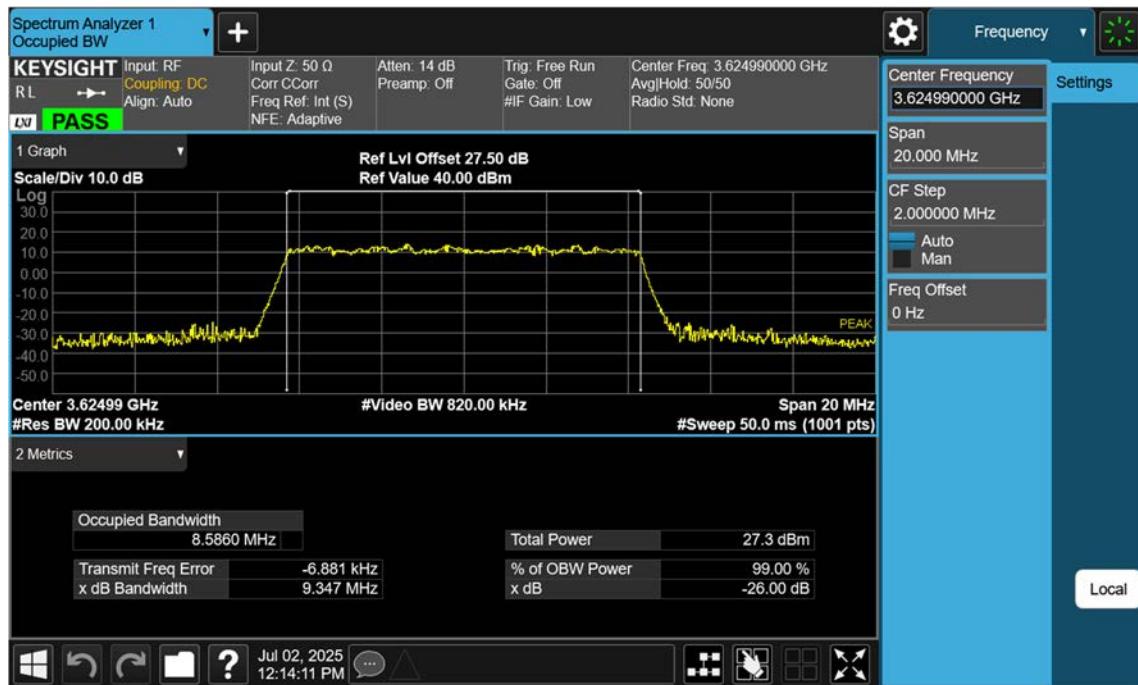
## NR48\_10 M\_OBW\_Mid\_BPSK\_FullIRB

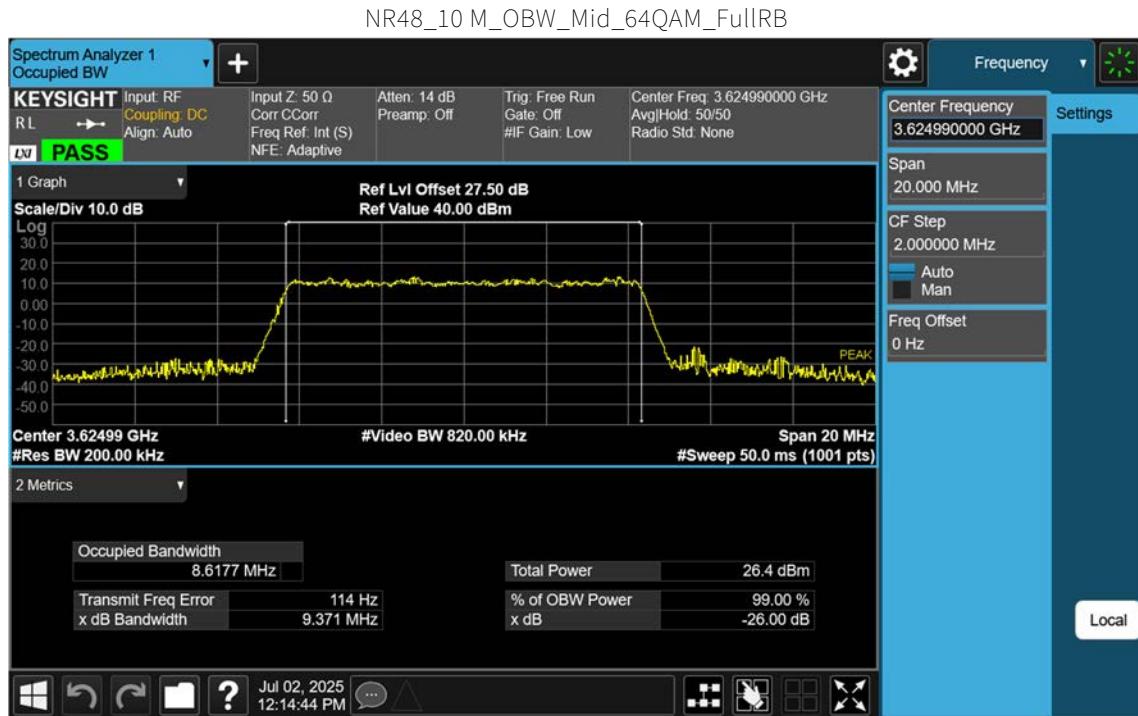


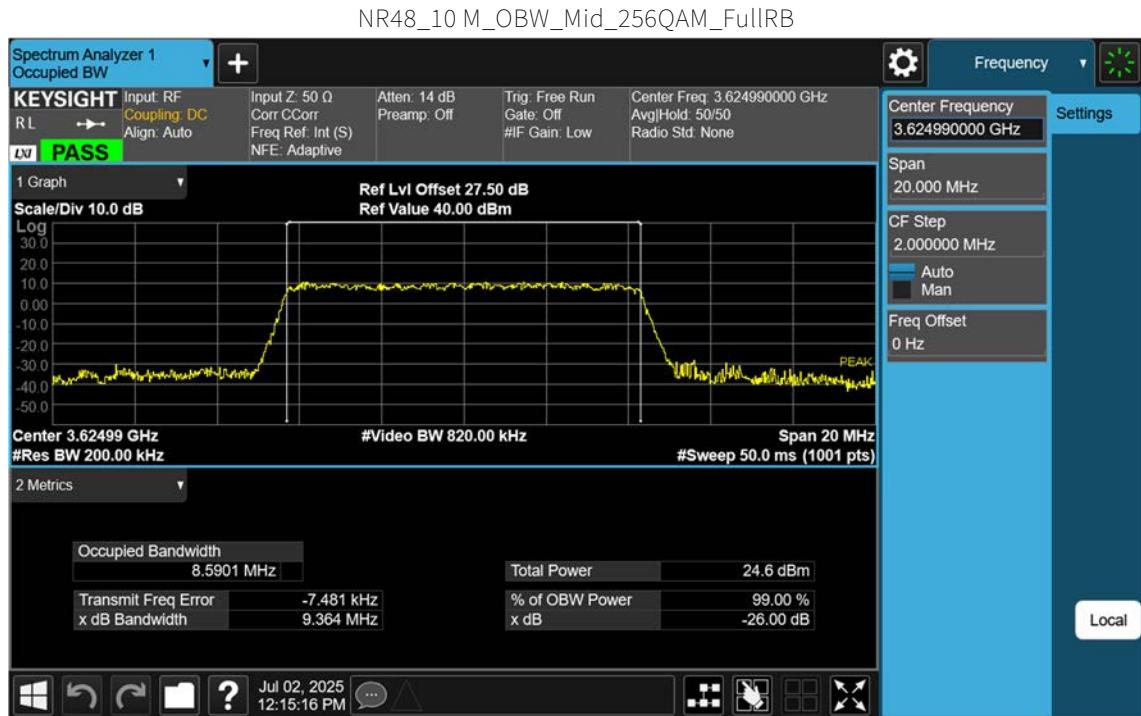
## NR48\_10 M\_OBW\_Mid\_QPSK\_FullIRB



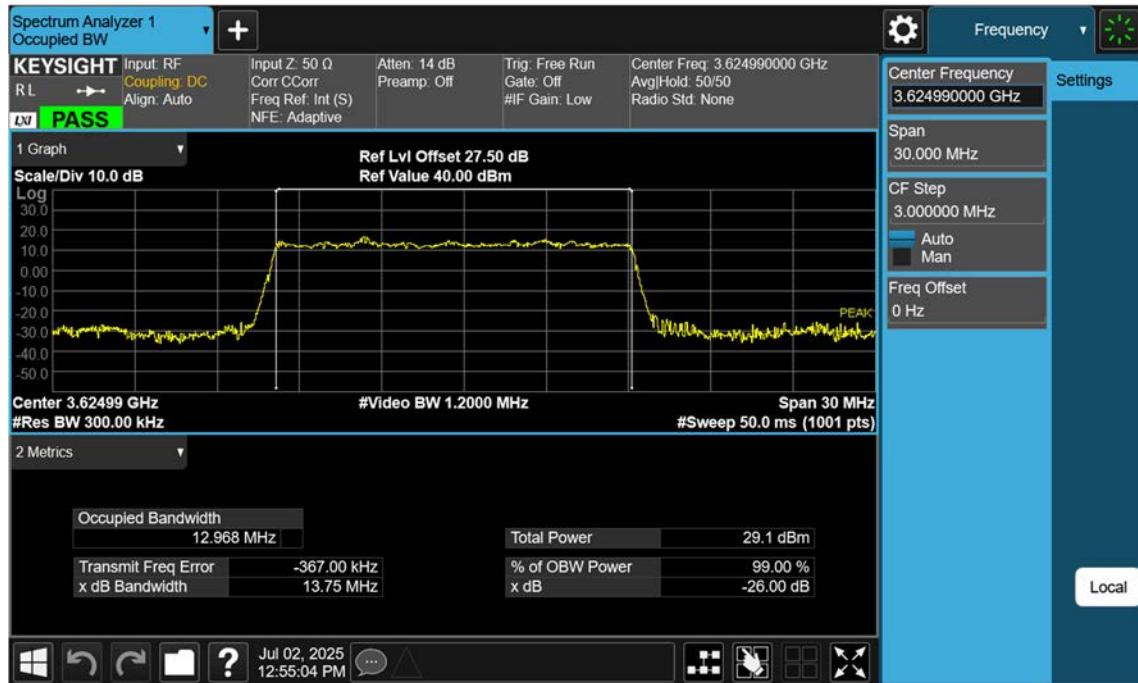
## NR48\_10 M\_OBW\_Mid\_16QAM\_FullRB



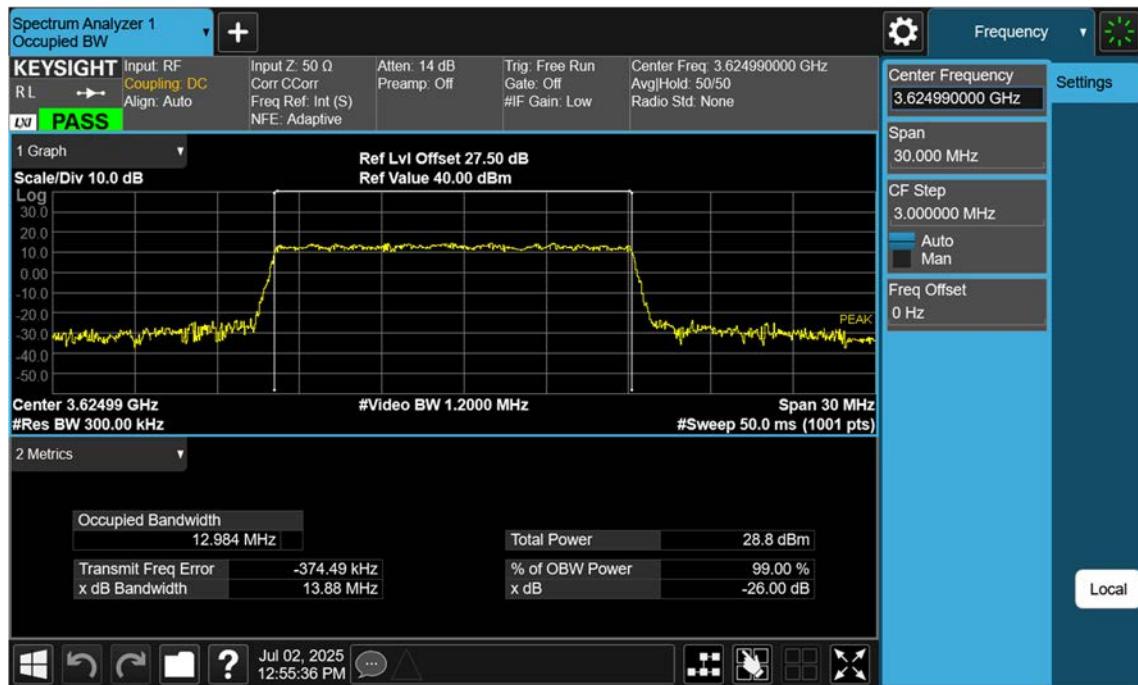


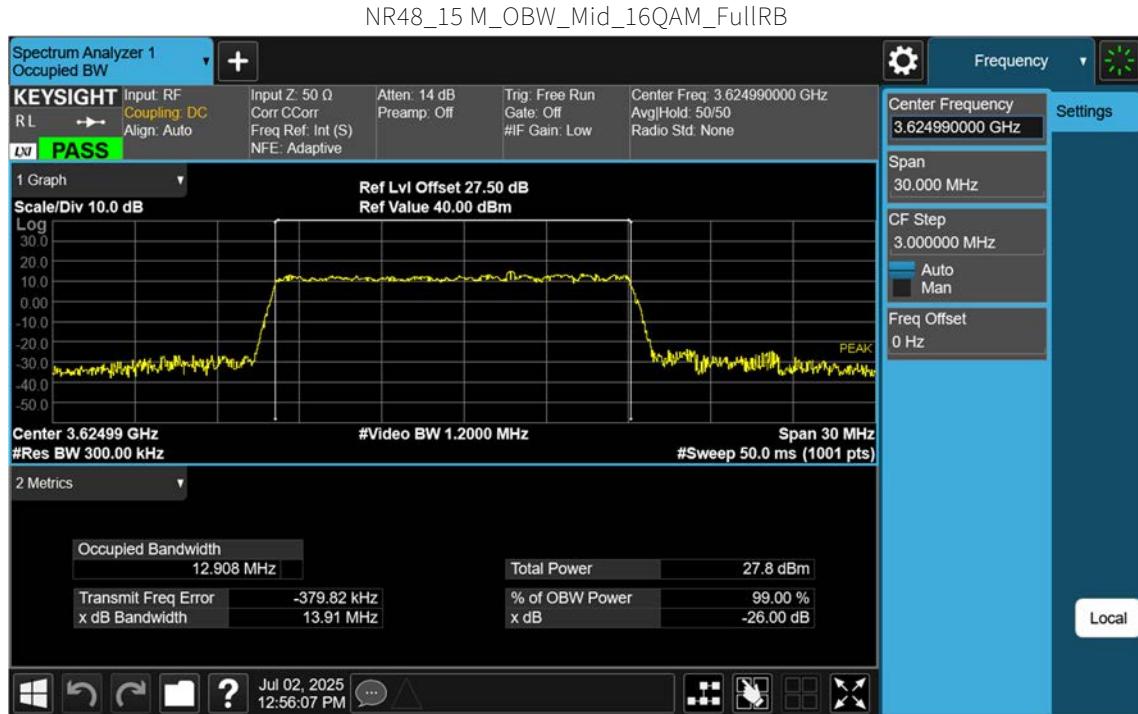


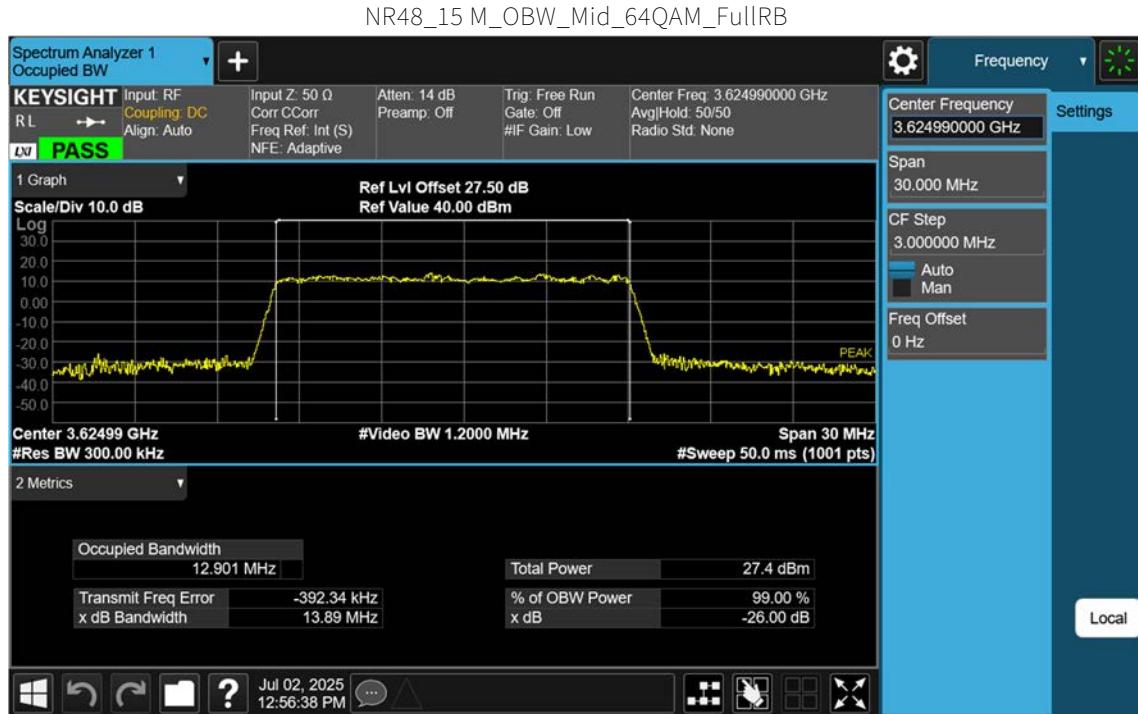
## NR48\_15 M\_OBW\_Mid\_BPSK\_FullIRB

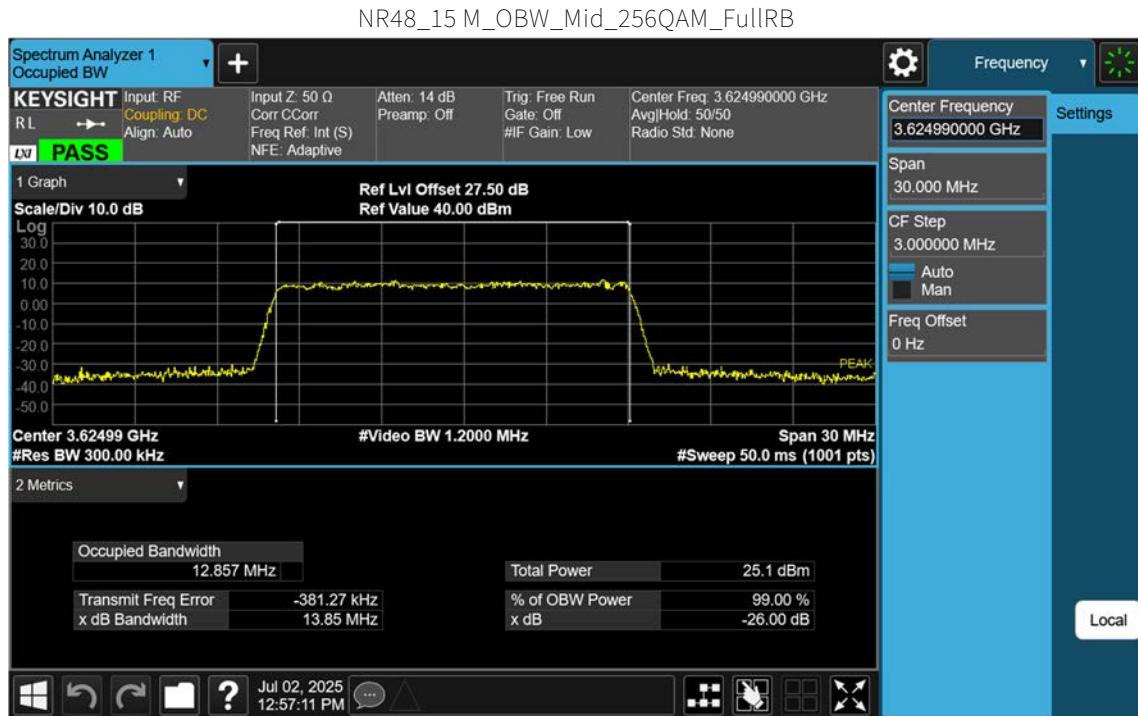


## NR48\_15 M\_OBW\_Mid\_QPSK\_FullIRB

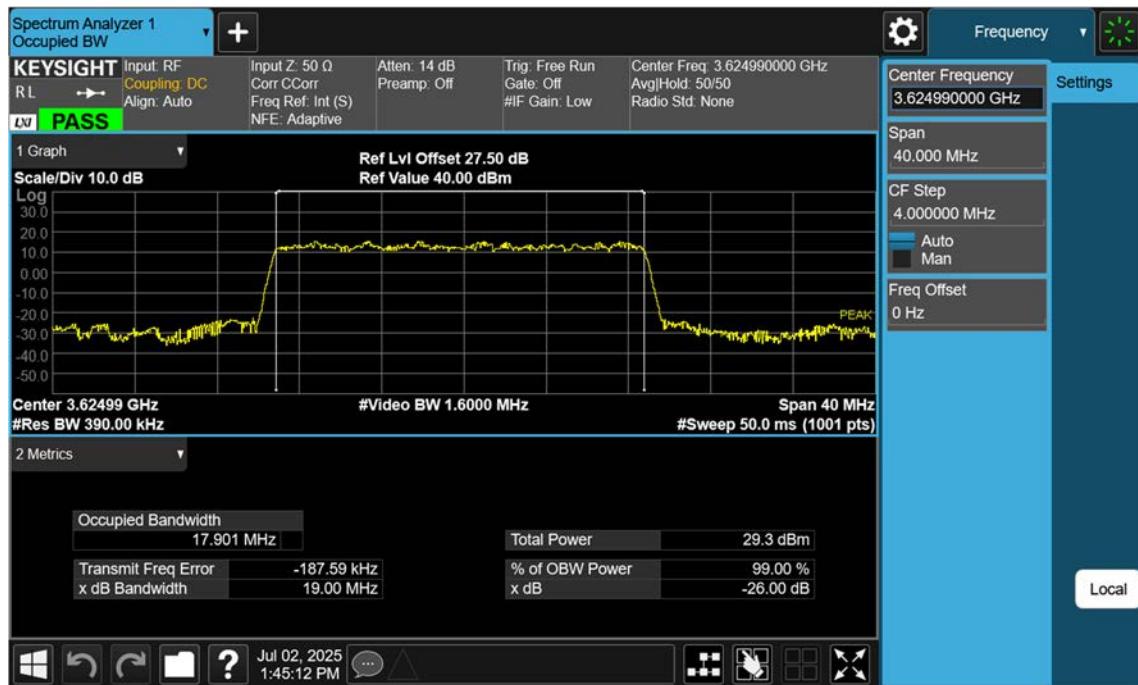




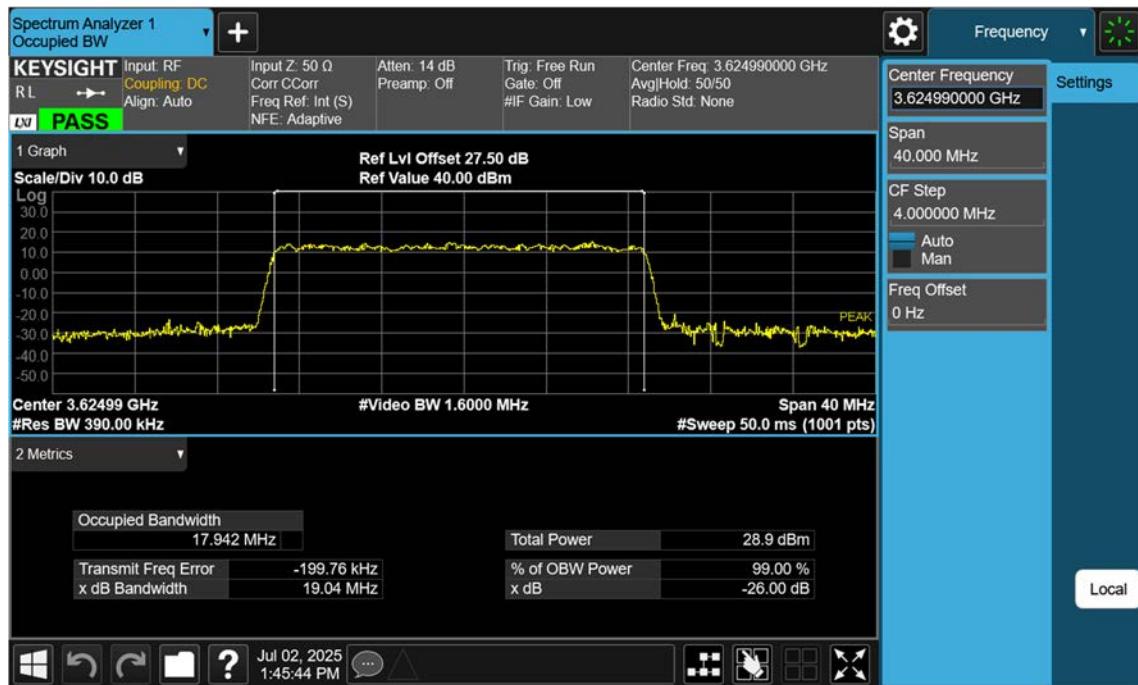


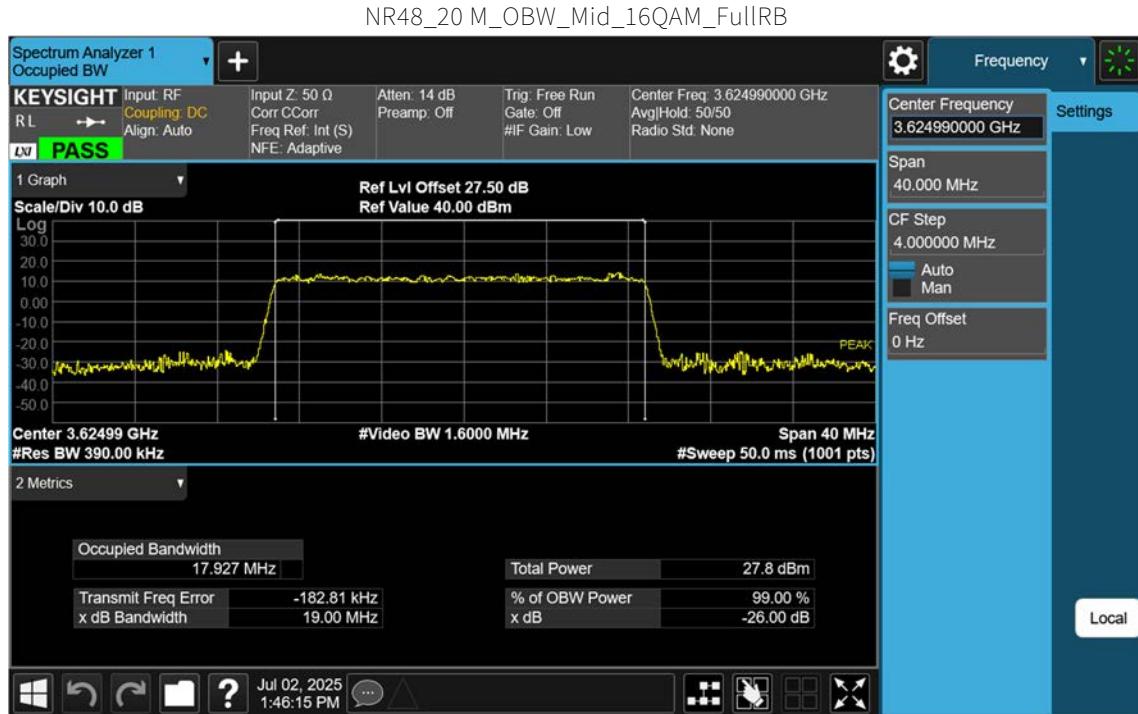


## NR48\_20 M\_OBW\_Mid\_BPSK\_FullIRB

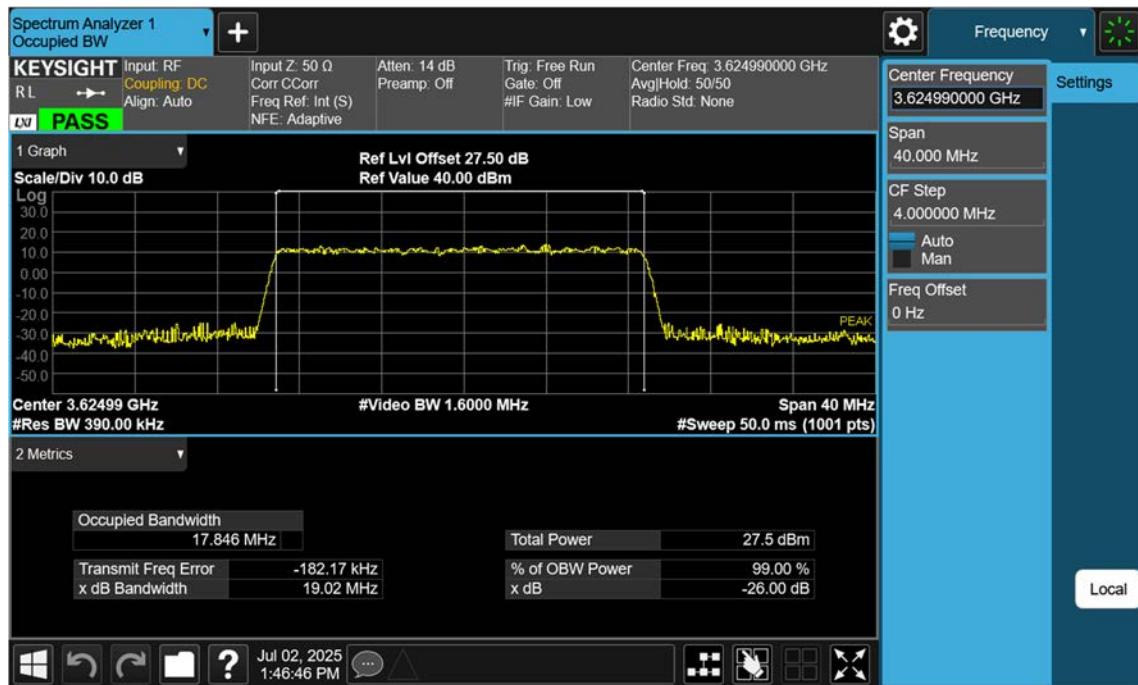


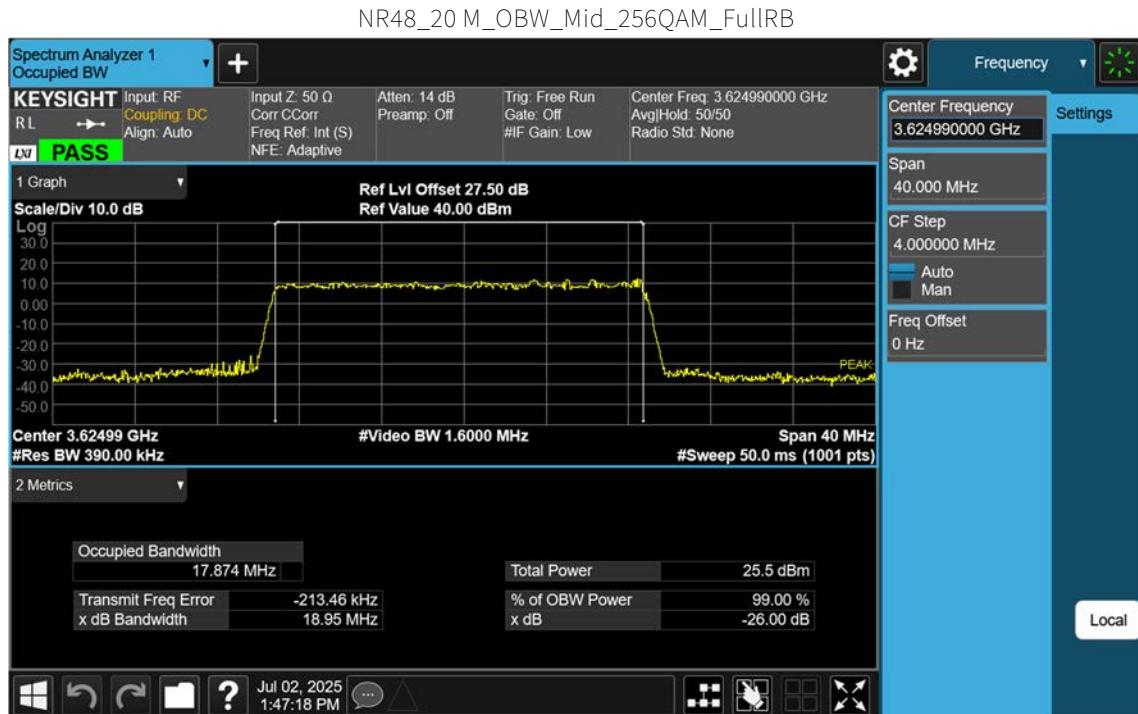
## NR48\_20 M\_OBW\_Mid\_QPSK\_FullIRB



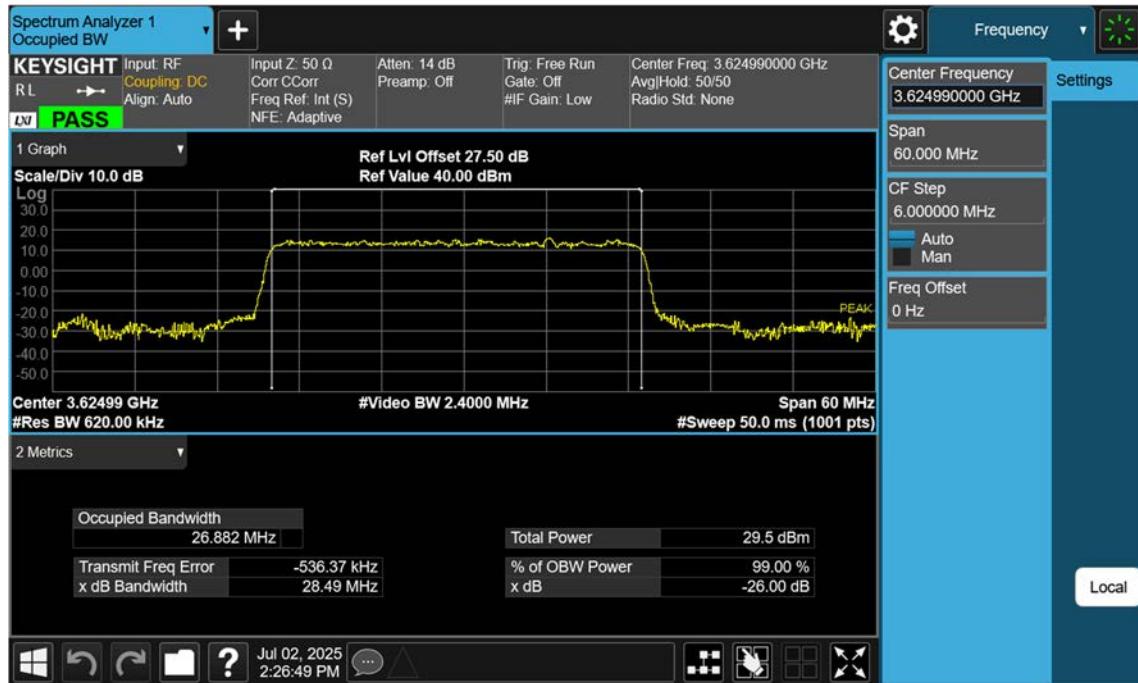


## NR48\_20 M\_OBW\_Mid\_64QAM\_FullRB

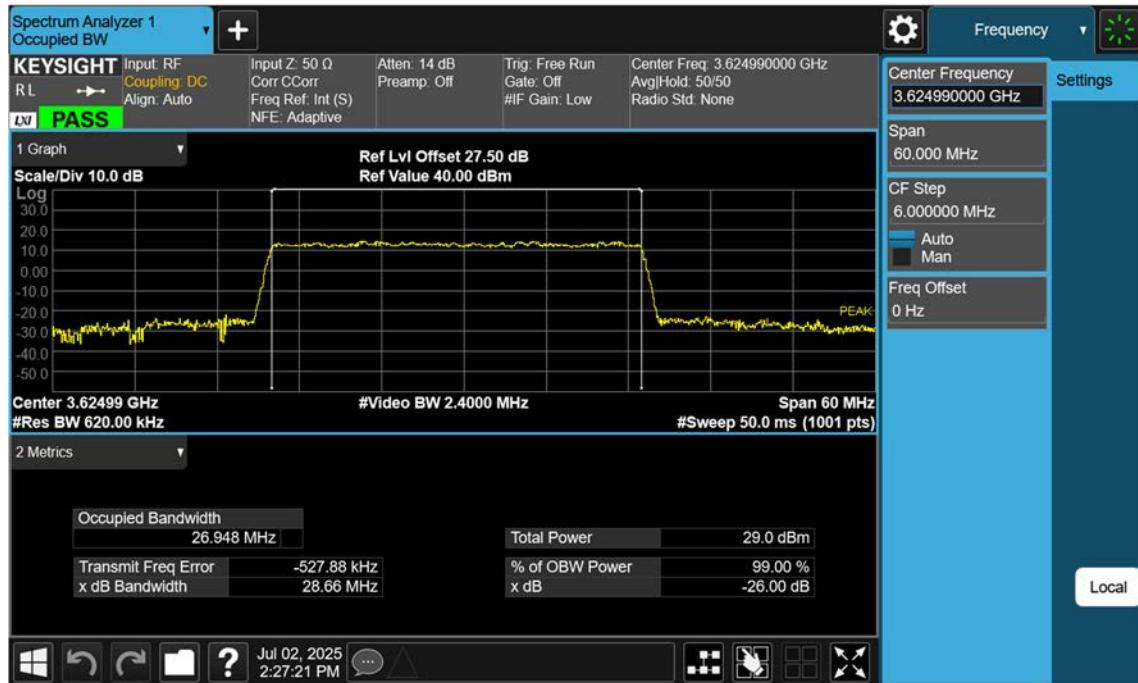




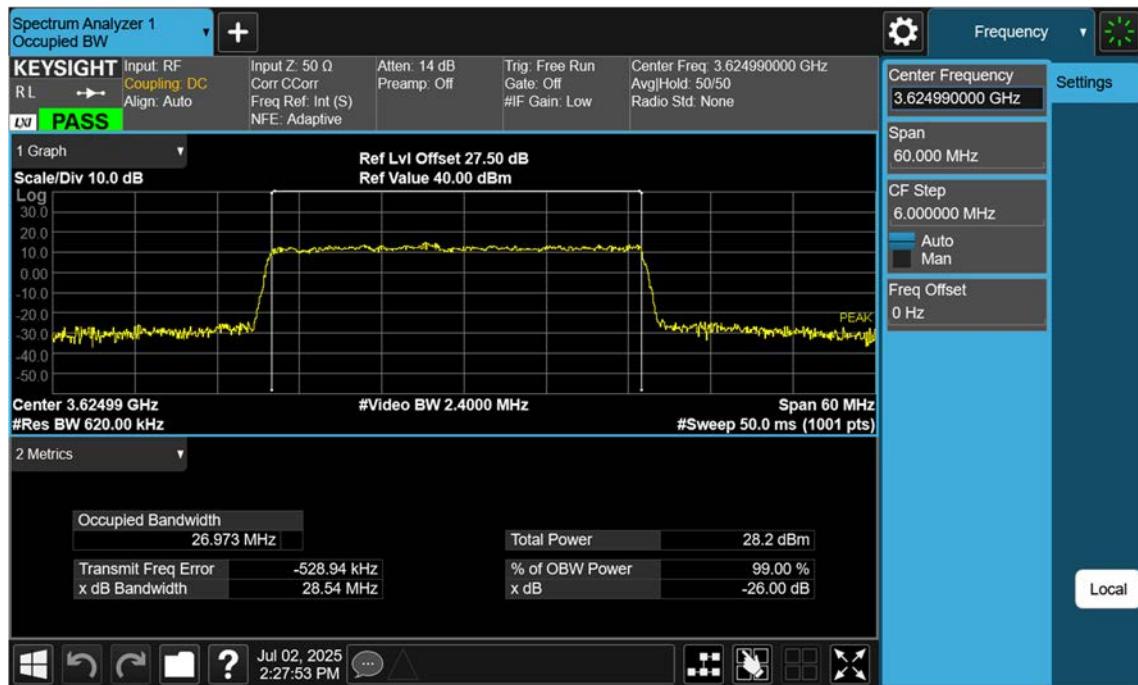
## NR48\_30 M\_OBW\_Mid\_BPSK\_FullIRB

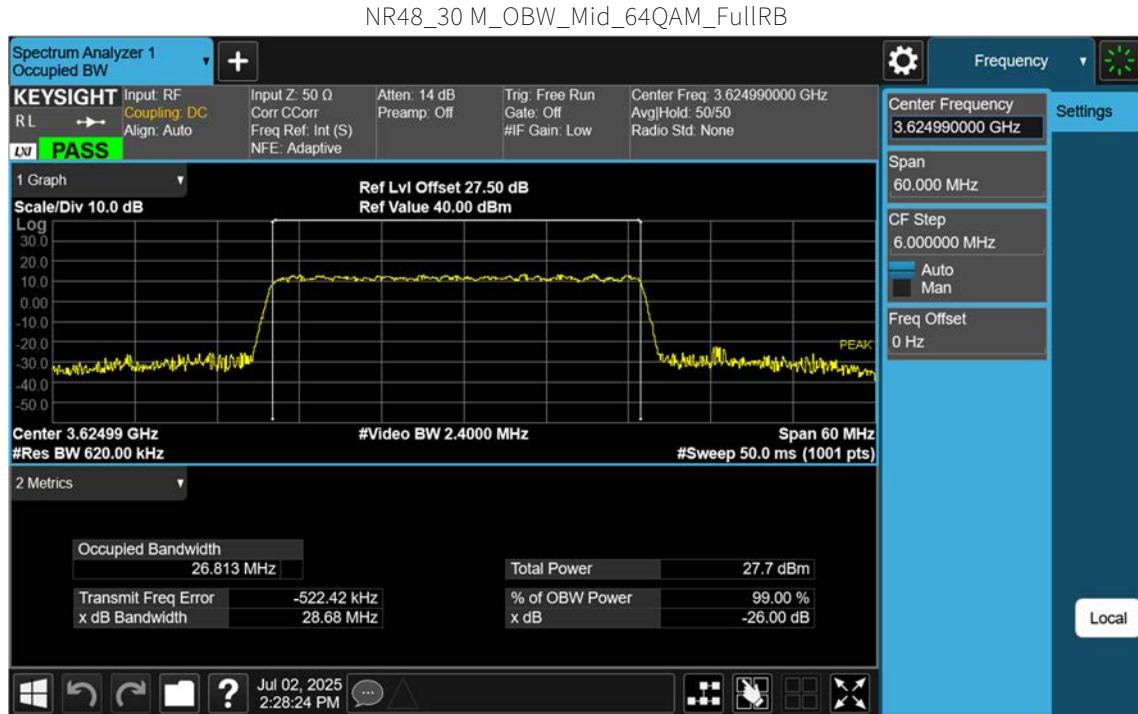


## NR48\_30 M\_OBW\_Mid\_QPSK\_FullIRB

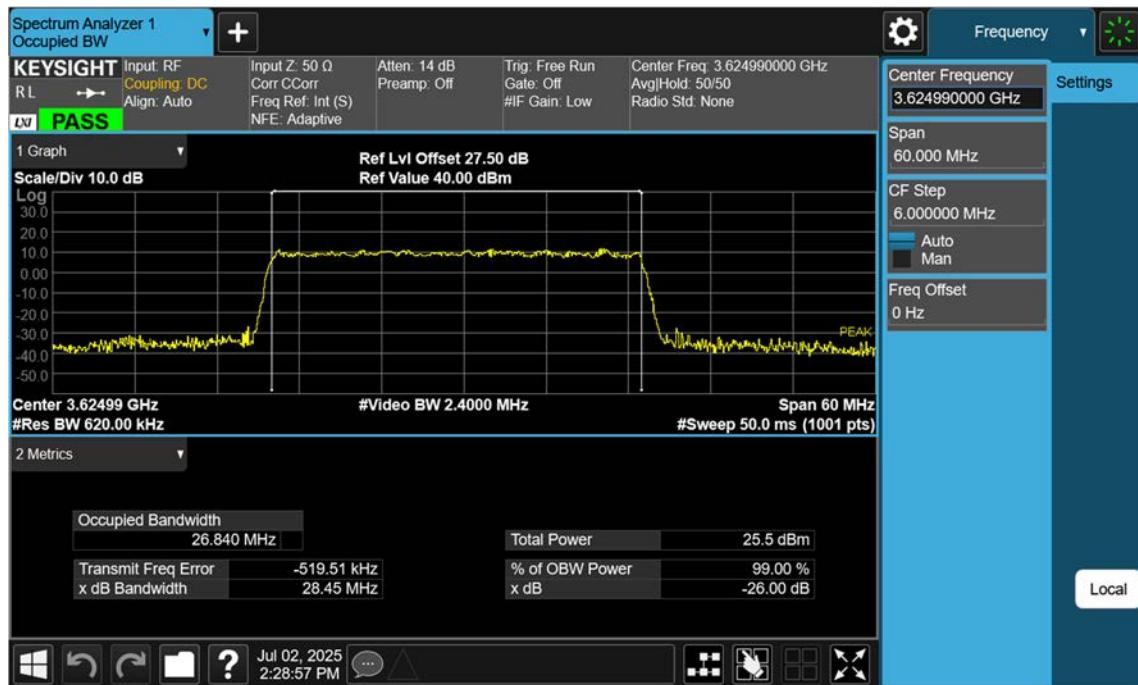


## NR48\_30 M\_OBW\_Mid\_16QAM\_FullRB

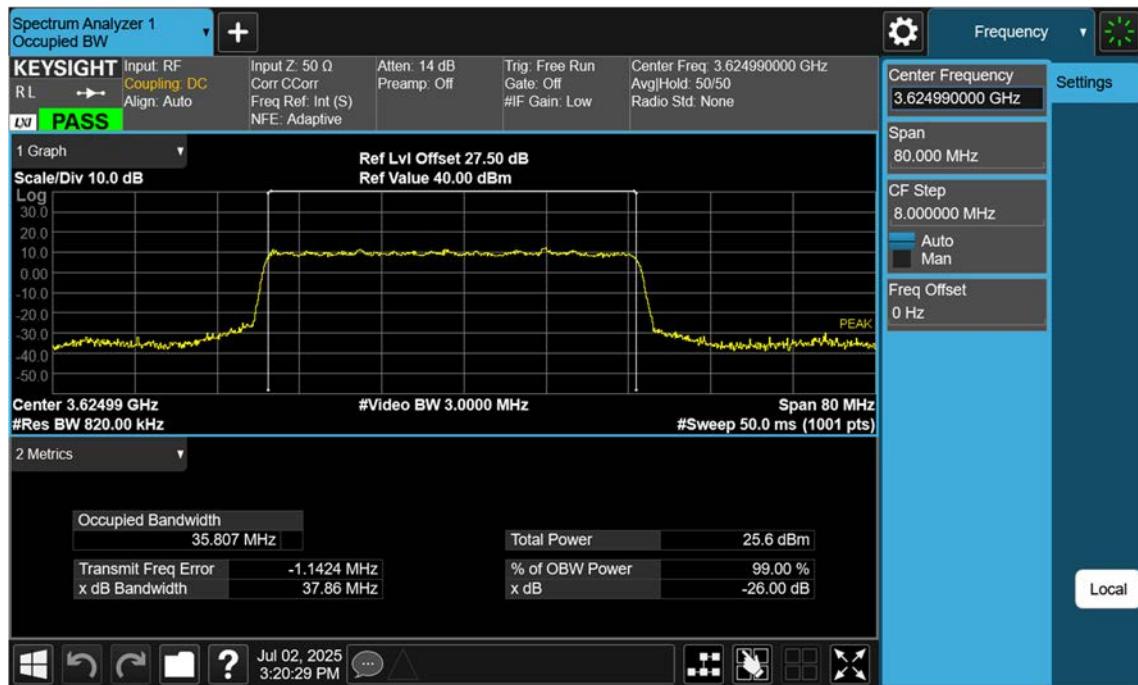




## NR48\_30 M\_OBW\_Mid\_256QAM\_FullRB



## NR48\_40 M\_OBW\_Mid\_BPSK\_FullIRB



## NR48\_40 M\_OBW\_Mid\_QPSK\_FullIRB

