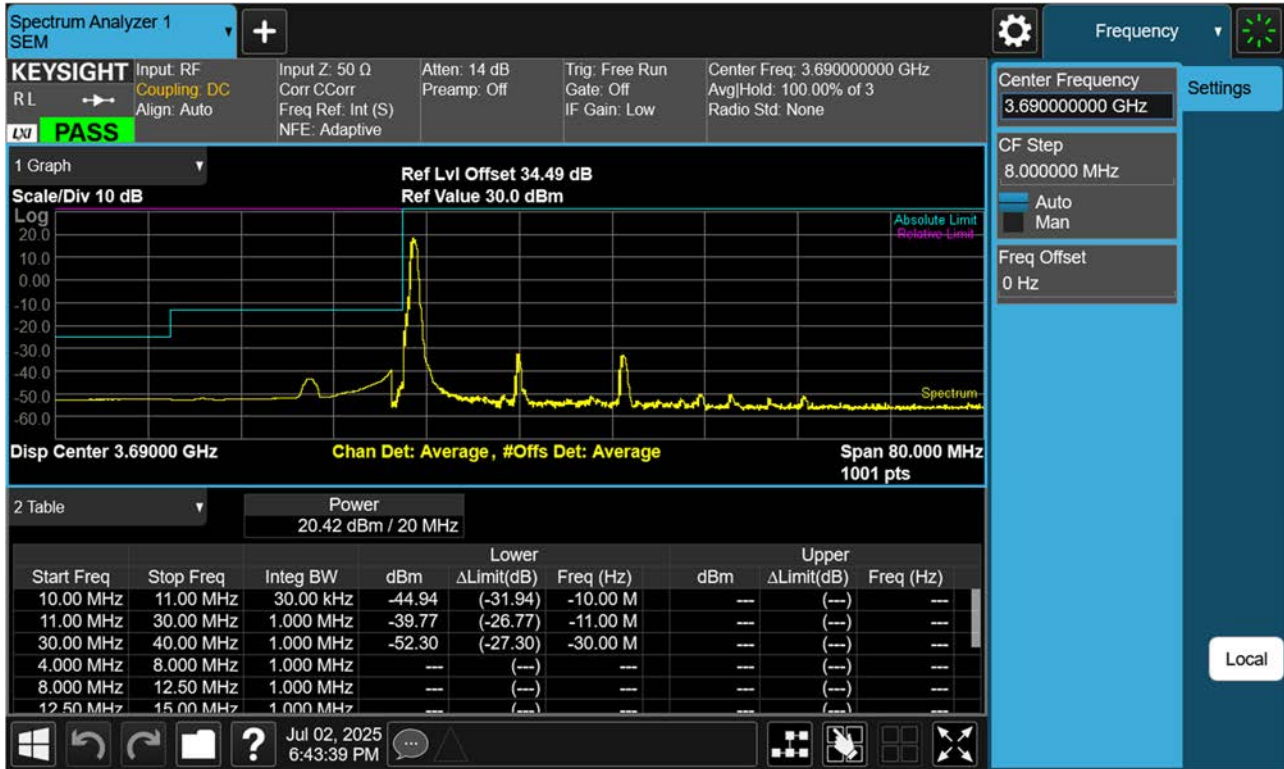
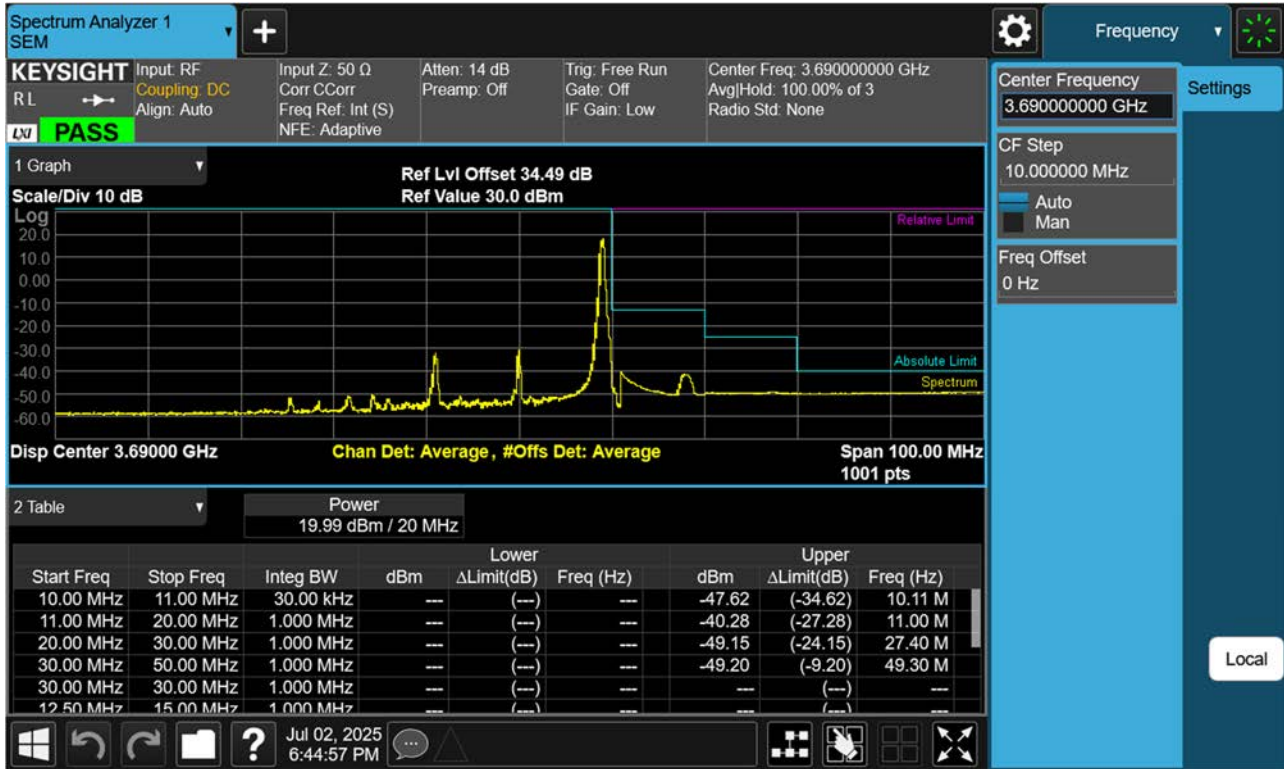


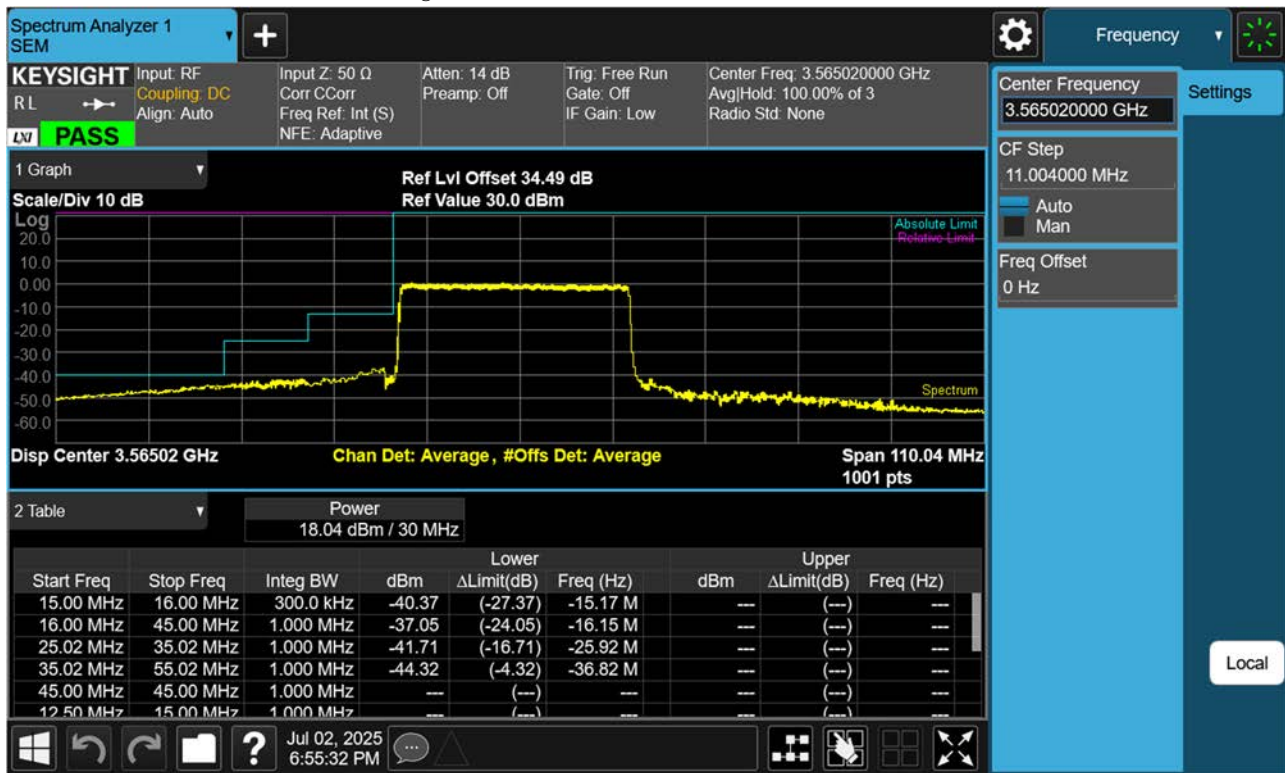
NR48_20 M_BandEdge(Lower)_High_3690.00 MHz_BPSK_1RB



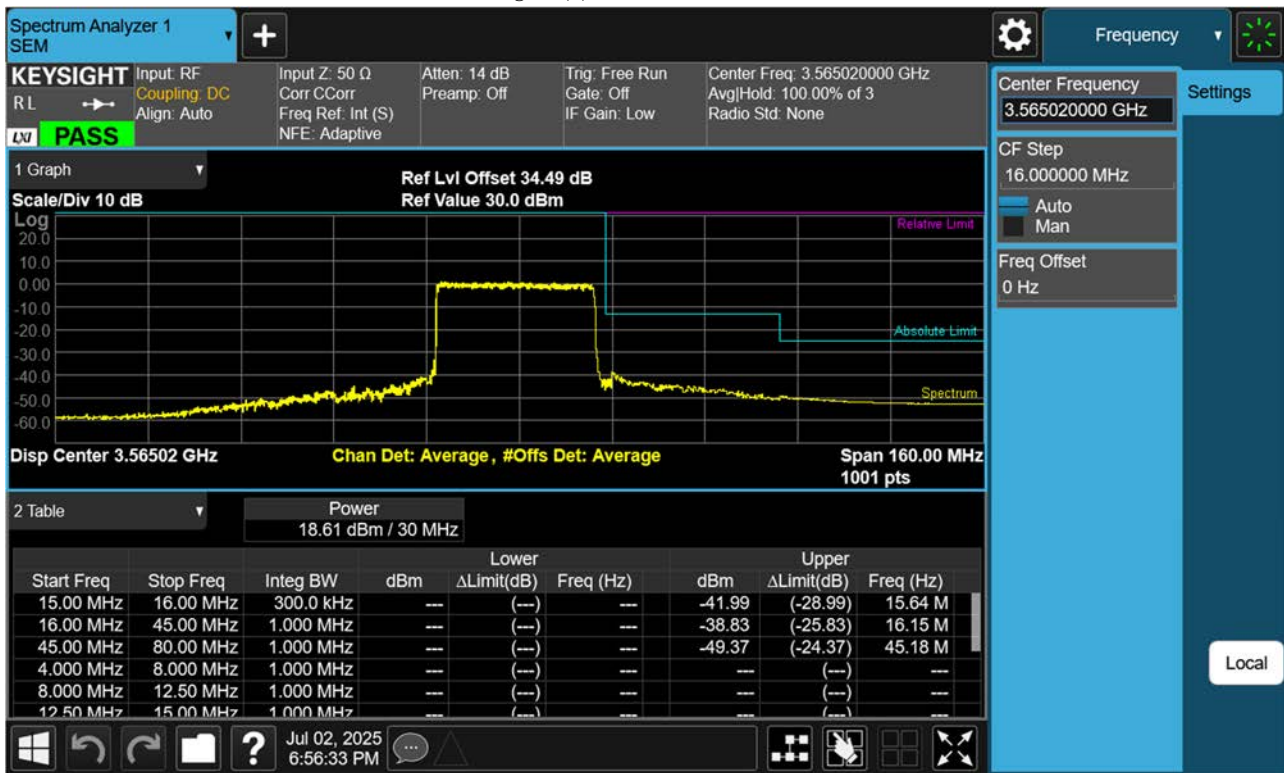
NR48_20 M_BandEdge(Upper)_High_ 3690.00 MHz_BPSK_1RB



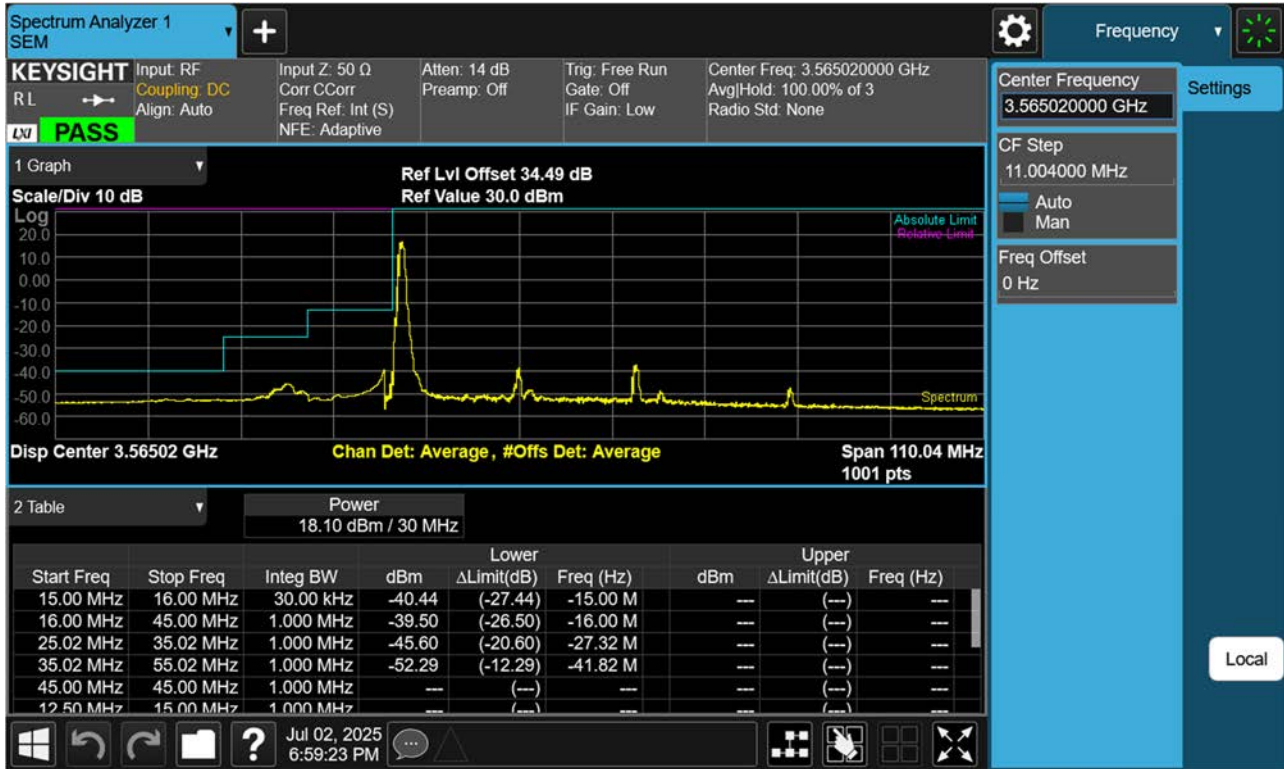
NR48_30 M BandEdge(Lower)_Low_3565.02 MHz_BPSK_FullRB_Below 3530MHz



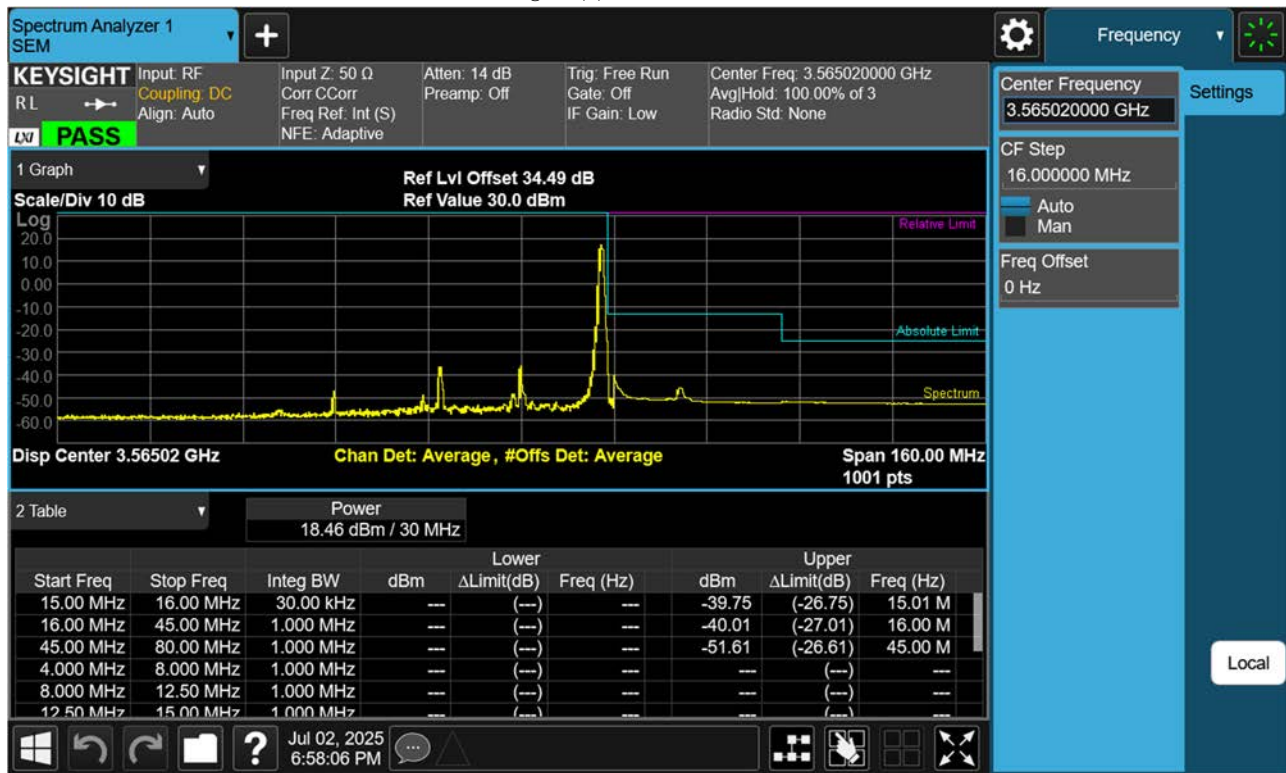
NR48_30 M_BandEdge(Upper)_Low_3565.02 MHz_BPSK_FullRB



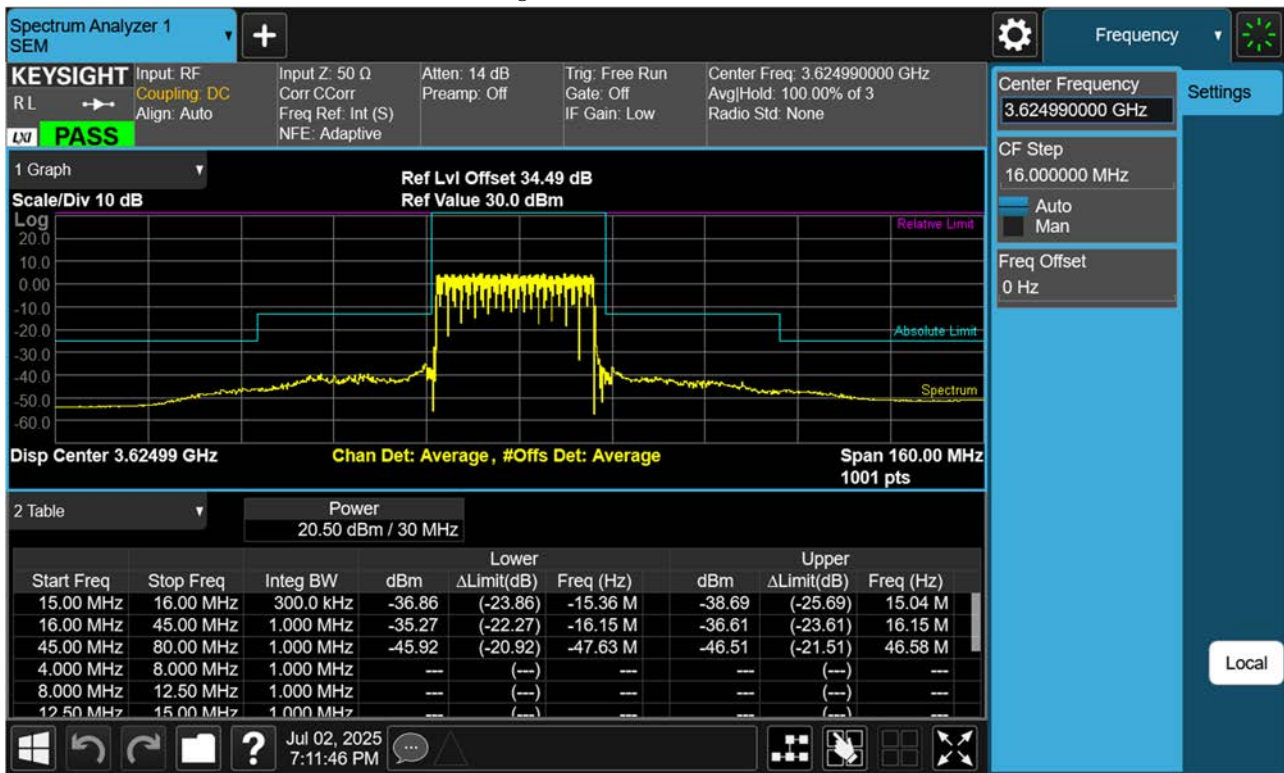
NR48_30 M_BandEdge(Lower)_Low_ 3565.02 MHz_BPSK_1RB



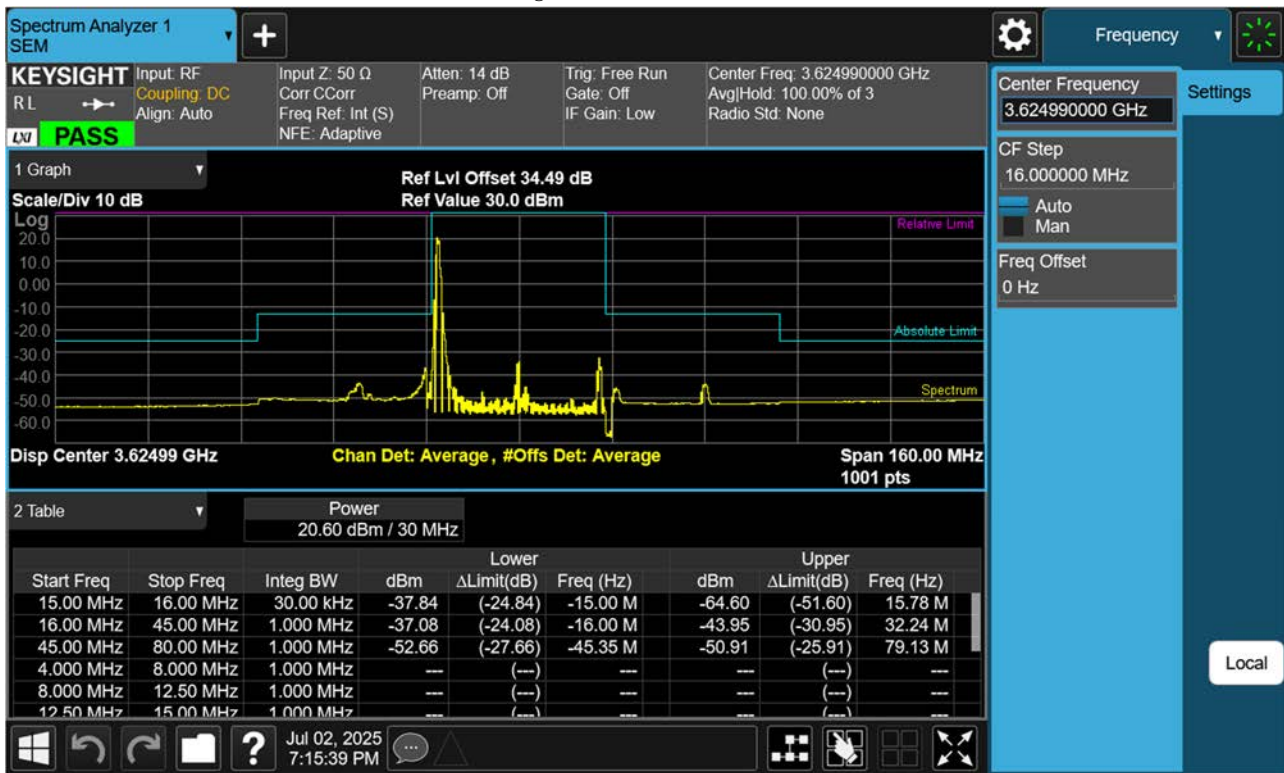
NR48_30 M_BandEdge(Upper)_Low_ 3565.02 MHz_BPSK_1RB



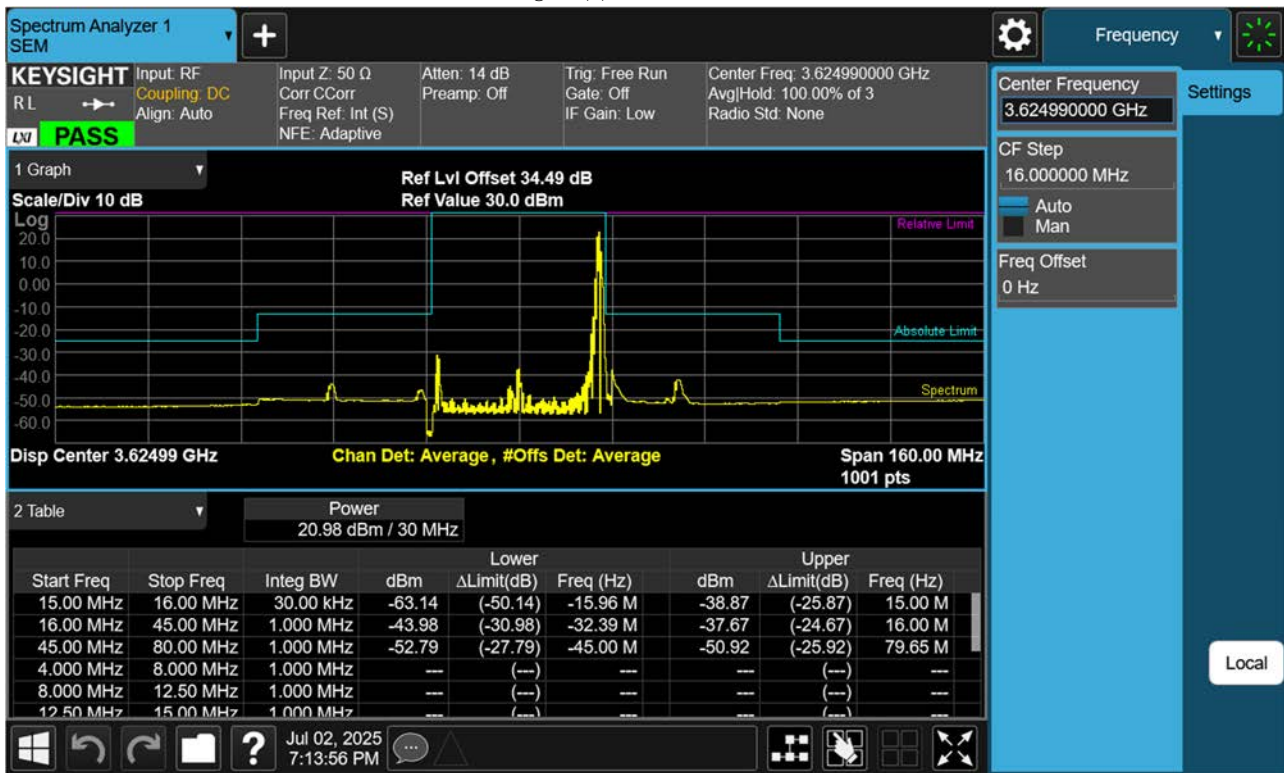
NR48_30 M_BandEdge(Center)_Mid_3624.99 MHz_BPSK_FullRB



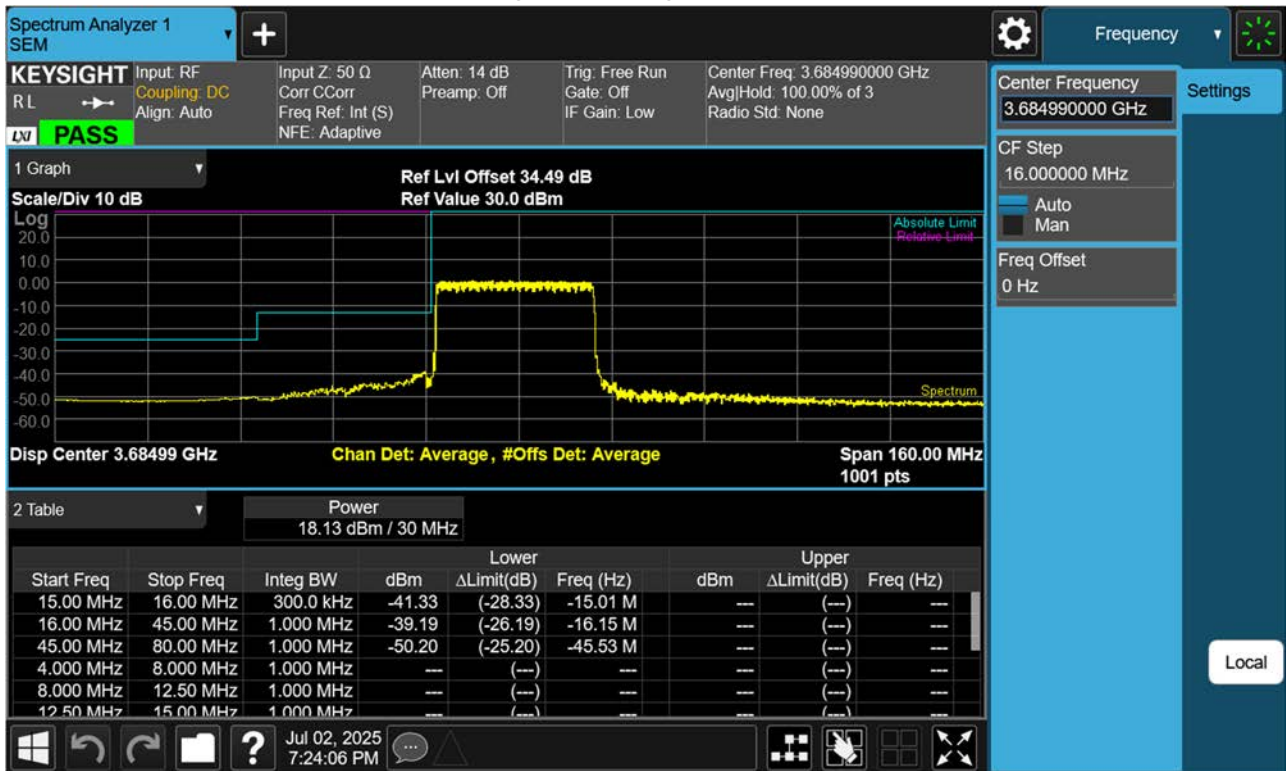
NR48_30 M_BandEdge(Lower)_Mid_3624.99 MHz_BPSK_1RB



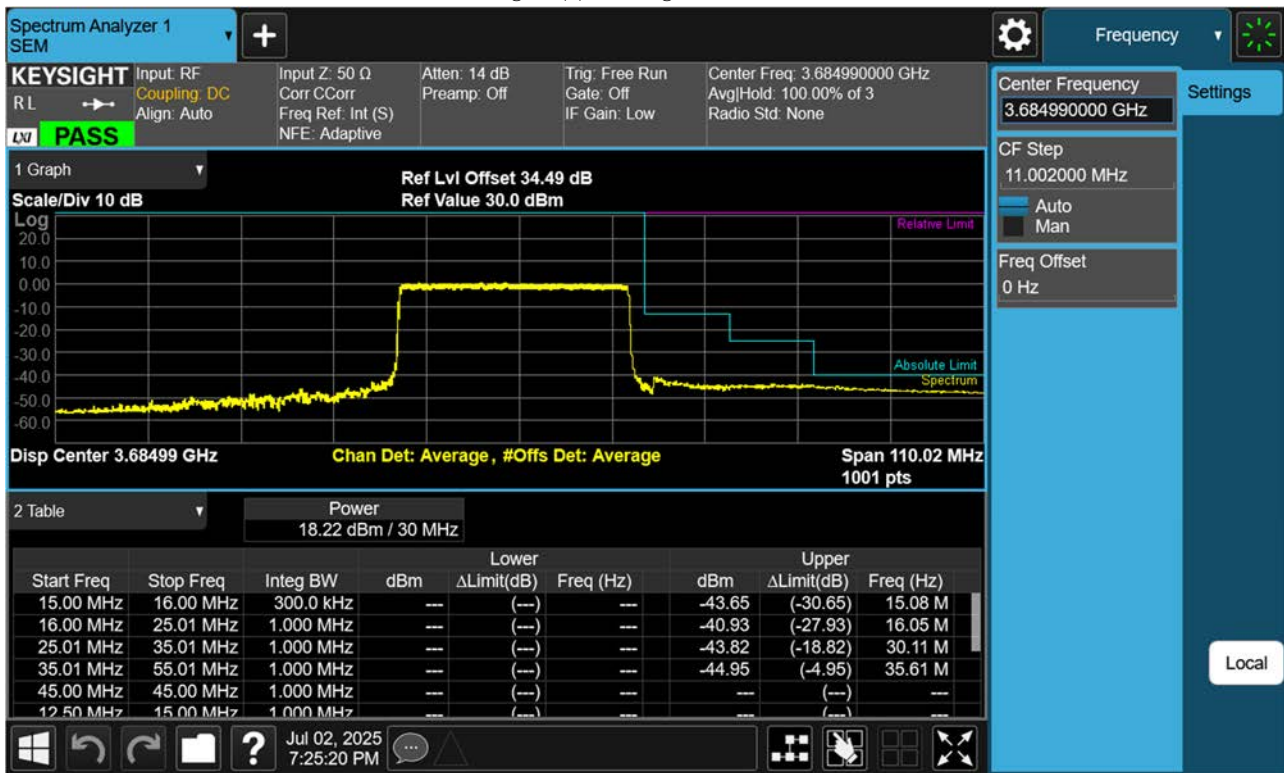
NR48_30 M_BandEdge(Upper)_Mid_3624.99 MHz_BPSK_1RB



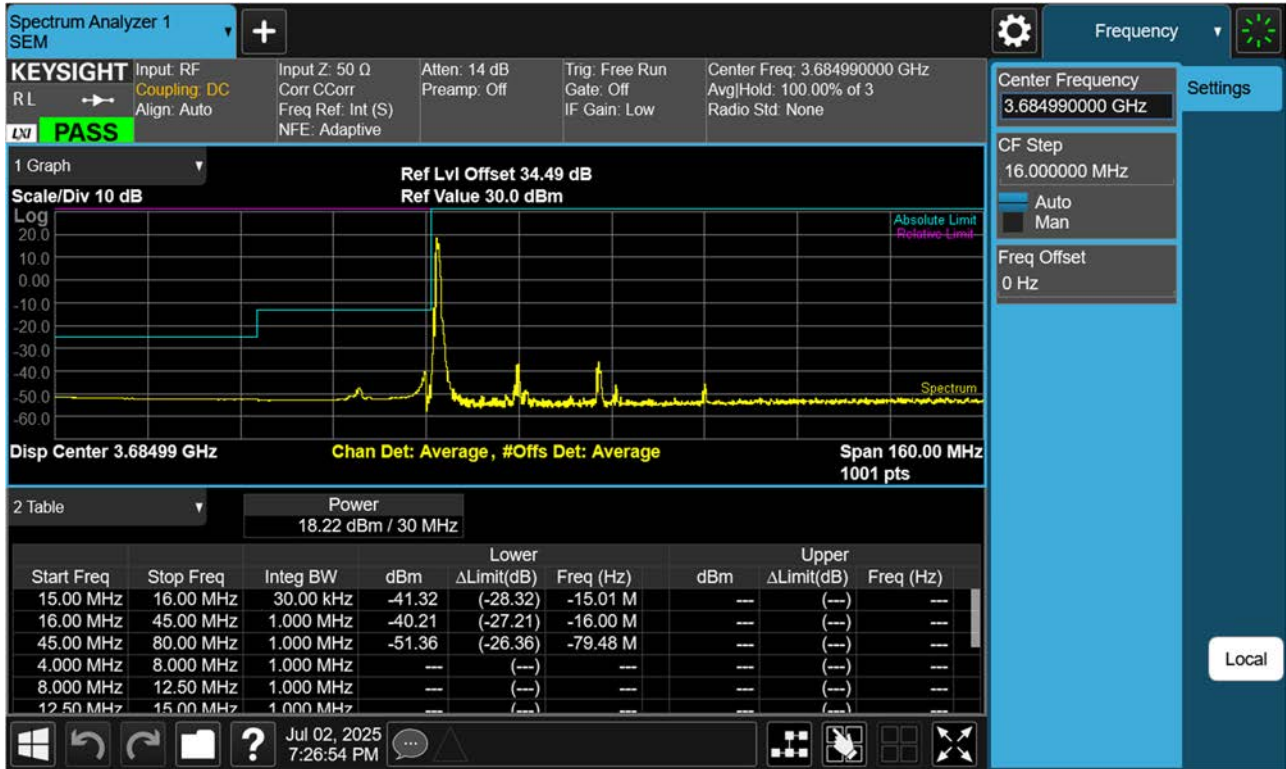
NR48_30 M_BandEdge(Lower)_High_ 3684.99 MHz_BPSK_FullRB



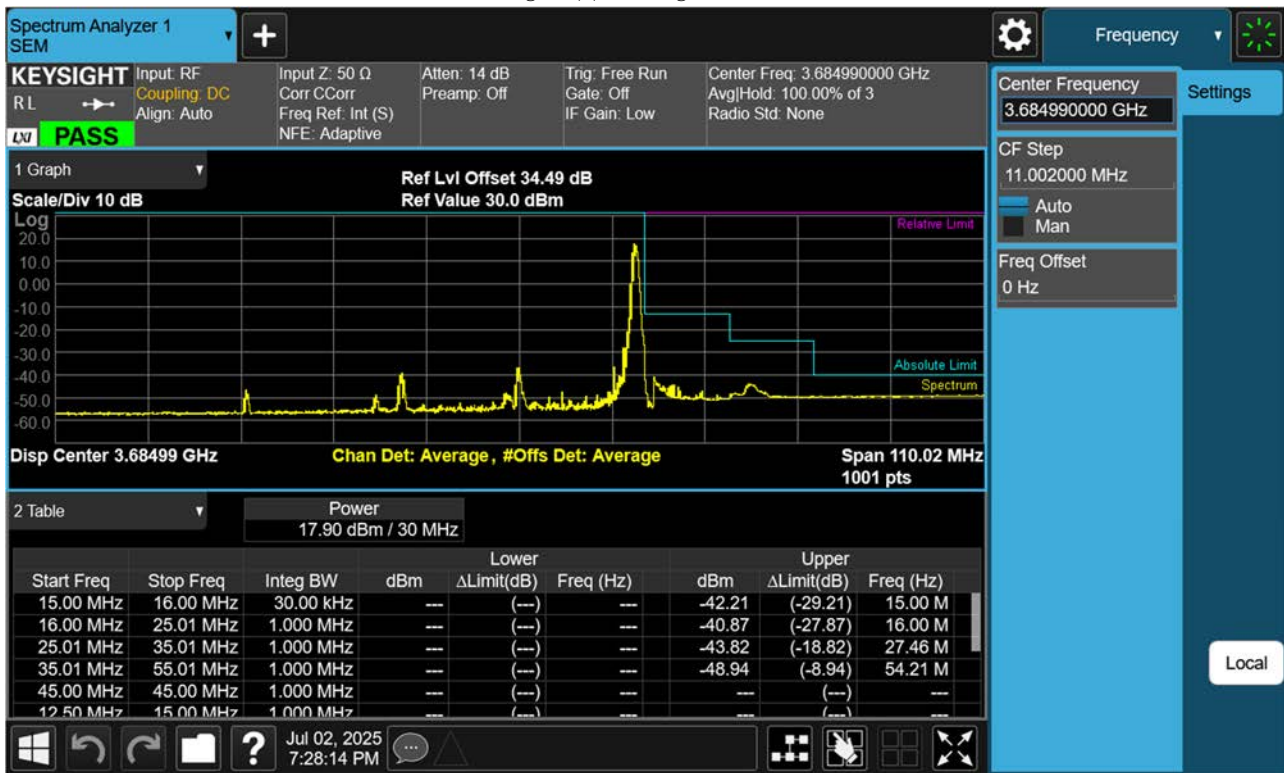
NR48_30 M_BandEdge(Upper)_High_ 3684.99 MHz_BPSK_FullRB



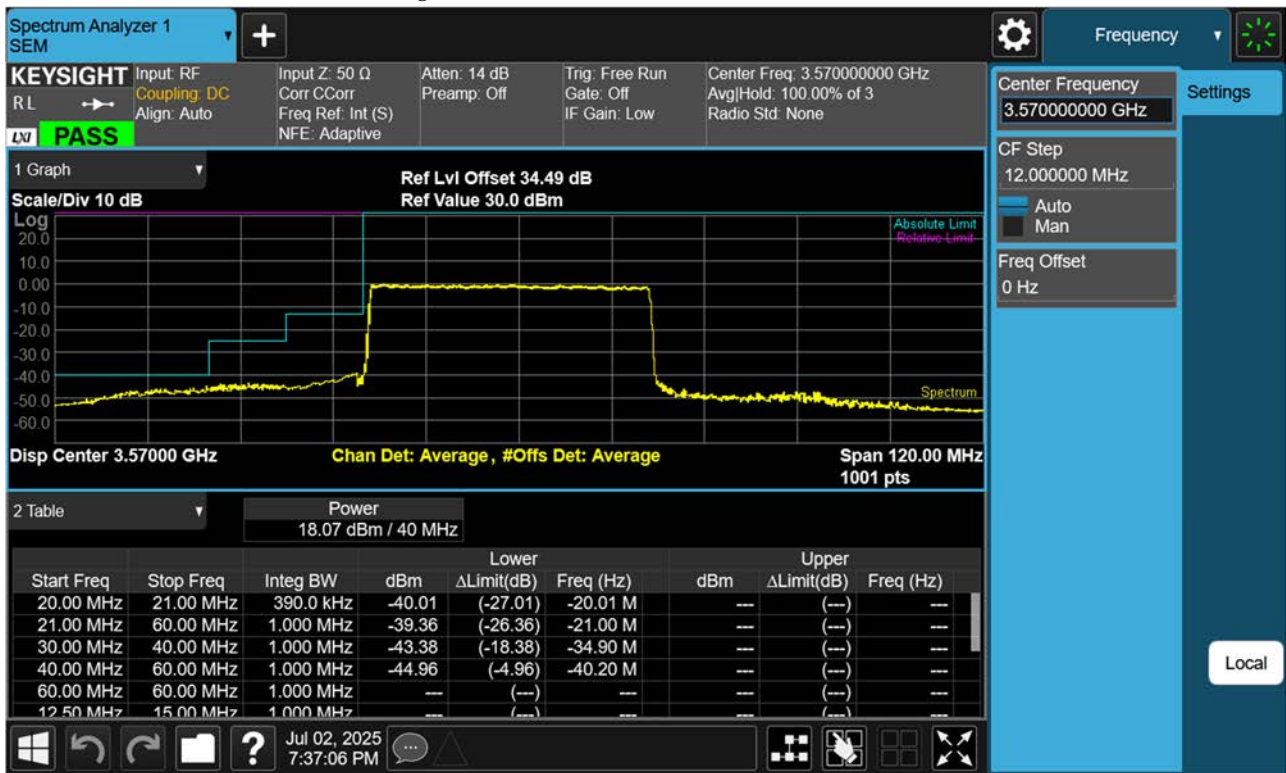
NR48_30 M_BandEdge(Lower)_High_3684.99 MHz_BPSK_1RB



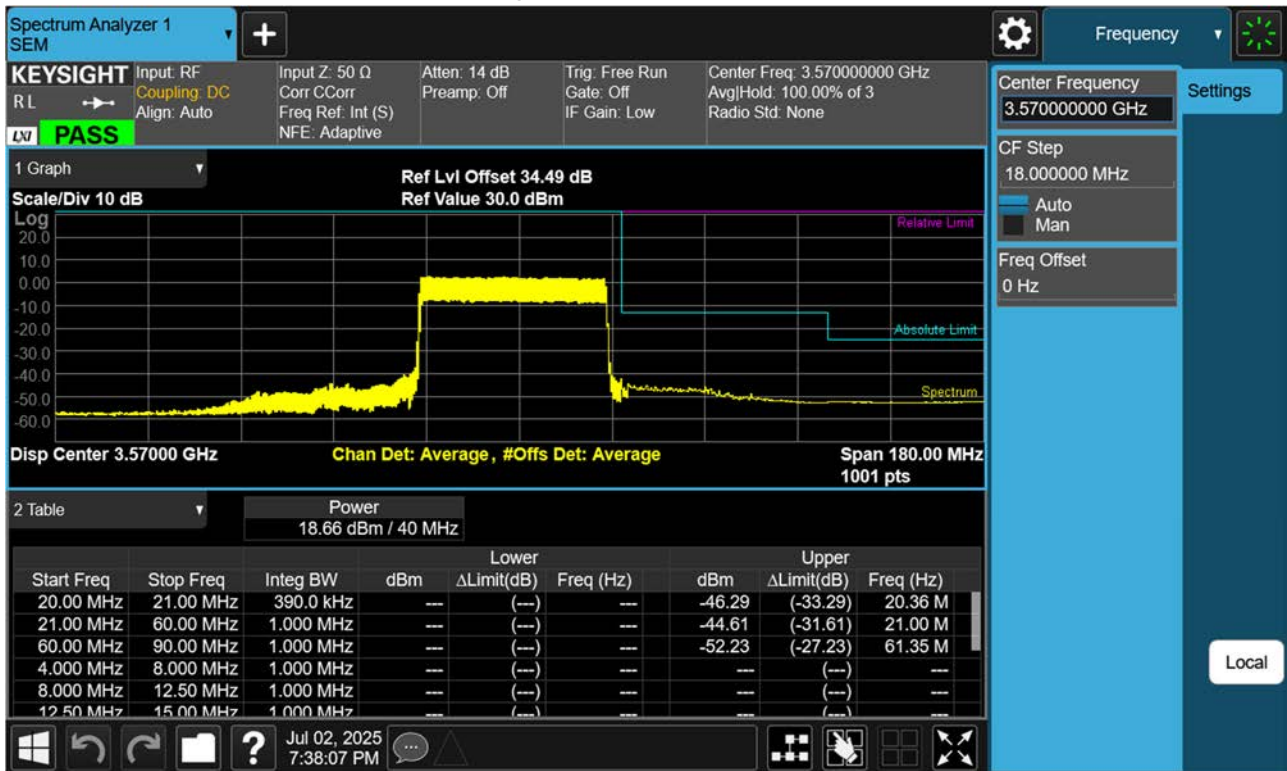
NR48_30 M_BandEdge(Upper)_High_ 3684.99 MHz_BPSK_1RB



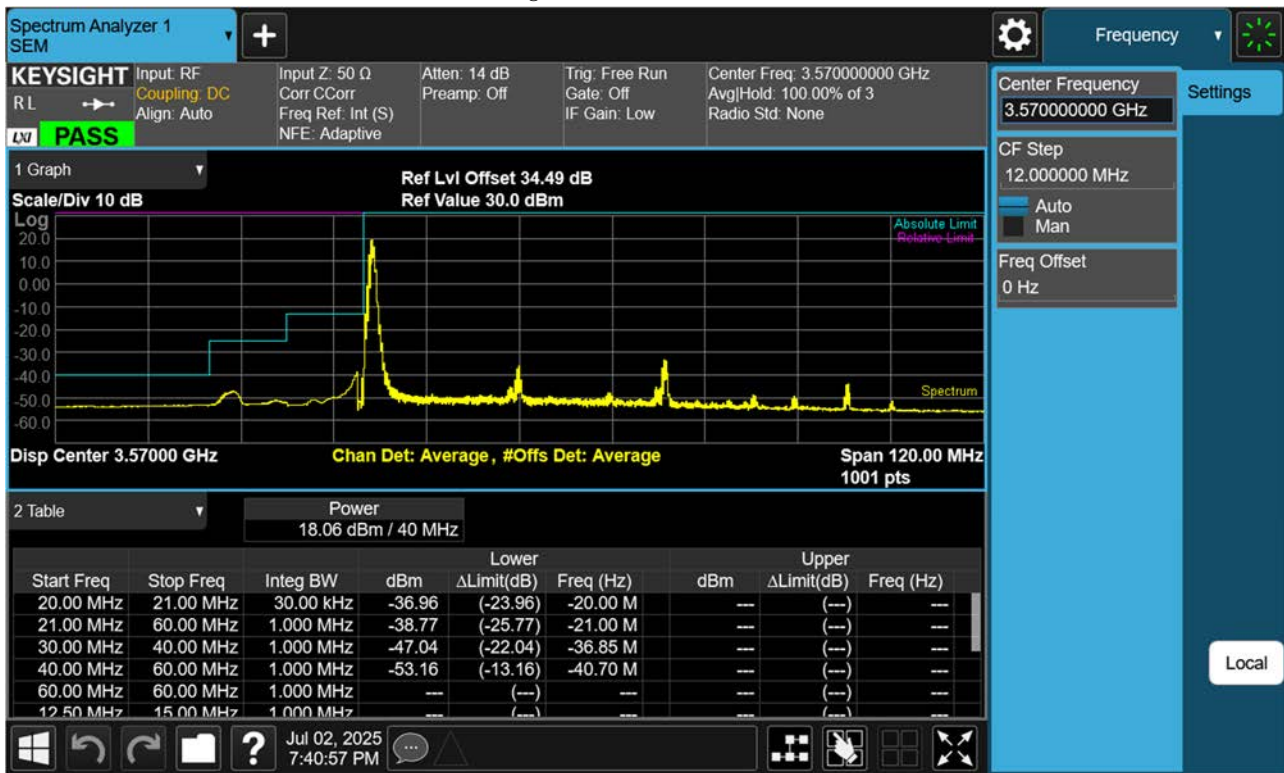
NR48_40 M BandEdge(Lower)_Low_ 3570.00 MHz_BPSK_FullRB_Below 3530MHz



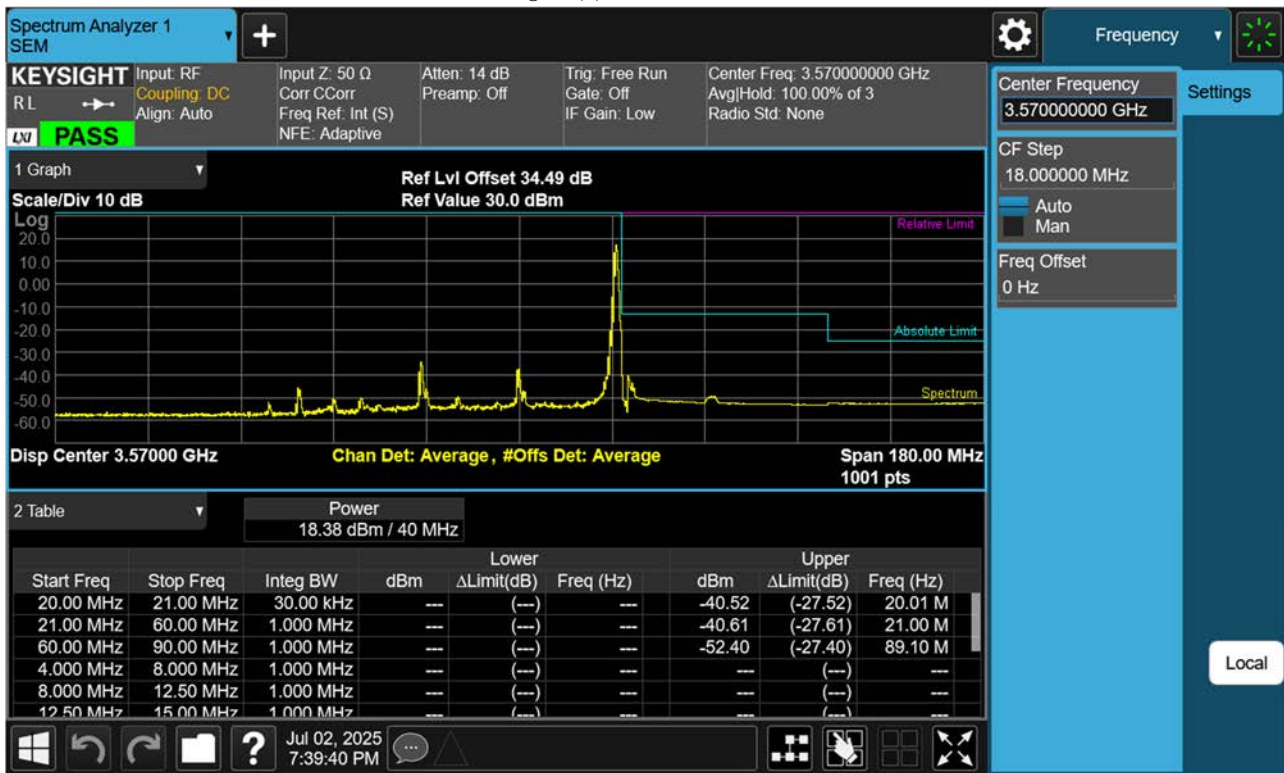
NR48_40 M_BandEdge(Upper)_Low_ 3570.00 MHz_BPSK_FullRB



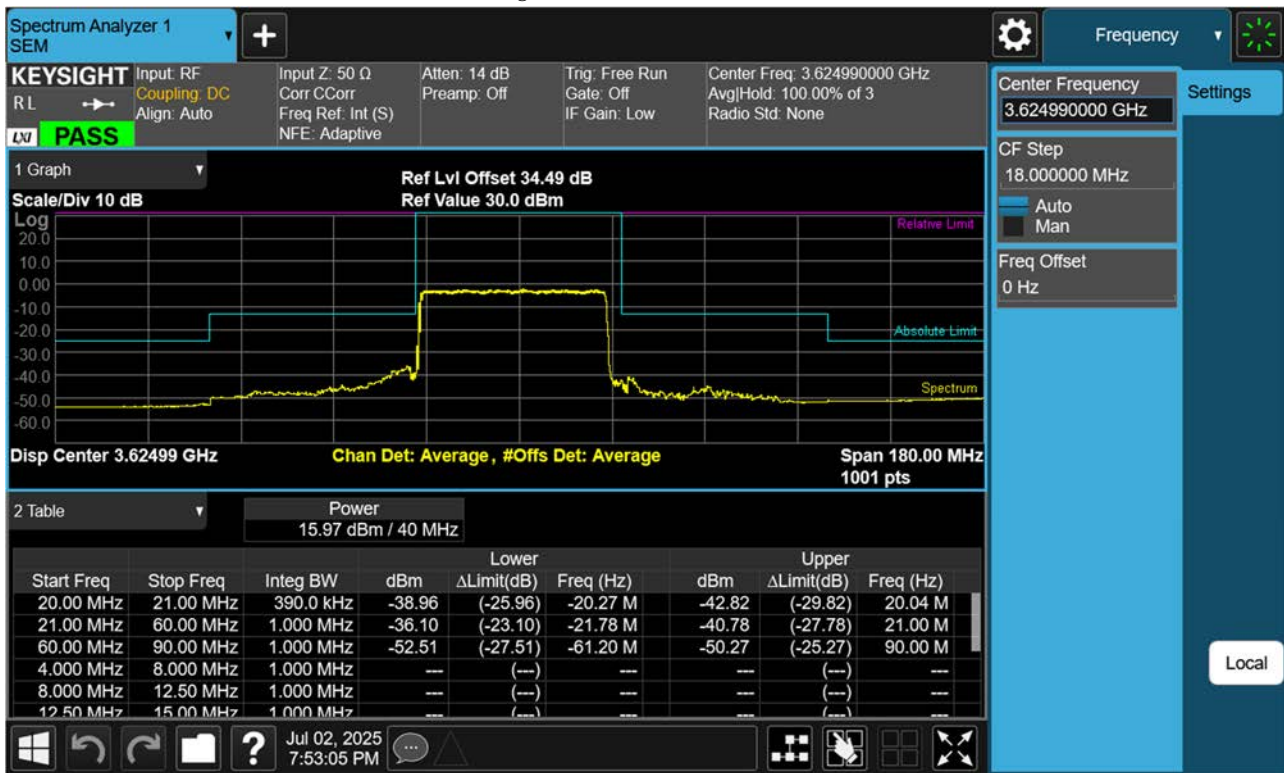
NR48_40 M_BandEdge(Lower)_Low_ 3570.00 MHz_BPSK_1RB



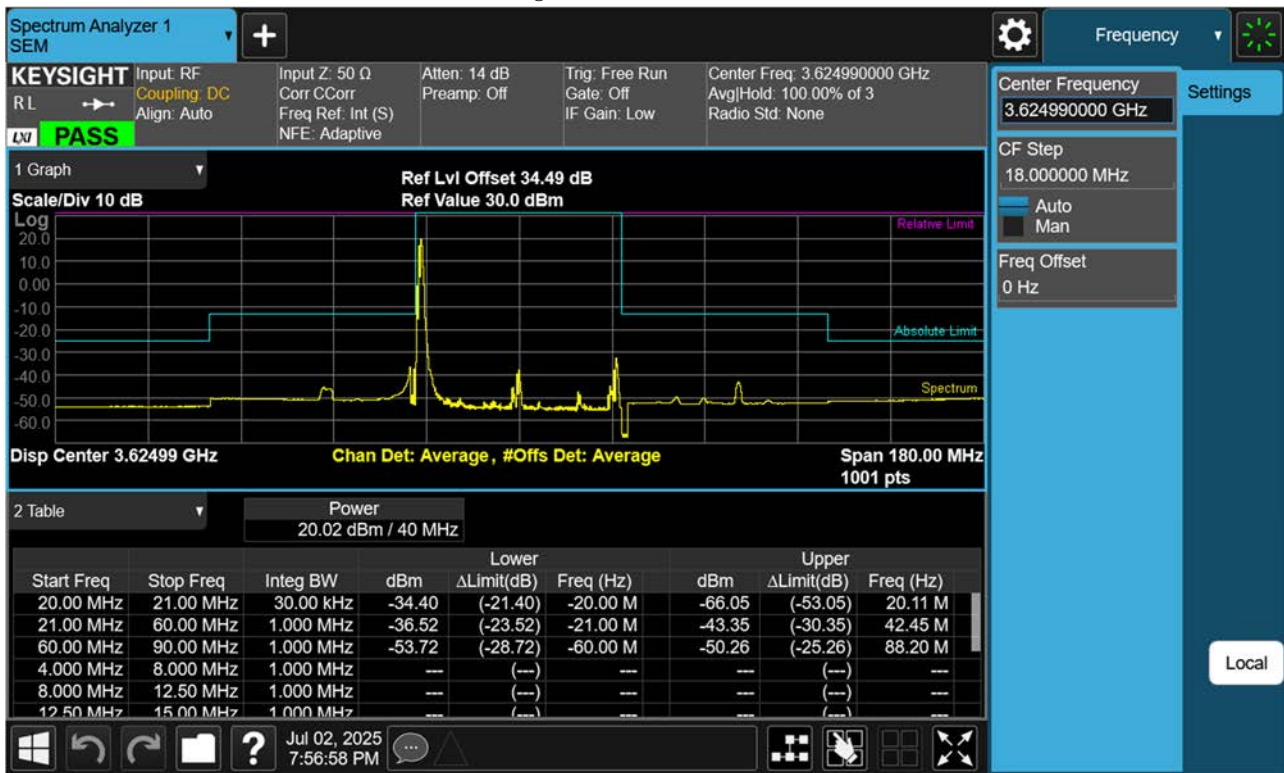
NR48_40 M_BandEdge(Upper)_Low_ 3570.00 MHz_BPSK_1RB



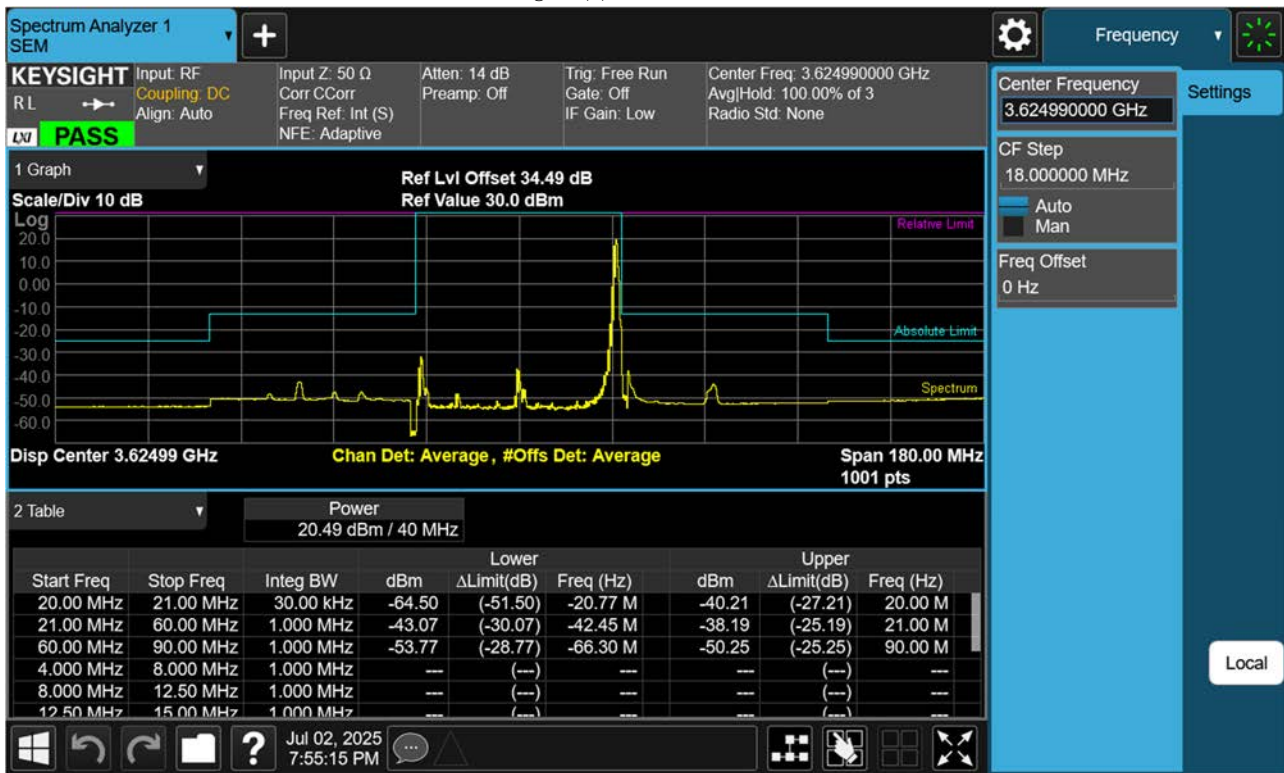
NR48_40 M_BandEdge(Center)_Mid_3624.99 MHz_BPSK_FullRB



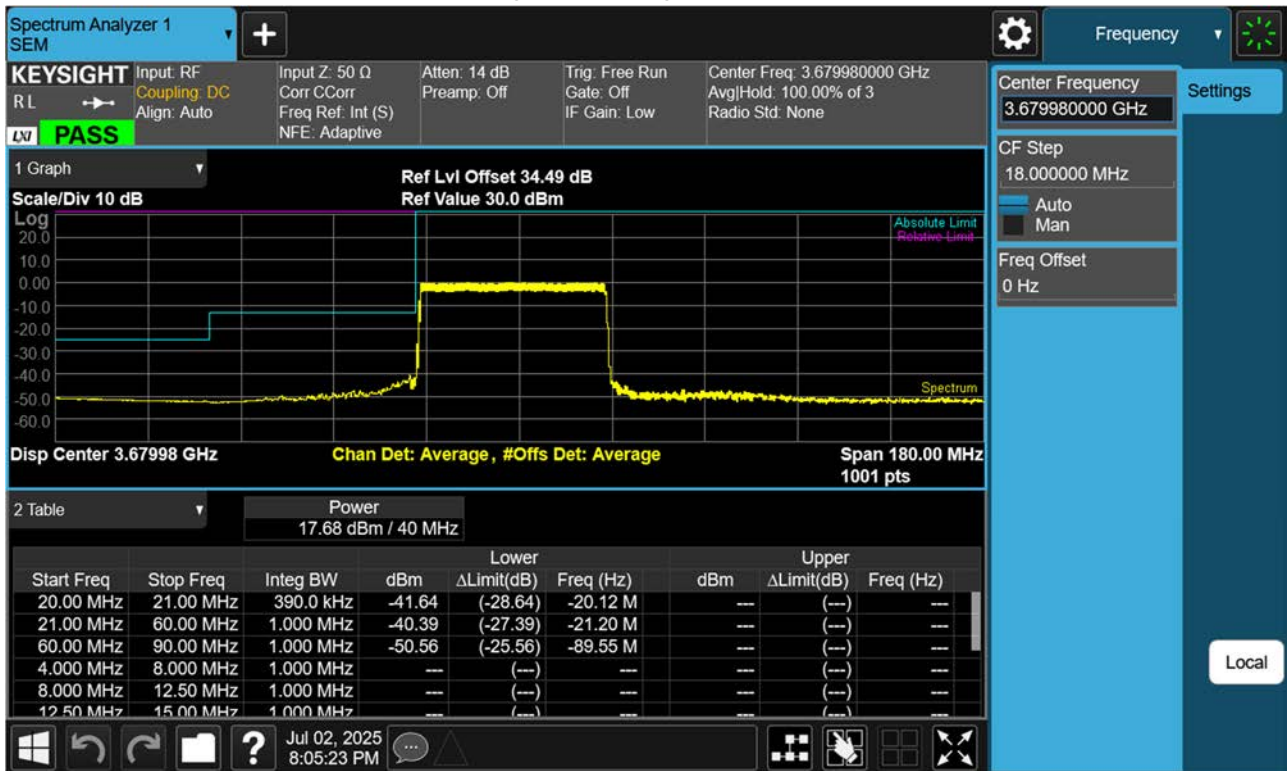
NR48_40 M_BandEdge(Lower)_Mid_3624.99 MHz_BPSK_1RB



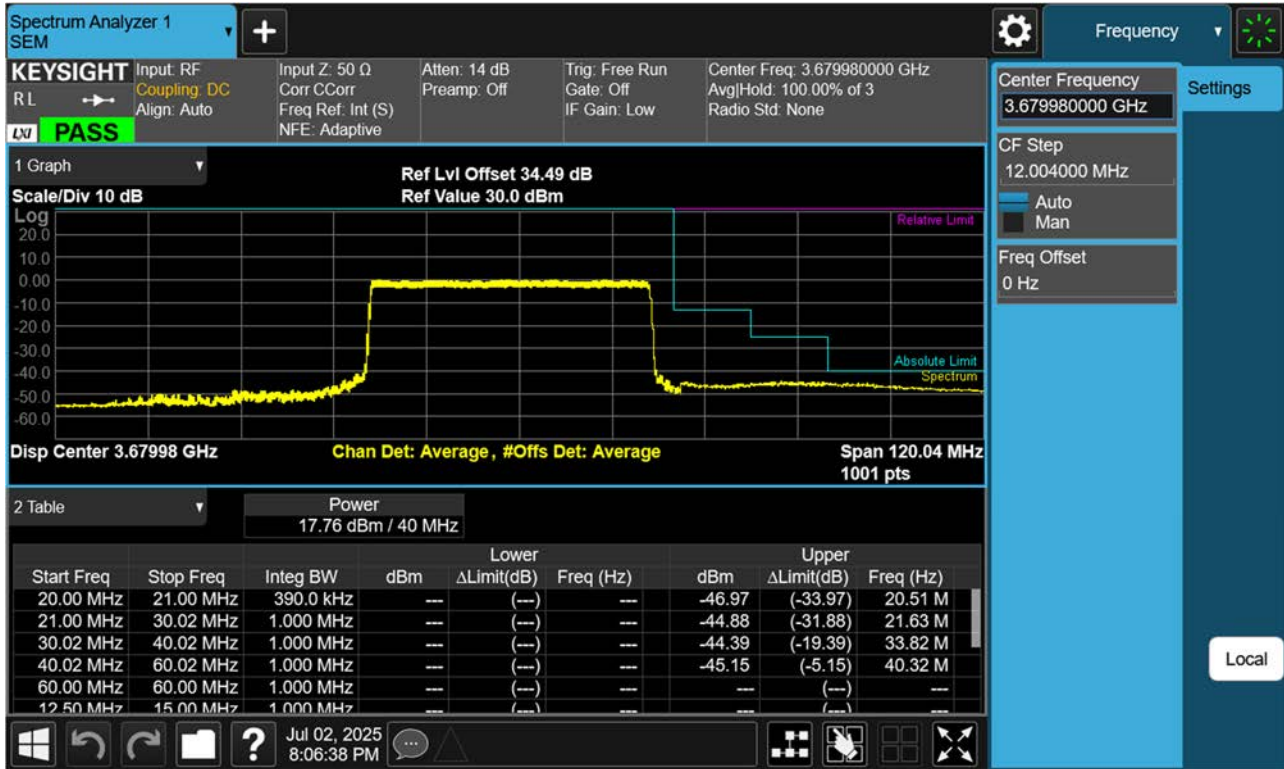
NR48_40 M_BandEdge(Upper)_Mid_3624.99 MHz_BPSK_1RB



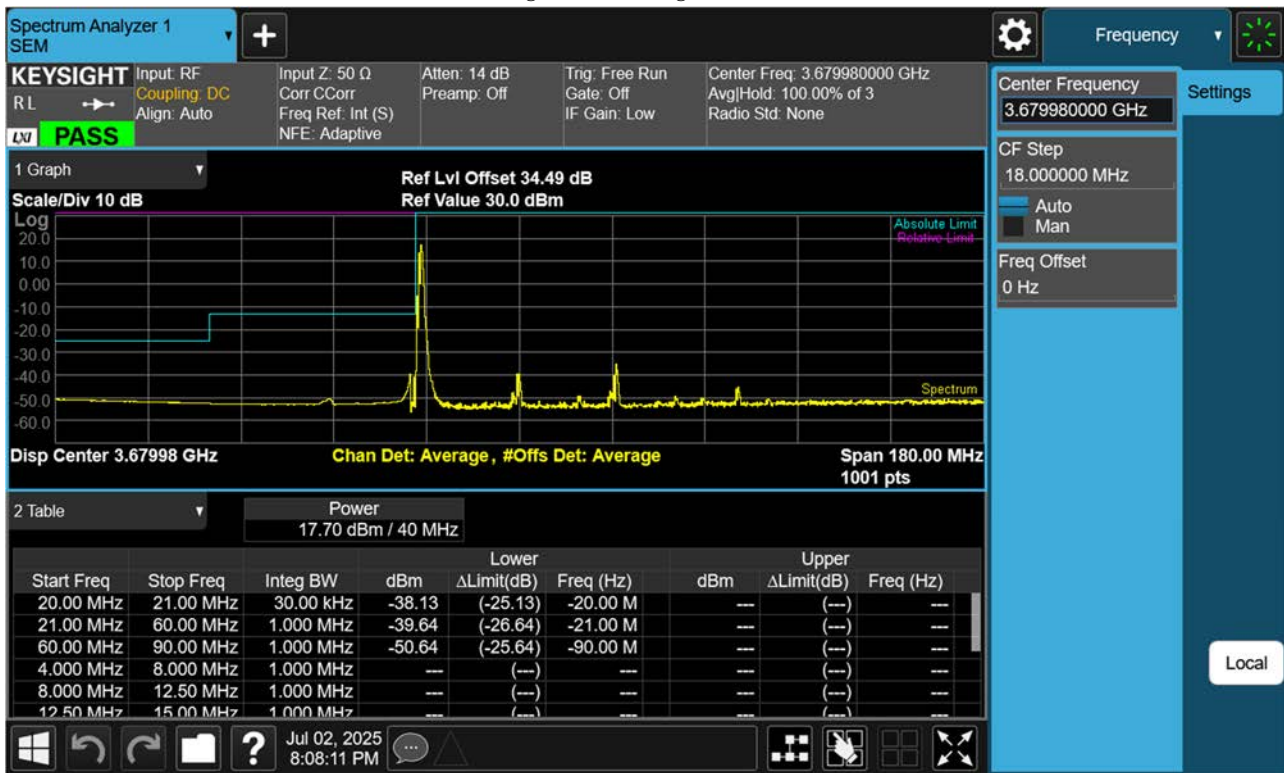
NR48_40 M_BandEdge(Lower)_High_ 3679.98 MHz_BPSK_FullRB



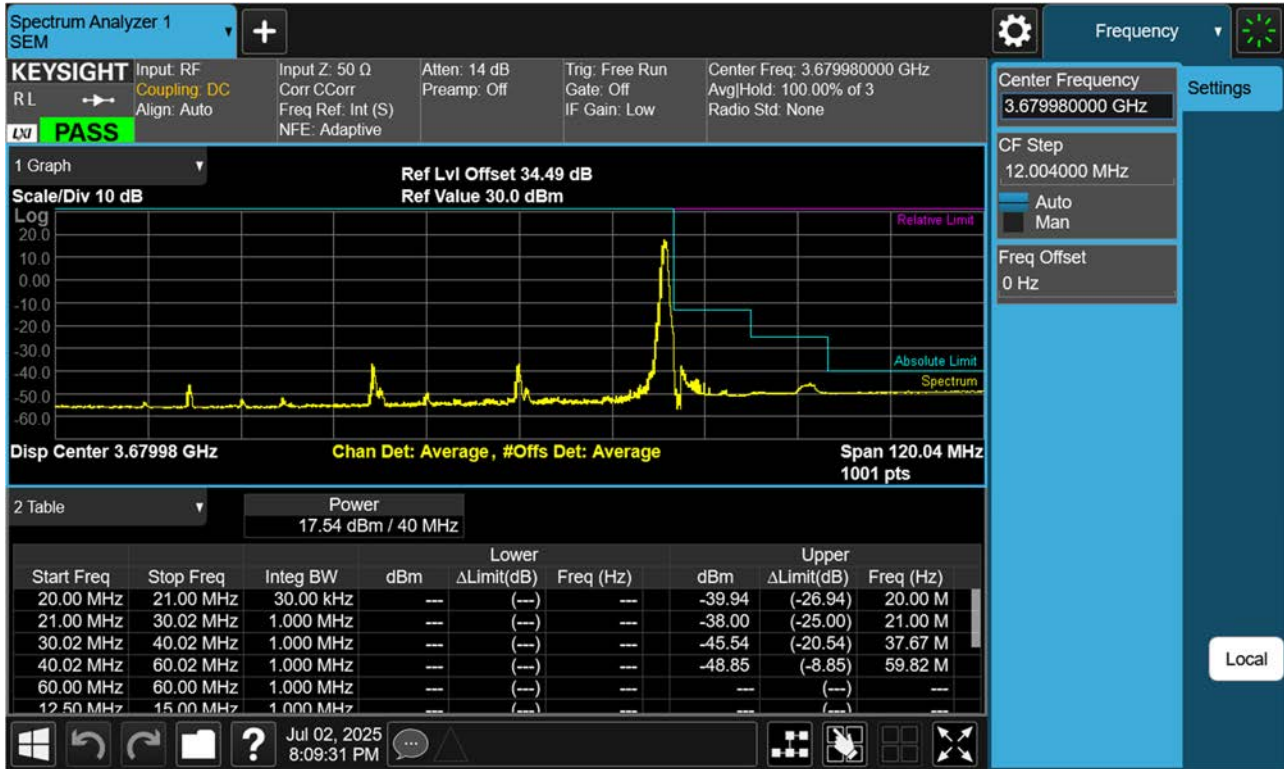
NR48_40 M_BandEdge(Upper)_High_ 3679.98 MHz_BPSK_FullRB_Above 3720MHz



NR48_40 M_BandEdge(Lower)_High_3679.98 MHz_BPSK_1RB



NR48_40 M_BandEdge(Upper)_High_ 3679.98 MHz_BPSK_1RB



12. TEST PLOTS (MIMO2)

NR48_10 M_PAR_Mid_BPSK_FullRB



NR48_10 M_PAR_Mid_QPSK_FullRB



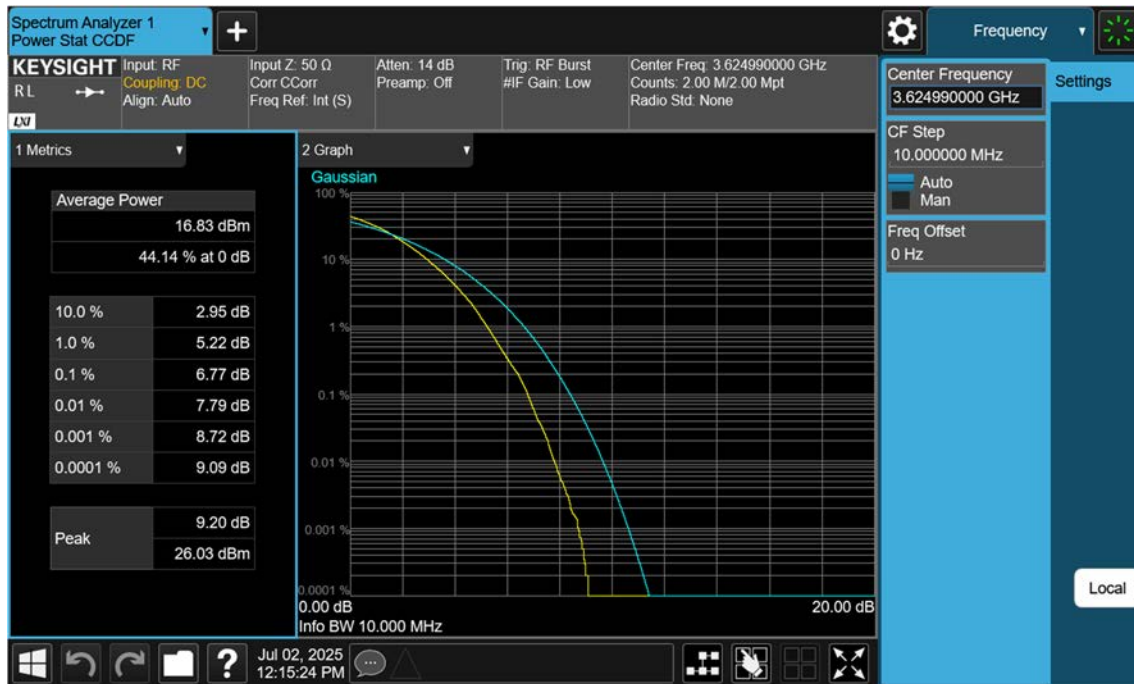
NR48_10 M_PAR_Mid_16QAM_FullRB



NR48_10 M_PAR_Mid_64QAM_FullRB



NR48_10 M_PAR_Mid_256QAM_FullRB



NR48_15 M_PAR_Mid_BPSK_FullRB



NR48_15 M_PAR_Mid_QPSK_FullRB



NR48_15 M_PAR_Mid_16QAM_FullRB



NR48_15 M_PAR_Mid_64QAM_FullRB



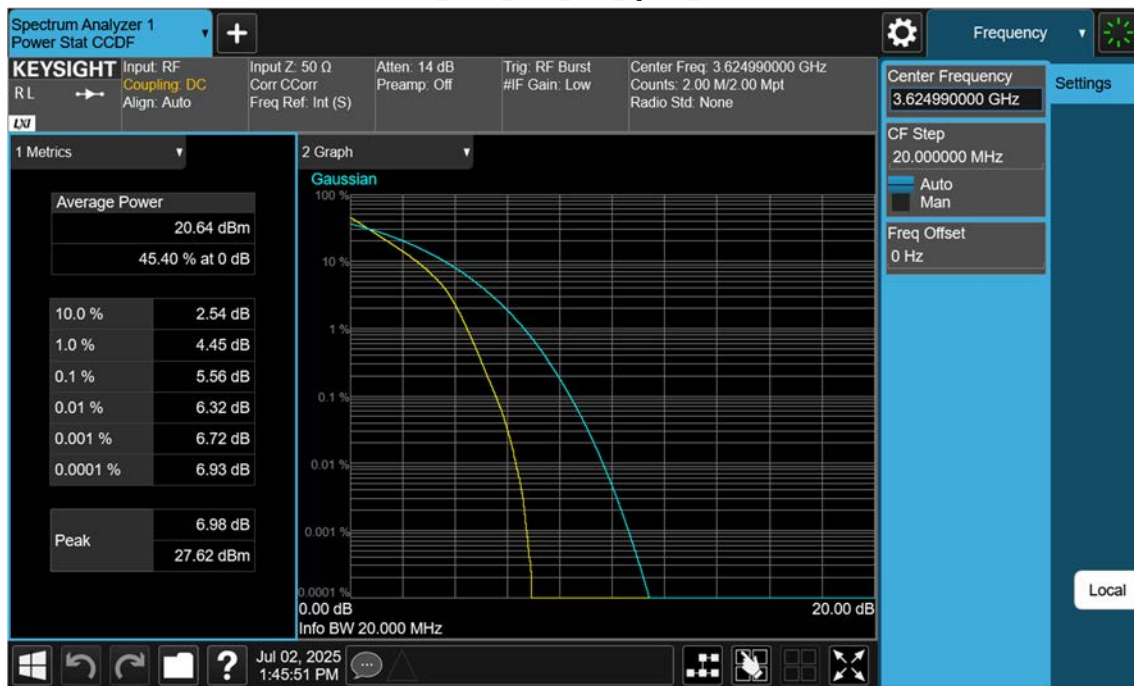
NR48_15 M_PAR_Mid_256QAM_FullRB



NR48_20 M_PAR_Mid_BPSK_FullRB



NR48_20 M_PAR_Mid_QPSK_FullRB



NR48_20 M_PAR_Mid_16QAM_FullRB



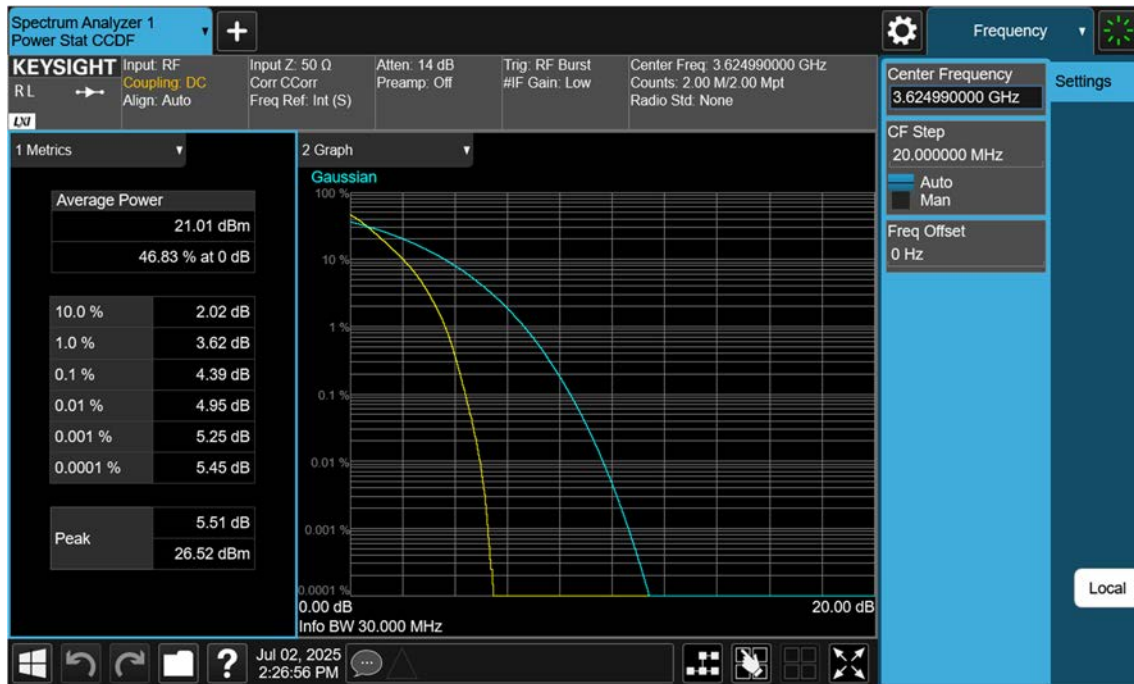
NR48_20 M_PAR_Mid_64QAM_FullRB



NR48_20 M_PAR_Mid_256QAM_FullRB



NR48_30 M_PAR_Mid_BPSK_FullRB



NR48_30 M_PAR_Mid_QPSK_FullRB



NR48_30 M_PAR_Mid_16QAM_FullRB



NR48_30 M_PAR_Mid_64QAM_FullRB



NR48_30 M_PAR_Mid_256QAM_FullRB



NR48_40 M_PAR_Mid_BPSK_FullRB



NR48_40 M_PAR_Mid_QPSK_FullRB



NR48_40 M_PAR_Mid_16QAM_FullRB



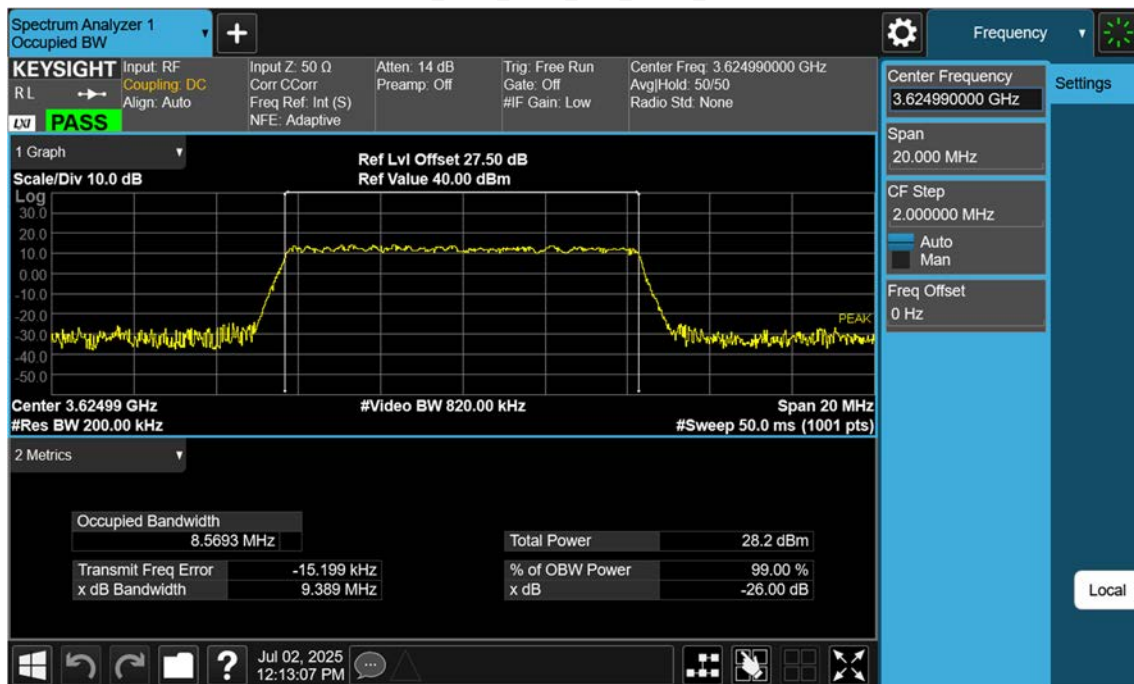
NR48_40 M_PAR_Mid_64QAM_FullRB



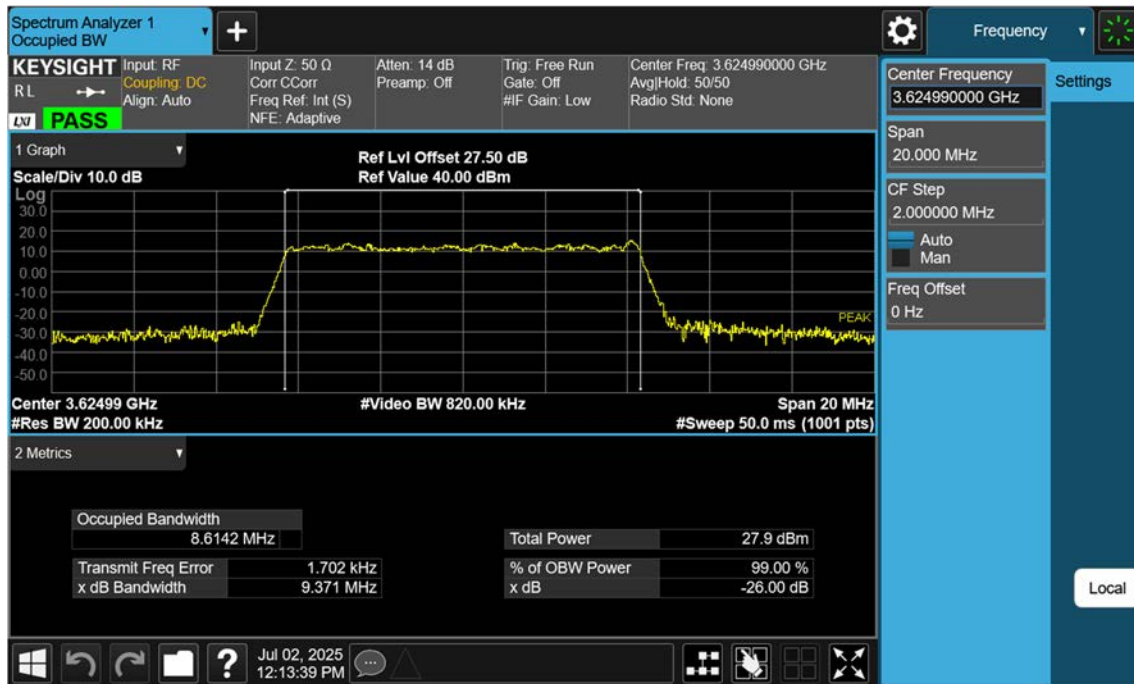
NR48_40 M_PAR_Mid_256QAM_FullRB



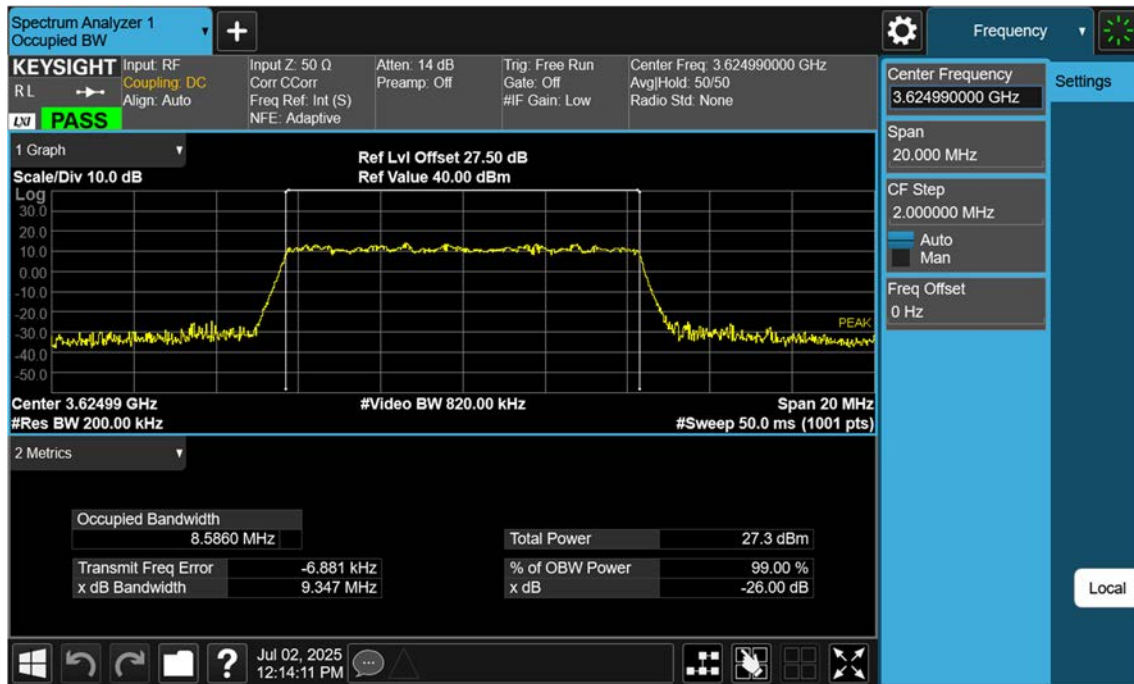
NR48_10 M_OBW_Mid_BPSK_FullRB



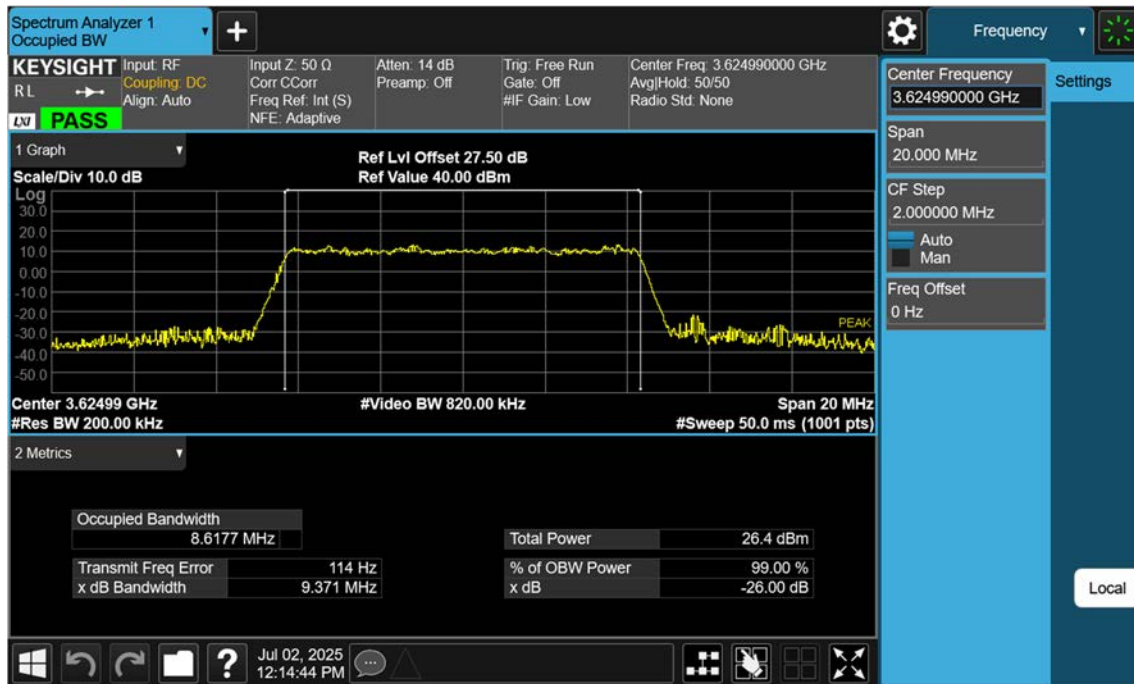
NR48_10 M_OBW_Mid_QPSK_FullRB



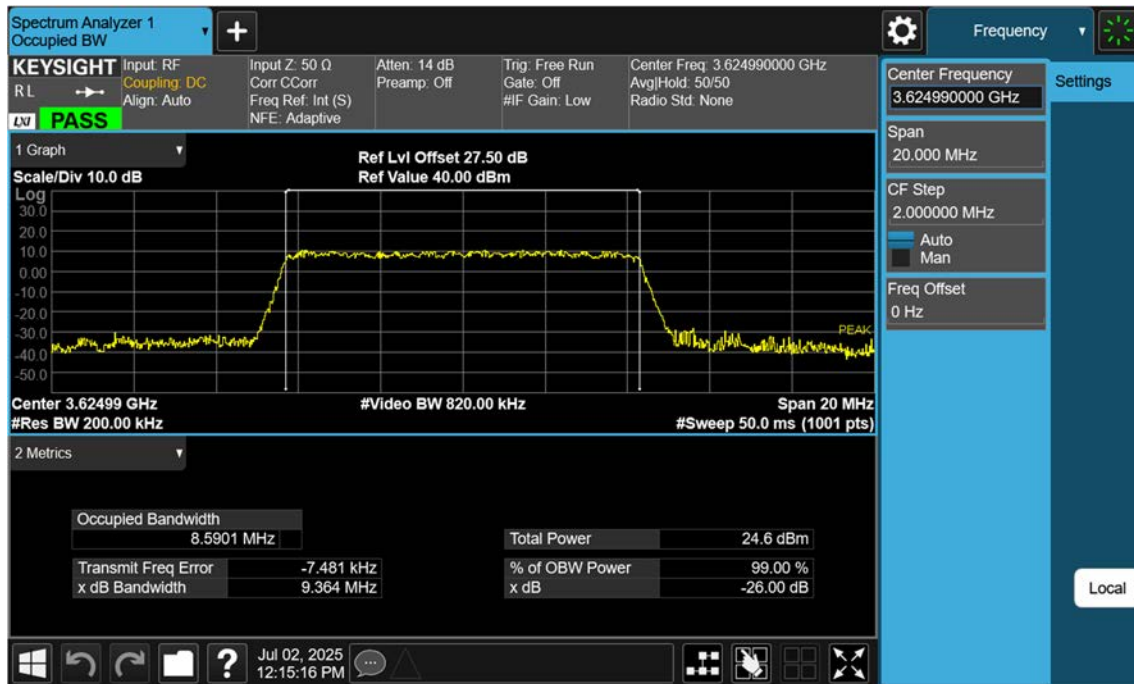
NR48_10 M_OBW_Mid_16QAM_FullRB



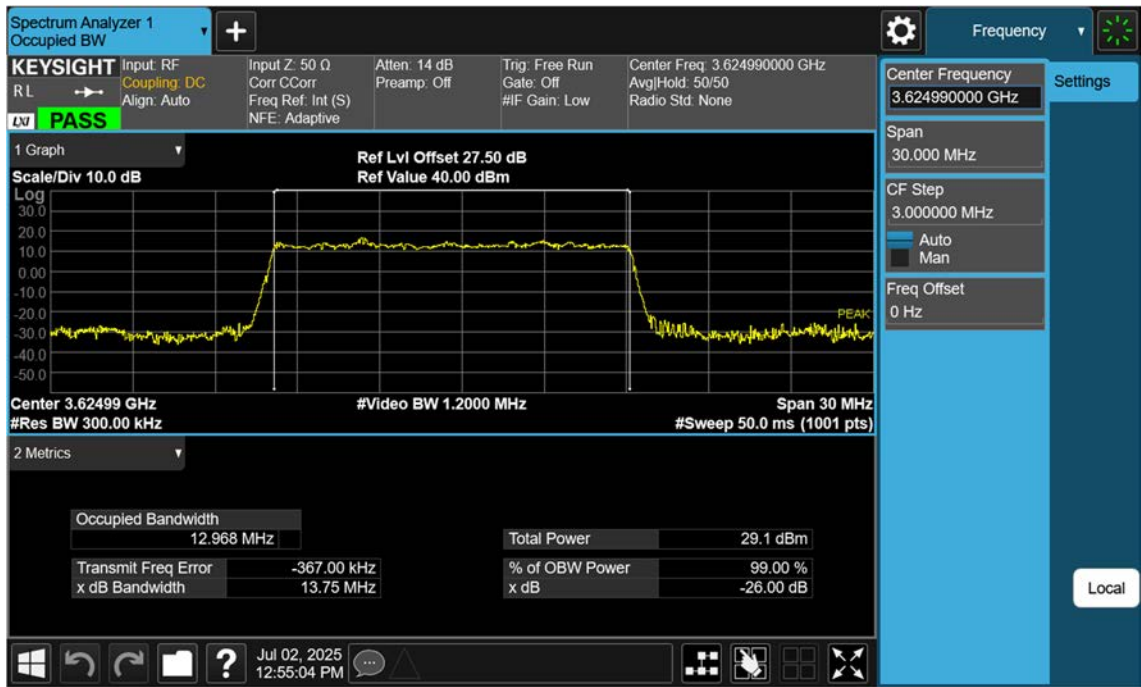
NR48_10 M_OBW_Mid_64QAM_FullRB



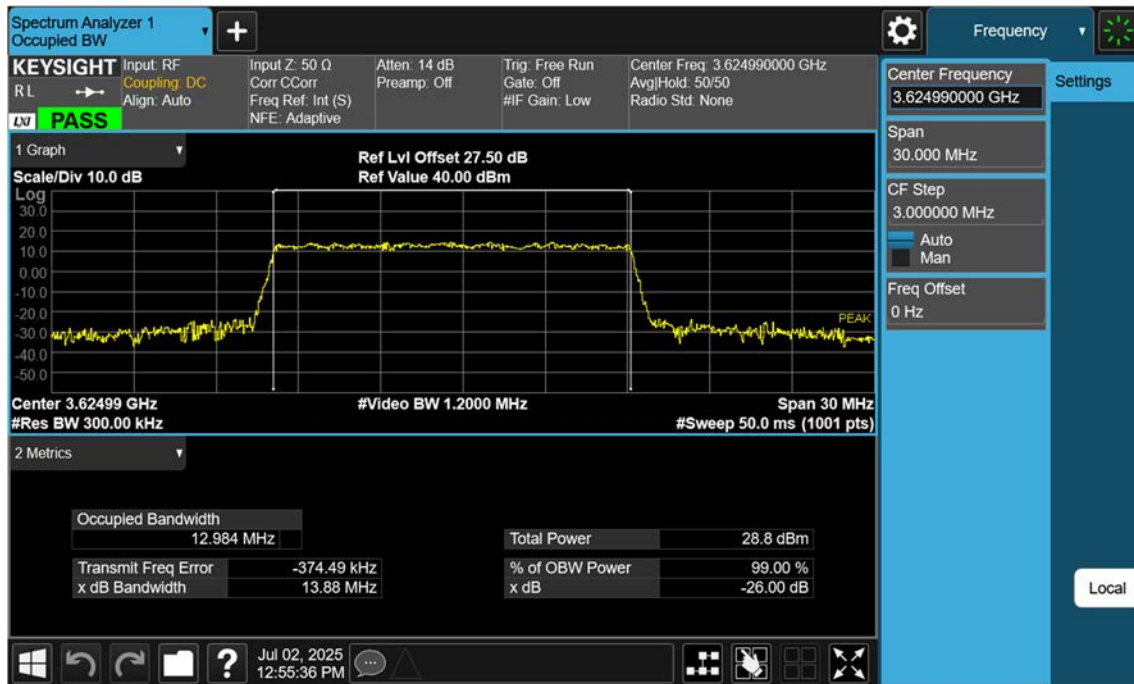
NR48_10 M_OBW_Mid_256QAM_FullRB



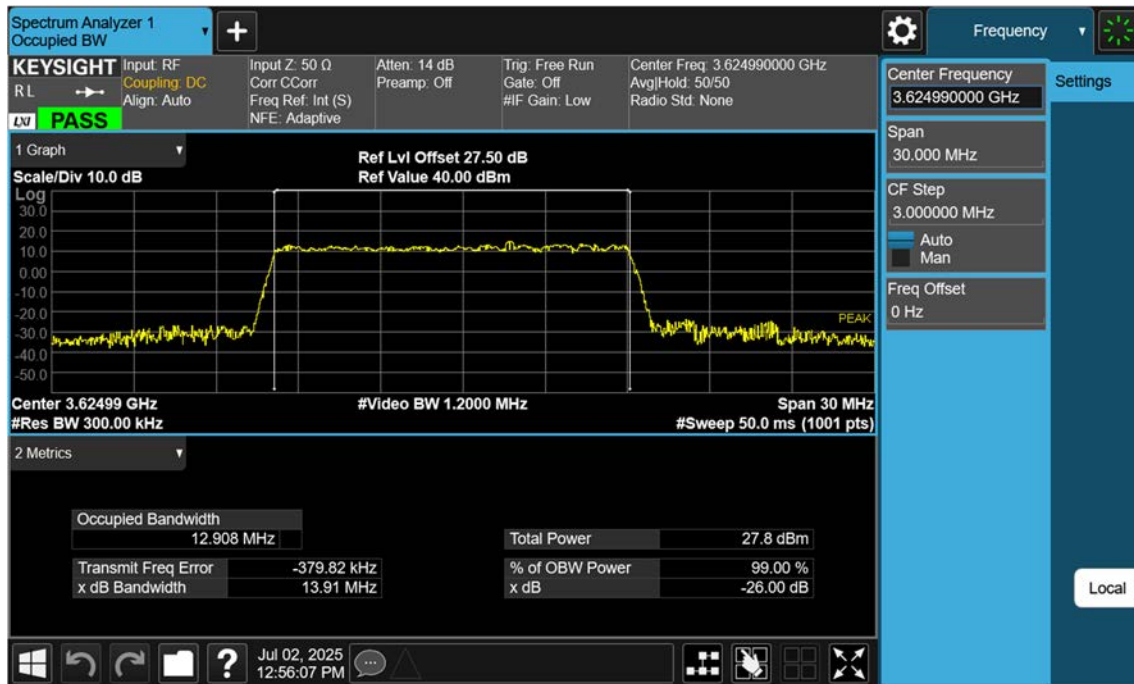
NR48_15 M_OBW_Mid_BPSK_FullRB



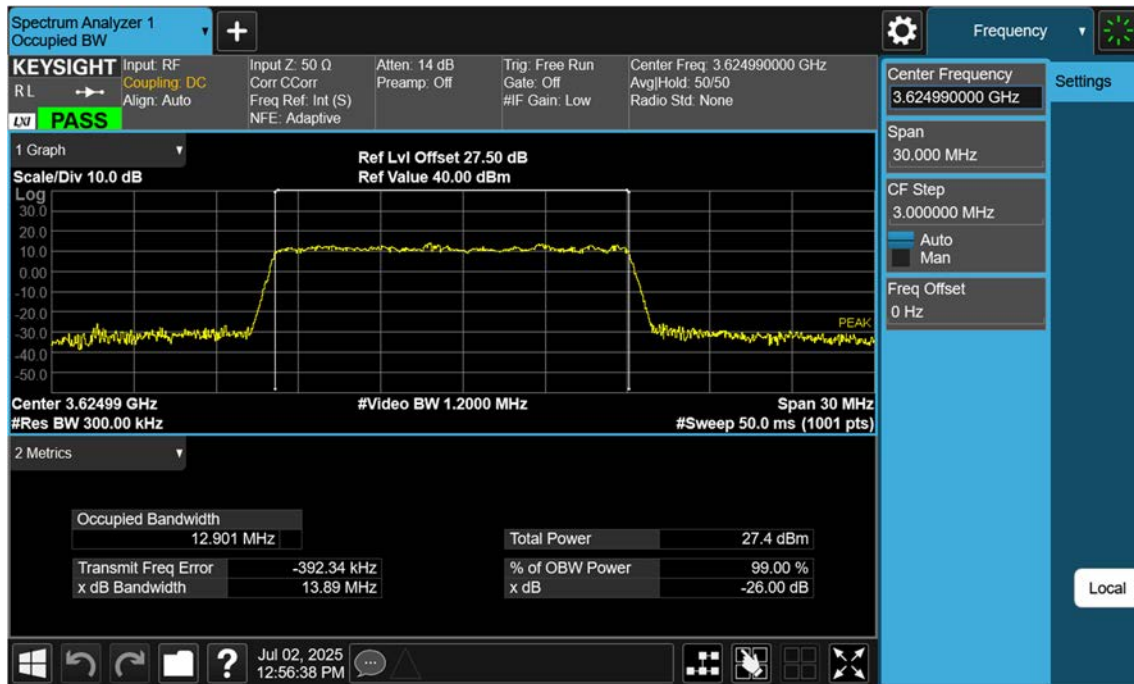
NR48_15 M_OBW_Mid_QPSK_FullRB



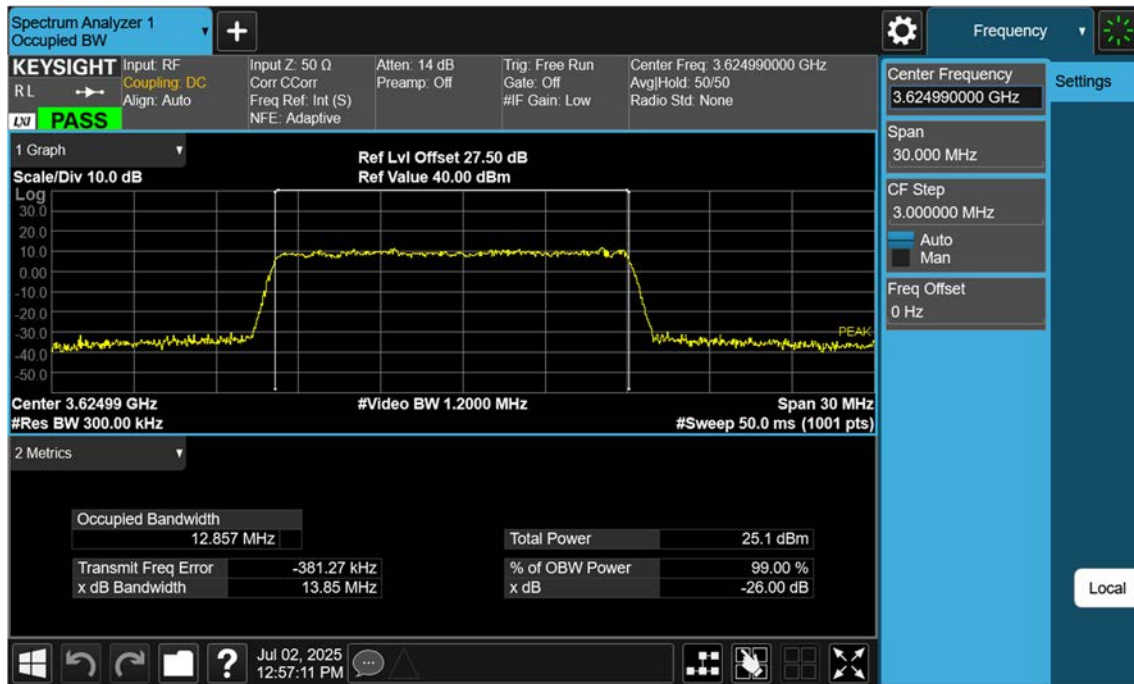
NR48_15 M_OBW_Mid_16QAM_FullRB



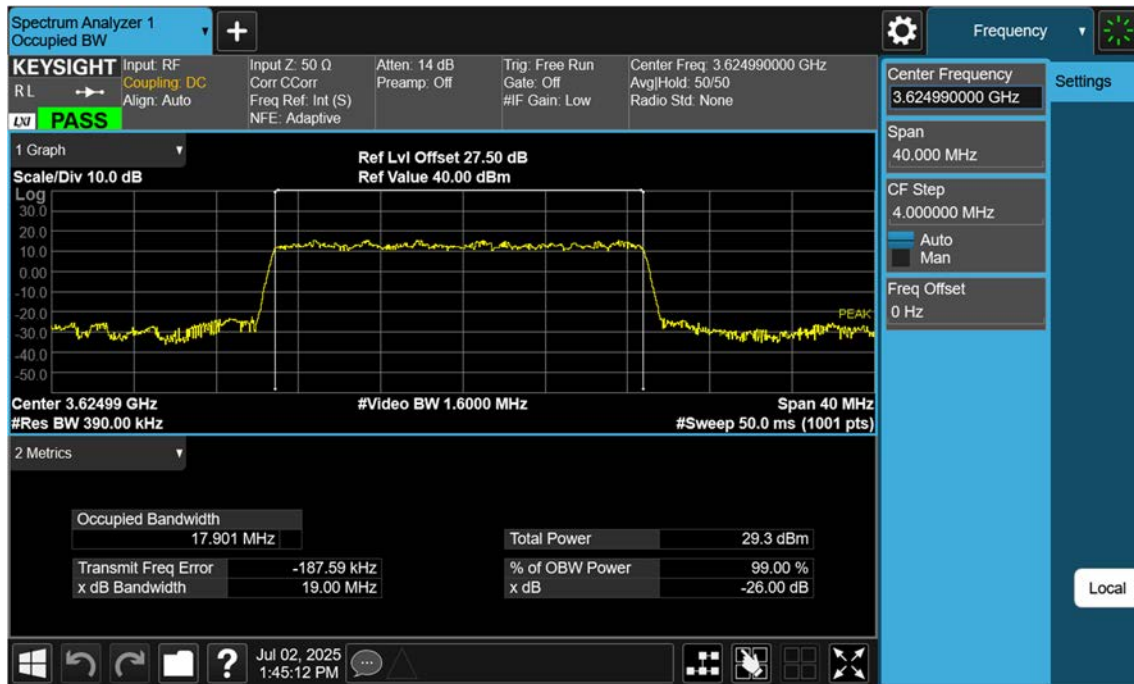
NR48_15 M_OBW_Mid_64QAM_FullRB



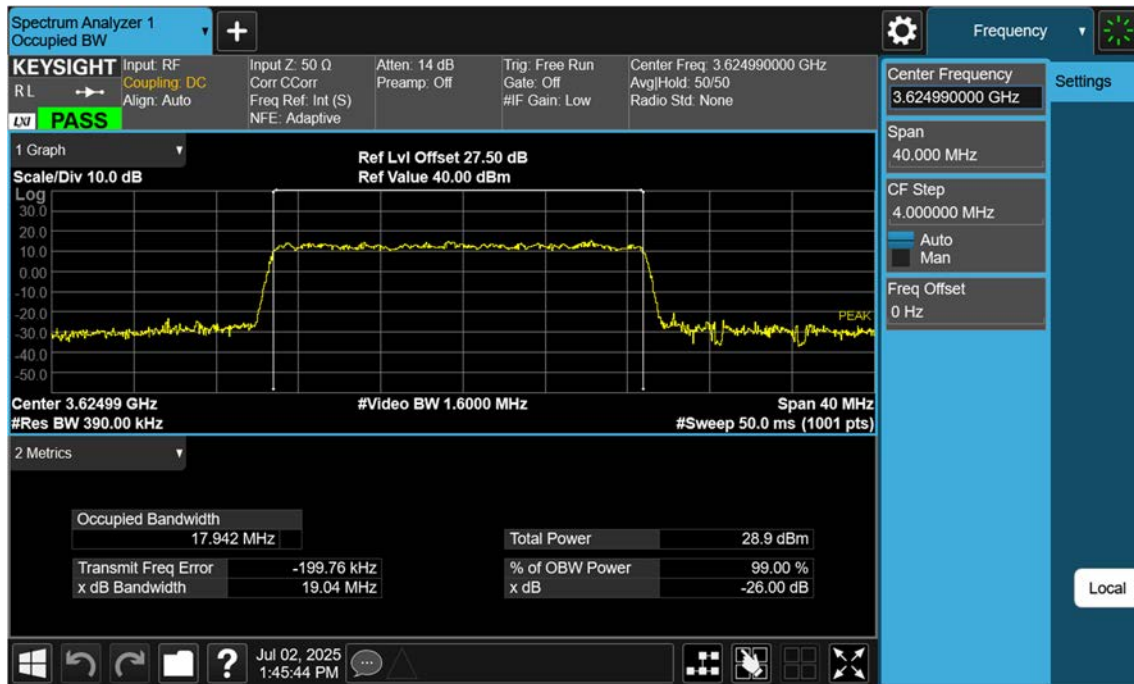
NR48_15 M_OBW_Mid_256QAM_FullRB



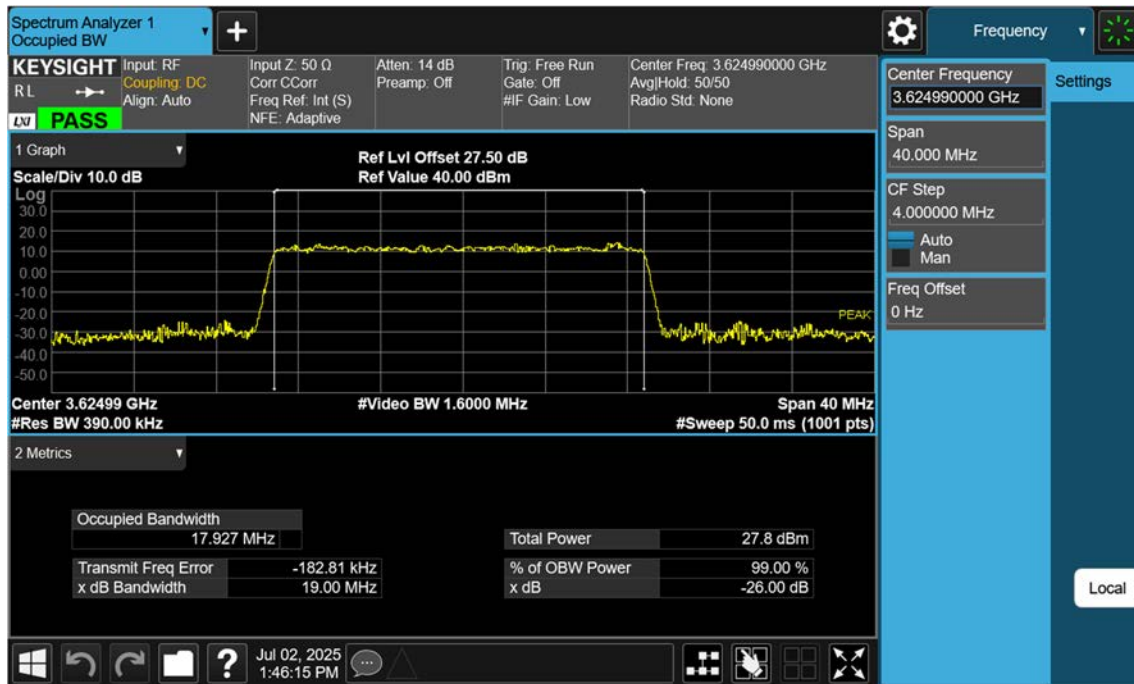
NR48_20 M_OBW_Mid_BPSK_FullRB



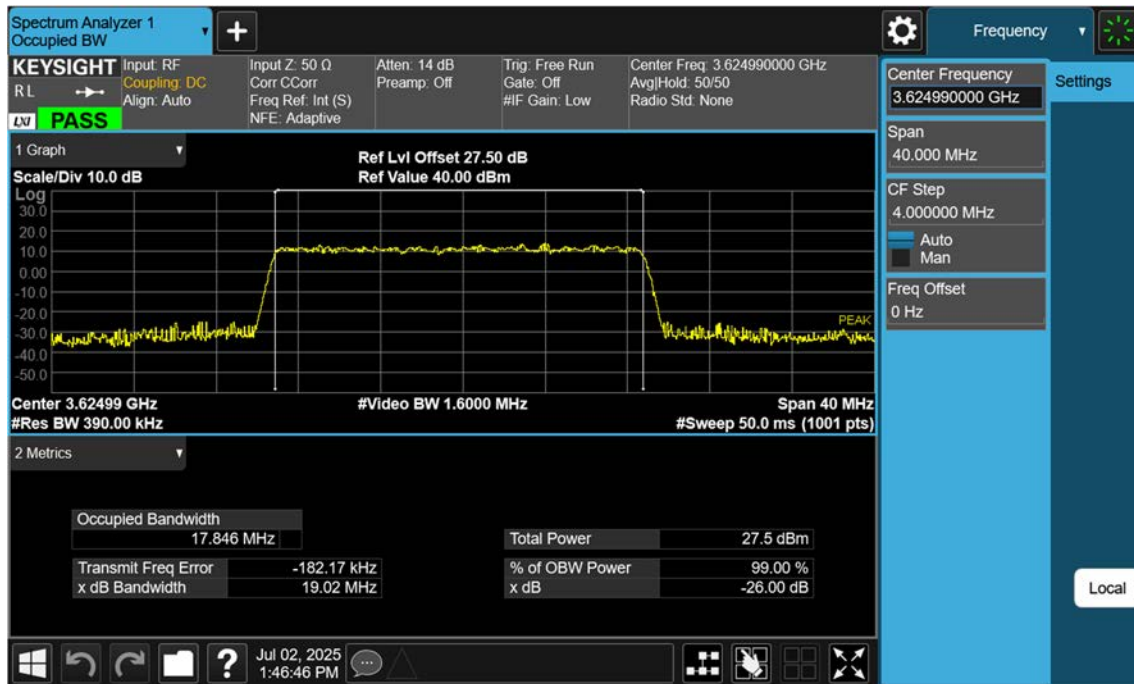
NR48_20 M_OBW_Mid_QPSK_FullRB



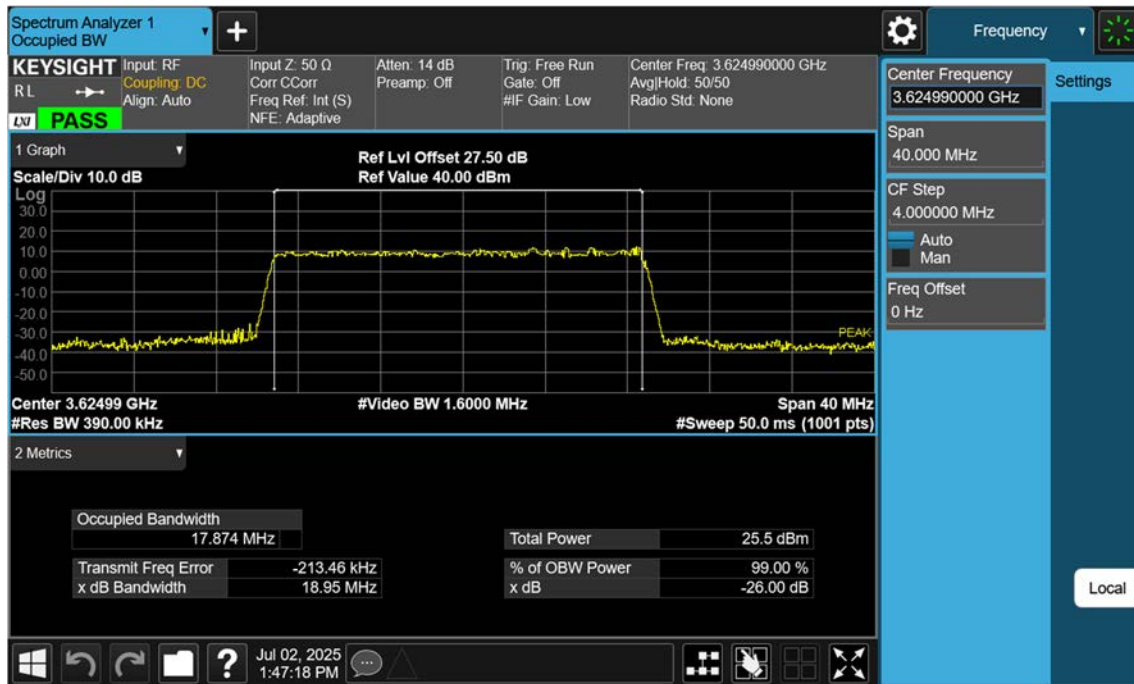
NR48_20 M_OBW_Mid_16QAM_FullRB



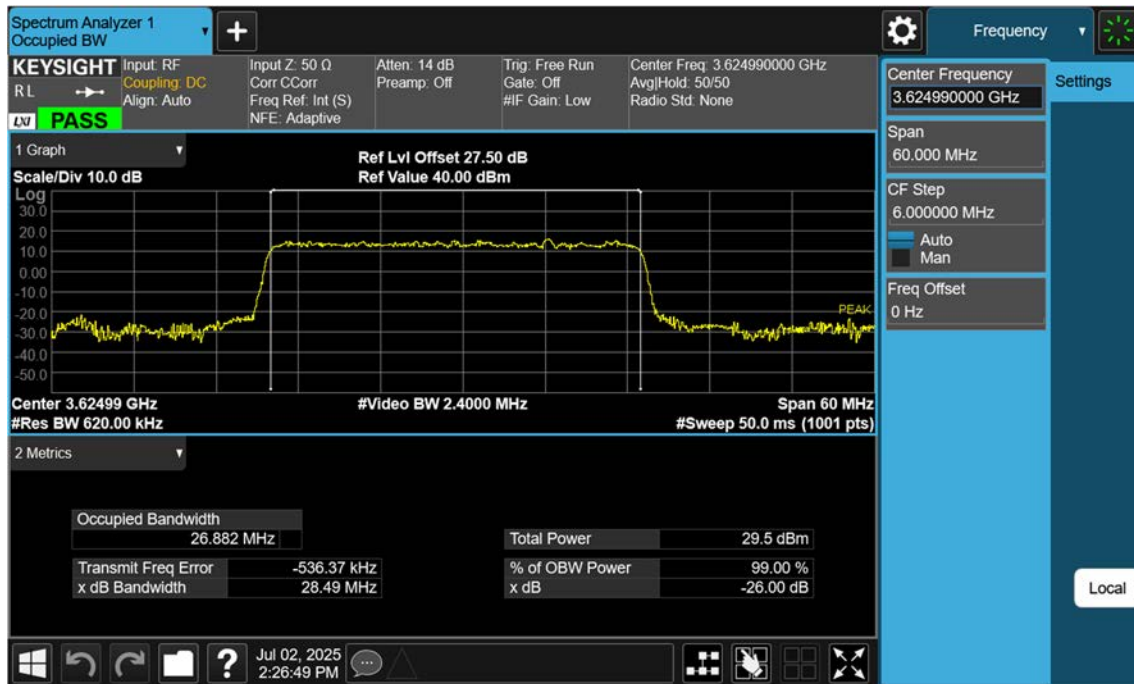
NR48_20 M_OBW_Mid_64QAM_FullRB



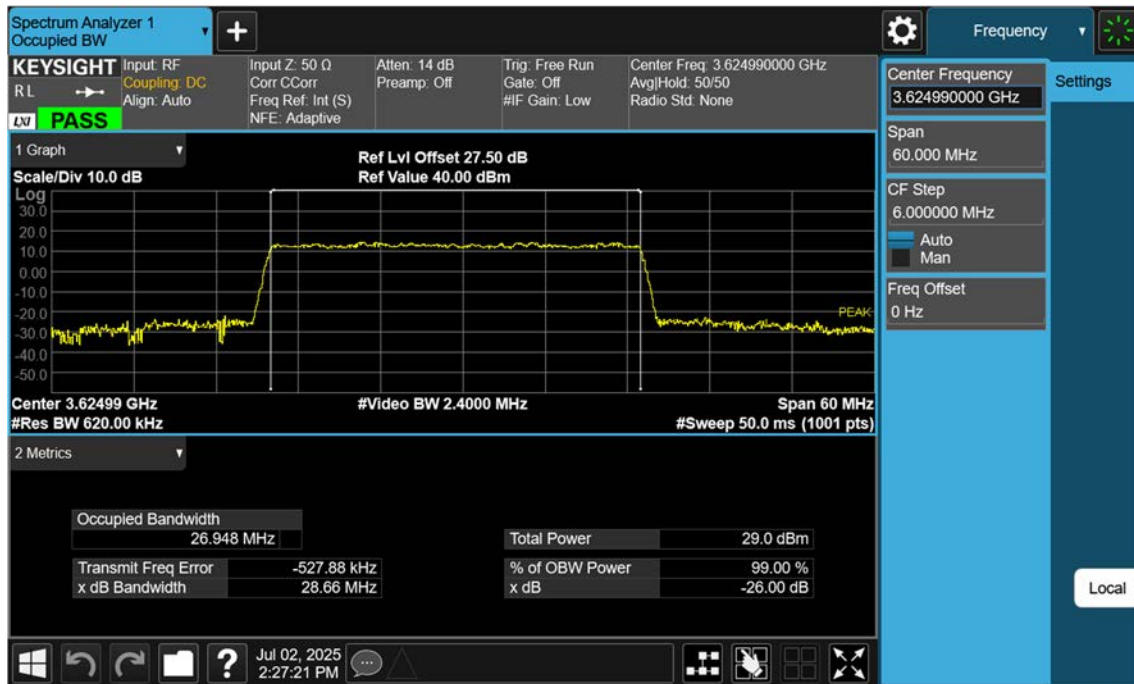
NR48_20 M_OBW_Mid_256QAM_FullRB



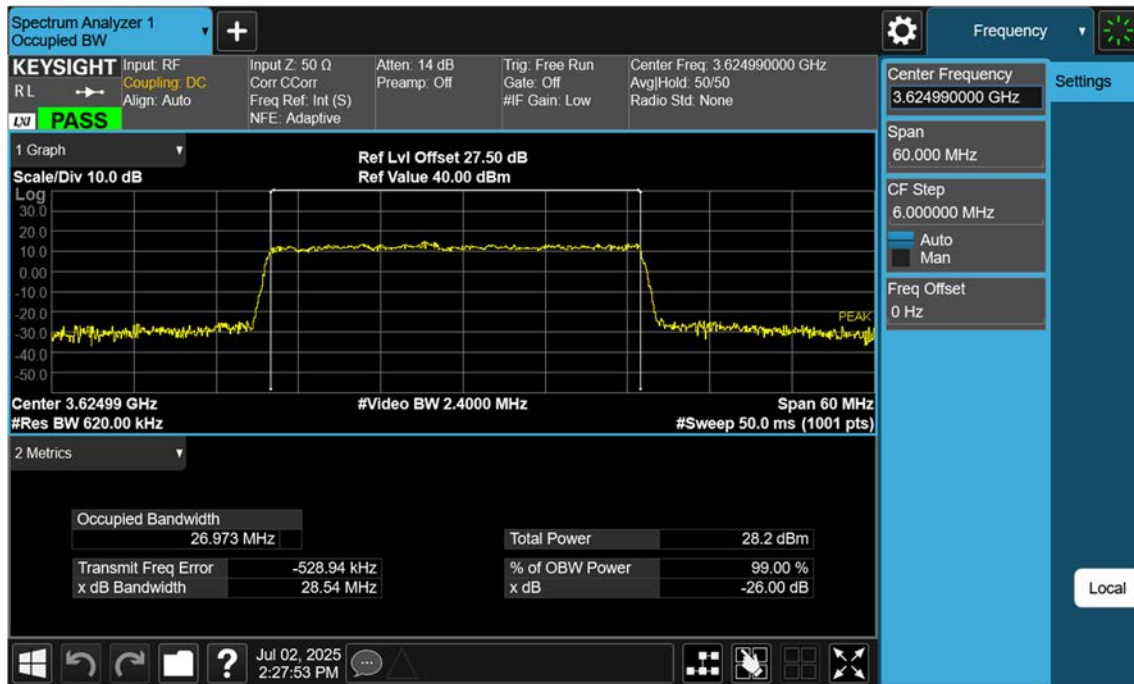
NR48_30 M_OBW_Mid_BPSK_FullRB



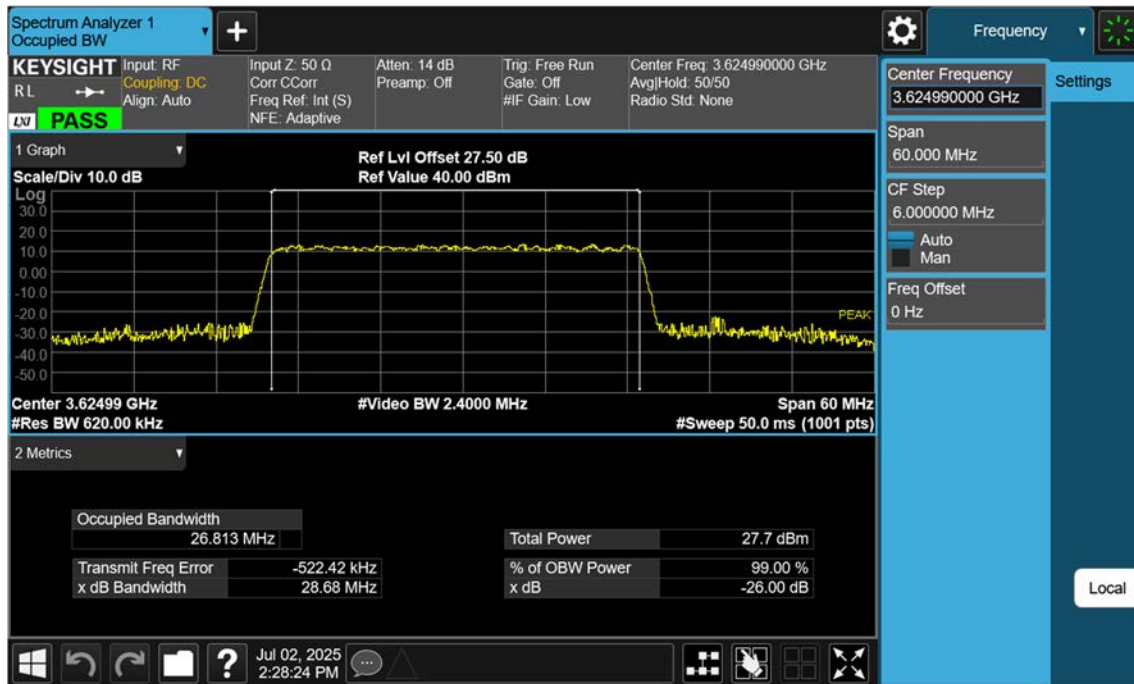
NR48_30 M_OBW_Mid_QPSK_FullRB



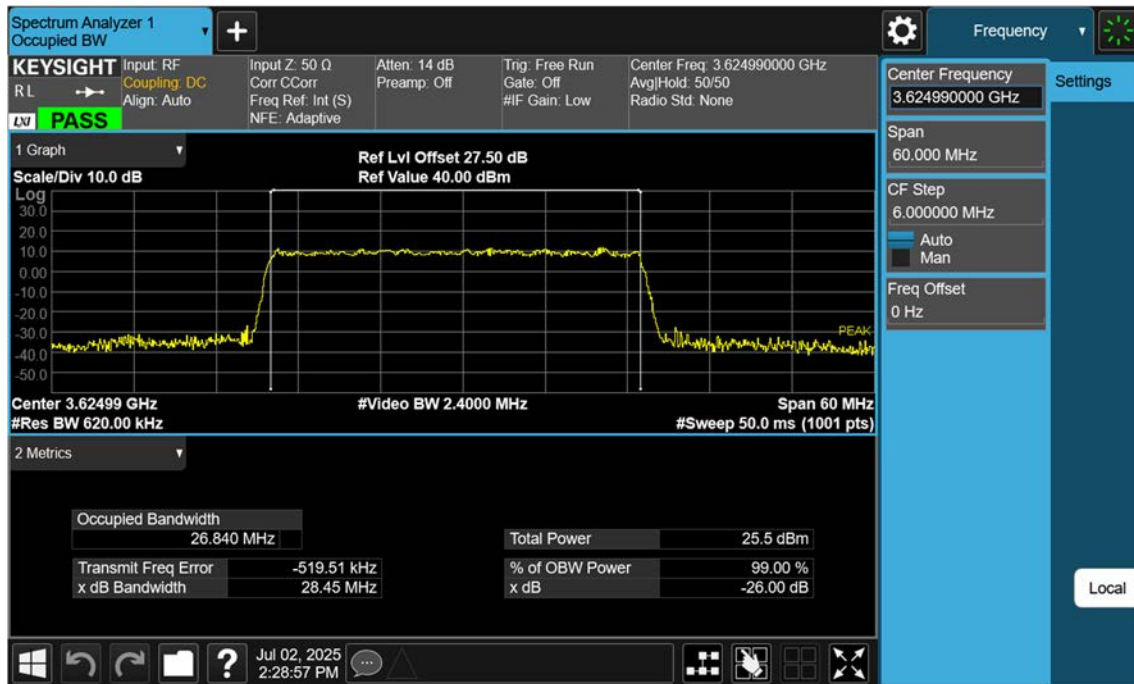
NR48_30 M_OBW_Mid_16QAM_FullRB



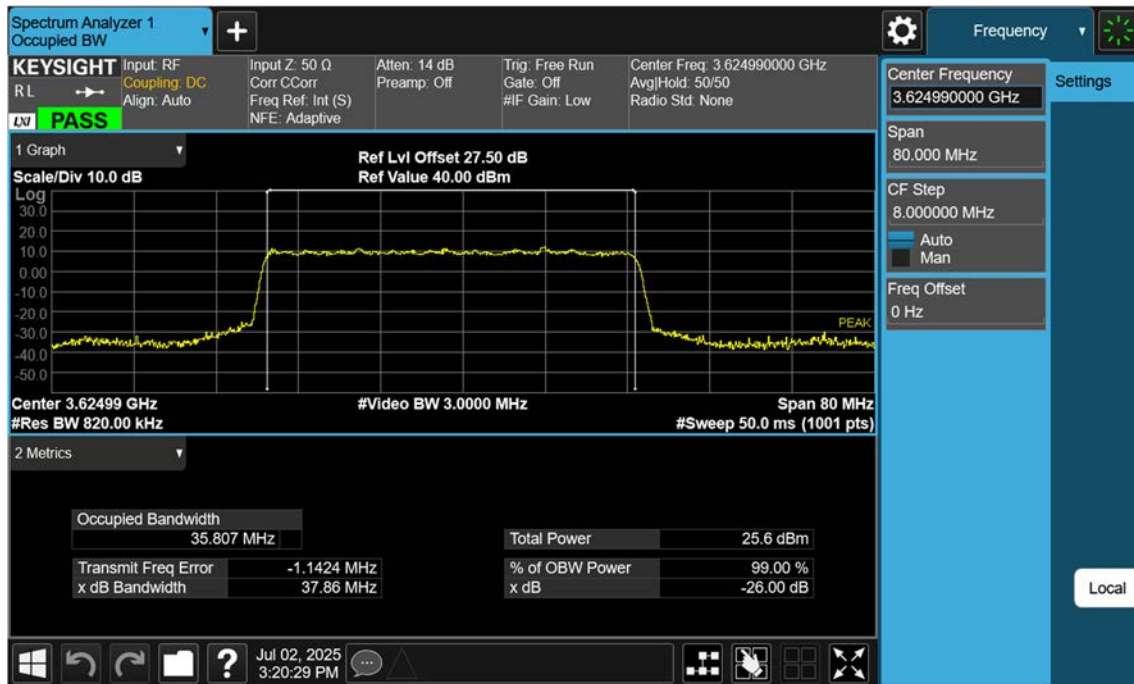
NR48_30 M_OBW_Mid_64QAM_FullRB



NR48_30 M_OBW_Mid_256QAM_FullRB



NR48_40 M_OBW_Mid_BPSK_FullRB



NR48_40 M_OBW_Mid_QPSK_FullRB

