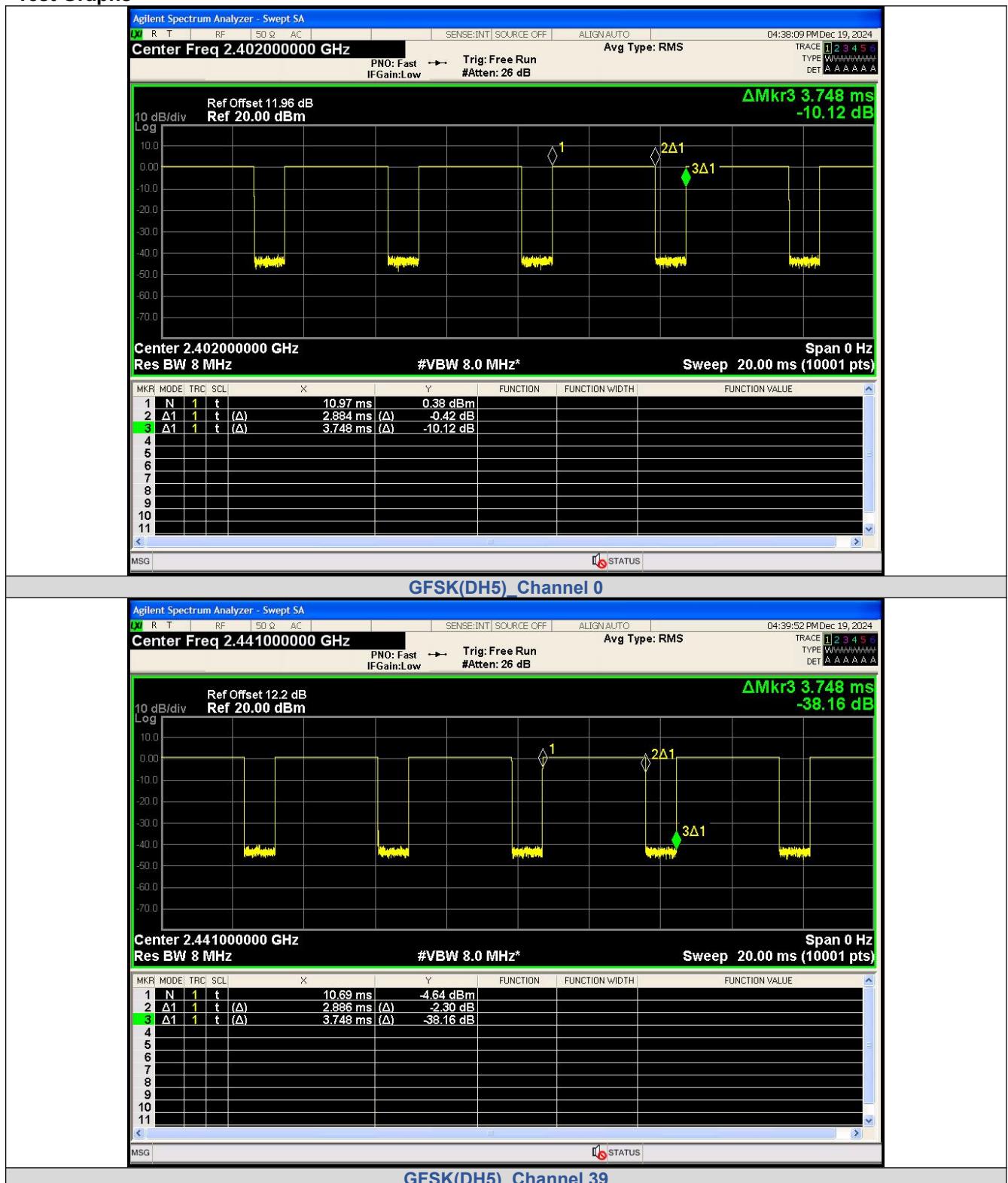


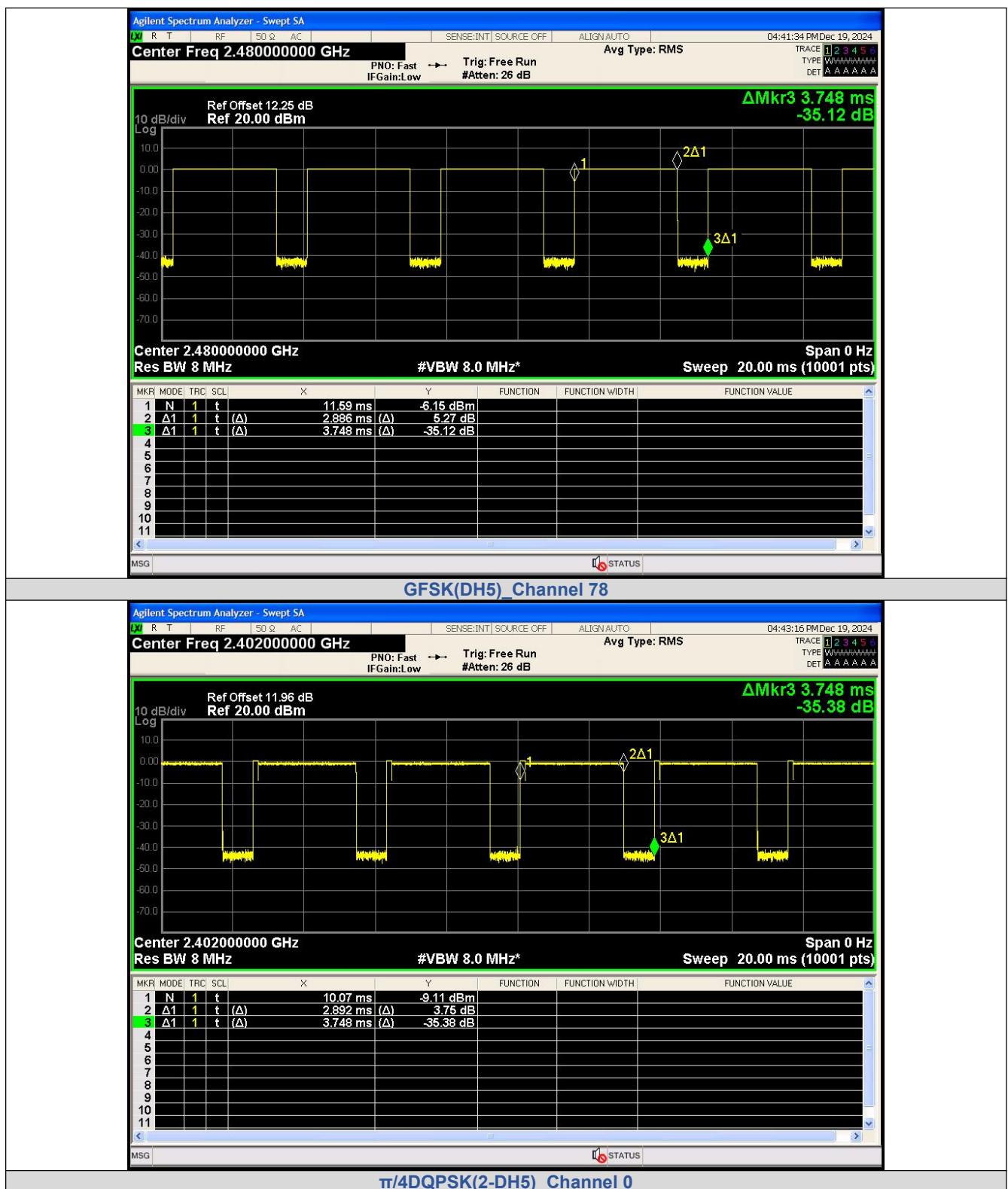
APPENDIX VI. Duty Cycle

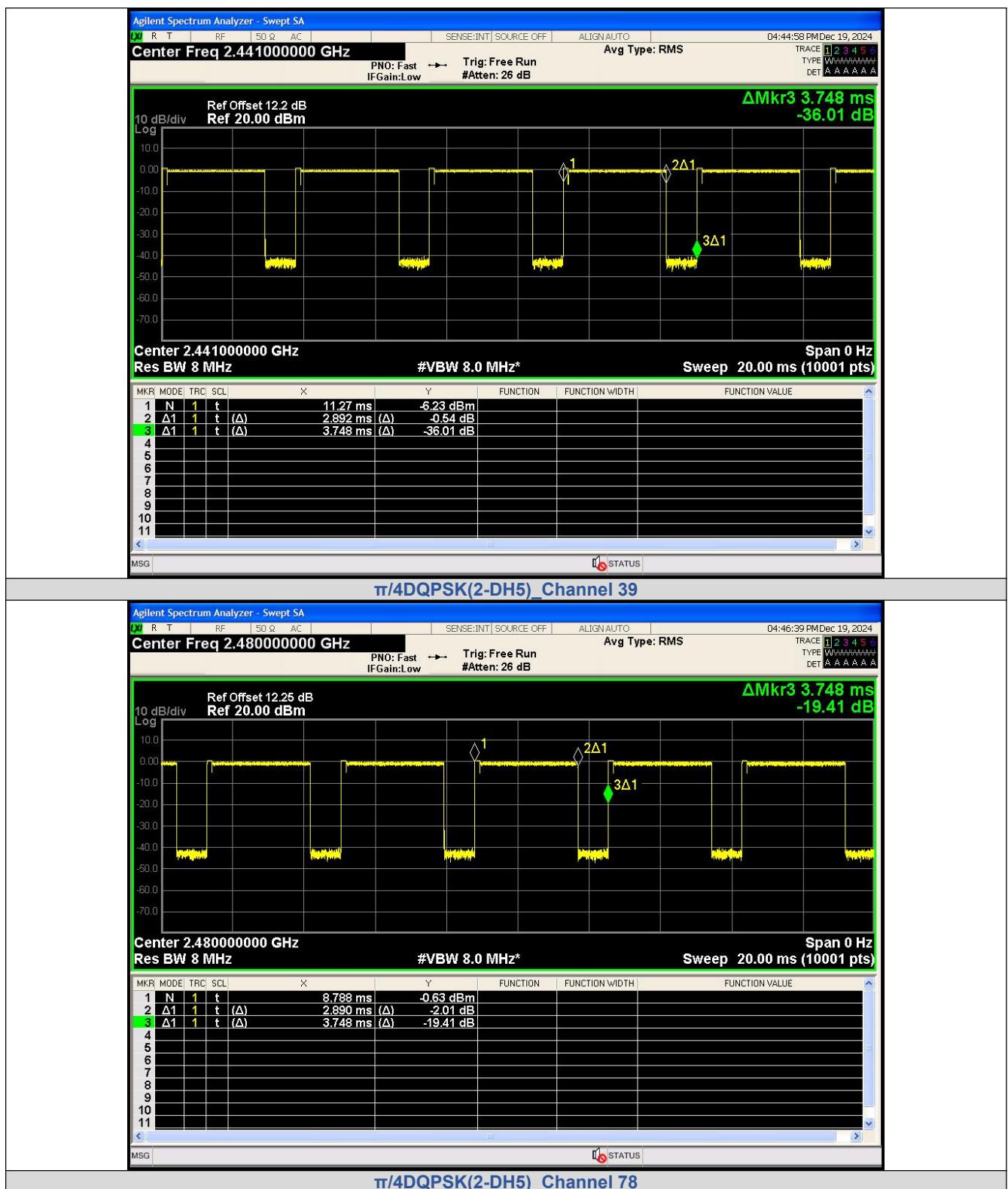
Test Result

Modulation	packets	Channel	On Time (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle (linear)	Duty Cycle Factor (dB)
GFSK	DH5	0	2.884	3.748	76.95	0.7695	1.1379
		39	2.886	3.748	77.00	0.7700	1.1351
		78	2.886	3.748	77.00	0.7700	1.1351
$\pi/4$ DQPSK	2-DH5	0	2.892	3.748	77.16	0.7716	1.1261
		39	2.892	3.748	77.16	0.7716	1.1261
		78	2.890	3.748	77.11	0.7711	1.1289

Test Graphs







APPENDIX VII. Dwell Time

Test Result

Modulation	Packet	Channel	Pulse Width (ms)	Number of Pulses in 31.6 seconds	Dwell Time (ms)	Limit (ms)	Result
GFSK	DH1	CH39 (2441MHz)	0.3792	319	120.96	< 400	PASS
	DH3		1.656	164	271.58		PASS
	DH5		2.872	104	298.69		PASS
π/4DQPSK	2-DH1		0.3912	317	124.01	< 400	PASS
	2-DH3		1.632	153	249.7		PASS
	2-DH5		2.896	107	309.87		PASS

Test Graphs

