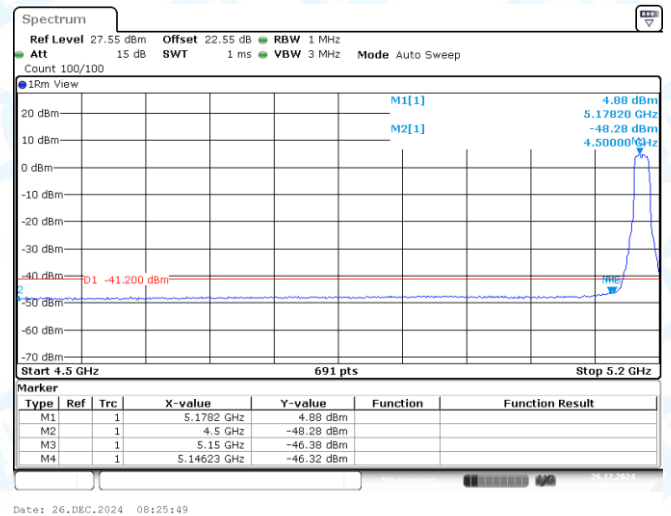
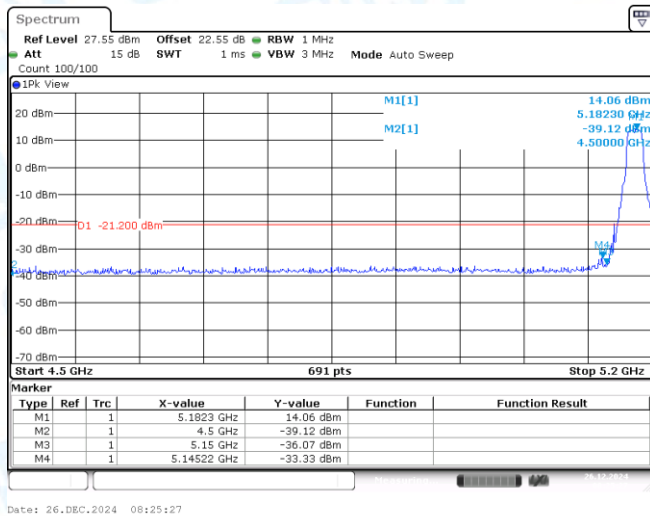
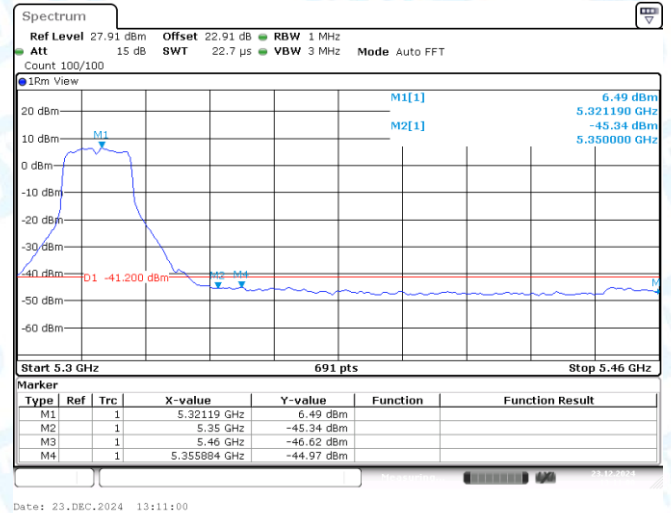
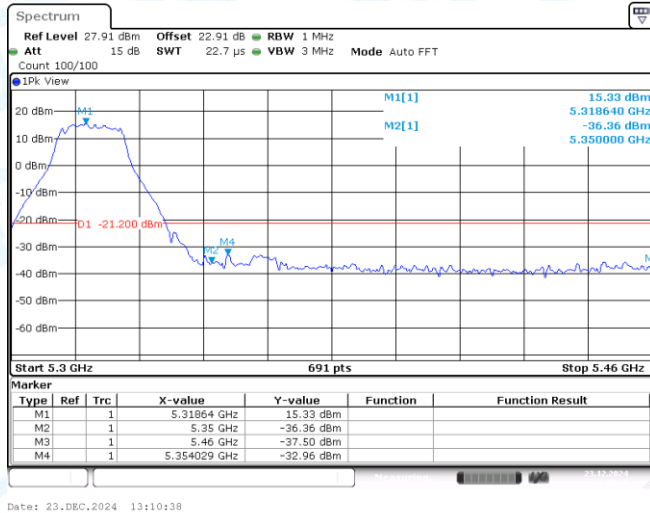


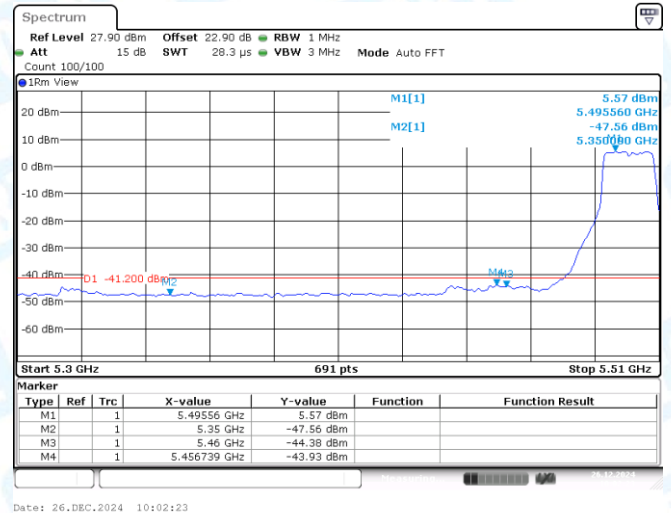
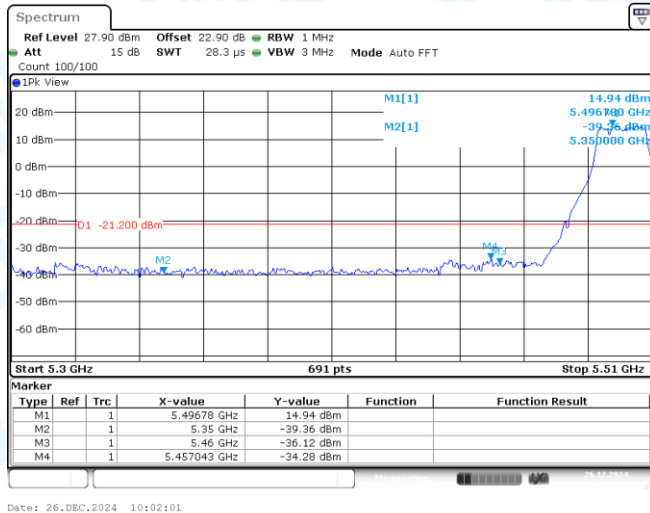
10.2 Test Graphs



11A-Ant1-5180-PASS



11A-Ant1-5320-PASS

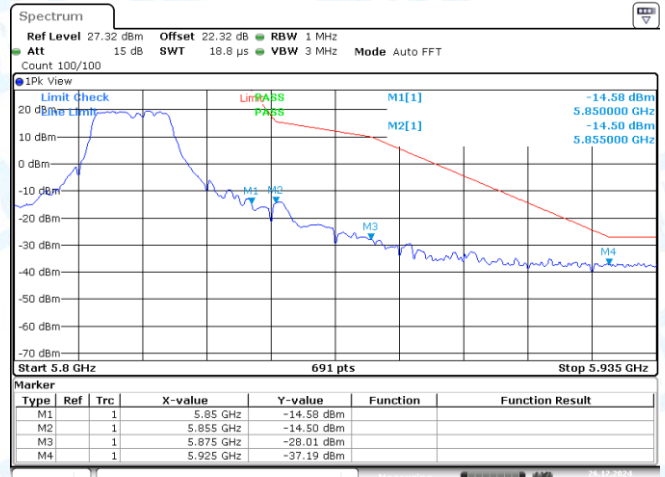


11A-Ant1-5500-PASS

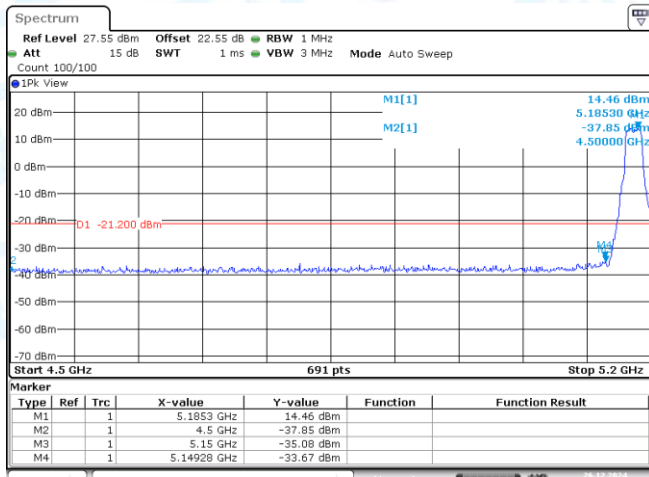
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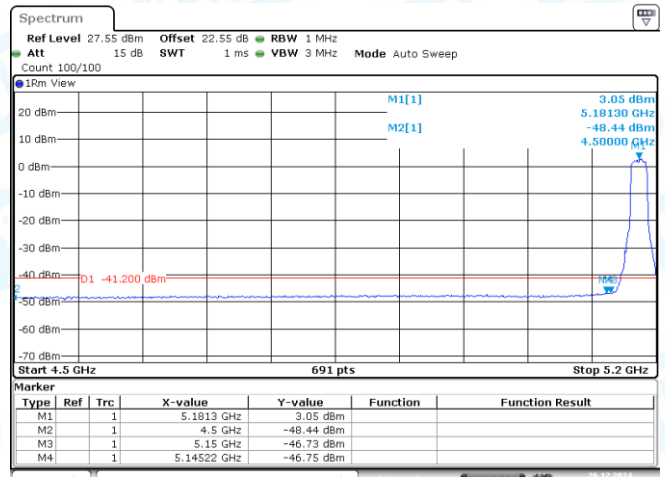
11A-Ant1-5745-PASS



11A-Ant1-5825-PASS



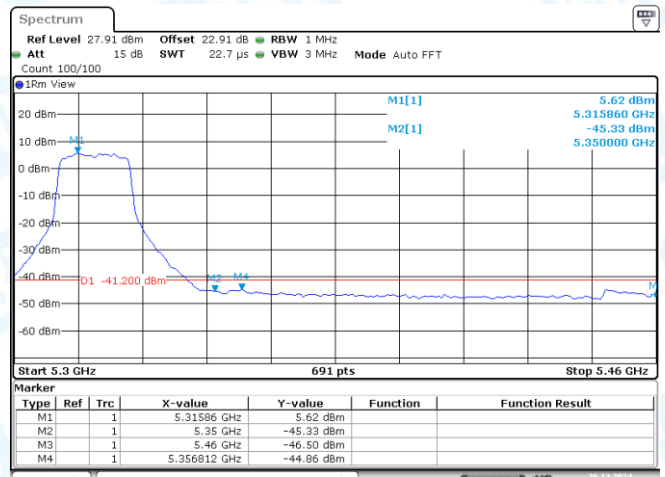
11N20SISO-Ant1-5180-PASS



11N20SISO-Ant1-5180-PASS



11N20SISO-Ant1-5320-PASS

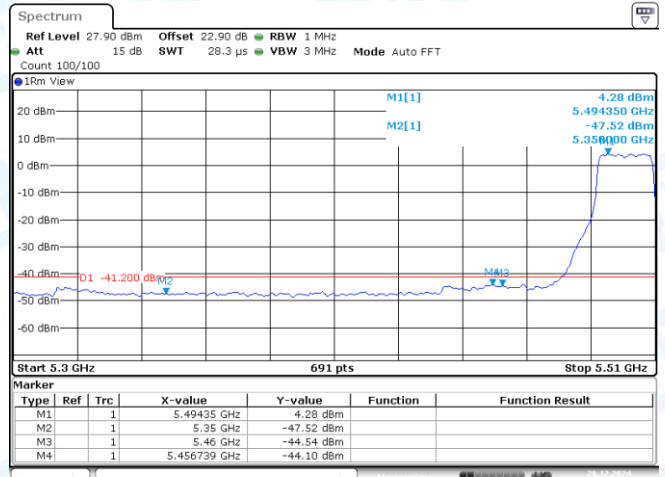


11N20SISO-Ant1-5320-PASS



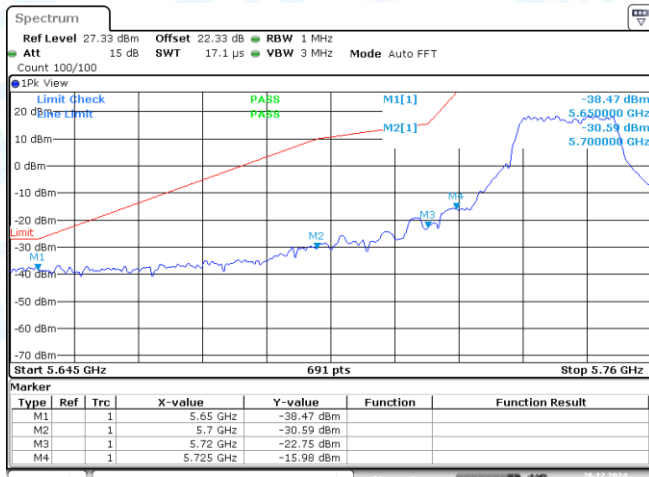
Date: 26.DEC.2024 10:45:14

11N20SISO-Ant1-5500-PASS



Date: 26.DEC.2024 10:45:36

11N20SISO-Ant1-5500-PASS



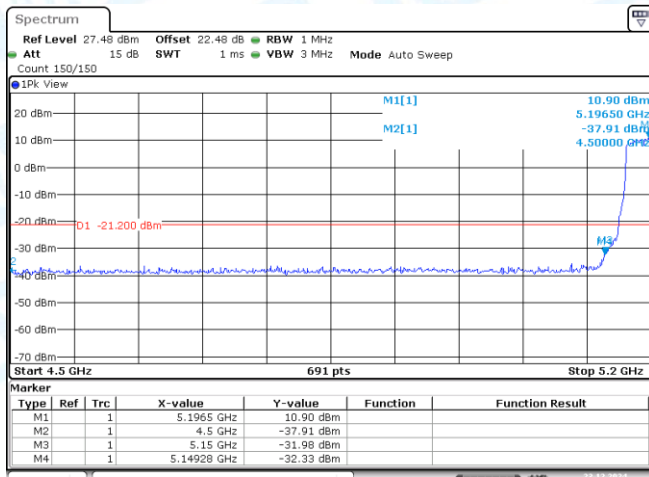
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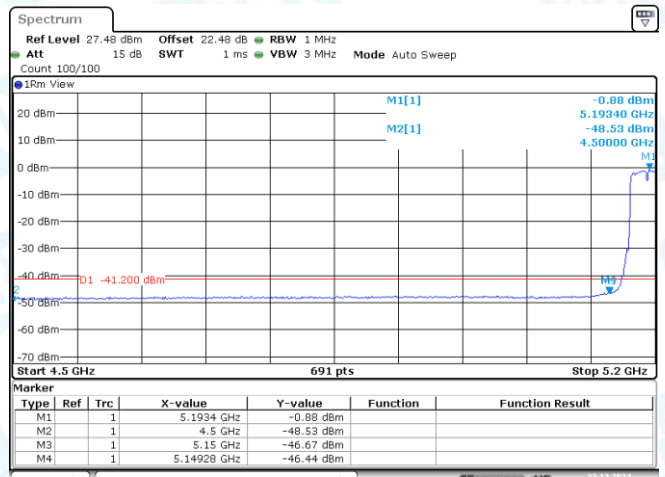
Date: 26.DEC.2024 14:58:15

11N20SISO-Ant1-5825-PASS



Date: 23.DEC.2024 13:25:04

11N40SISO-Ant1-5190-PASS

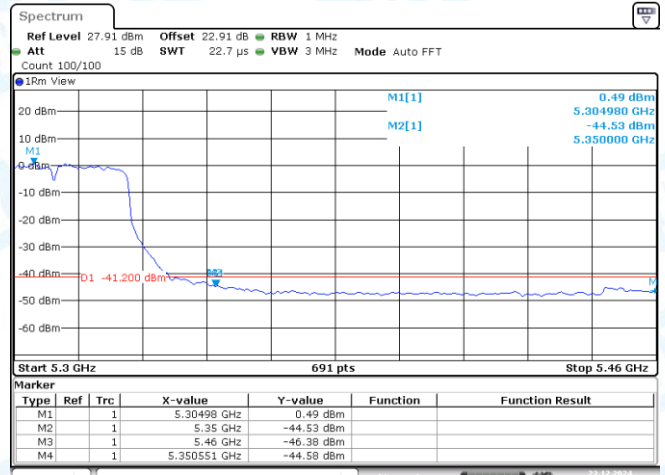


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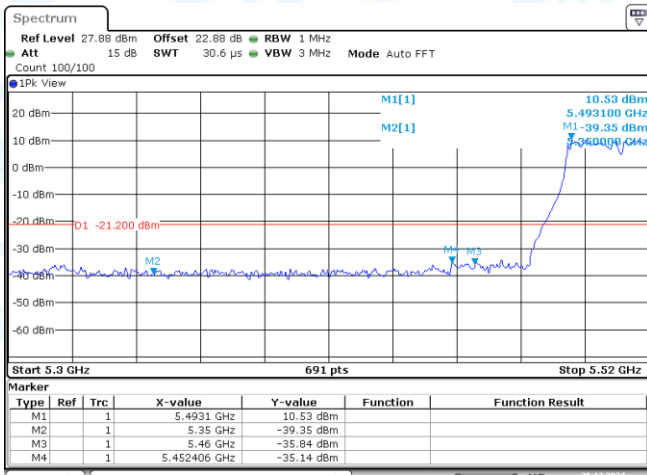
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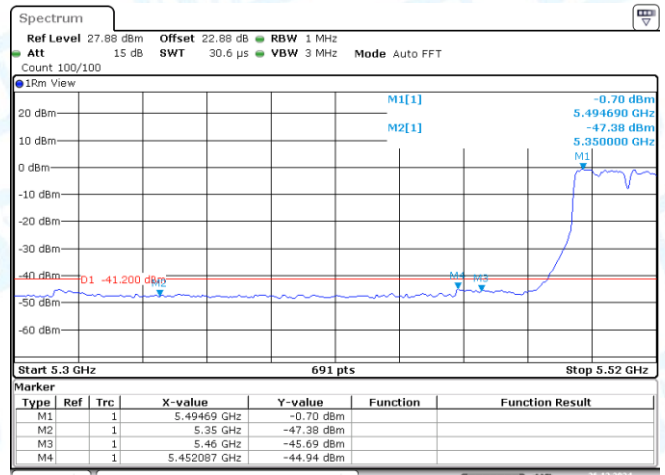
11N40SISO-Ant1-5310-PASS



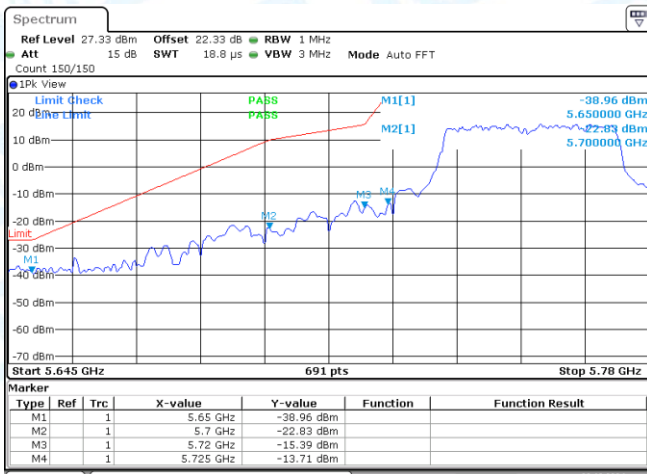
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11N40SISO-Ant1-5310-PASS



11N40SISO-Ant1-5310-PASS



11N40SISO-Ant1-5510-PASS



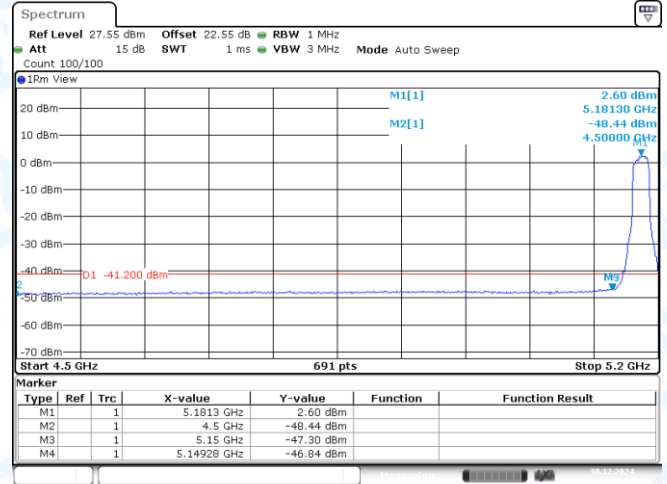
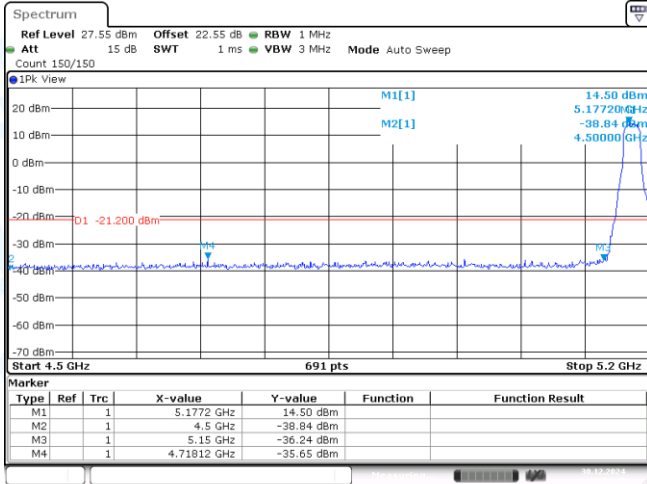
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11N40SISO-Ant1-5755-PASS

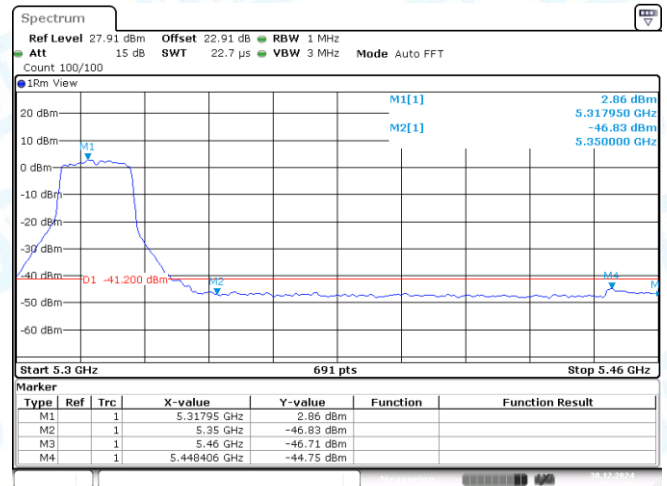
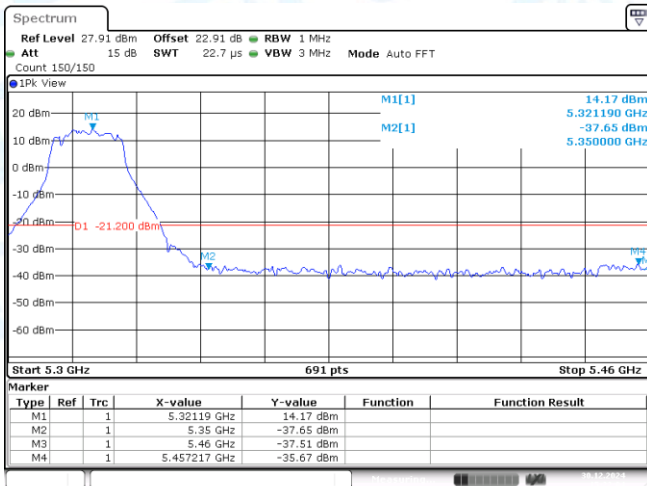


11N40SISO-Ant1-5795-PASS



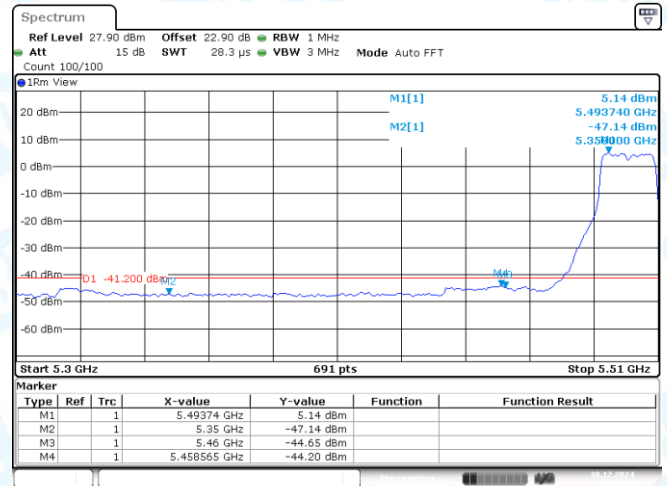
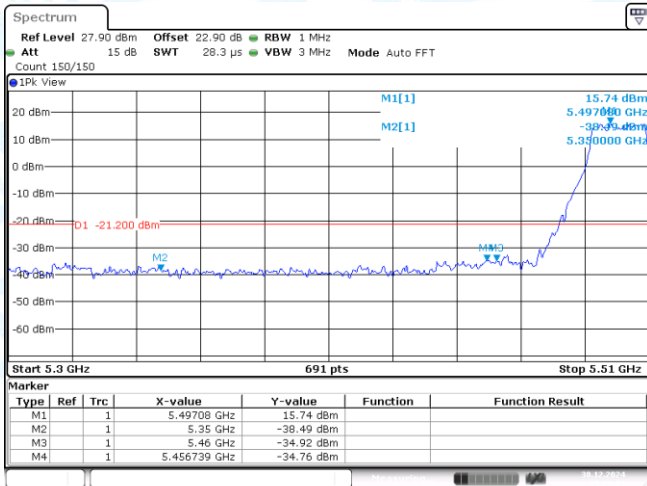
11AC20SISO-Ant1-5180-PASS

11AC20SISO-Ant1-5180-PASS



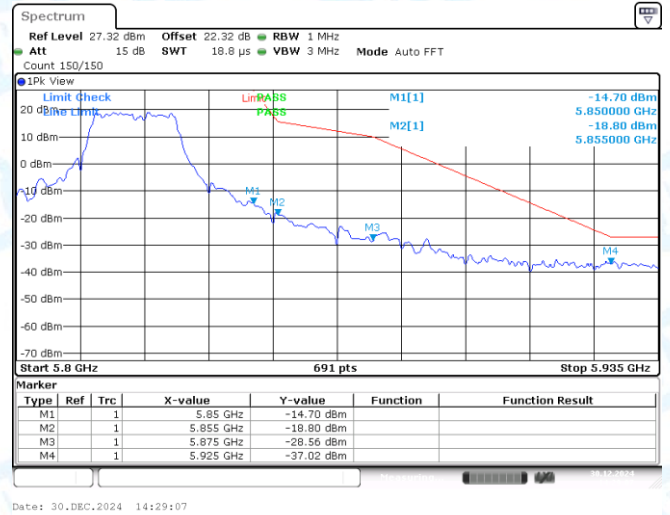
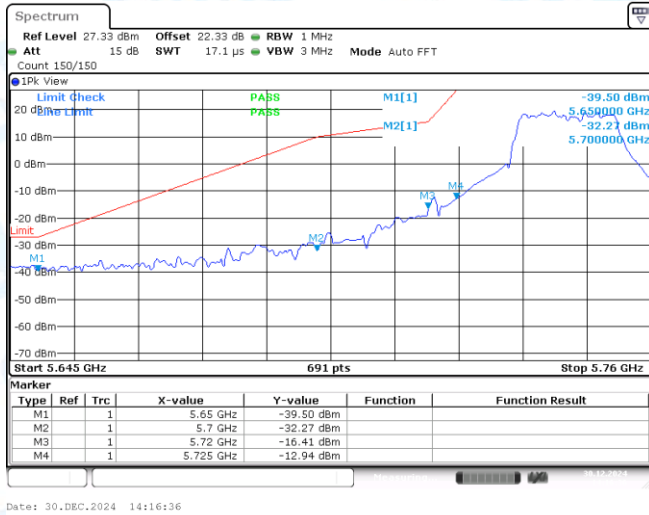
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11AC20SISO-Ant1-5320-PASS



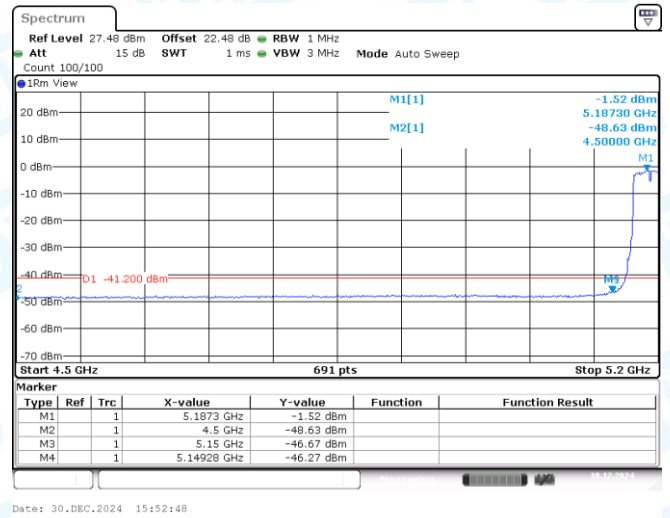
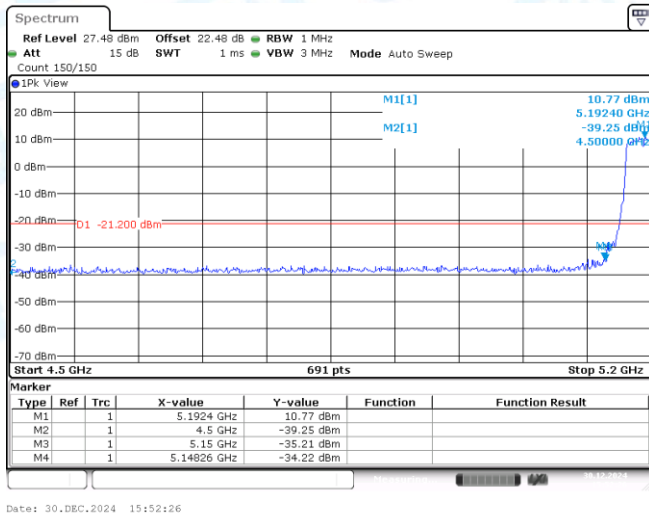
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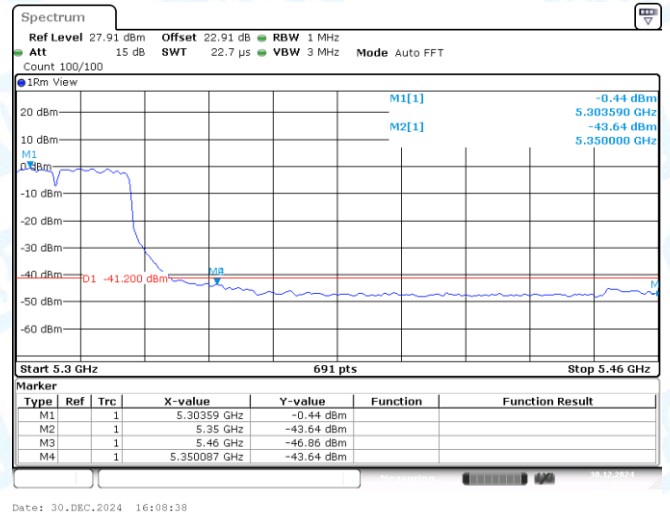
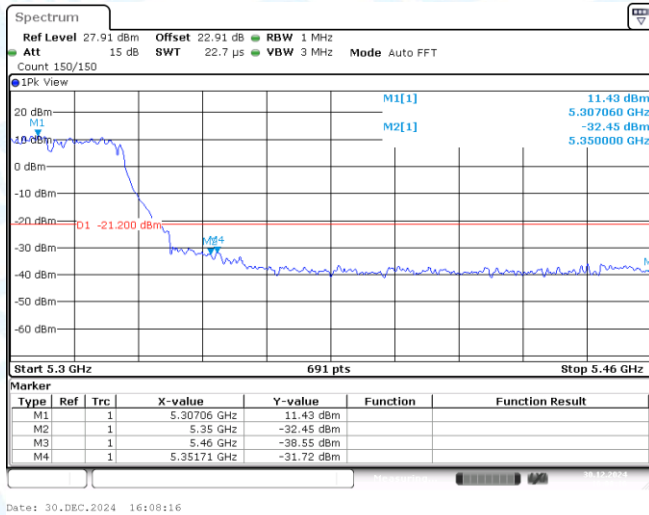
11AC20SISO-Ant1-5745-PASS

11AC20SISO-Ant1-5825-PASS



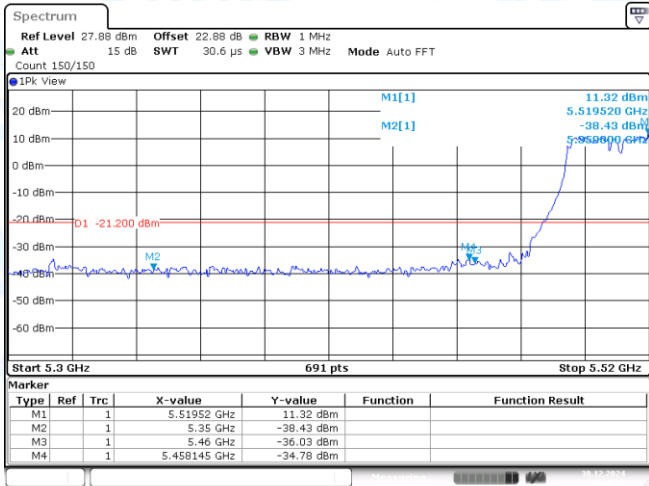
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11AC40SISO-Ant1-5190-PASS

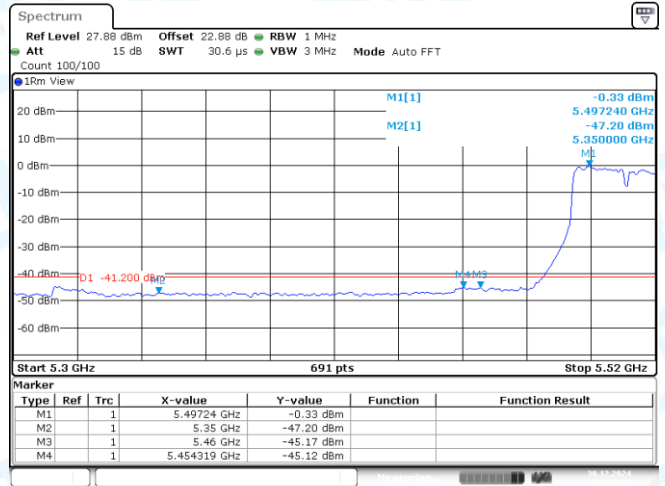


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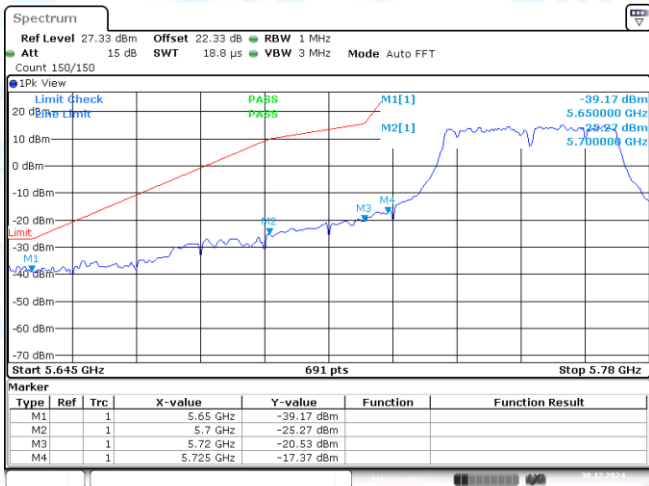
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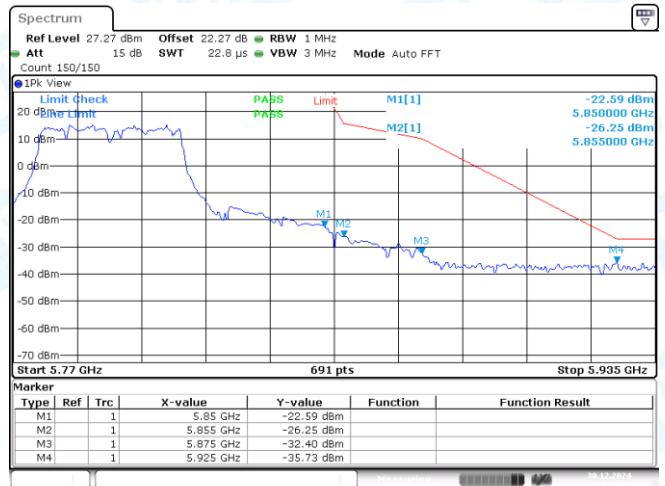
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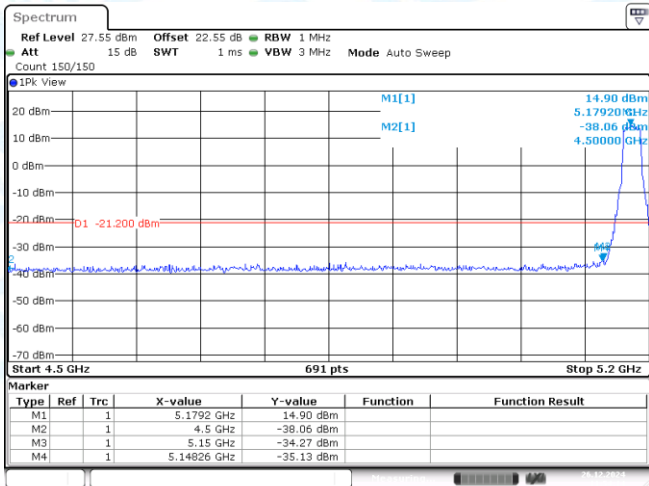
11AC40SISO-Ant1-5510-PASS



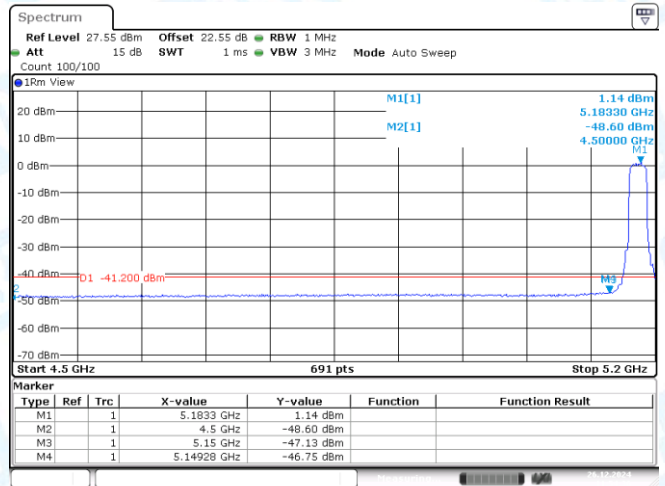
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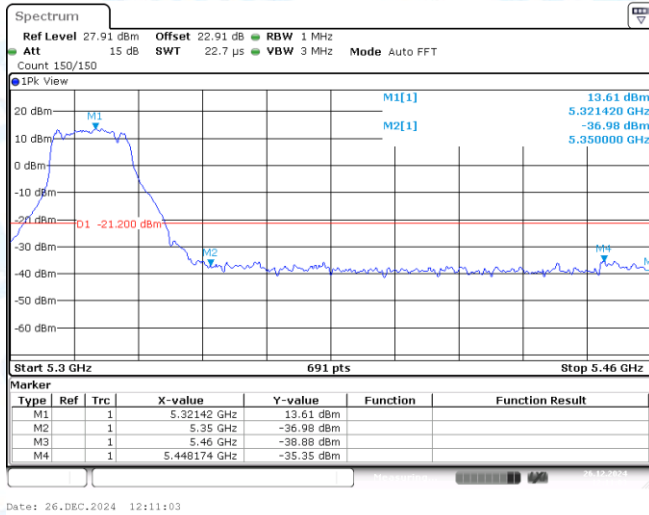
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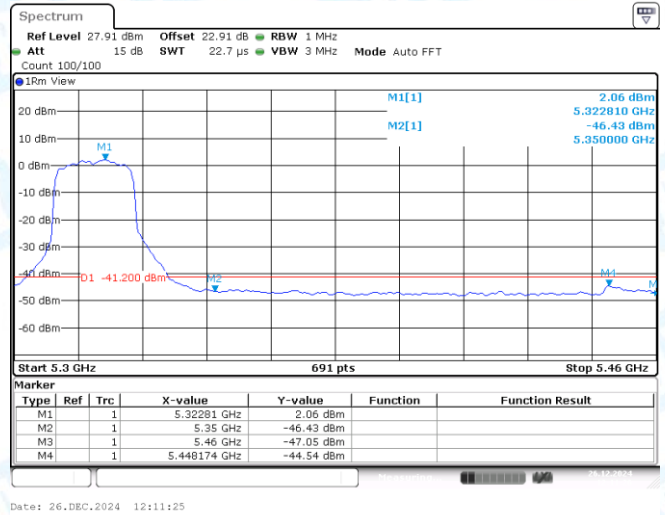
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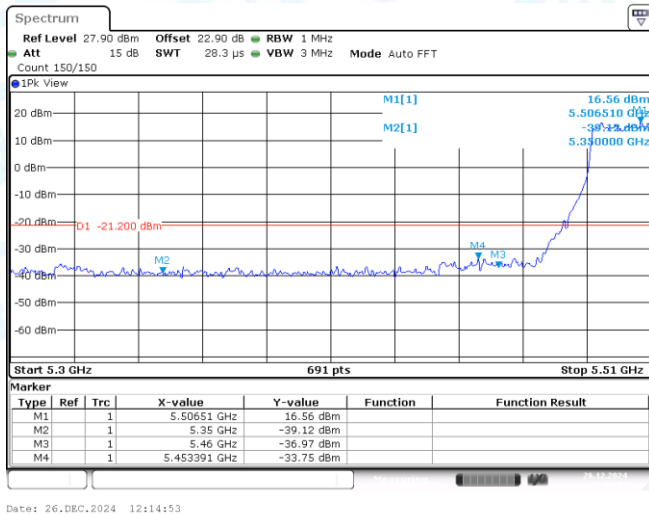
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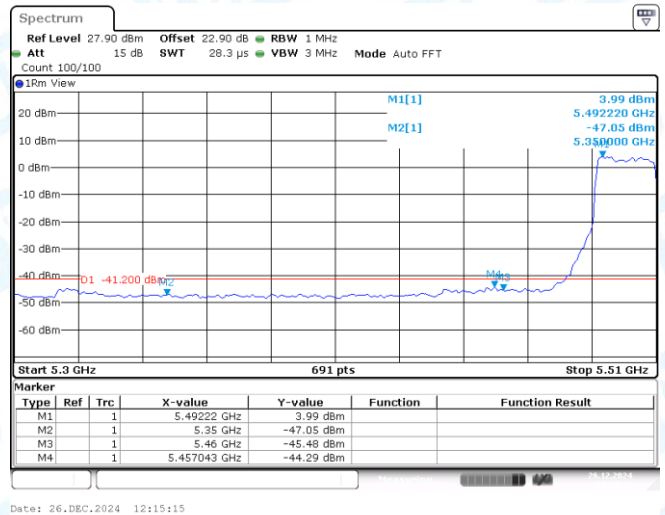
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11AX20SISO-Ant1-5320-PASS



11AX20SISO-Ant1-5320-PASS



11AX20SISO-Ant1-5320-PASS



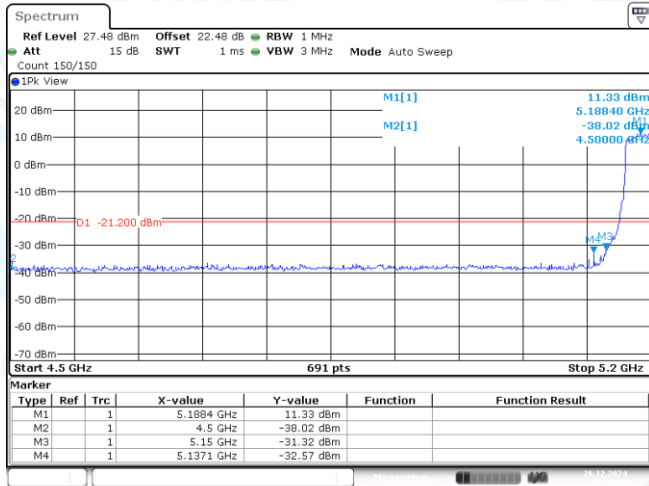
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11AX20SISO-Ant1-5500-PASS

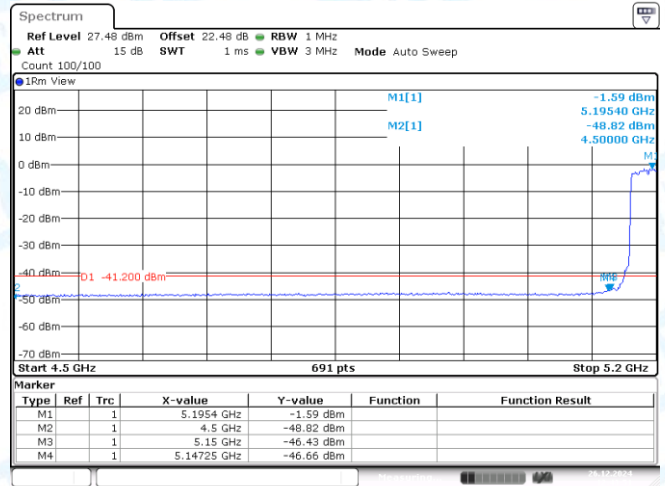
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11AX20SISO-Ant1-5825-PASS



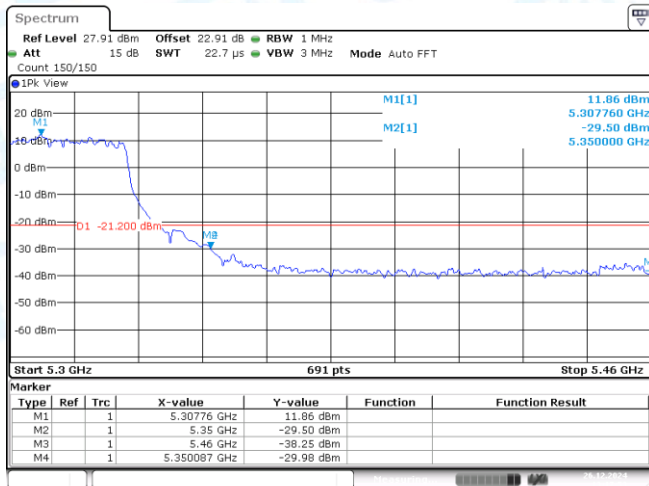
Date: 26.DEC.2024 12:43:29

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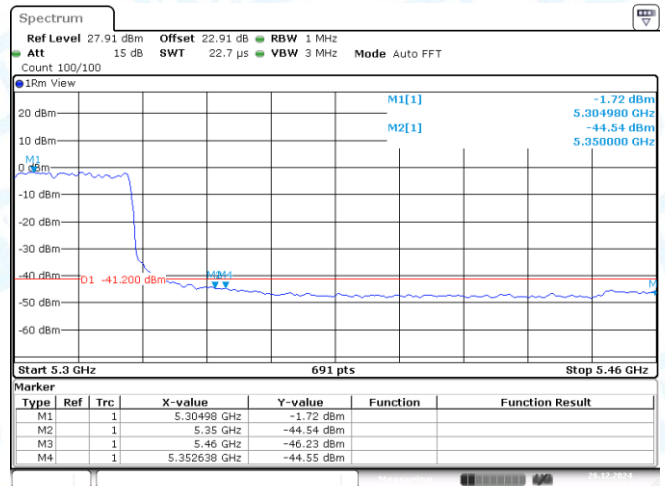
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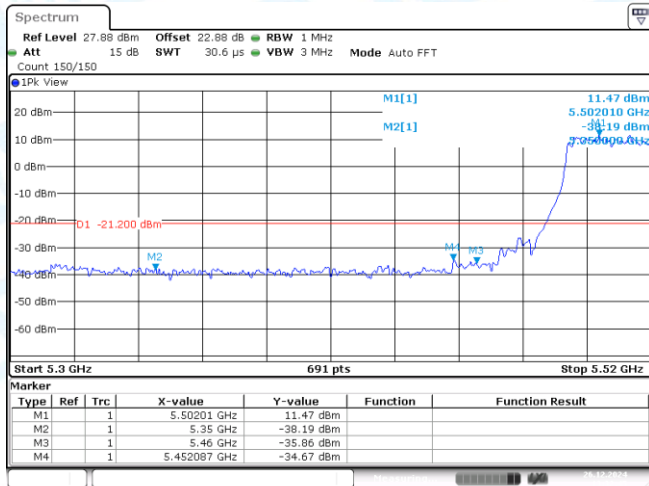
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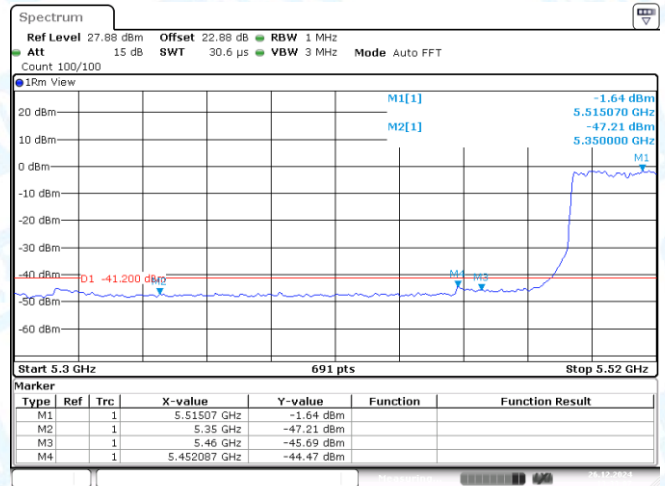
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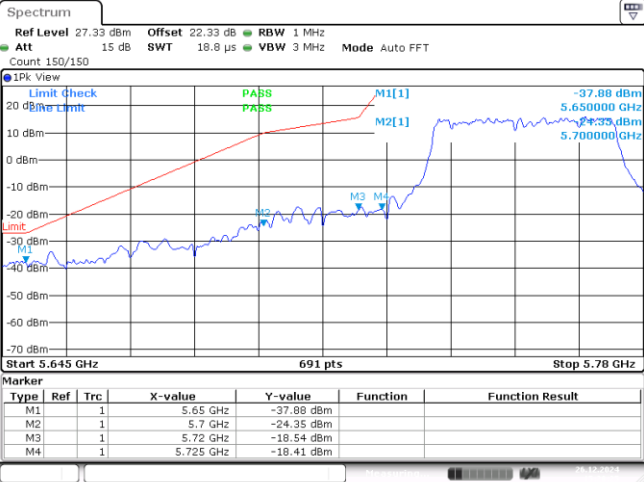
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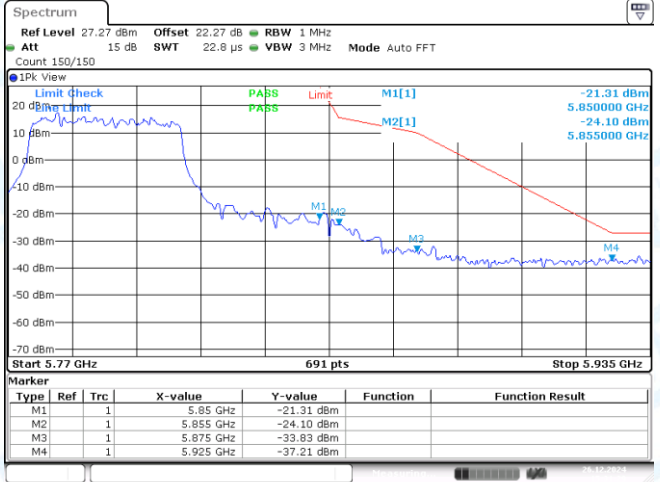


Date: 26.DEC.2024 13:33:06

11AX40SISO-Ant1-5510-PASS



11AX40SISO-Ant1-5755-PASS



11AX40SISO-Ant1-5795-PASS

-----END OF THE REPORT-----