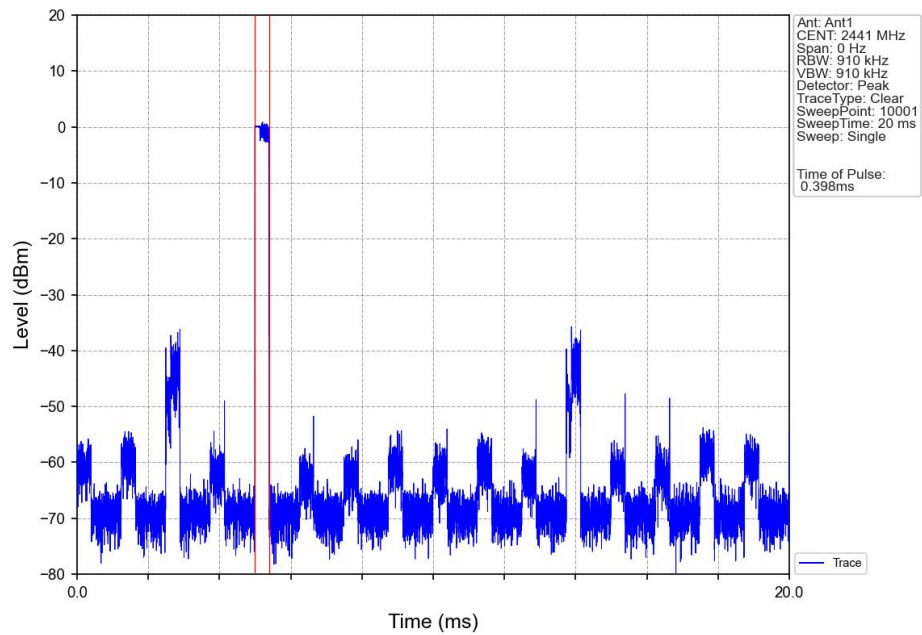
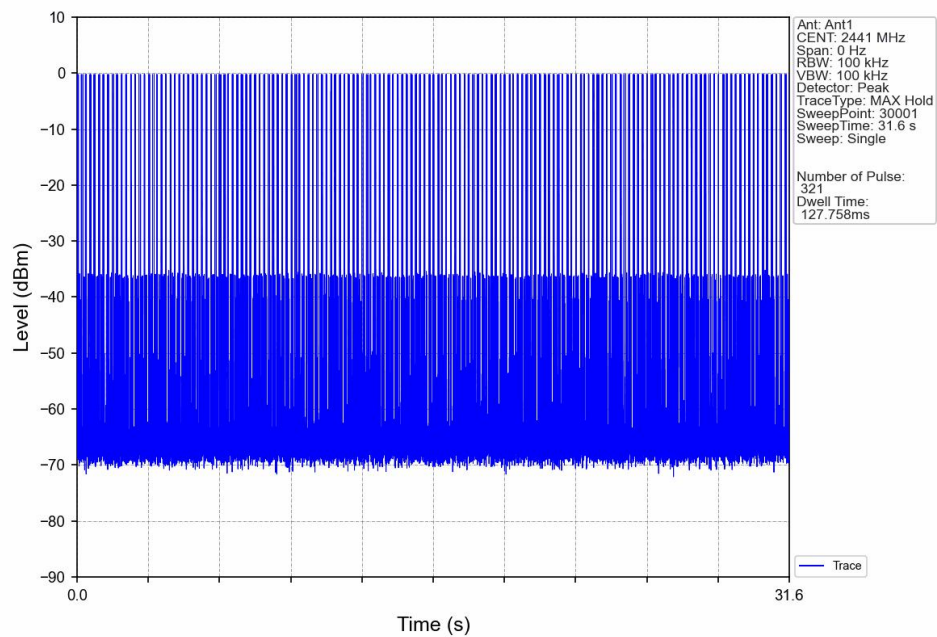


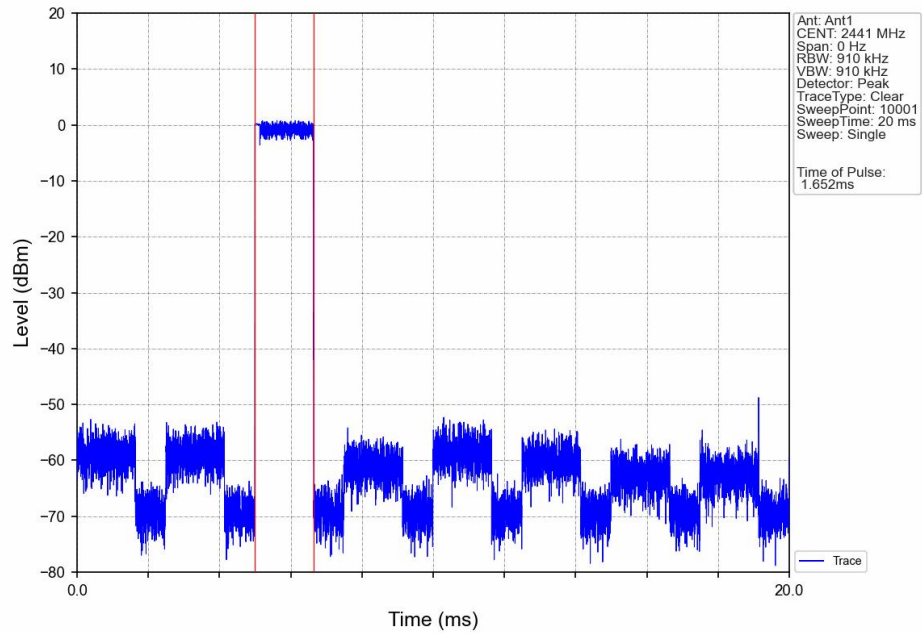
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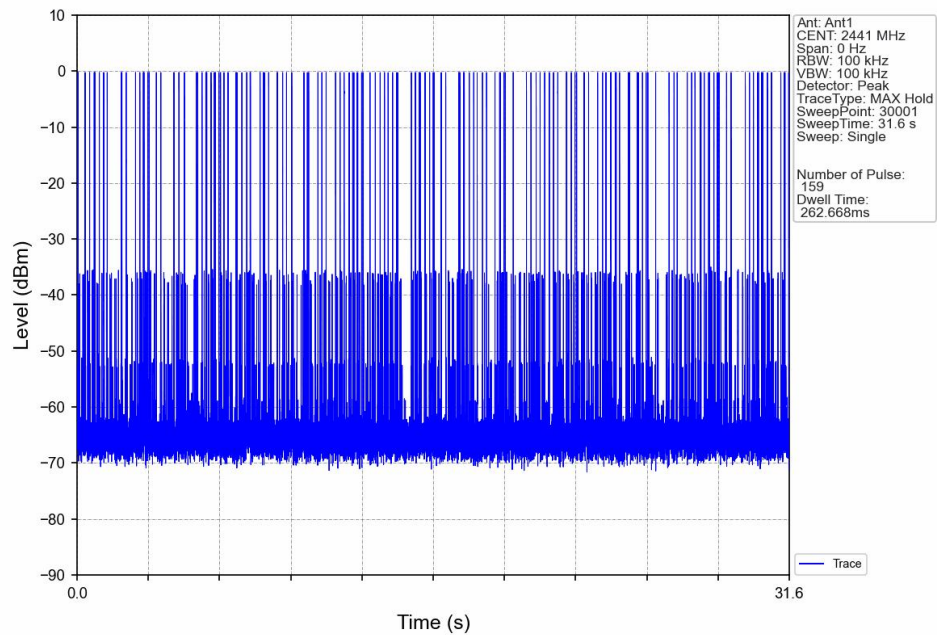
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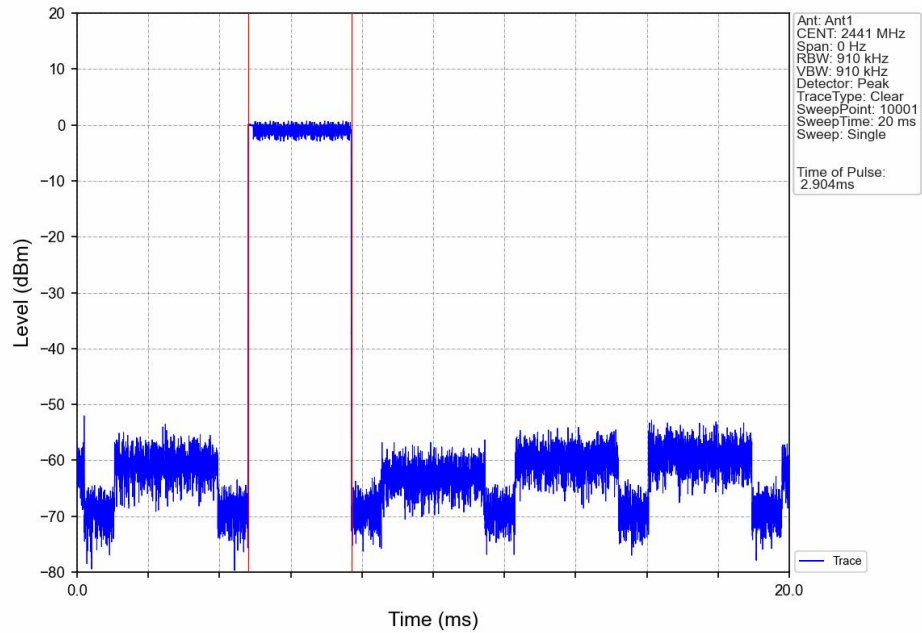
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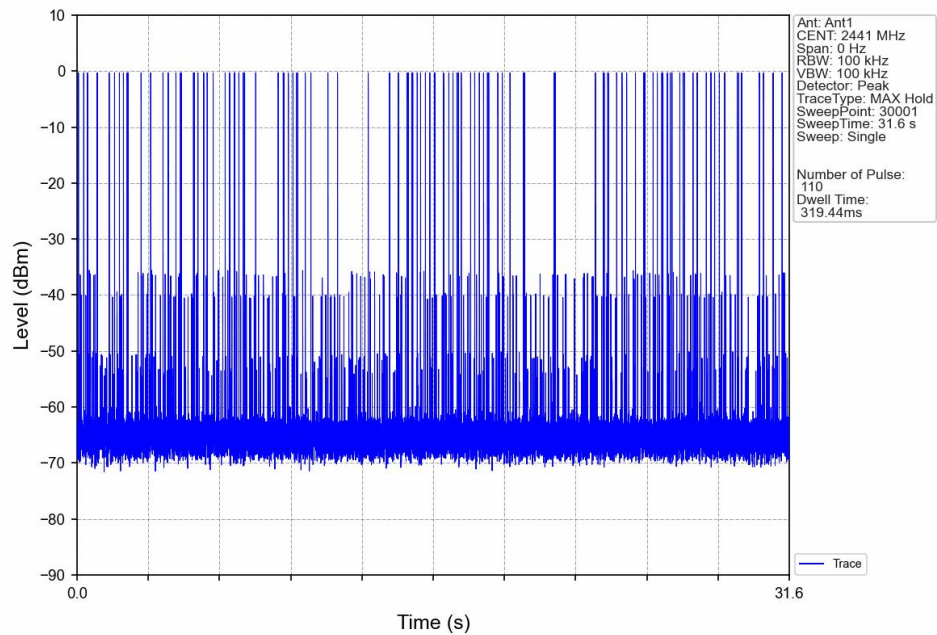
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8DPSK_3DH5_HOPP_Ant1_NTNV



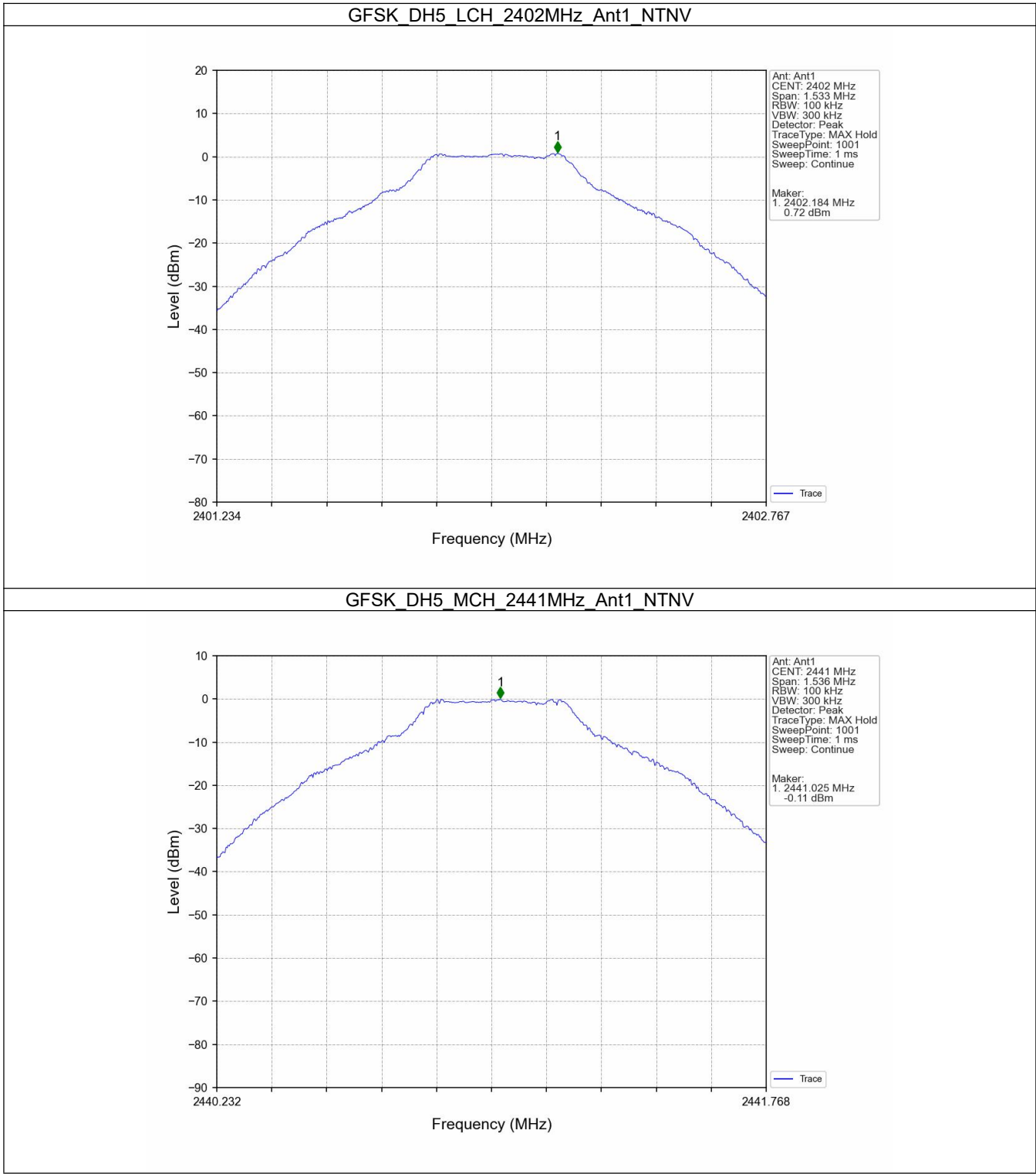
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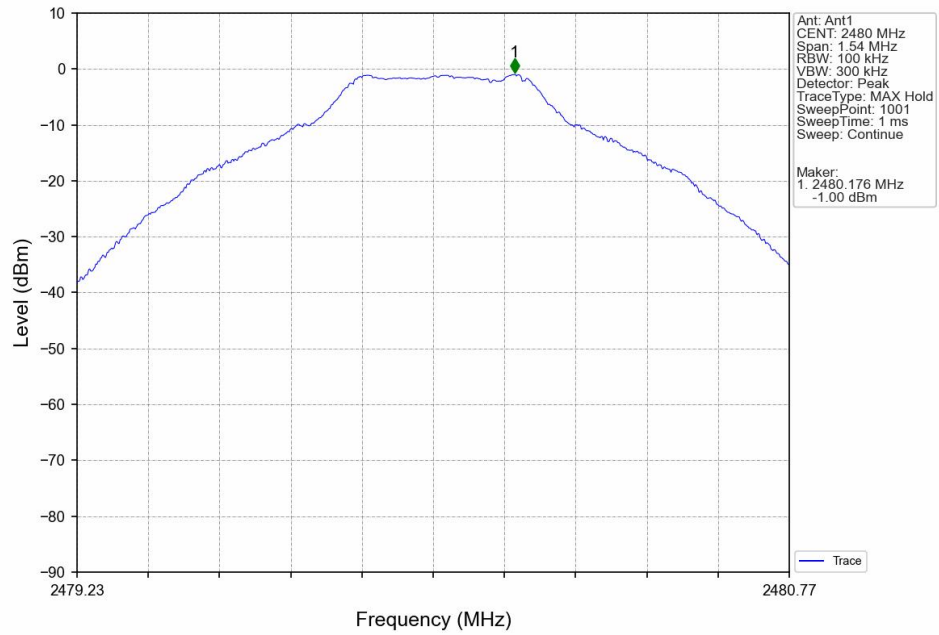
Note1: Refer to FCC Part 15.247 (d) and ANSI C63.10-2013, the channel contains the maximum PSD level was used to establish the reference level.

7.2 Test Graph

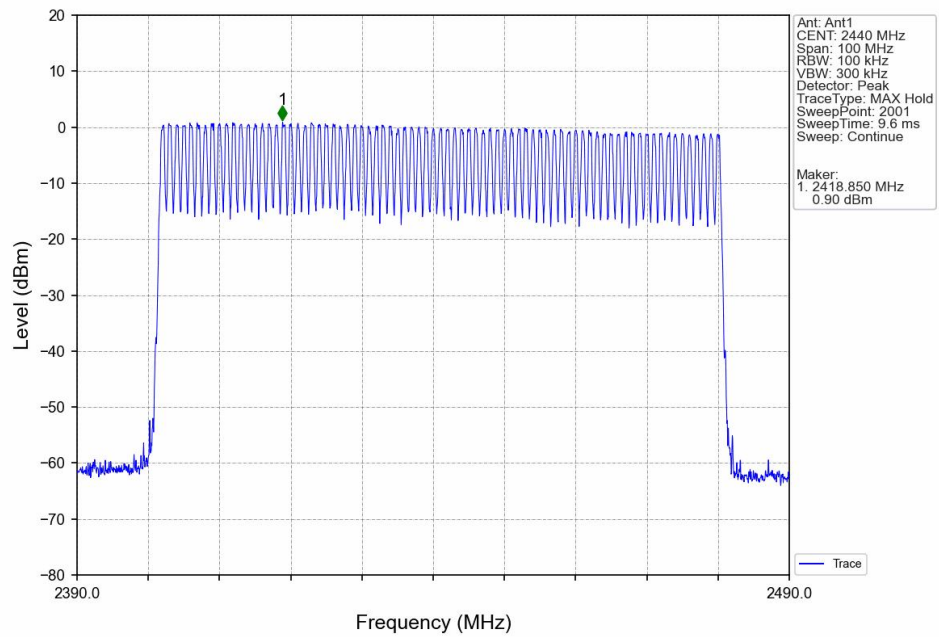
7.2.1 Ref



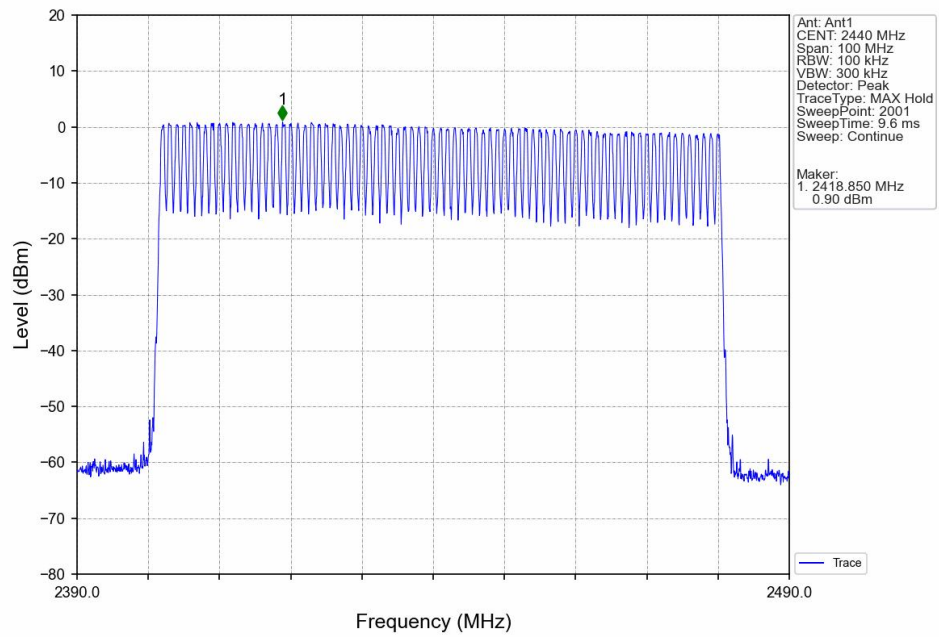
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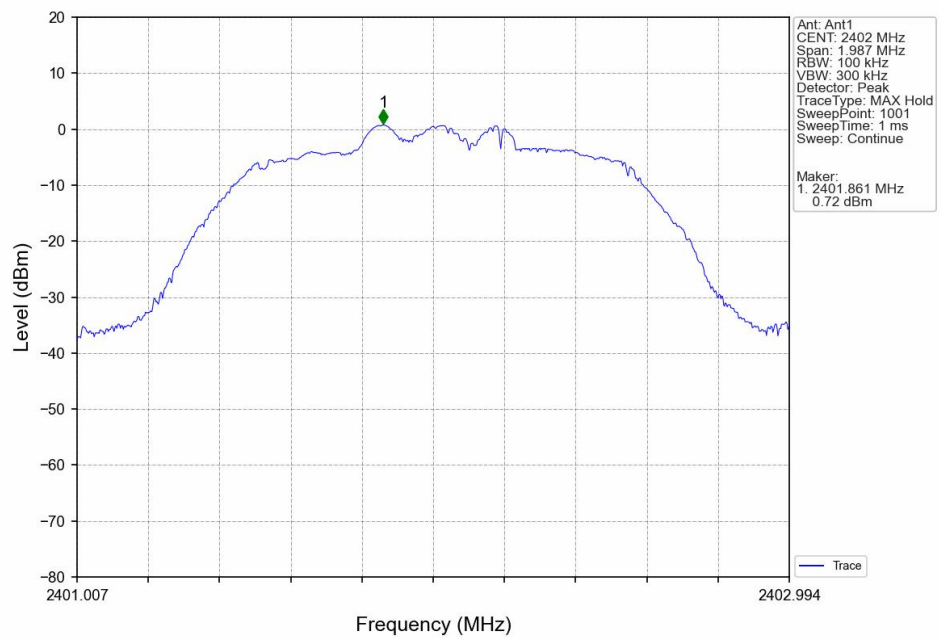
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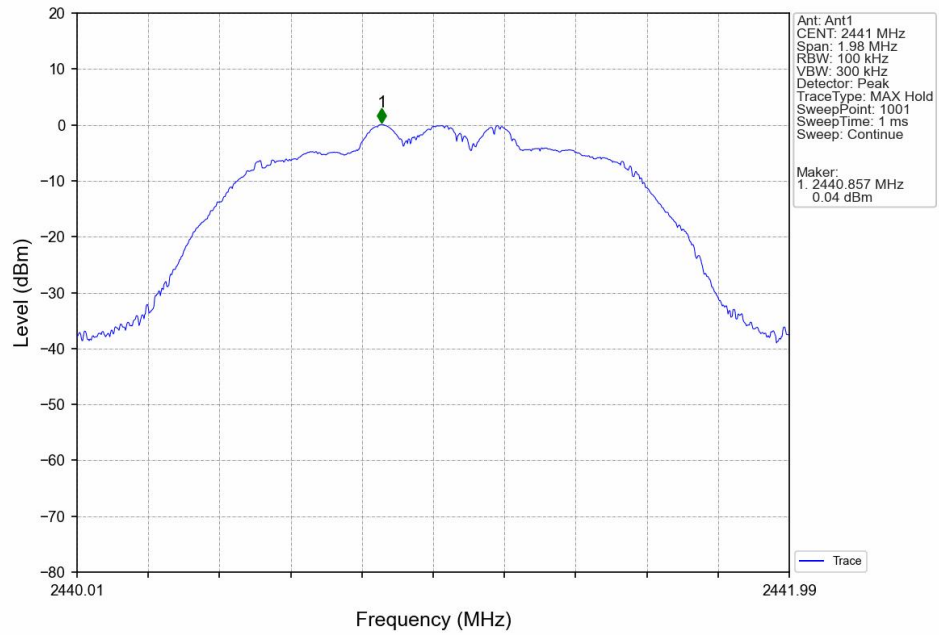
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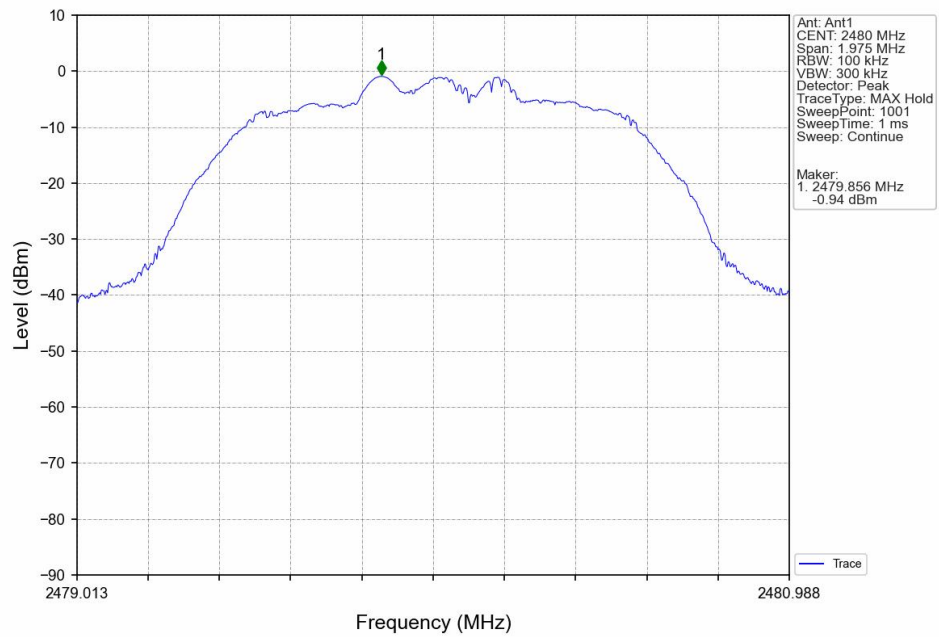
Pi/4DQPSK_2DH5_LCH_2402MHz_Ant1_NTNV



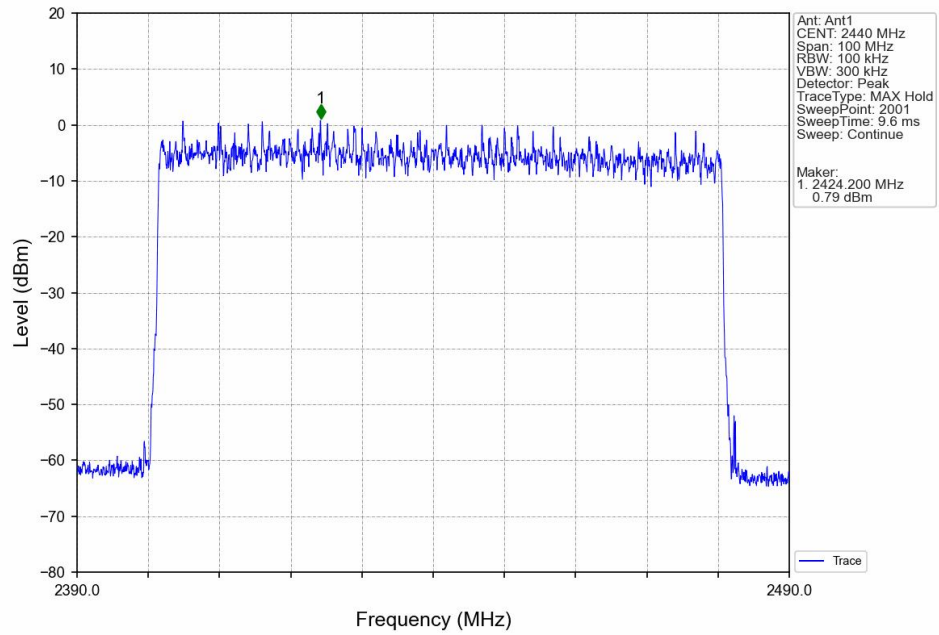
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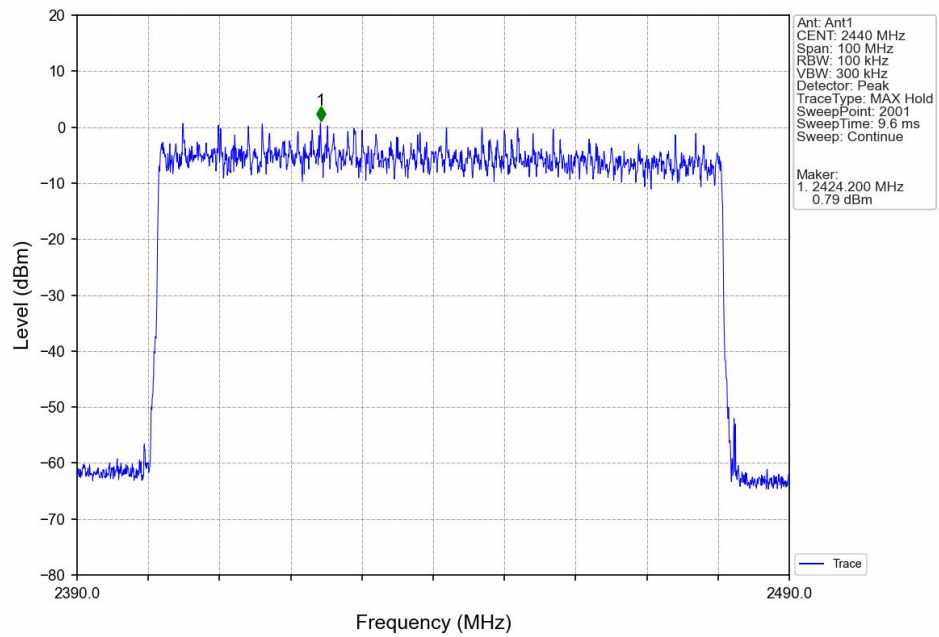
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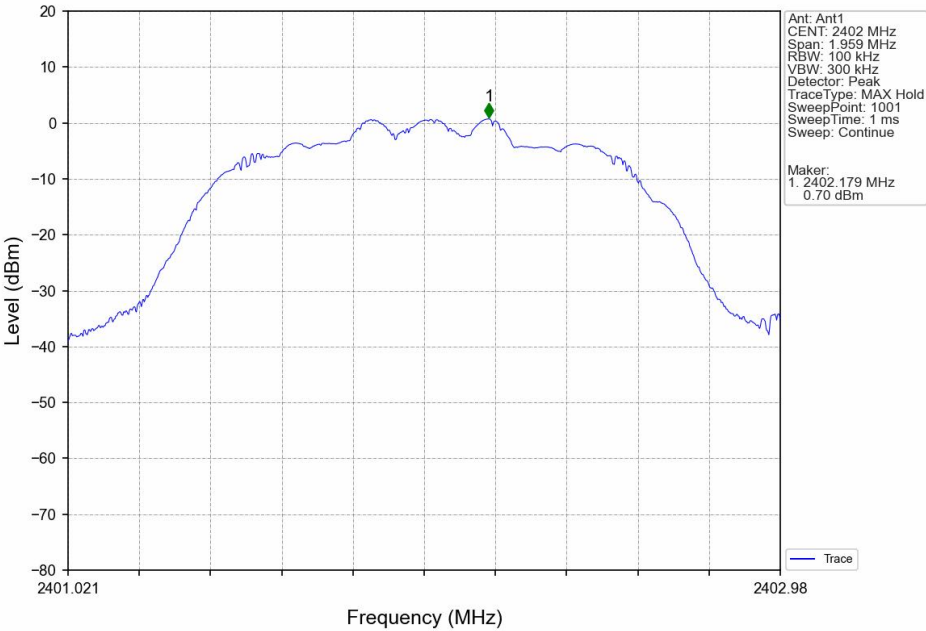
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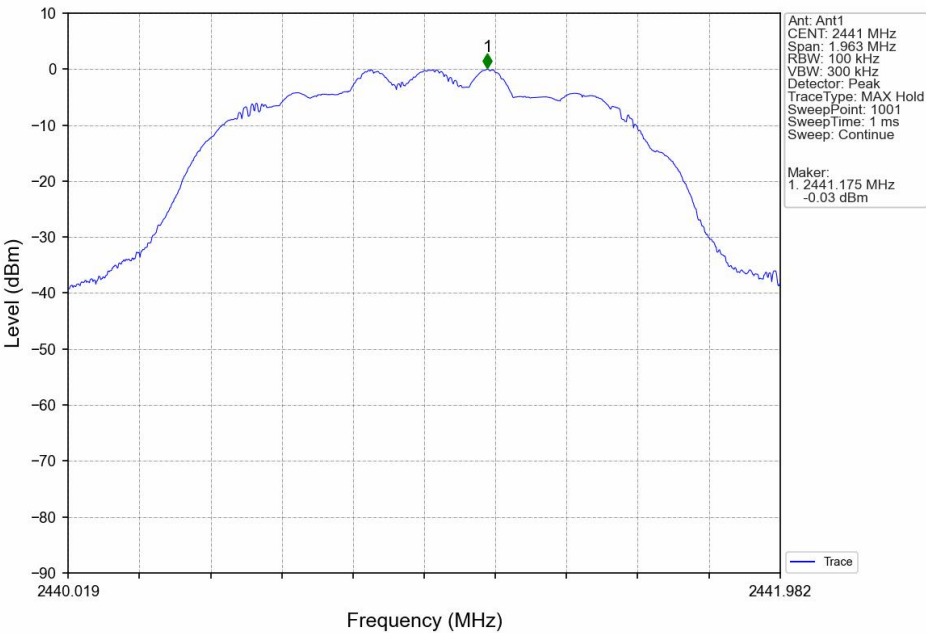
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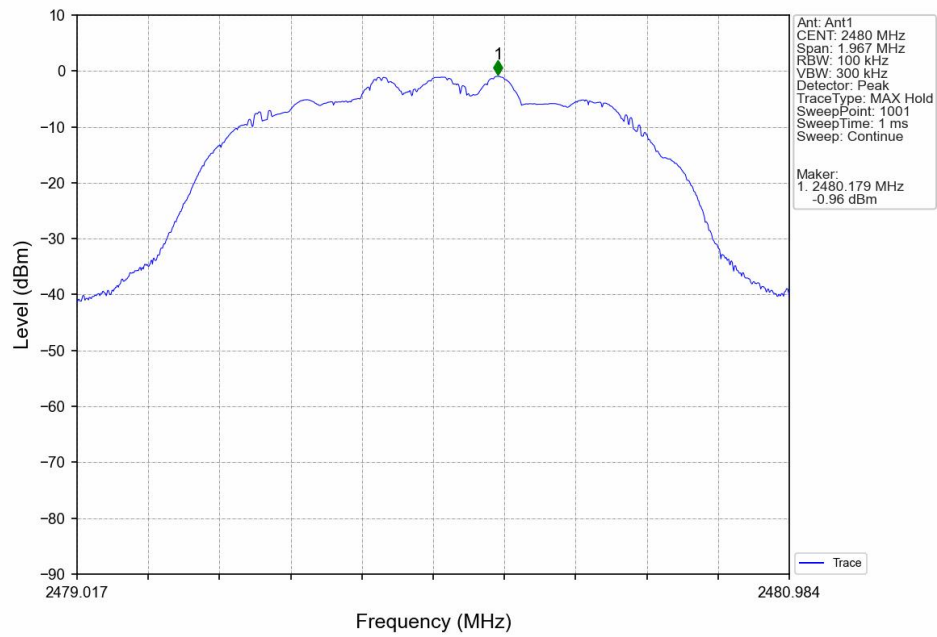
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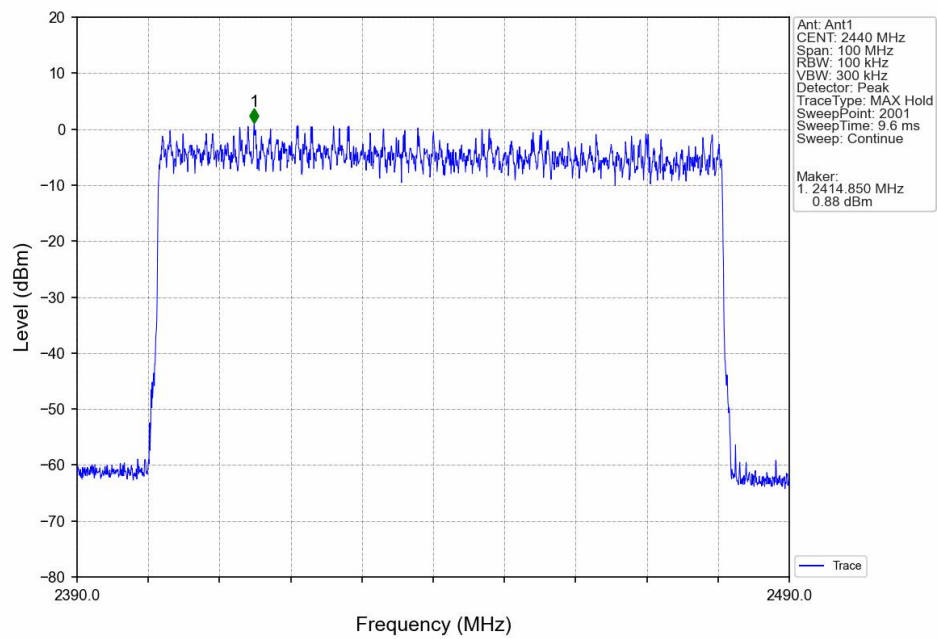
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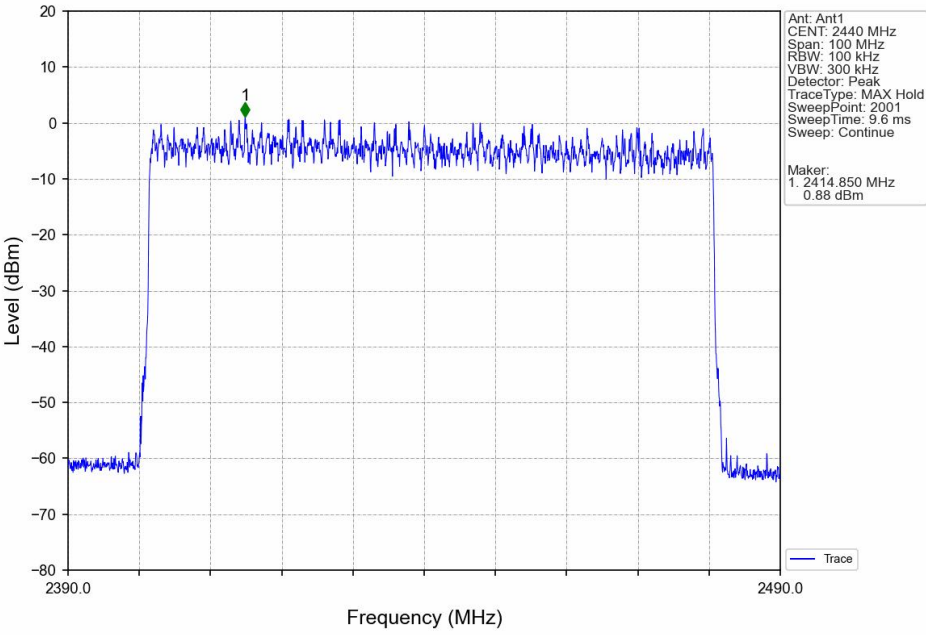
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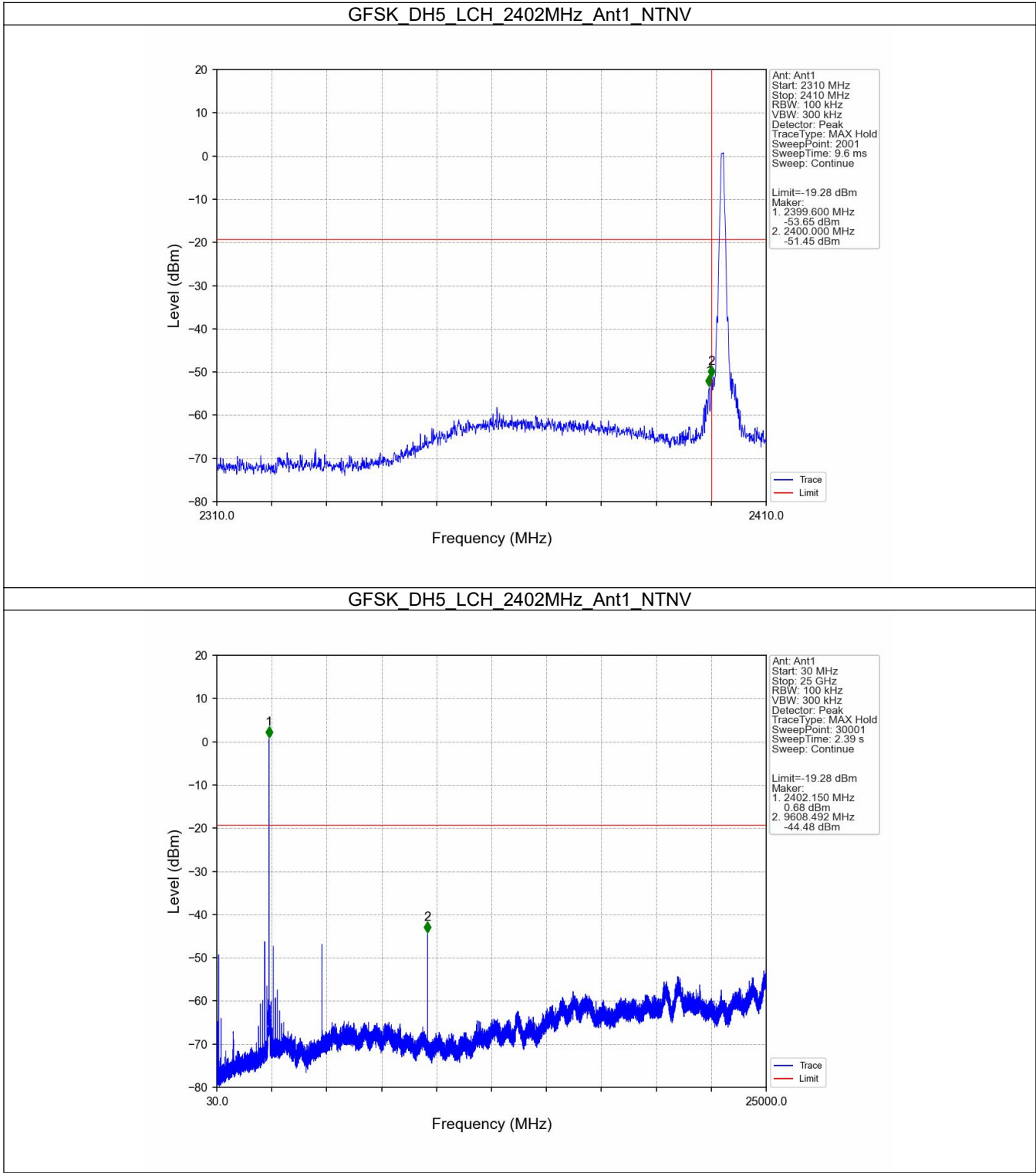
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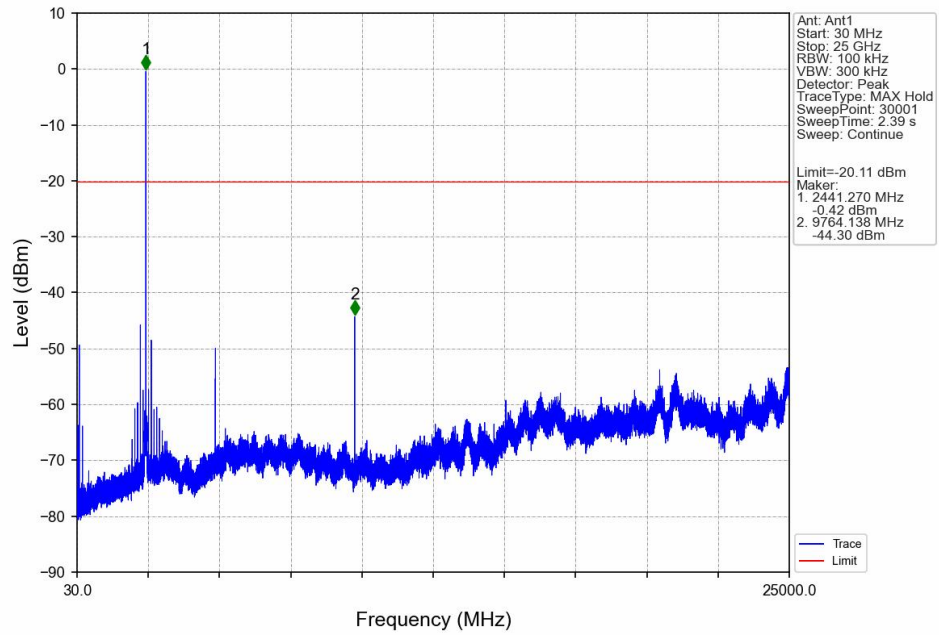
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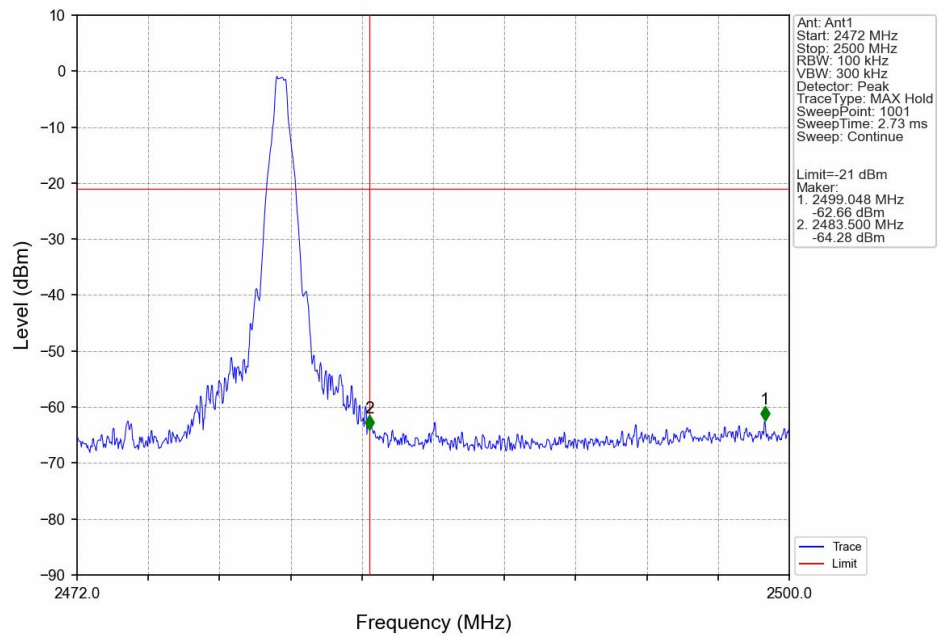
7.2.2 CSE



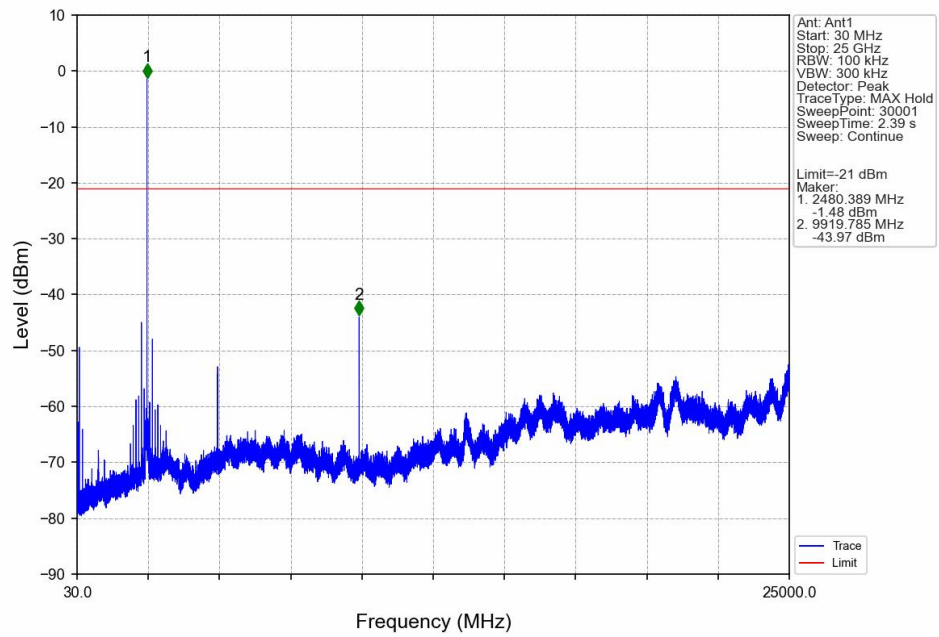
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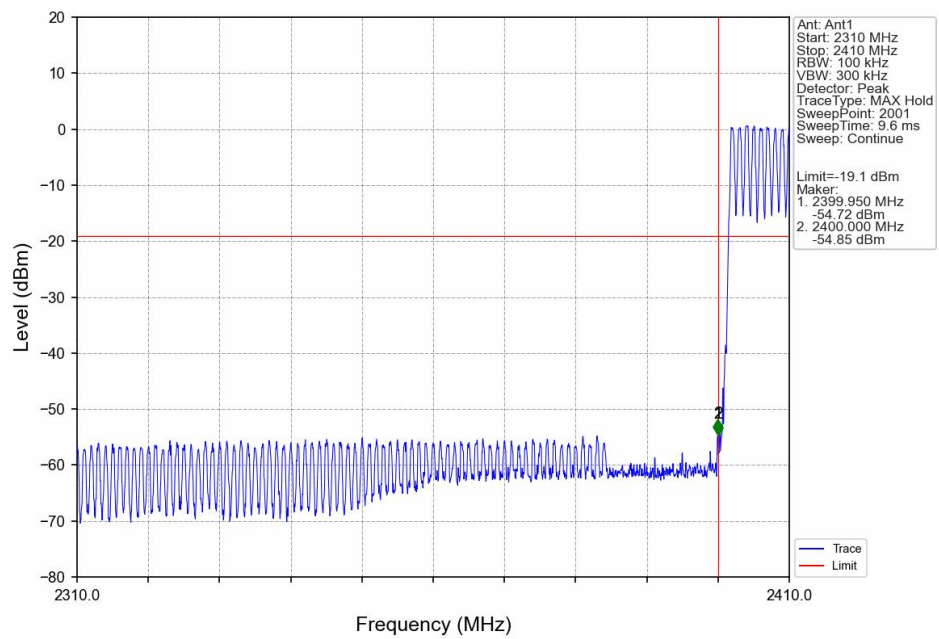
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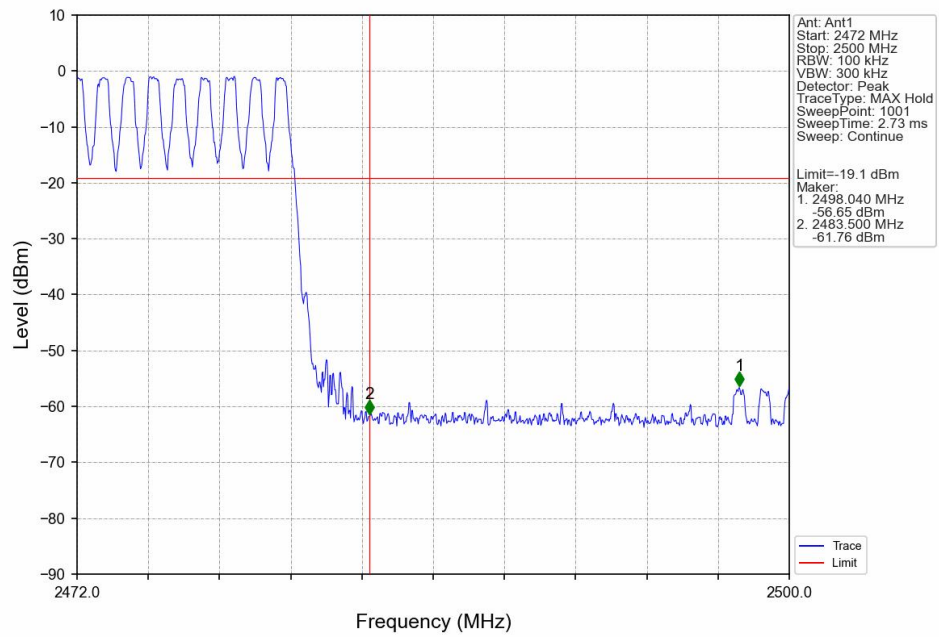
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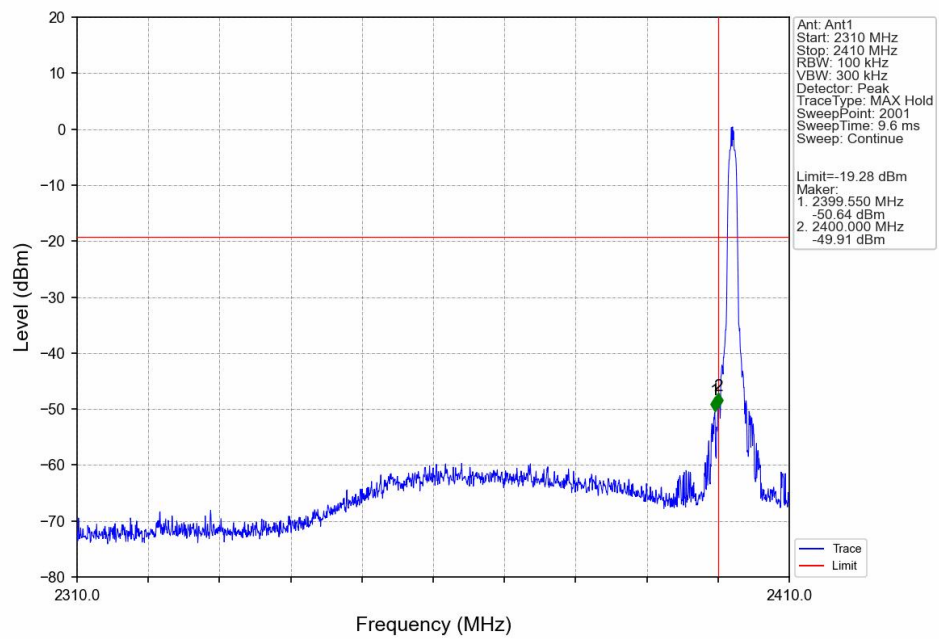
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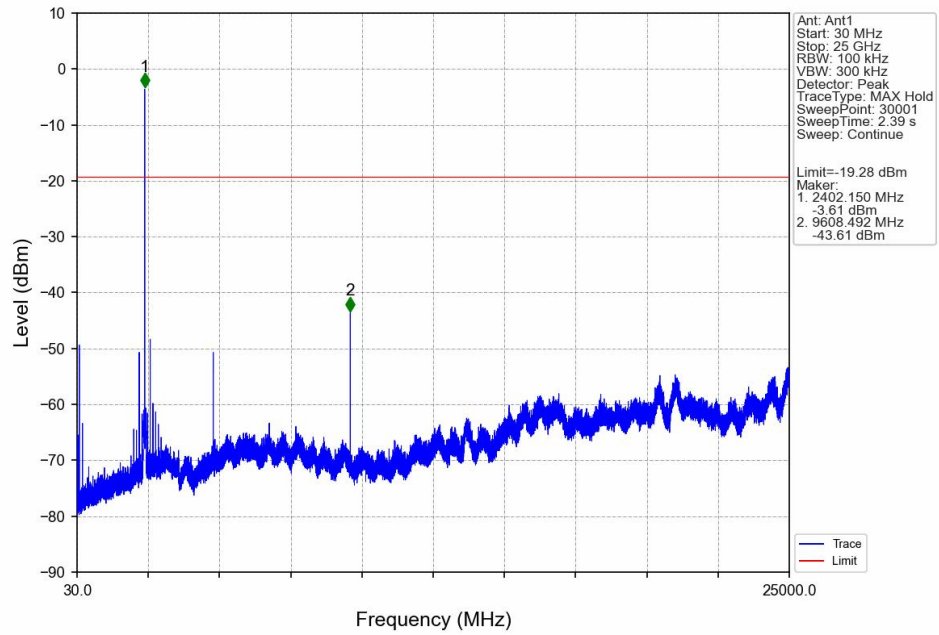
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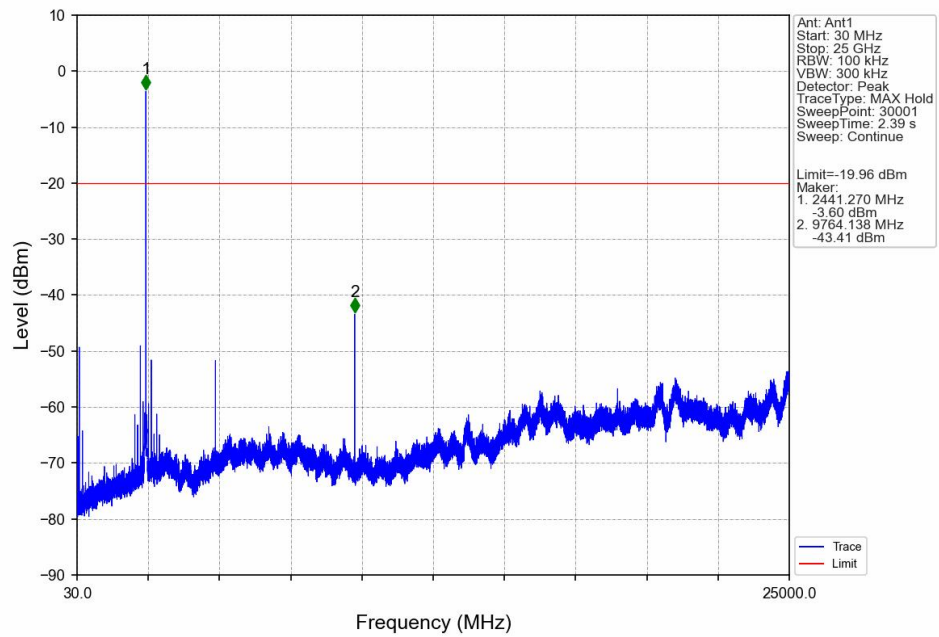
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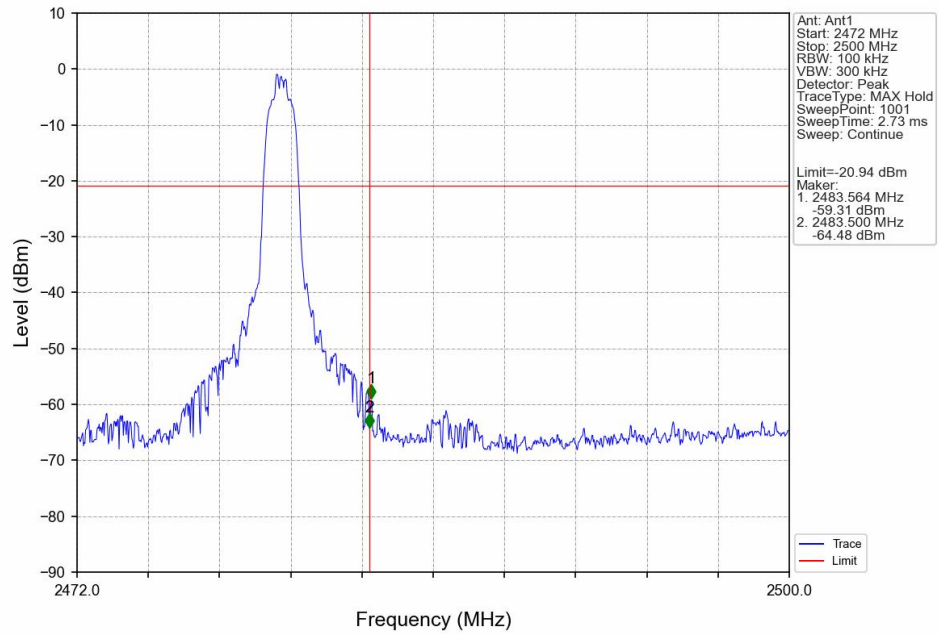
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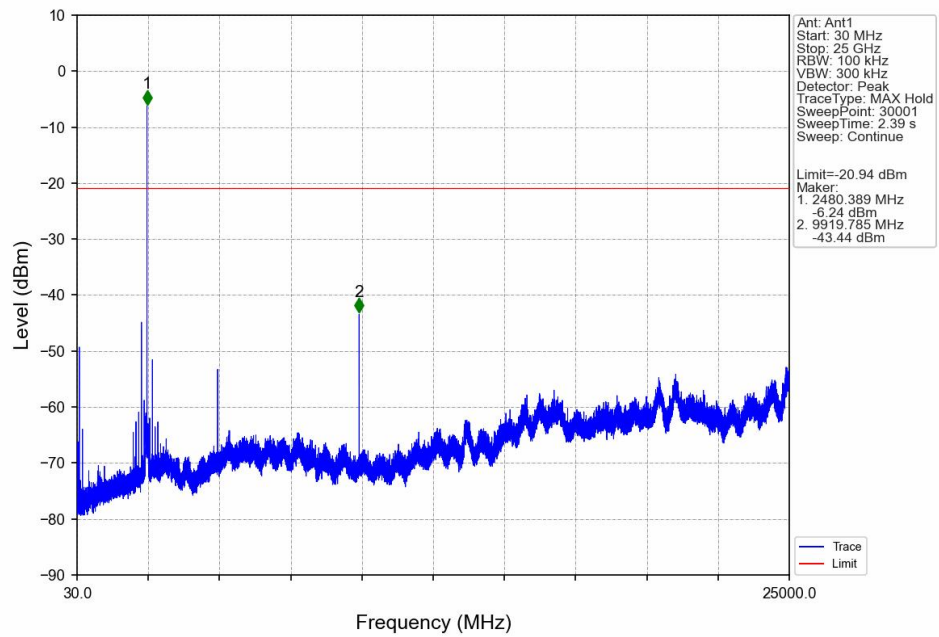
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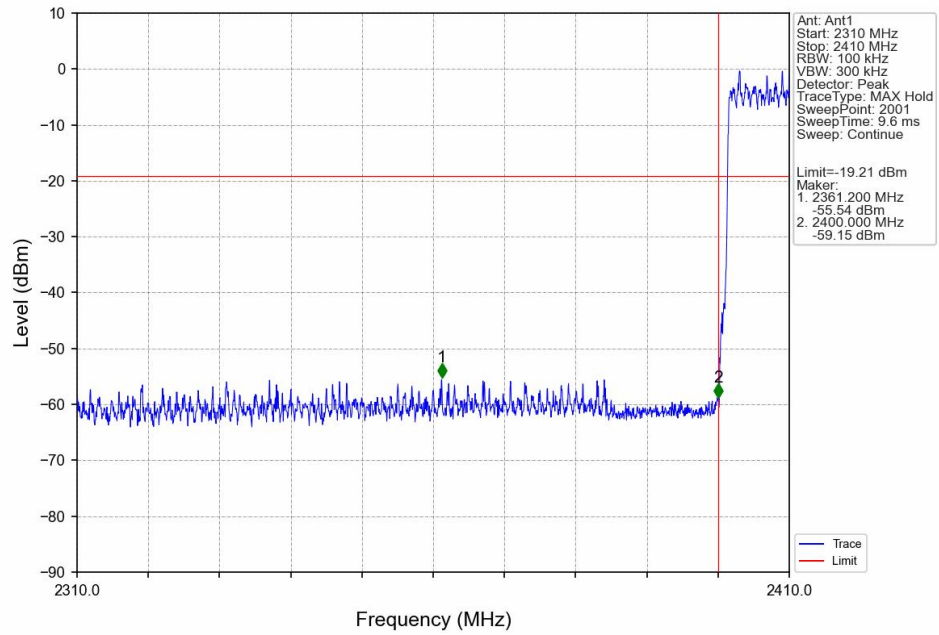
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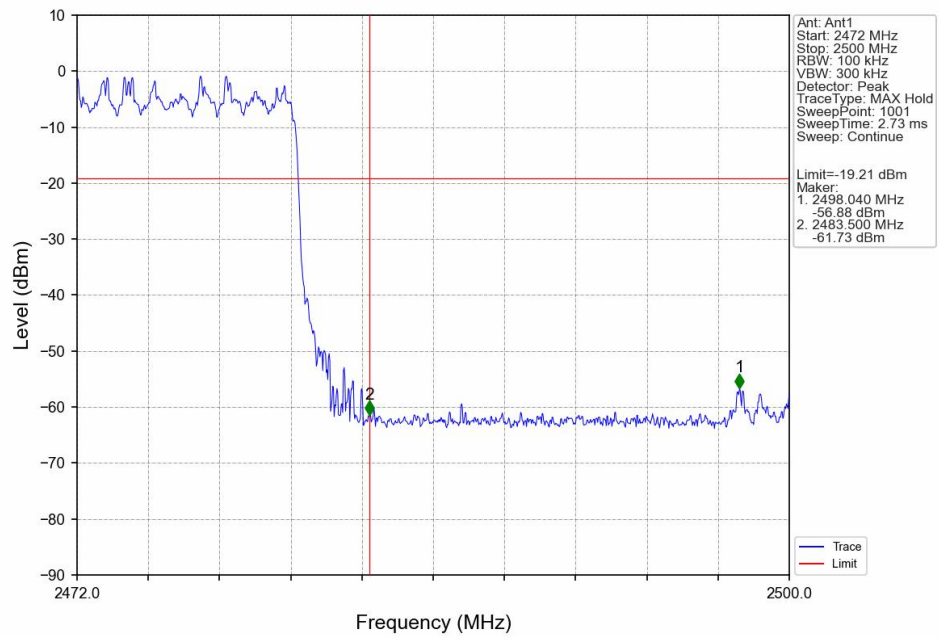
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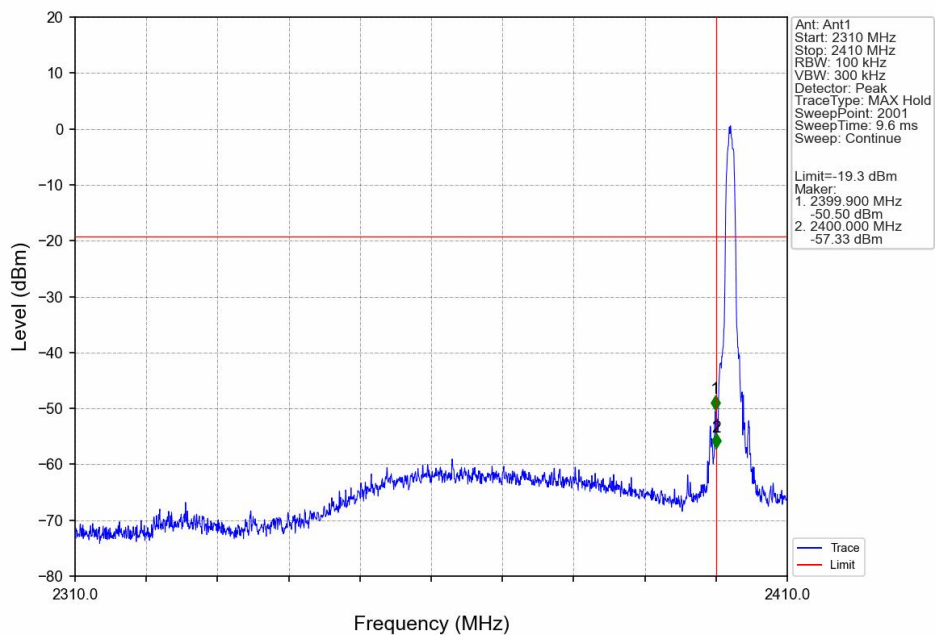
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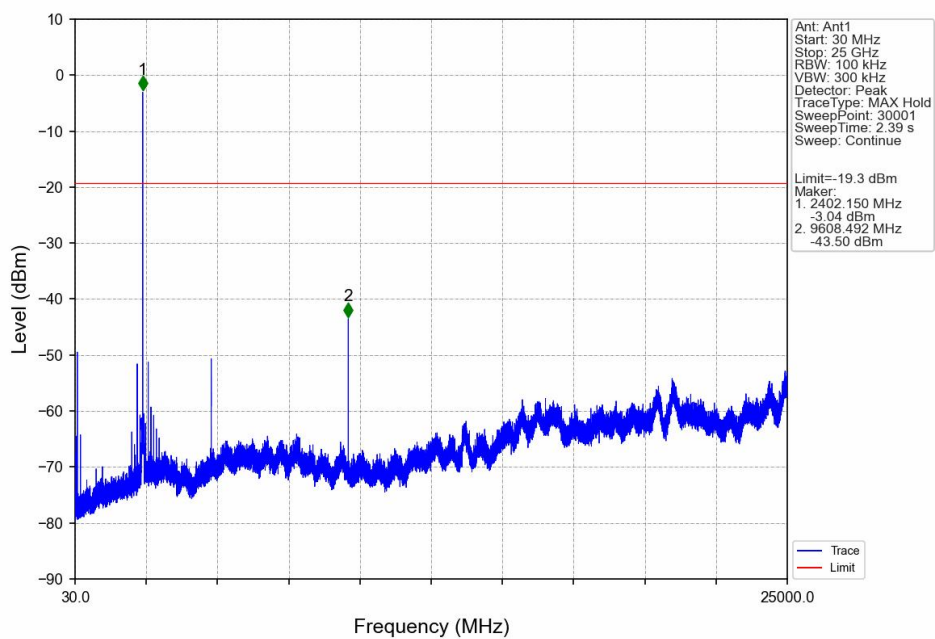
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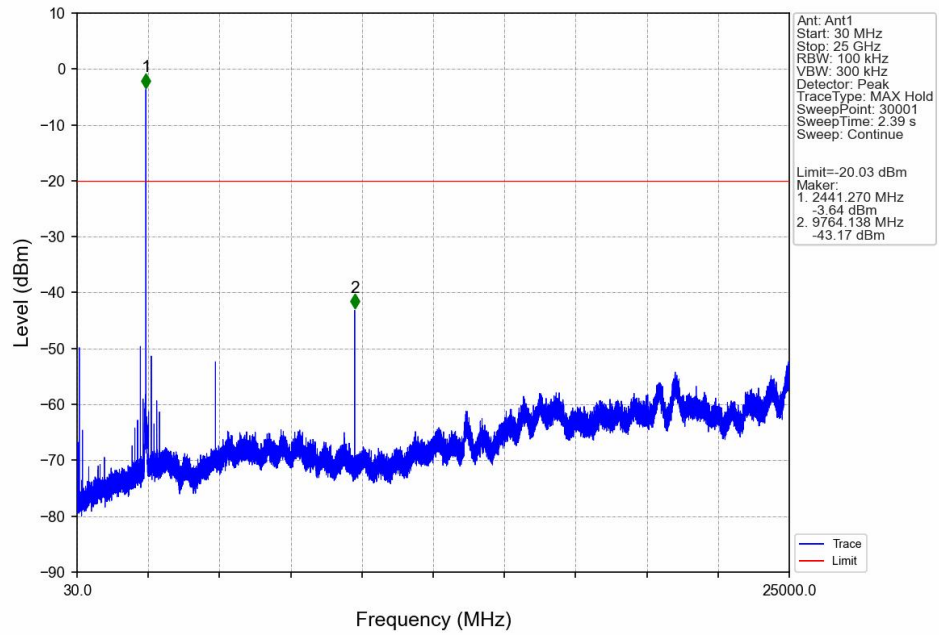
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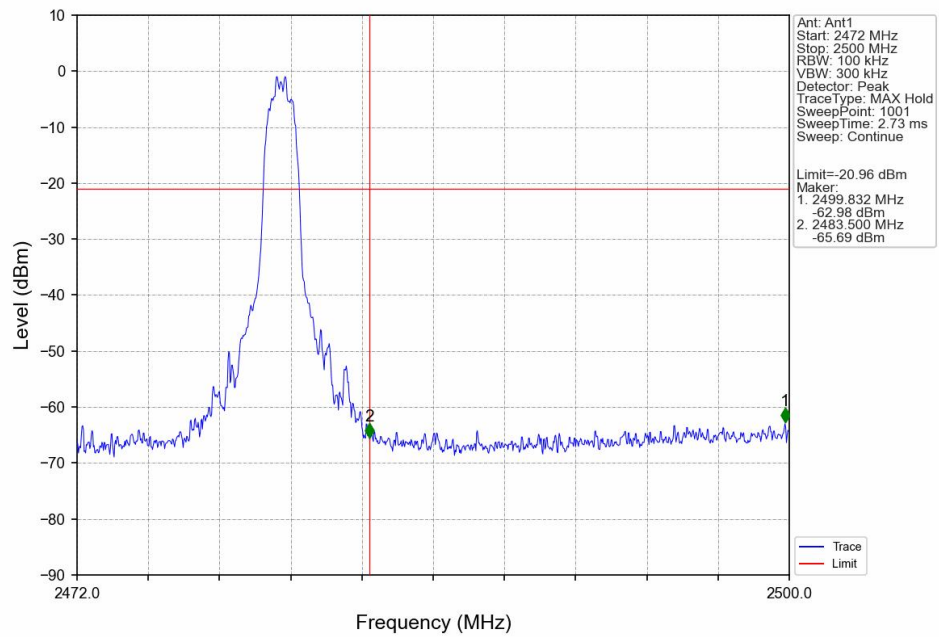
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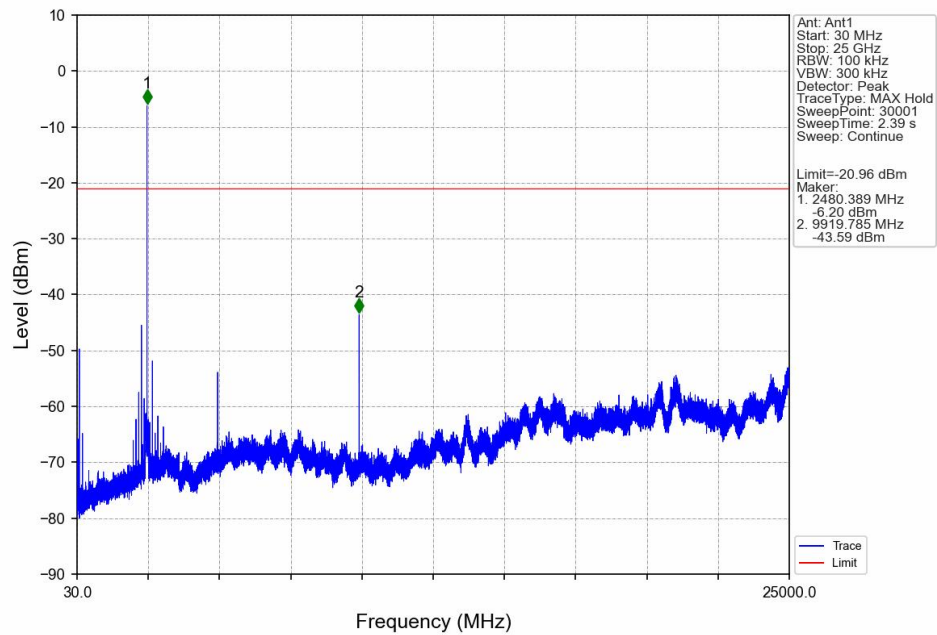
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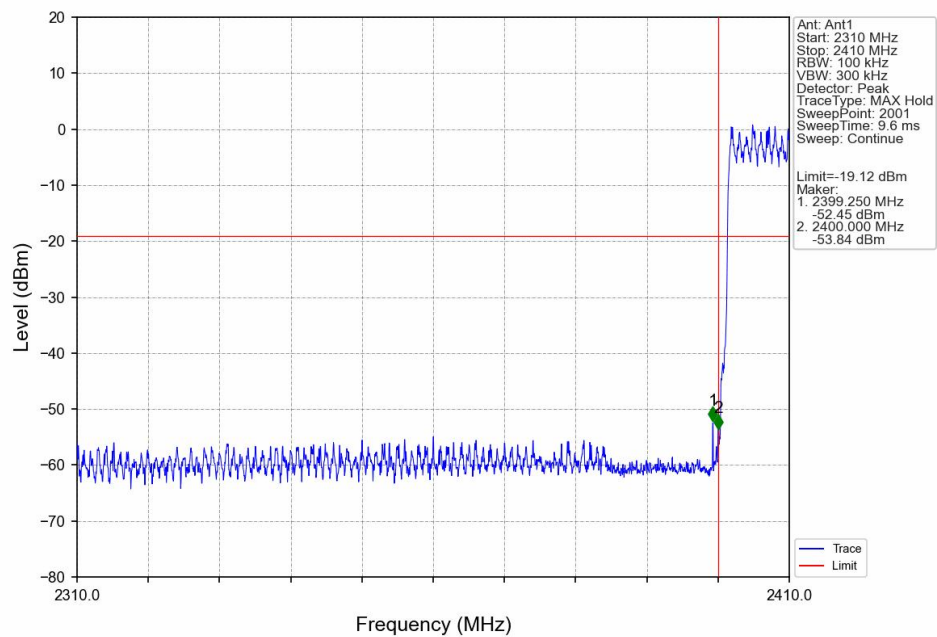
8DPSK_3DH5_HCH_2480MHz_Ant1_NTNV



8DPSK_3DH5_HCH_2480MHz_Ant1_NTNV



8DPSK_3DH5_HOPP_Ant1_NTNV



8DPSK_3DH5_HOPP_Ant1_NTNV

