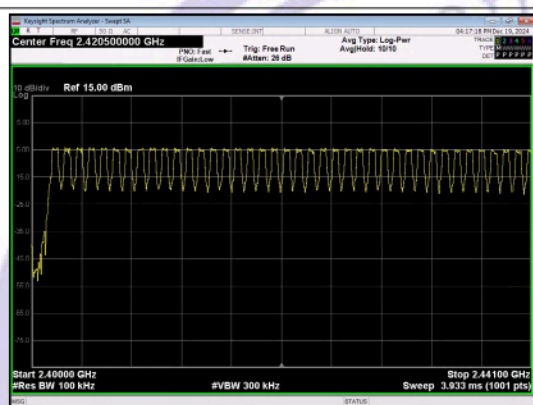
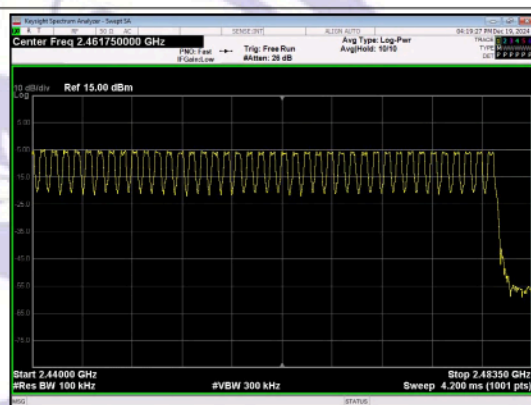


11.4 Test Data

Mode	Frequency (MHz)	Packet Type	Num of Hopping Frequencies		Verdict
			ANT1	Limit	
GFSK	HOPP	DH1	79	≥ 15	Pass
$\pi/4$ DQPSK	HOPP	2DH1	79	≥ 15	Pass
8DPSK	HOPP	3DH1	79	≥ 15	Pass



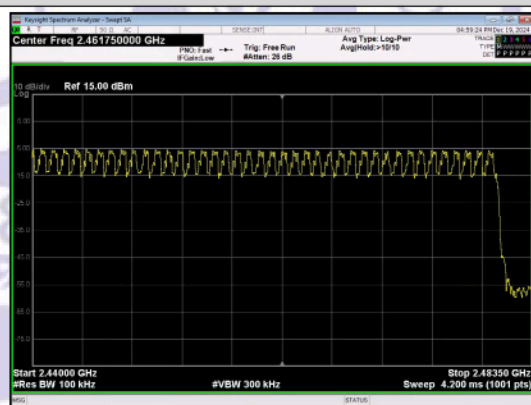
Low End Spectrum Channel Hopping Plot
GFSK



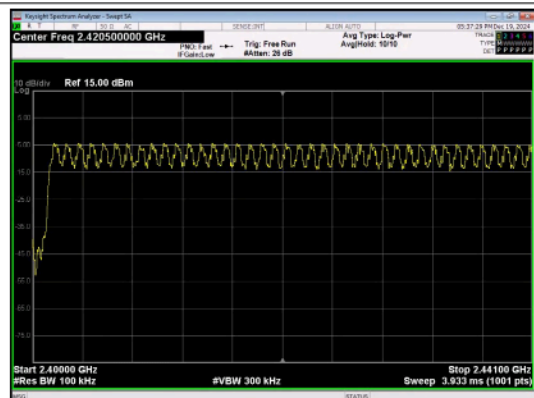
High End Spectrum Channel Hopping Plot
GFSK



Low End Spectrum Channel Hopping Plot
 $\pi/4$ DQPSK



High End Spectrum Channel Hopping Plot
 $\pi/4$ DQPSK



Low End Spectrum Channel Hopping Plot
8DPSK



High End Spectrum Channel Hopping Plot
8DPSK

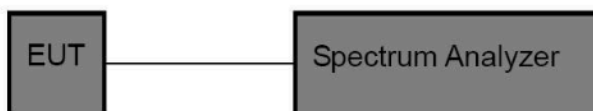


12 Dwell Time Test

12.1 Test Standard and Limit

Test Standard	FCC Part15 C Section 15.247 (a)(1) & RSS-247.5.1(4)
Test Limit	0.4 sec

12.2 Test Setup



12.3 Test Procedure

The EUT must have its hopping function enabled. Use the following spectrum analyzer settings:

1. Span= zero span, centered on a hopping channel
2. Set the RBW = 1 MHz.
3. Set the VBW = 3 MHz.
4. Sweep time = as necessary to capture the entire dwell time per hopping channel.
5. Detector function = clear write.
6. Trace mode = normal.
7. Allow trace to fully stabilize.

12.4 Test Data

Modulation	Packet	Channel	Pulse Width (ms)	Number of Pulses in 31.6 seconds	Dwell Time (ms)	Limit (ms)	Result
GFSK	DH5	CH0 (2402MHz)	2.896	96	278.02	< 400	PASS
	DH5	CH39 (2441MHz)	2.880	111	319.68		PASS
	DH5	CH78 (2480MHz)	2.896	100	289.6		PASS
$\pi/4$ DQPSK	2-DH5	CH0 (2402MHz)	2.896	114	330.14		PASS
	2-DH5	CH39 (2441MHz)	2.888	111	320.57		PASS
	2-DH5	CH78 (2480MHz)	2.896	113	327.25		PASS
8DPSK	3-DH5	CH0 (2402MHz)	2.880	93	267.84		PASS
	3-DH5	CH39 (2441MHz)	2.880	98	282.24		PASS
	3-DH5	CH78 (2480MHz)	2.896	119	344.62		PASS

Test Plots

