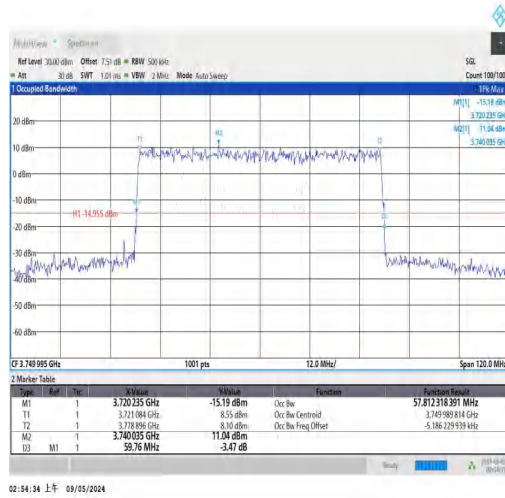
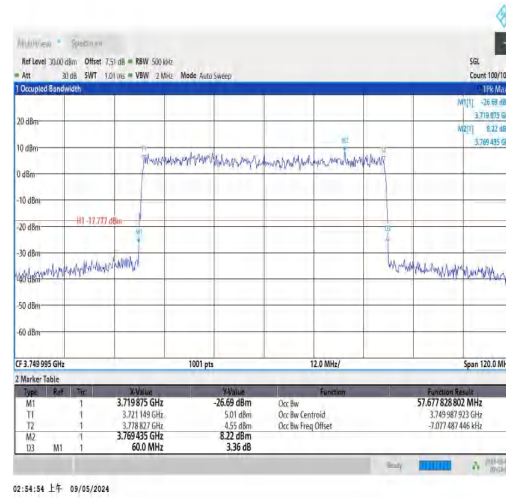




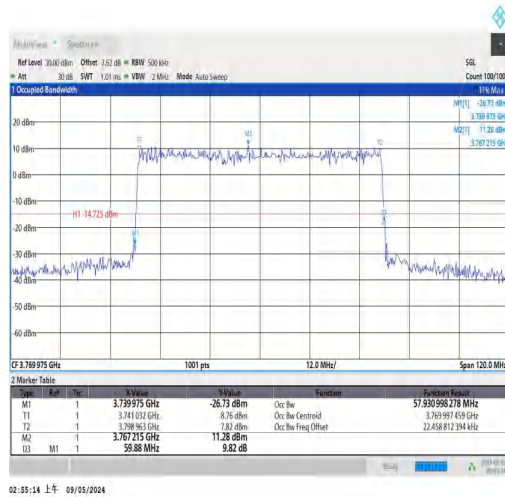
NTNV_N78-3700-3800_PC3_30_60_M_TID5_Ant

1#1



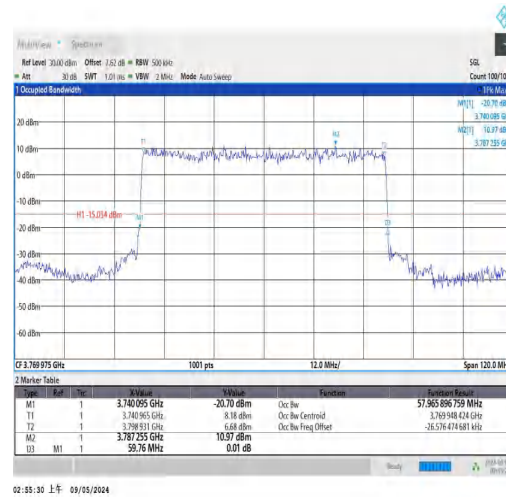
NTNV_N78-3700-3800_PC3_30_60_M_TID6_Ant

1#1



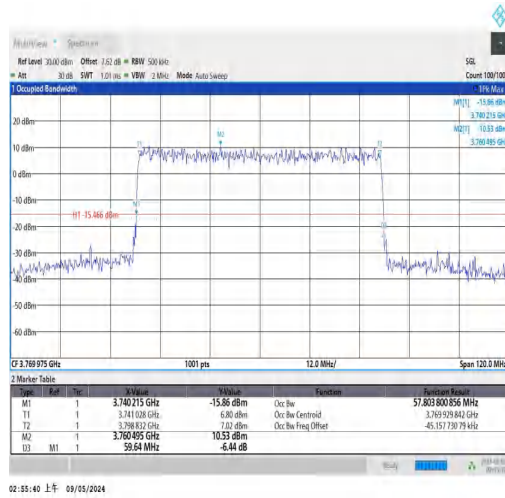
NTNV_N78-3700-3800_PC3_30_60_H_TID1_Ant

1#1

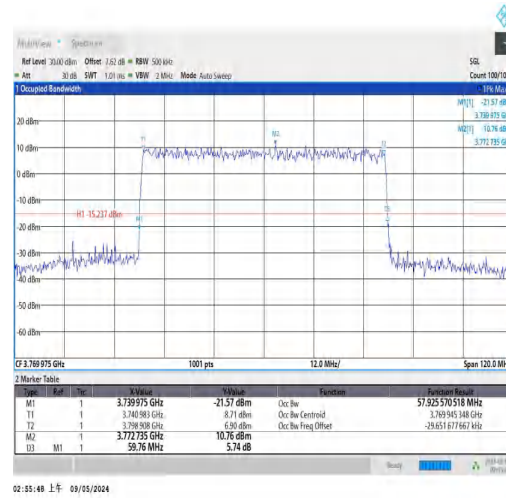


NTNV_N78-3700-3800_PC3_30_60_H_TID2_Ant

1#1



02:55:40 上午 09/05/2024



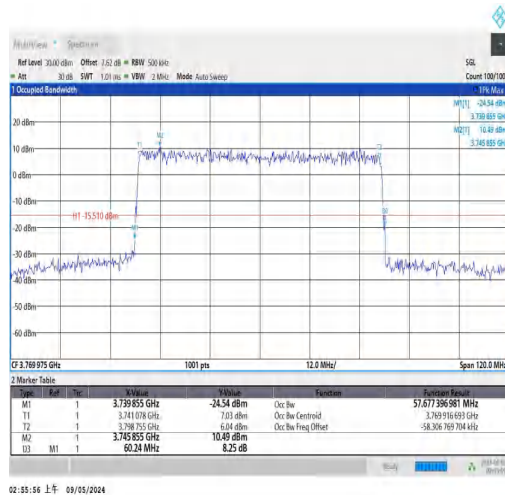
02:55:48 上午 09/05/2024

NTNV_N78-3700-3800_PC3_30_60_H_TID3_Ant

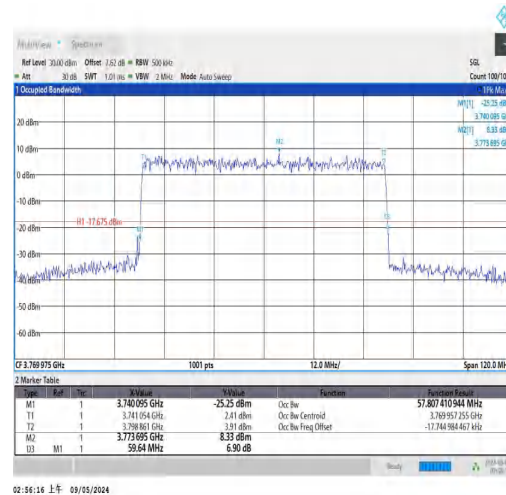
1#1

NTNV_N78-3700-3800_PC3_30_60_H_TID4_Ant

1#1



02:55:56 上午 09/05/2024



02:56:14 上午 09/05/2024

NTNV_N78-3700-3800_PC3_30_60_H_TID5_Ant

1#1

NTNV_N78-3700-3800_PC3_30_60_H_TID6_Ant

1#1



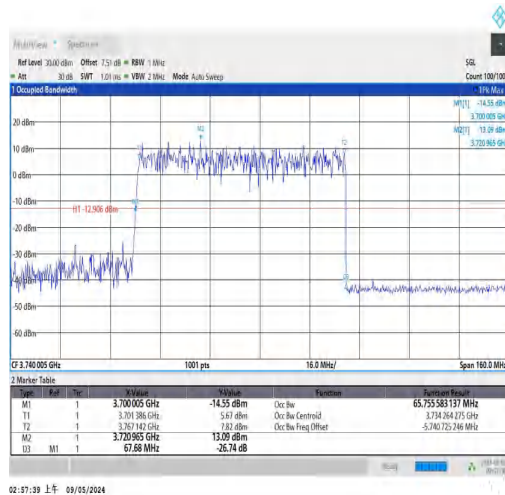
NTNV_N78-3700-3800_PC3_30_80_L_TID1_Ant

1#1



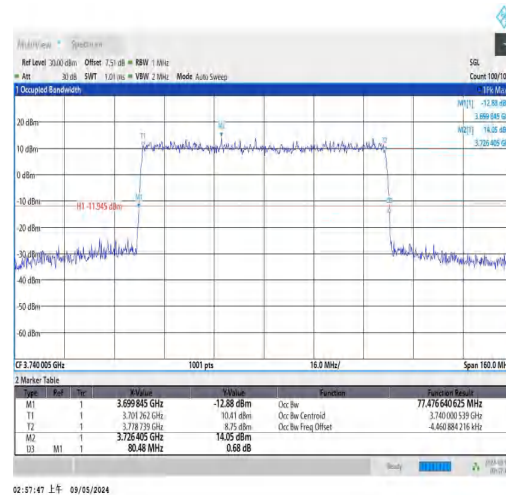
NTNV_N78-3700-3800_PC3_30_80_L_TID2_Ant

1#1



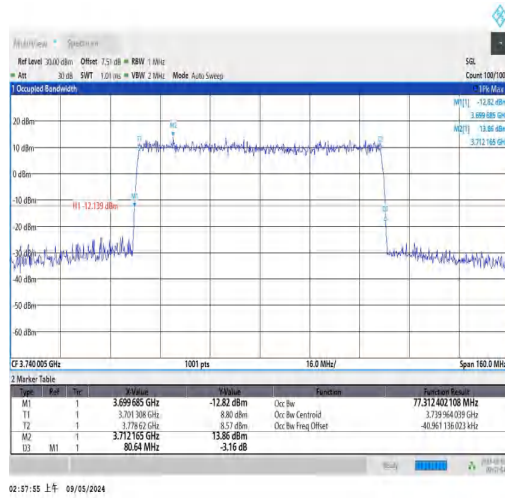
NTNV_N78-3700-3800_PC3_30_80_L_TID3_Ant

1#1



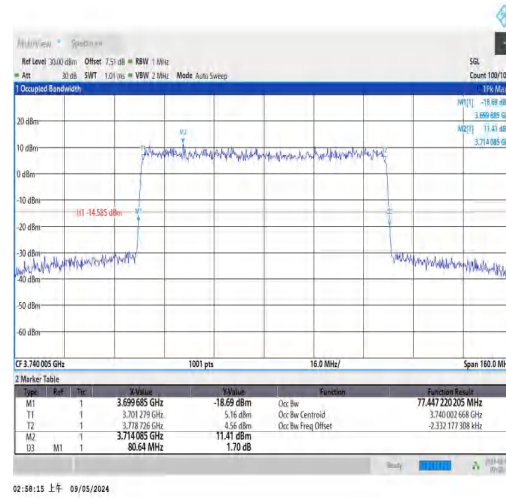
NTNV_N78-3700-3800_PC3_30_80_L_TID4_Ant

1#1



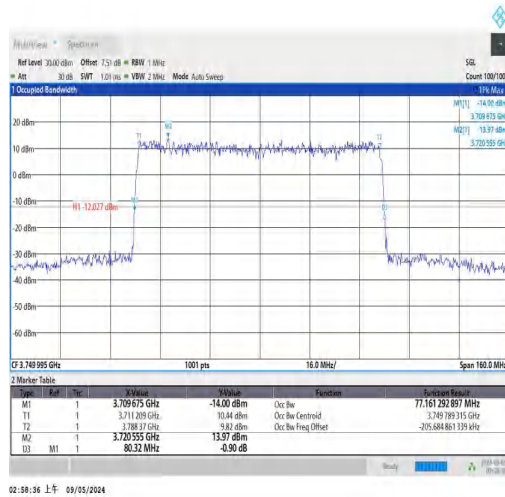
NTNV_N78-3700-3800_PC3_30_80_L_TID5_Ant

1#1



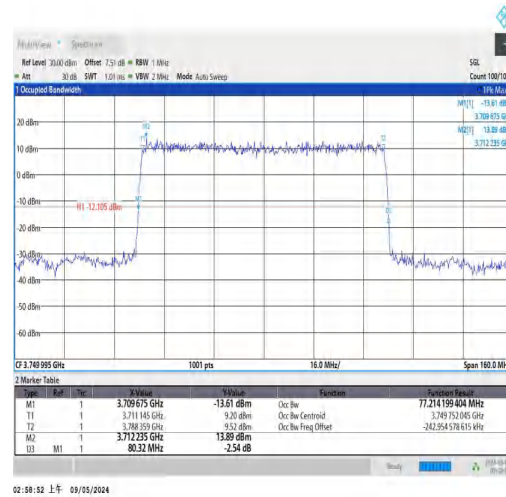
NTNV_N78-3700-3800_PC3_30_80_L_TID6_Ant

1#1



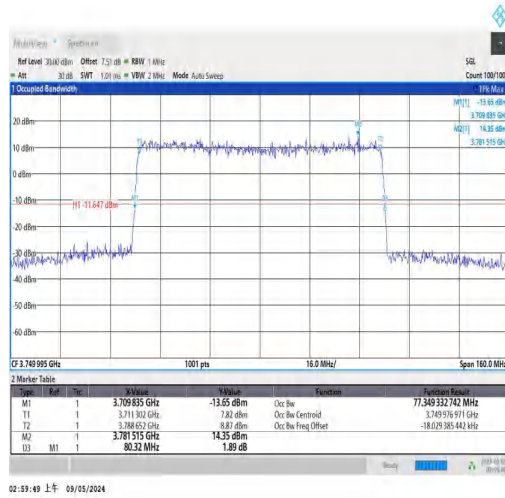
NTNV_N78-3700-3800_PC3_30_80_M_TID1_Ant

1#1

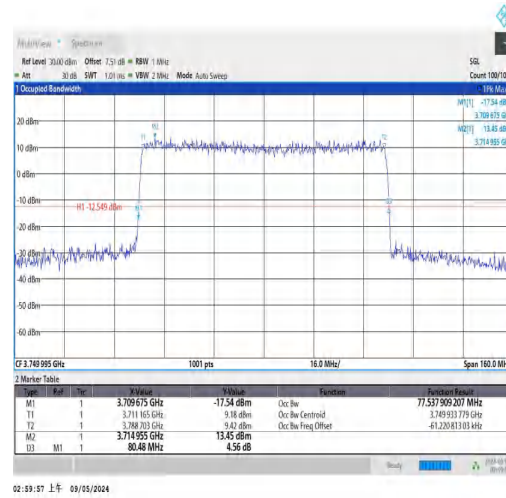


NTNV_N78-3700-3800_PC3_30_80_M_TID2_Ant

1#1



02:59:49 上午 09/05/2024



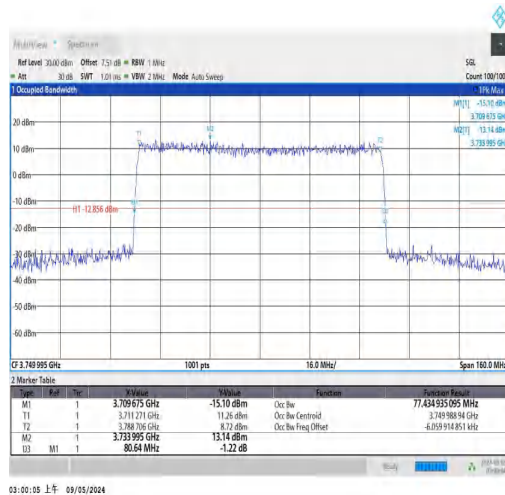
02:59:57 上午 09/05/2024

NTNV_N78-3700-3800_PC3_30_80_M_TID3_Ant

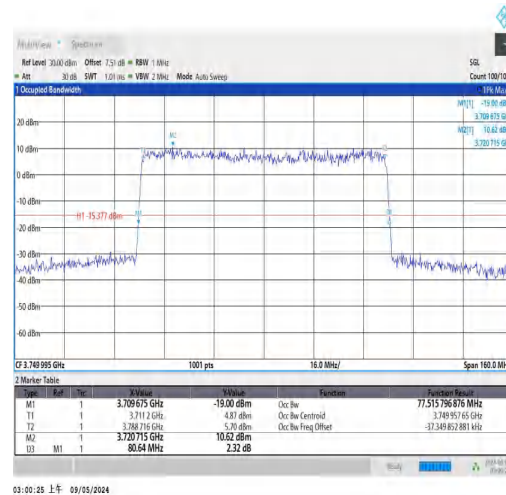
NTNV_N78-3700-3800_PC3_30_80_M_TID4_Ant

1#1

1#1



03:00:08 上午 09/05/2024



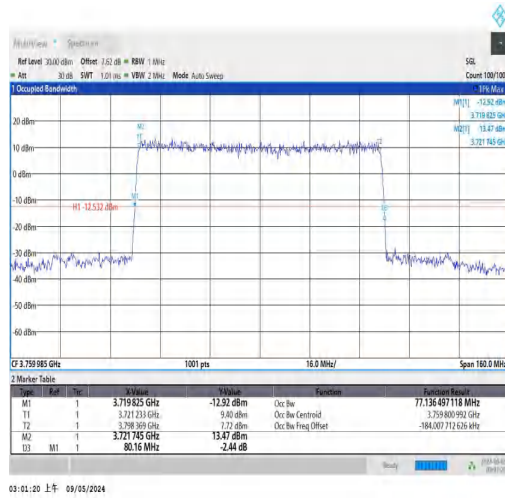
03:00:28 上午 09/05/2024

NTNV_N78-3700-3800_PC3_30_80_M_TID5_Ant

NTNV_N78-3700-3800_PC3_30_80_M_TID6_Ant

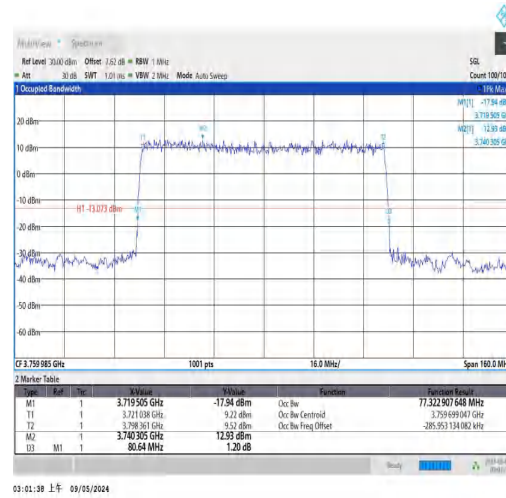
1#1

1#1



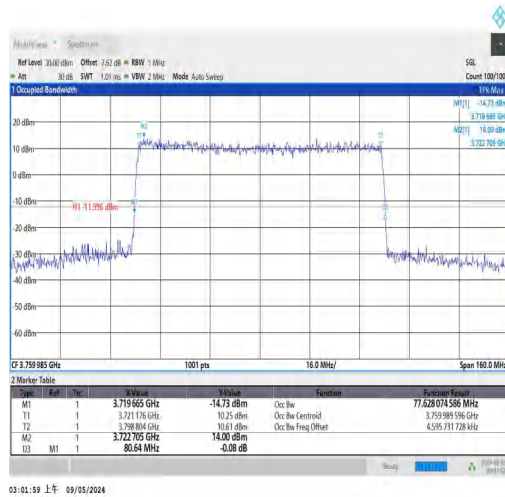
NTNV_N78-3700-3800_PC3_30_80_H_TID1_Ant

1#1



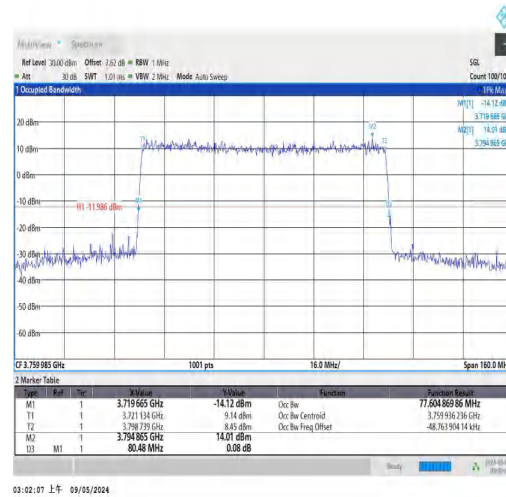
NTNV_N78-3700-3800_PC3_30_80_H_TID2_Ant

1#1



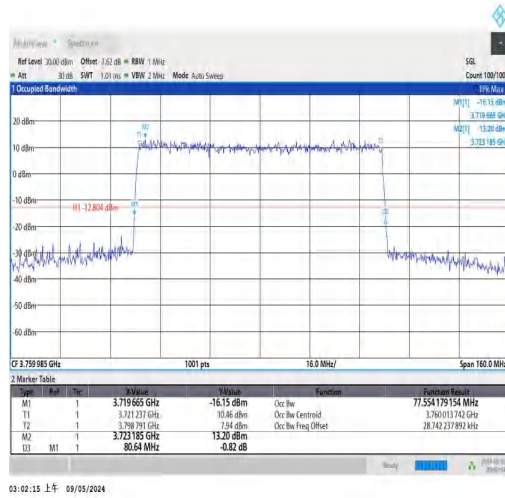
NTNV_N78-3700-3800_PC3_30_80_H_TID3_Ant

1#1

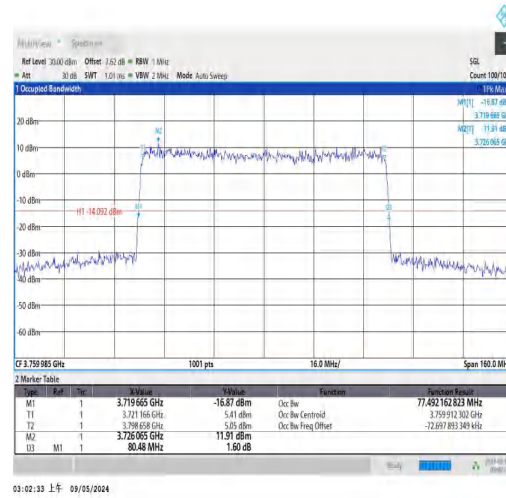


NTNV_N78-3700-3800_PC3_30_80_H_TID4_Ant

1#1



03:02:15 上午 09/05/2024



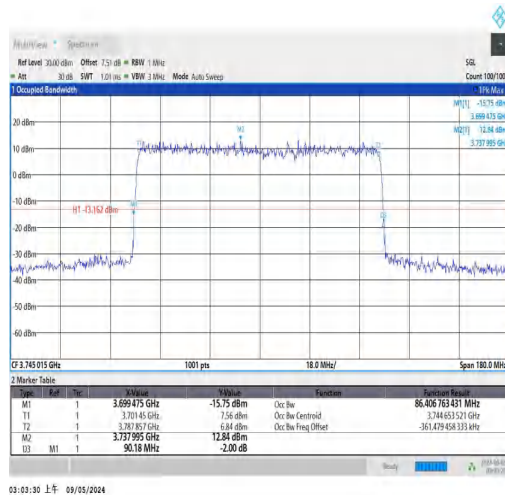
03:02:33 上午 09/05/2024

NTNV_N78-3700-3800_PC3_30_80_H_TID5_Ant

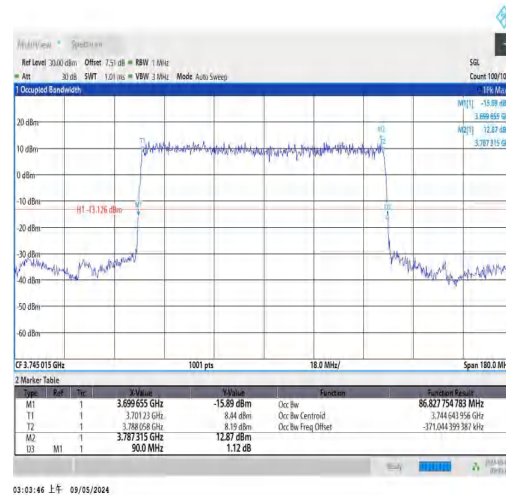
1#1

NTNV_N78-3700-3800_PC3_30_80_H_TID6_Ant

1#1



03:03:30 上午 09/05/2024



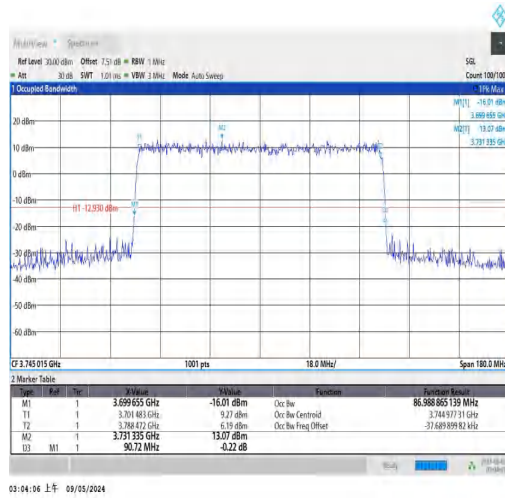
03:03:46 上午 09/05/2024

NTNV_N78-3700-3800_PC3_30_90_L_TID1_Ant

1#1

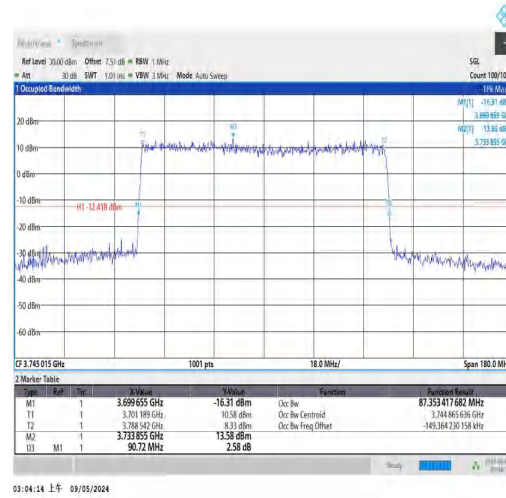
NTNV_N78-3700-3800_PC3_30_90_L_TID2_Ant

1#1



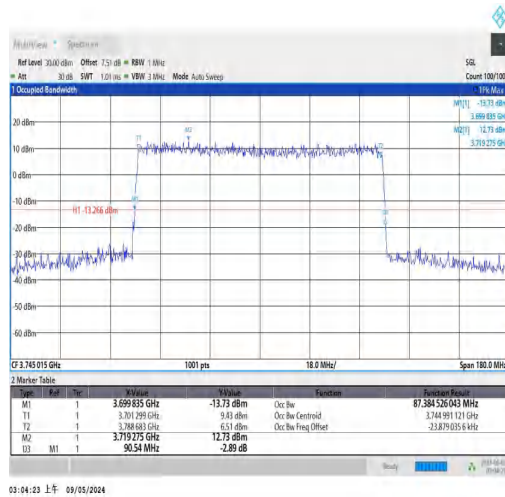
NTNV_N78-3700-3800_PC3_30_90_L_TID3_Ant

1#1



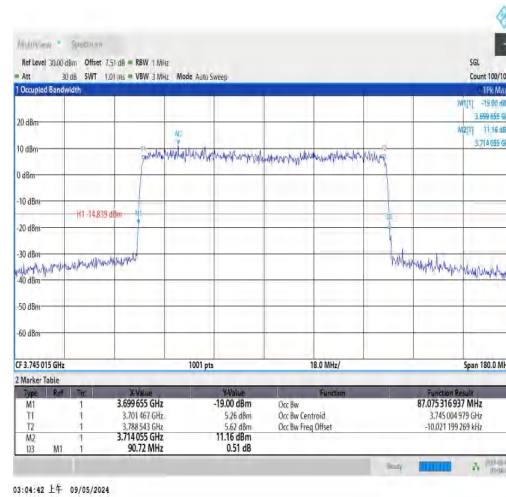
NTNV_N78-3700-3800_PC3_30_90_L_TID4_Ant

1#1



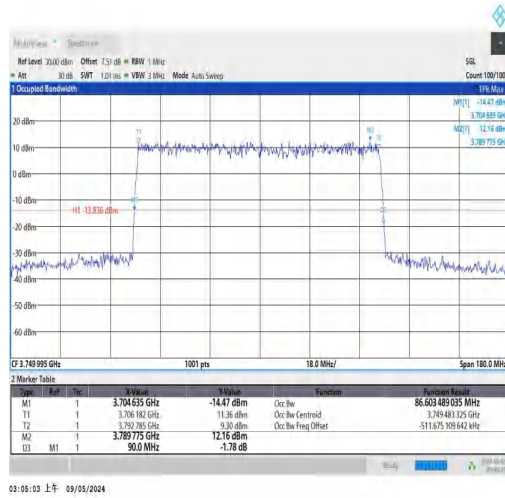
NTNV_N78-3700-3800_PC3_30_90_L_TID5_Ant

1#1



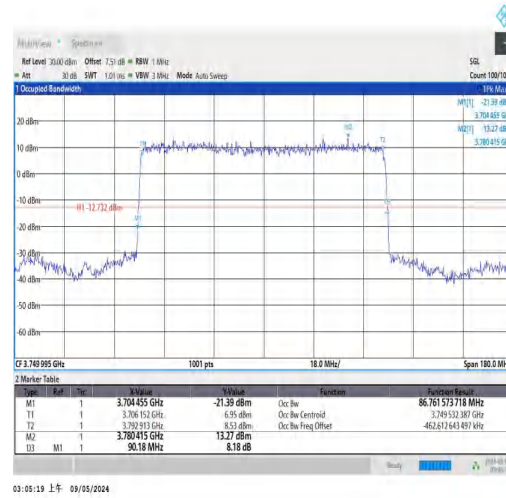
NTNV_N78-3700-3800_PC3_30_90_L_TID6_Ant

1#1



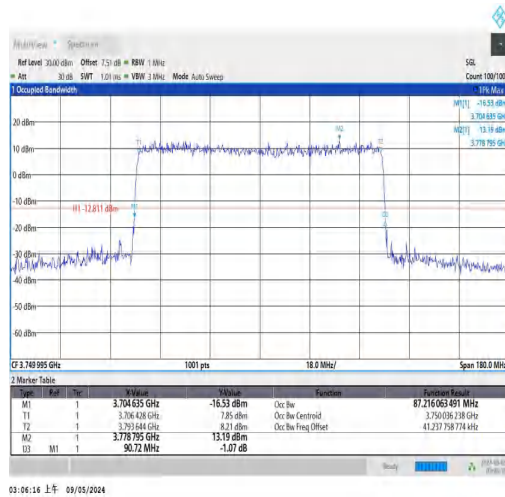
NTNV_N78-3700-3800_PC3_30_90_M_TID1_Ant

1#1



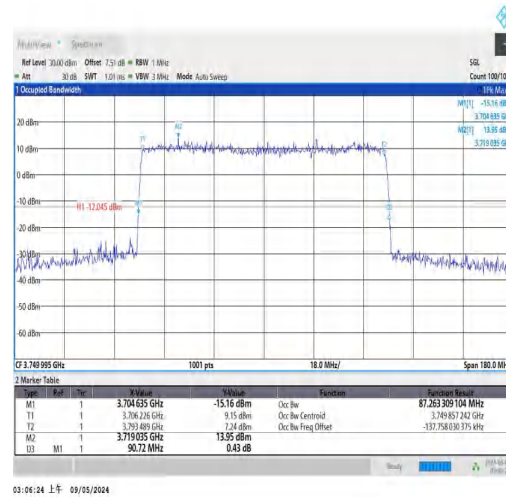
NTNV_N78-3700-3800_PC3_30_90_M_TID2_Ant

1#1



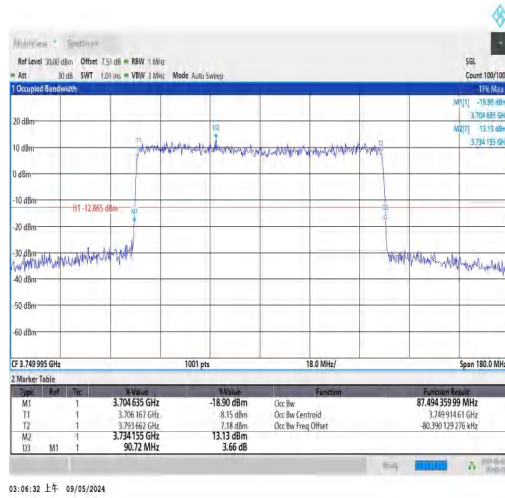
NTNV_N78-3700-3800_PC3_30_90_M_TID3_Ant

1#1



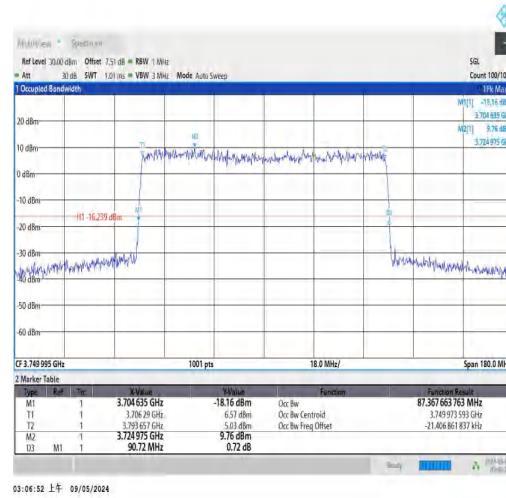
NTNV_N78-3700-3800_PC3_30_90_M_TID4_Ant

1#1



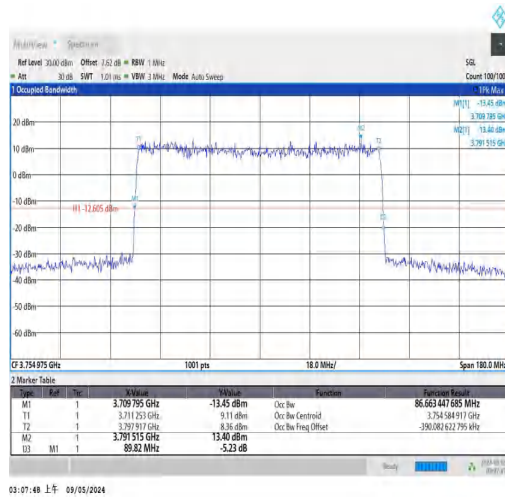
NTNV_N78-3700-3800_PC3_30_90_M_TID5_Ant

1#1



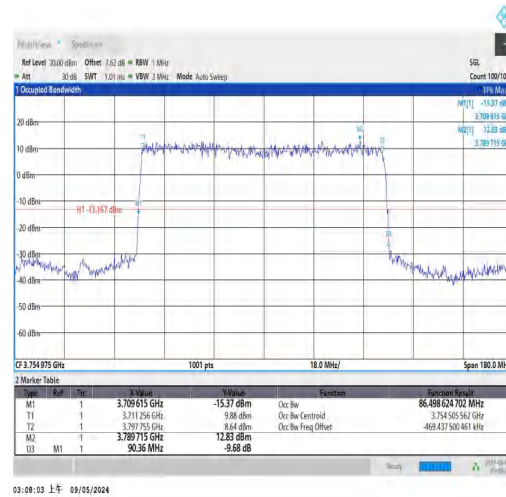
NTNV_N78-3700-3800_PC3_30_90_M_TID6_Ant

1#1



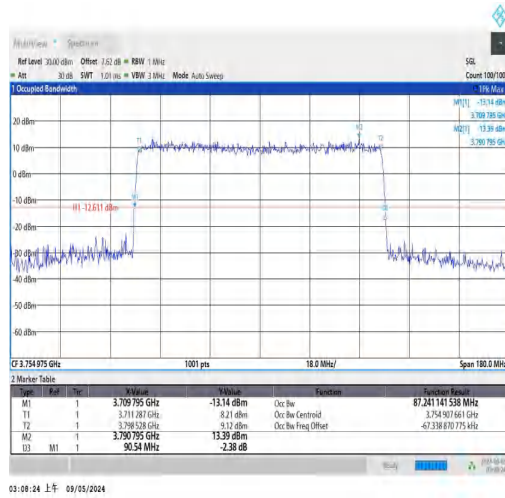
NTNV_N78-3700-3800_PC3_30_90_H_TID1_Ant

1#1



NTNV_N78-3700-3800_PC3_30_90_H_TID2_Ant

1#1



03:09:24 上午 09/05/2024



03:09:33 上午 09/05/2024

NTNV_N78-3700-3800_PC3_30_90_H_TID3_Ant

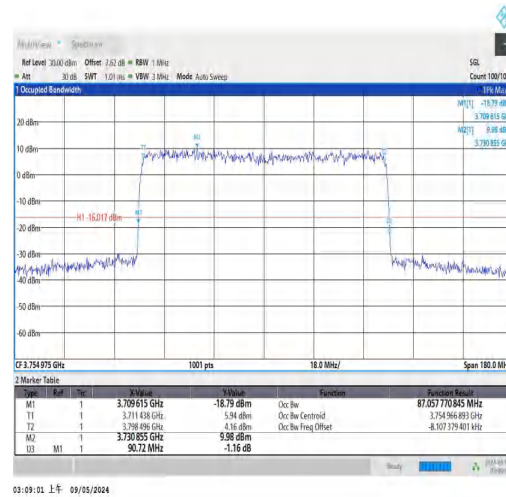
1#1

NTNV_N78-3700-3800_PC3_30_90_H_TID4_Ant

1#1



03:09:41 上午 09/05/2024



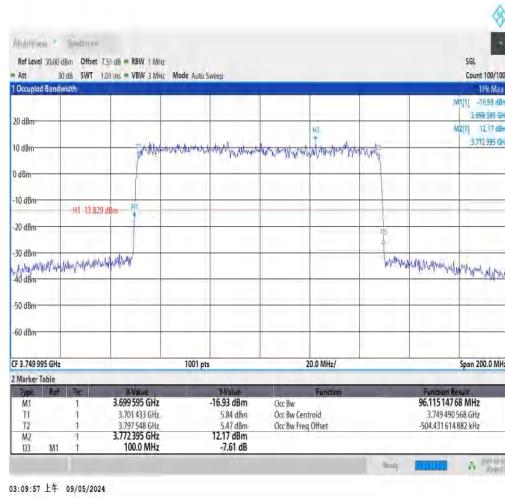
03:09:02 上午 09/05/2024

NTNV_N78-3700-3800_PC3_30_90_H_TID5_Ant

1#1

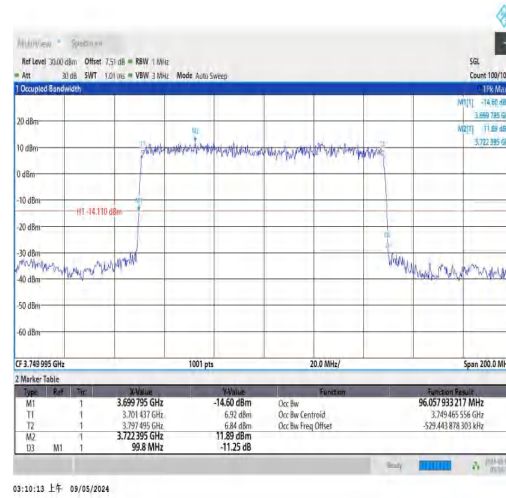
NTNV_N78-3700-3800_PC3_30_90_H_TID6_Ant

1#1



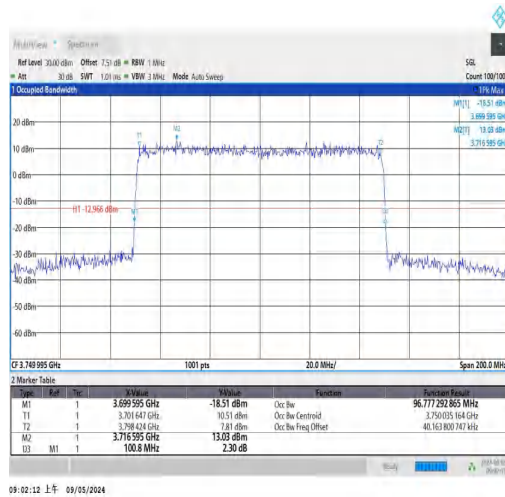
NTNV_N78-3700-3800_PC3_30_100_L_TID1_An

t1#1



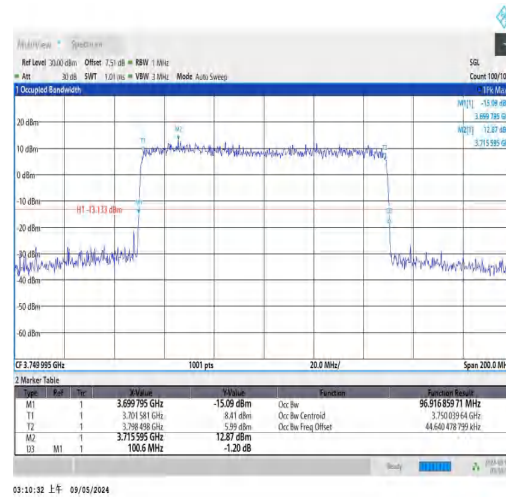
NTNV_N78-3700-3800_PC3_30_100_L_TID2_An

t1#1



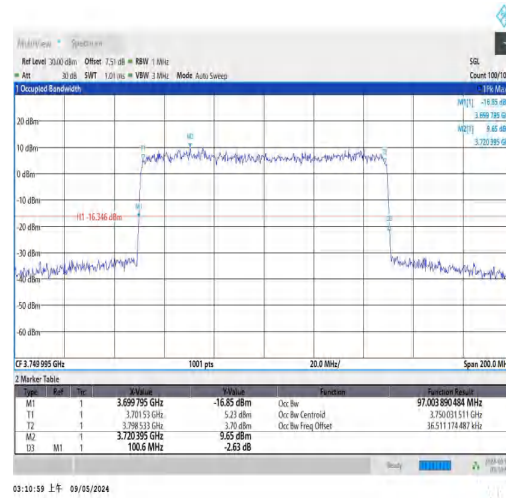
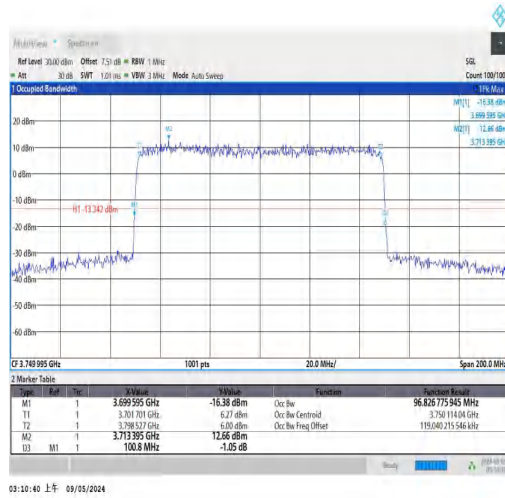
NTNV_N78-3700-3800_PC3_30_100_L_TID3_An

t1#1



NTNV_N78-3700-3800_PC3_30_100_L_TID4_An

t1#1

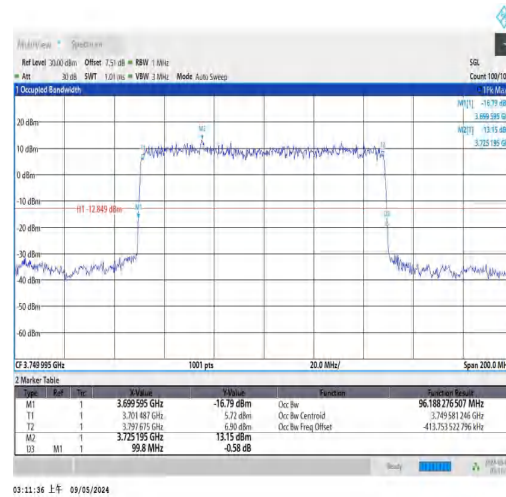
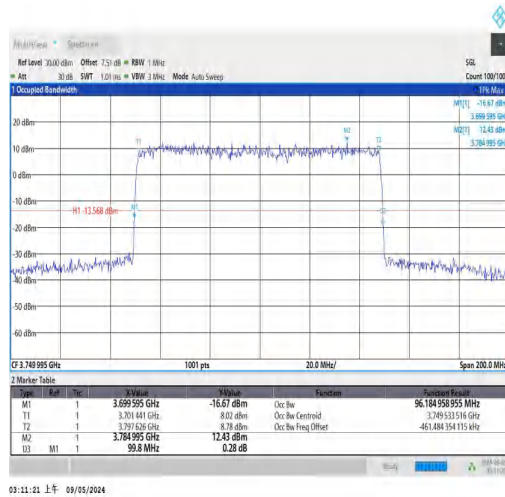


NTNV_N78-3700-3800_PC3_30_100_L_TID5_An

NTNV_N78-3700-3800_PC3_30_100_L_TID6_An

t1#1

t1#1

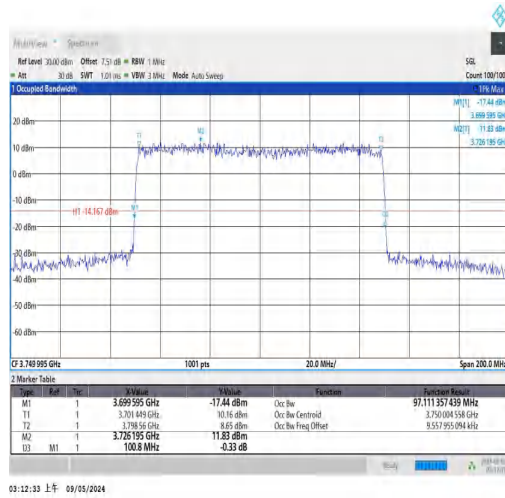


NTNV_N78-3700-3800_PC3_30_100_M_TID1_A

NTNV_N78-3700-3800_PC3_30_100_M_TID2_A

nt1#1

nt1#1



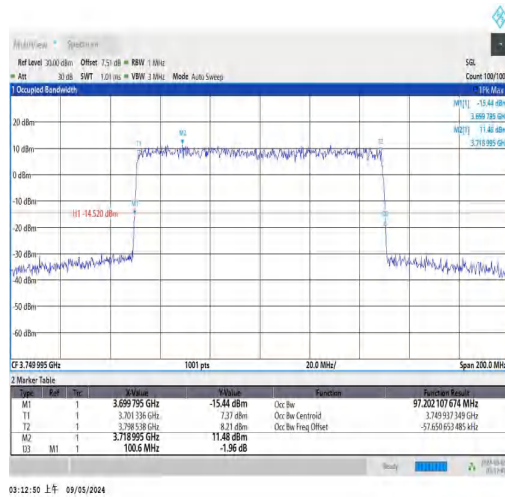
NTNV_N78-3700-3800_PC3_30_100_M_TID3_A

nt1#1



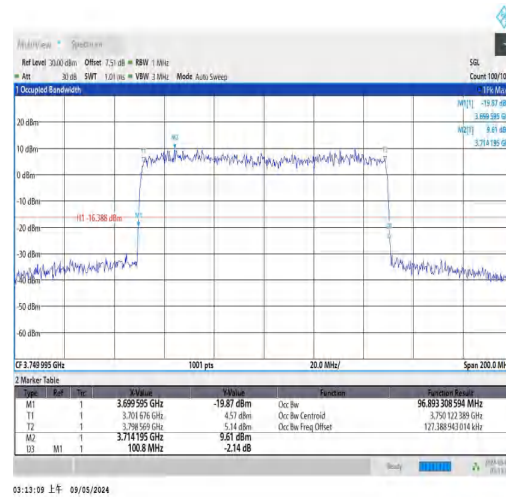
NTNV_N78-3700-3800_PC3_30_100_M_TID4_A

nt1#1



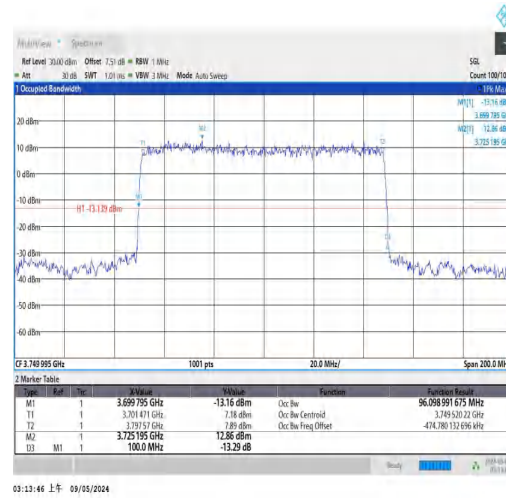
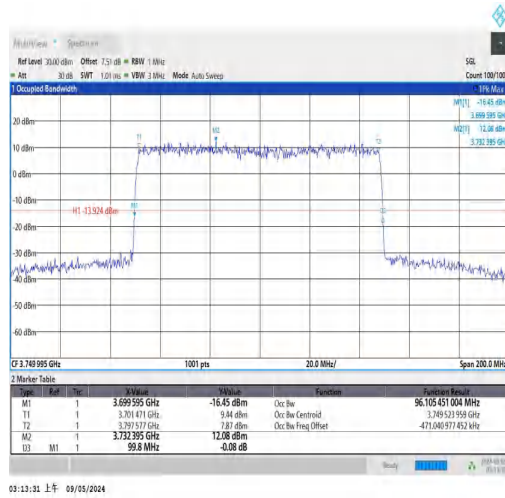
NTNV_N78-3700-3800_PC3_30_100_M_TID5_A

nt1#1



NTNV_N78-3700-3800_PC3_30_100_M_TID6_A

nt1#1

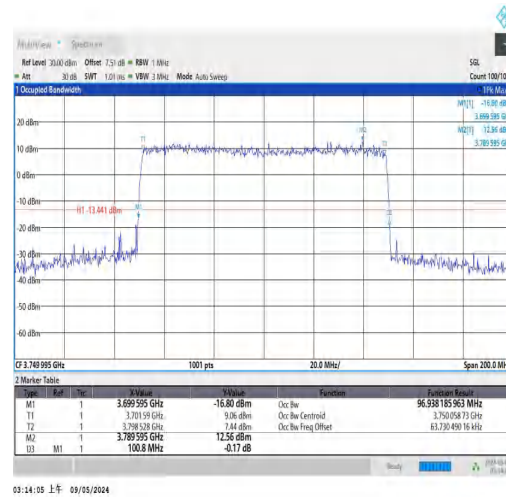
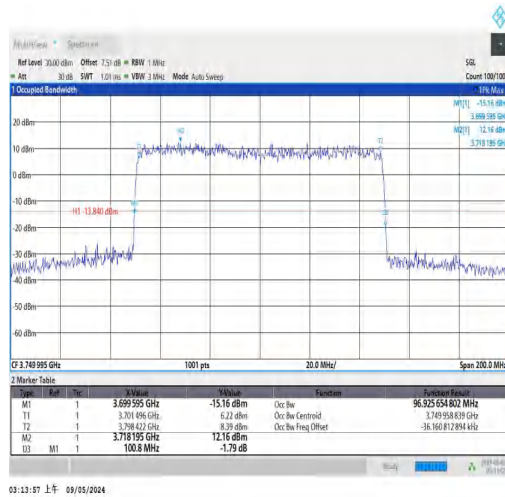


NTNV_N78-3700-3800_PC3_30_100_H_TID1_An

NTNV_N78-3700-3800_PC3_30_100_H_TID2_An

t1#1

t1#1

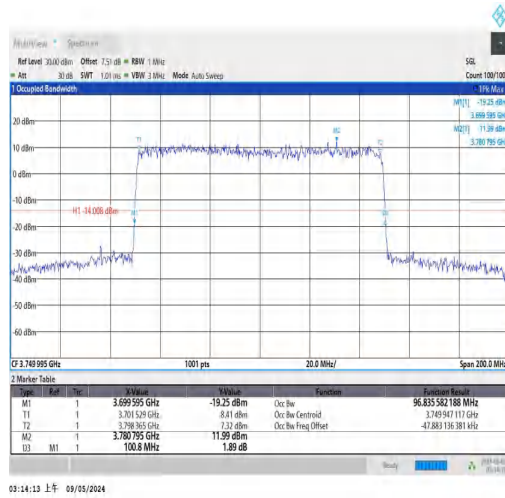


NTNV_N78-3700-3800_PC3_30_100_H_TID3_An

NTNV_N78-3700-3800_PC3_30_100_H_TID4_An

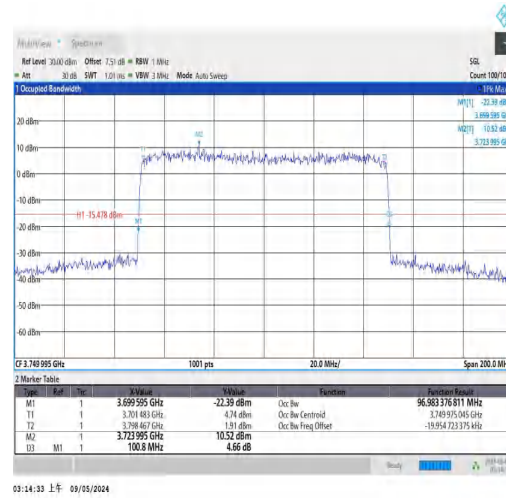
t1#1

t1#1



NTNV_N78-3700-3800_PC3_30_100_H_TID5_An

t1#1



NTNV_N78-3700-3800_PC3_30_100_H_TID6_An

t1#1



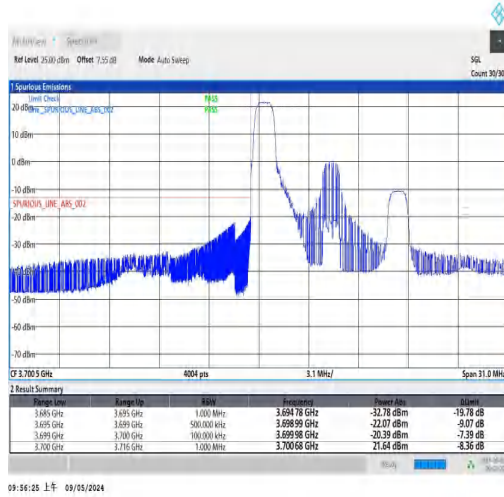
Appendix D: Band Edge

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
N78-3700-3800	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N78-3700-3800	30	10	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3700-3800	30	10	DFT-QPSK	L	Outer_Full	see graph	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N78-3700-3800	30	10	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N78-3700-3800	30	10	DFT-QPSK	H	Outer_Full	see graph	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N78-3700-3800	30	50	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3700-3800	30	50	DFT-QPSK	L	Outer_Full	see graph	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N78-3700-3800	30	50	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N78-3700-3800	30	50	DFT-QPSK	H	Outer_Full	see graph	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	see graph	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	see graph	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS

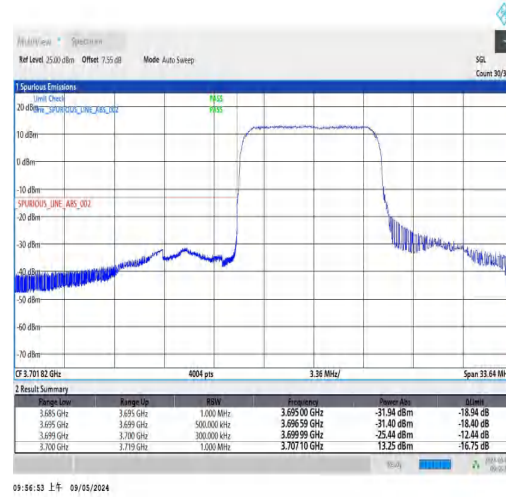


Test Graphs



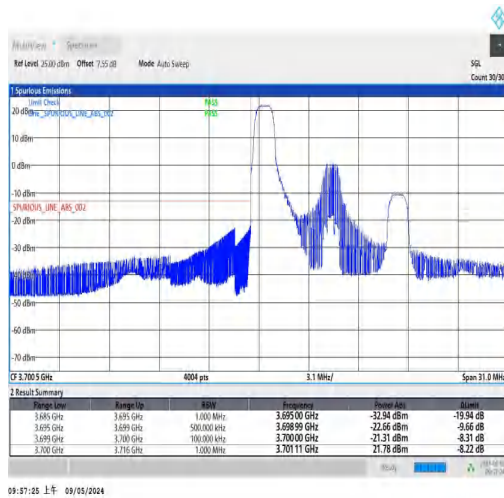
NTNV_N78-3700-3800_PC3_30_10_L_TID1_RID

1#1



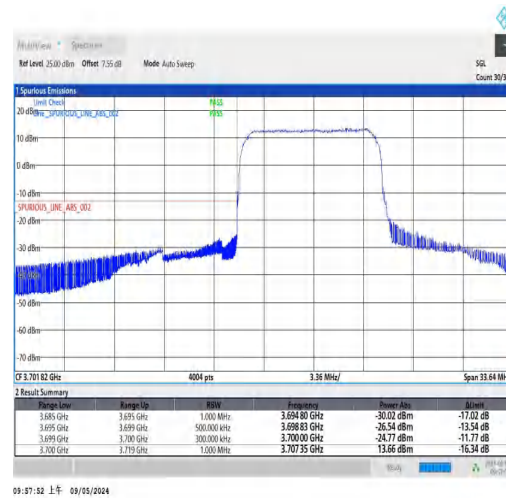
NTNV_N78-3700-3800_PC3_30_10_L_TID3_RID

1#1



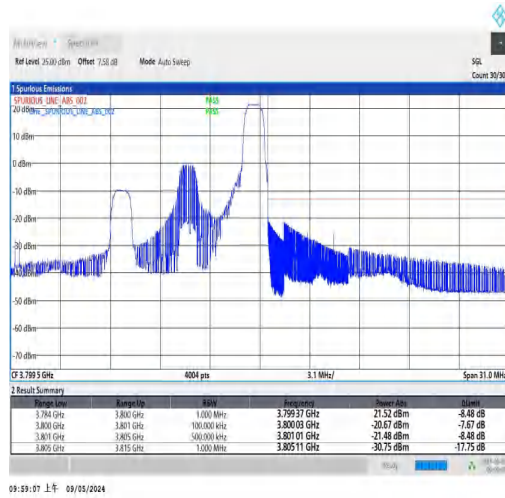
NTNV_N78-3700-3800_PC3_30_10_L_TID4_RID

1#1



NTNV_N78-3700-3800_PC3_30_10_L_TID6_RID

1#1



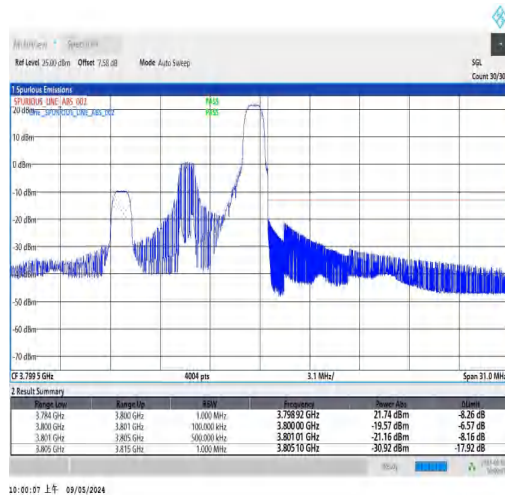
NTNV_N78-3700-3800_PC3_30_10_H_TID2_RID

1#1



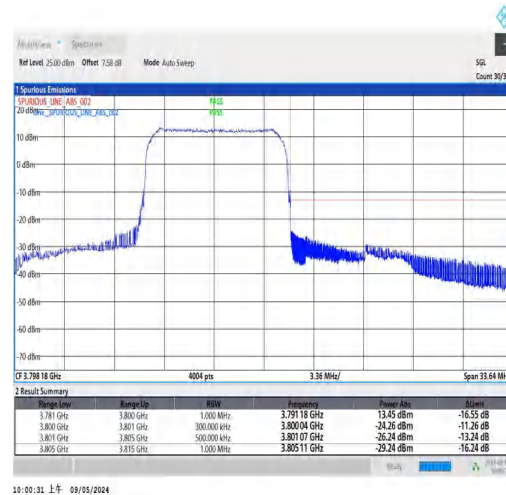
NTNV_N78-3700-3800_PC3_30_10_H_TID3_RID

1#1



NTNV_N78-3700-3800_PC3_30_10_H_TID5_RID

1#1



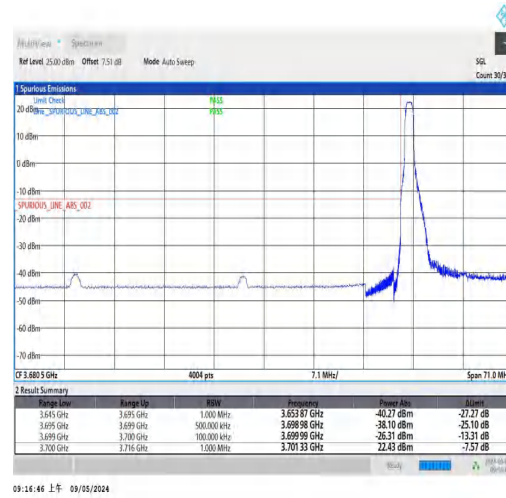
NTNV_N78-3700-3800_PC3_30_10_H_TID6_RID

1#1



NTNV_N78-3700-3800_PC3_30_50_L_TID3_RID

1#1



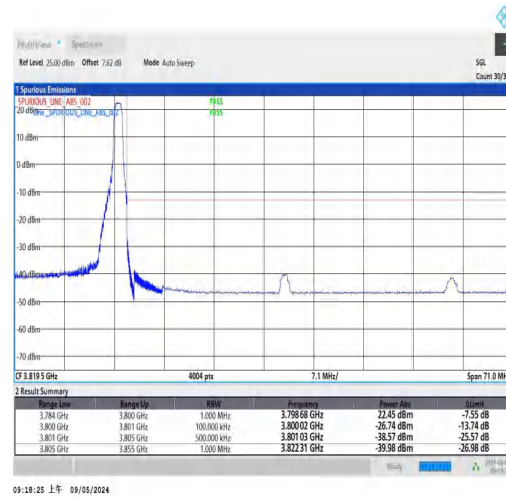
NTNV_N78-3700-3800_PC3_30_50_L_TID4_RID

1#1



NTNV_N78-3700-3800_PC3_30_50_L_TID6_RID

1#1



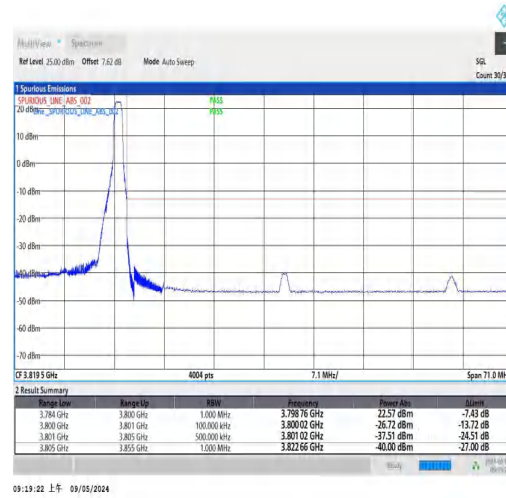
NTNV_N78-3700-3800_PC3_30_50_H_TID2_RID

1#1



NTNV_N78-3700-3800_PC3_30_50_H_TID3_RID

1#1



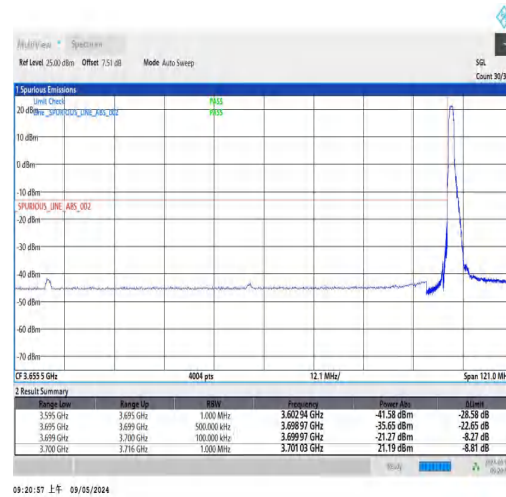
NTNV_N78-3700-3800_PC3_30_50_H_TID5_RID

1#1



NTNV_N78-3700-3800_PC3_30_50_H_TID6_RID

1#1

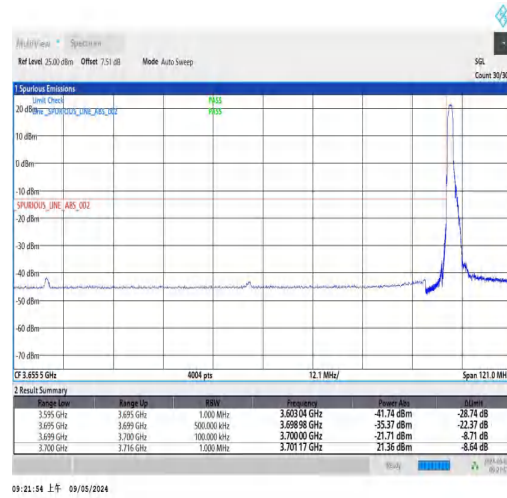


NTNV_N78-3700-3800_PC3_30_100_L_TID1_RID

D1#1



09:21:22 上午 09/05/2024



09:21:54 上午 09/05/2024

NTNV_N78-3700-3800_PC3_30_100_L_TID3_RI

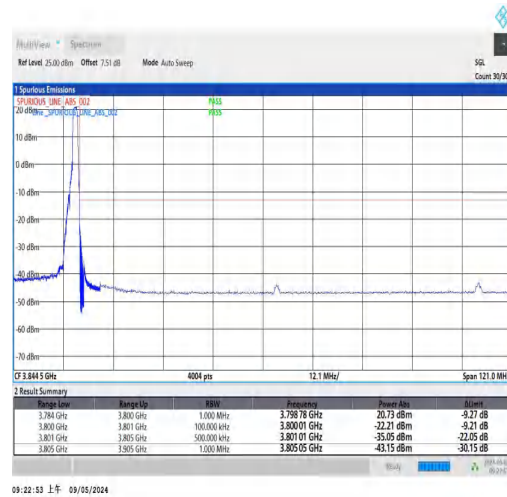
D1#1

NTNV_N78-3700-3800_PC3_30_100_L_TID4_RI

D1#1



09:22:19 上午 09/05/2024



09:22:53 上午 09/05/2024

NTNV_N78-3700-3800_PC3_30_100_L_TID6_RI

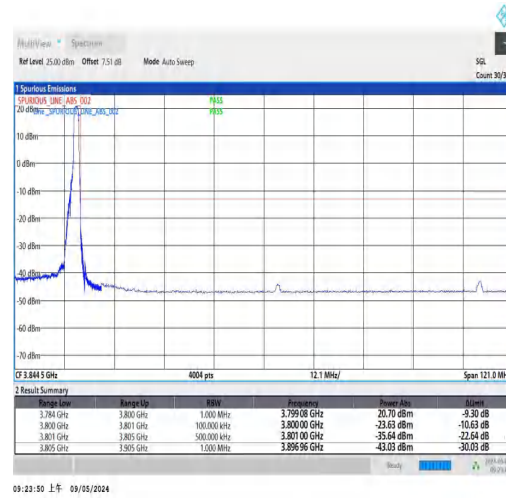
D1#1

NTNV_N78-3700-3800_PC3_30_100_H_TID2_RI

D1#1



09:23:17 上午 09/05/2024



09:23:50 上午 09/05/2024

NTNV_N78-3700-3800_PC3_30_100_H_TID3_RI

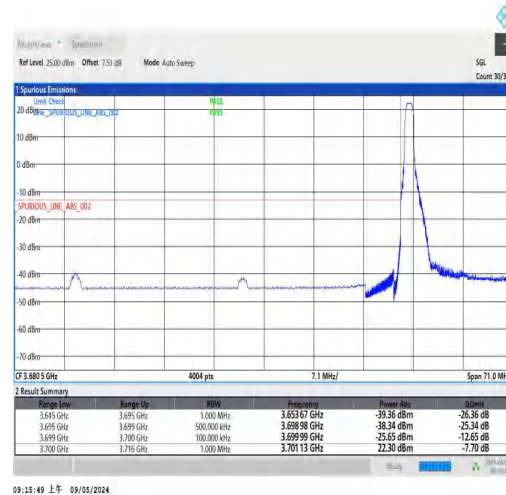
D1#1

NTNV_N78-3700-3800_PC3_30_100_H_TID5_RI

D1#1



09:24:14 上午 09/05/2024



09:15:49 上午 09/05/2024

NTNV_N78-3700-3800_PC3_30_100_H_TID6_RI

D1#1

NTNV_N78-3700-3800_PC3_30_50_L_TID1_RID

1#1



Appendix E: Conducted Spurious Emission

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Start Freq (MHz)	Stop Freq (MHz)	Result (dBm)	Limit (dBm)	Verdict
N78-3700-3800	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	0.009	0.15	-77.28	-43	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	0.15	30	-83.16	-33	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	30	1000	-69.95	-13	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	1000	6000	-32.42	-13	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	6000	18000	-23.20	-13	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	18000	40000	-62.77	-13	PASS
N78-3700-3800	30	10	DFT-QPSK	L	Edge_1RB_Left	0.009	0.15	-78.79	-43	PASS
N78-3700-3800	30	10	DFT-QPSK	L	Edge_1RB_Left	0.15	30	-83.17	-33	PASS
N78-3700-3800	30	10	DFT-QPSK	L	Edge_1RB_Left	30	1000	-69.95	-13	PASS
N78-3700-3800	30	10	DFT-QPSK	L	Edge_1RB_Left	1000	6000	-34.37	-13	PASS
N78-3700-3800	30	10	DFT-QPSK	L	Edge_1RB_Left	6000	18000	-21.70	-13	PASS
N78-3700-3800	30	10	DFT-QPSK	L	Edge_1RB_Left	18000	40000	-63.07	-13	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	0.009	0.15	-78.78	-43	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	0.15	30	-83.17	-33	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	30	1000	-69.90	-13	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	1000	6000	-33.99	-13	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	6000	18000	-21.34	-13	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	18000	40000	-62.96	-13	PASS
N78-3700-3800	30	10	DFT-QPSK	M	Edge_1RB_Left	0.009	0.15	-74.90	-43	PASS
N78-3700-3800	30	10	DFT-QPSK	M	Edge_1RB_Left	0.15	30	-82.97	-33	PASS
N78-3700-3800	30	10	DFT-QPSK	M	Edge_1RB_Left	30	1000	-69.68	-13	PASS
N78-3700-3800	30	10	DFT-QPSK	M	Edge_1RB_Left	1000	6000	-34.33	-13	PASS
N78-3700-3800	30	10	DFT-QPSK	M	Edge_1RB_Left	6000	18000	-23.77	-13	PASS
N78-3700-3800	30	10	DFT-QPSK	M	Edge_1RB_Left	18000	40000	-62.79	-13	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	0.009	0.15	-73.34	-43	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	0.15	30	-83.25	-33	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-70.09	-13	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	1000	6000	-33.98	-13	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	6000	18000	-22.00	-13	PASS
N78-3700-3800	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	18000	40000	-62.98	-13	PASS
N78-3700-3800	30	10	DFT-QPSK	H	Edge_1RB_Left	0.009	0.15	-74.43	-43	PASS
N78-3700-3800	30	10	DFT-QPSK	H	Edge_1RB_Left	0.15	30	-83.13	-33	PASS



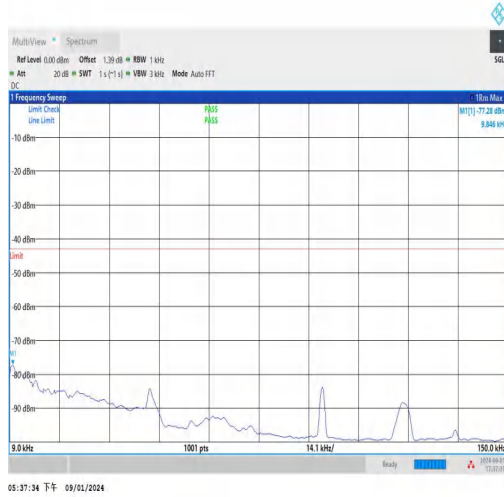
N78-3700-3800	30	10	DFT-QPSK	H	Edge_1RB_Left	30	1000	-69.67	-13	PASS
N78-3700-3800	30	10	DFT-QPSK	H	Edge_1RB_Left	1000	6000	-34.94	-13	PASS
N78-3700-3800	30	10	DFT-QPSK	H	Edge_1RB_Left	6000	18000	-27.62	-13	PASS
N78-3700-3800	30	10	DFT-QPSK	H	Edge_1RB_Left	18000	40000	-62.94	-13	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	0.009	0.15	-78.18	-43	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	0.15	30	-84.66	-33	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	30	1000	-70.20	-13	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	1000	6000	-36.70	-13	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	6000	18000	-37.88	-13	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	18000	40000	-62.66	-13	PASS
N78-3700-3800	30	50	DFT-QPSK	L	Edge_1RB_Left	0.009	0.15	-78.48	-43	PASS
N78-3700-3800	30	50	DFT-QPSK	L	Edge_1RB_Left	0.15	30	-84.61	-33	PASS
N78-3700-3800	30	50	DFT-QPSK	L	Edge_1RB_Left	30	1000	-69.62	-13	PASS
N78-3700-3800	30	50	DFT-QPSK	L	Edge_1RB_Left	1000	6000	-28.49	-13	PASS
N78-3700-3800	30	50	DFT-QPSK	L	Edge_1RB_Left	6000	18000	-21.40	-13	PASS
N78-3700-3800	30	50	DFT-QPSK	L	Edge_1RB_Left	18000	40000	-62.93	-13	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	0.009	0.15	-76.41	-43	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	0.15	30	-84.81	-33	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	30	1000	-70.41	-13	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	1000	6000	-33.46	-13	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	6000	18000	-19.24	-13	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	18000	40000	-63.28	-13	PASS
N78-3700-3800	30	50	DFT-QPSK	M	Edge_1RB_Left	0.009	0.15	-74.96	-43	PASS
N78-3700-3800	30	50	DFT-QPSK	M	Edge_1RB_Left	0.15	30	-84.62	-33	PASS
N78-3700-3800	30	50	DFT-QPSK	M	Edge_1RB_Left	30	1000	-69.75	-13	PASS
N78-3700-3800	30	50	DFT-QPSK	M	Edge_1RB_Left	1000	6000	-28.44	-13	PASS
N78-3700-3800	30	50	DFT-QPSK	M	Edge_1RB_Left	6000	18000	-18.25	-13	PASS
N78-3700-3800	30	50	DFT-QPSK	M	Edge_1RB_Left	18000	40000	-63.20	-13	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	0.009	0.15	-75.00	-43	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	0.15	30	-84.77	-33	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-70.06	-13	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	1000	6000	-28.74	-13	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	6000	18000	-19.54	-13	PASS
N78-3700-3800	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	18000	40000	-63.10	-13	PASS
N78-3700-3800	30	50	DFT-QPSK	H	Edge_1RB_Left	0.009	0.15	-73.67	-43	PASS
N78-3700-3800	30	50	DFT-QPSK	H	Edge_1RB_Left	0.15	30	-84.73	-33	PASS
N78-3700-3800	30	50	DFT-QPSK	H	Edge_1RB_Left	30	1000	-69.24	-13	PASS
N78-3700-3800	30	50	DFT-QPSK	H	Edge_1RB_Left	1000	6000	-28.27	-13	PASS
N78-3700-3800	30	50	DFT-QPSK	H	Edge_1RB_Left	6000	18000	-54.81	-13	PASS
N78-3700-3800	30	50	DFT-QPSK	H	Edge_1RB_Left	18000	40000	-63.14	-13	PASS



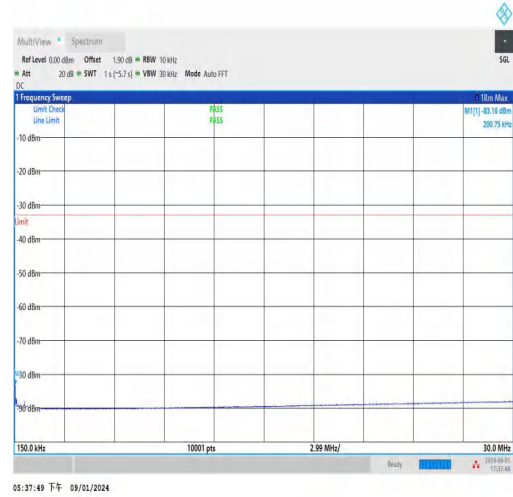
N78-3700-3800	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	0.009	0.15	-79.62	-43	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	0.15	30	-84.81	-33	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	30	1000	-69.66	-13	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	1000	6000	-29.91	-13	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	6000	18000	-24.44	-13	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	18000	40000	-62.86	-13	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Edge_1RB_Left	0.009	0.15	-78.53	-43	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Edge_1RB_Left	0.15	30	-84.92	-33	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Edge_1RB_Left	30	1000	-70.12	-13	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Edge_1RB_Left	1000	6000	-30.05	-13	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Edge_1RB_Left	6000	18000	-27.20	-13	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Edge_1RB_Left	18000	40000	-63.09	-13	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	0.009	0.15	-81.00	-43	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	0.15	30	-84.88	-33	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	30	1000	-69.91	-13	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	1000	6000	-30.26	-13	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	6000	18000	-23.73	-13	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	18000	40000	-63.22	-13	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Edge_1RB_Left	0.009	0.15	-79.13	-43	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Edge_1RB_Left	0.15	30	-84.86	-33	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Edge_1RB_Left	30	1000	-69.79	-13	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Edge_1RB_Left	1000	6000	-30.09	-13	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Edge_1RB_Left	6000	18000	-23.01	-13	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Edge_1RB_Left	18000	40000	-63.10	-13	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	0.009	0.15	-79.14	-43	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	0.15	30	-84.85	-33	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-69.99	-13	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	1000	6000	-29.58	-13	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	6000	18000	-23.54	-13	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	18000	40000	-62.86	-13	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Edge_1RB_Left	0.009	0.15	-79.23	-43	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Edge_1RB_Left	0.15	30	-84.91	-33	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Edge_1RB_Left	30	1000	-69.69	-13	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Edge_1RB_Left	1000	6000	-29.84	-13	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Edge_1RB_Left	6000	18000	-29.80	-13	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Edge_1RB_Left	18000	40000	-63.24	-13	PASS



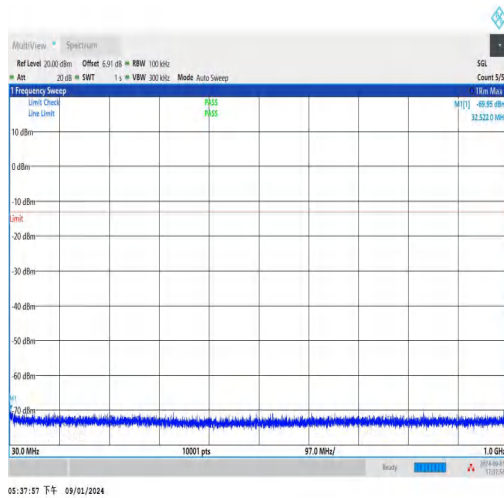
Test Graphs



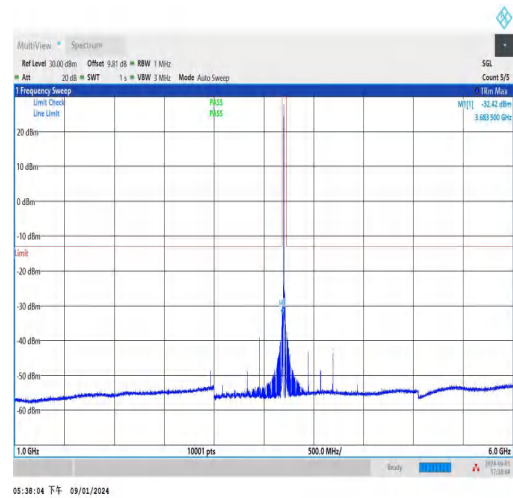
NTNV_N78-3700-3800_PC3_30_10_L_TID1_N/A
_0.009_0.15_#1



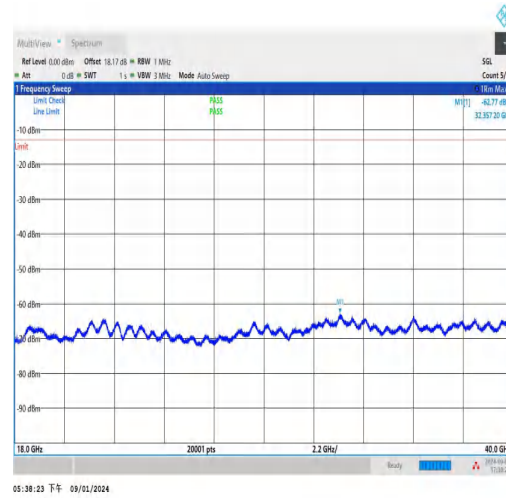
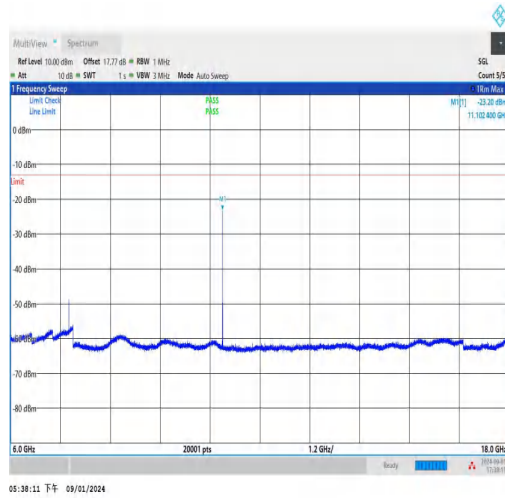
NTNV_N78-3700-3800_PC3_30_10_L_TID1_N/A
_0.15_30_#1



NTNV_N78-3700-3800_PC3_30_10_L_TID1_N/A
_30_1000_#1

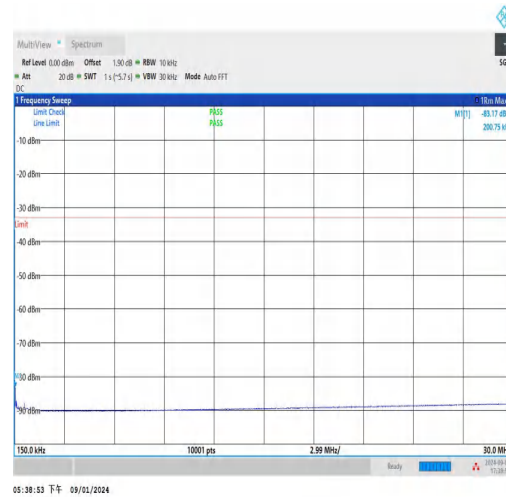
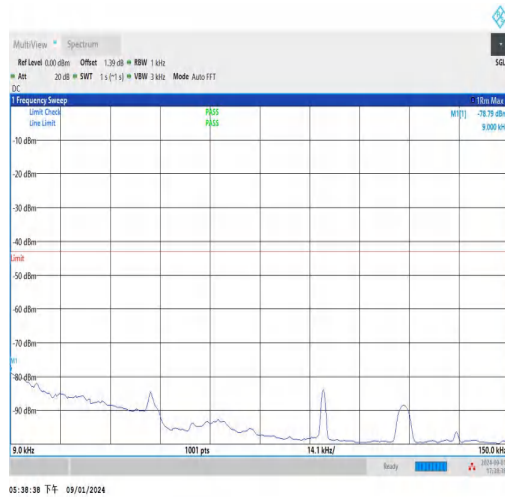


NTNV_N78-3700-3800_PC3_30_10_L_TID1_N/A
_1000_6000_#1



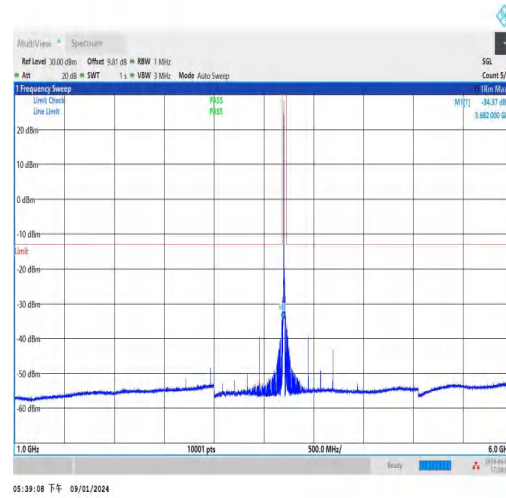
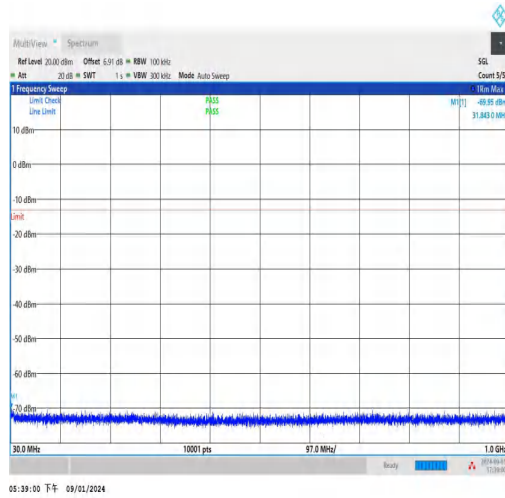
NTNV_N78-3700-3800_PC3_30_10_L_TID1_N/A
_6000_18000_#1

NTNV_N78-3700-3800_PC3_30_10_L_TID1_N/A
_18000_40000_#1



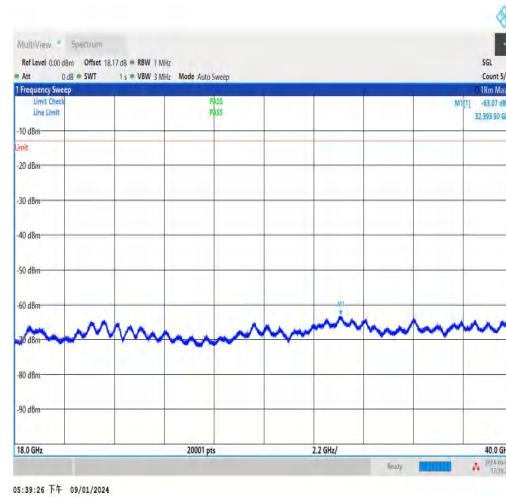
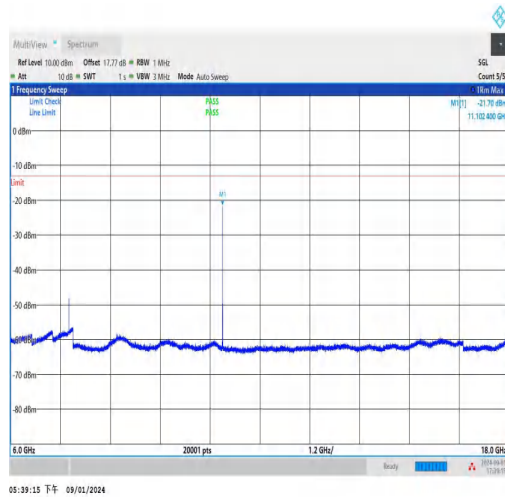
NTNV_N78-3700-3800_PC3_30_10_L_TID2_N/A
_0.009_0.15_#1

NTNV_N78-3700-3800_PC3_30_10_L_TID2_N/A
_0.15_30_#1



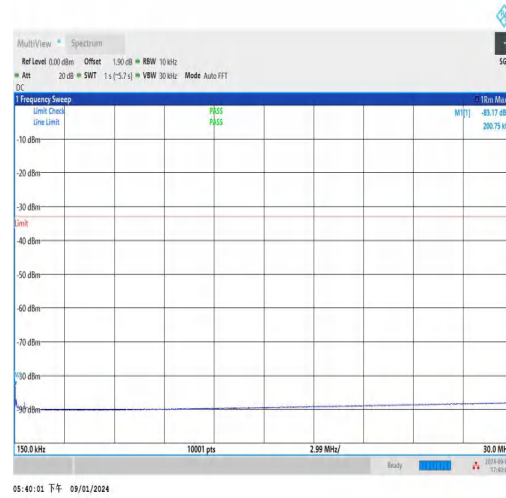
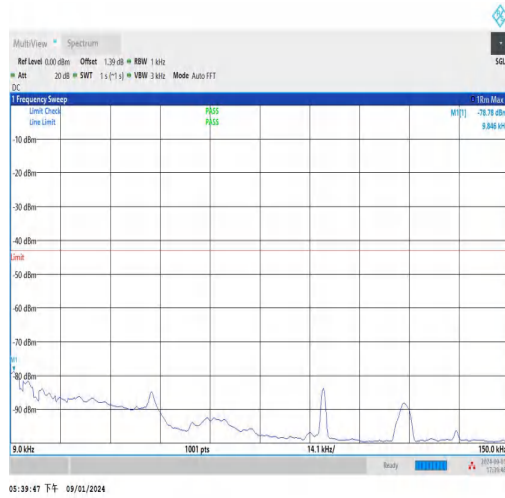
NTNV_N78-3700-3800_PC3_30_10_L_TID2_N/A
_30_1000_#1

NTNV_N78-3700-3800_PC3_30_10_L_TID2_N/A
_1000_6000_#1



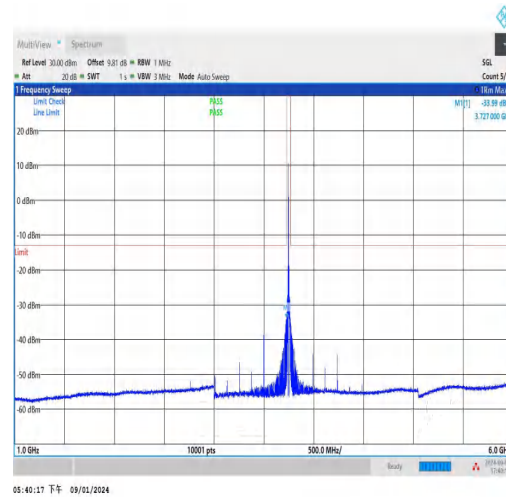
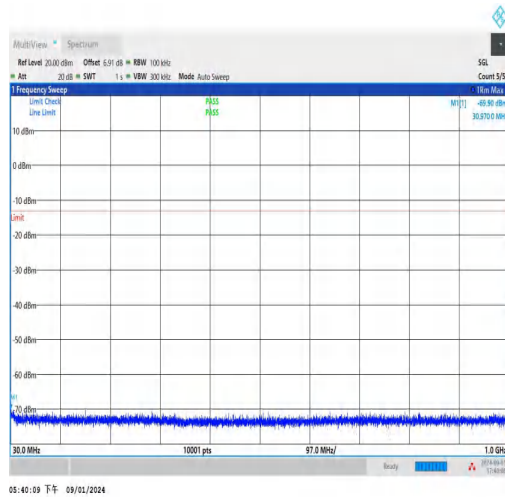
NTNV_N78-3700-3800_PC3_30_10_L_TID2_N/A
_6000_18000_#1

NTNV_N78-3700-3800_PC3_30_10_L_TID2_N/A
_18000_40000_#1



NTNV_N78-3700-3800_PC3_30_10_M_TID1_N/A
_0.009_0.15_#1

NTNV_N78-3700-3800_PC3_30_10_M_TID1_N/A
_0.15_30_#1

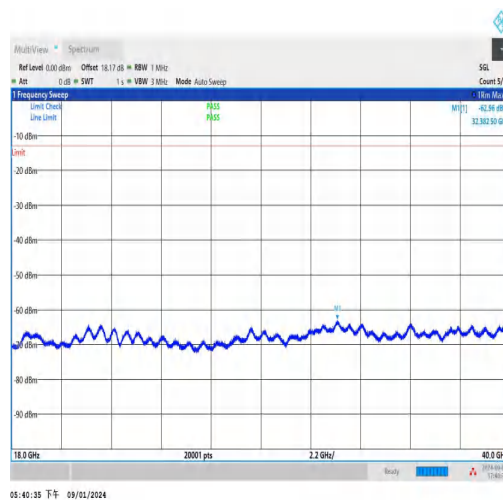


NTNV_N78-3700-3800_PC3_30_10_M_TID1_N/A
_30_1000_#1

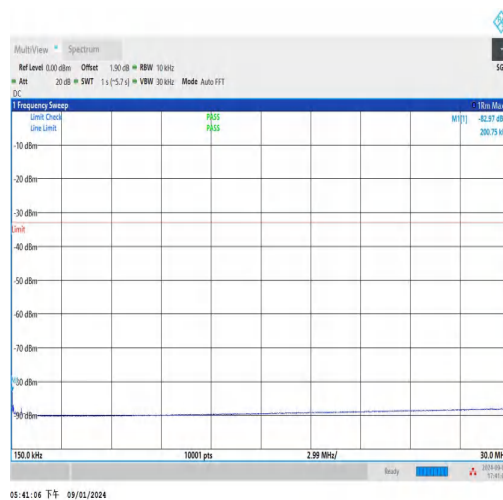
NTNV_N78-3700-3800_PC3_30_10_M_TID1_N/A
_1000_6000_#1



Page 91 of 116



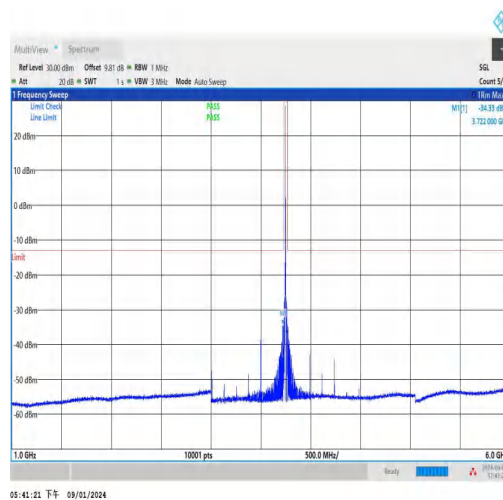
NTNV_N78-3700-3800_PC3_30_10_M_TID1_N/A



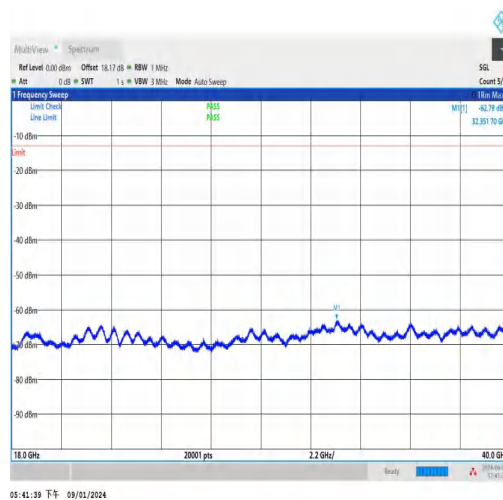
NTNV_N78-3700-3800_PC3_30_10_M_TID2_N/A
0.15 30 #1



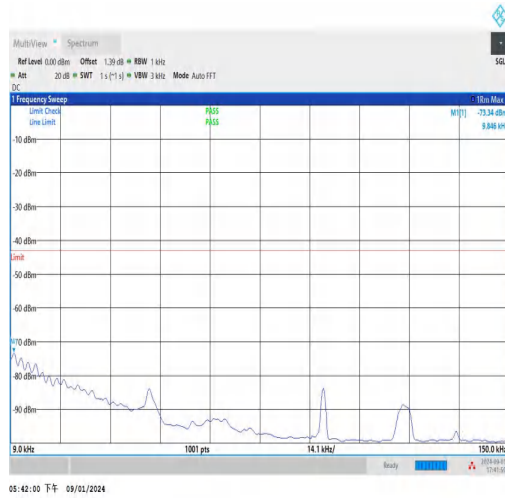
Page 92 of 116



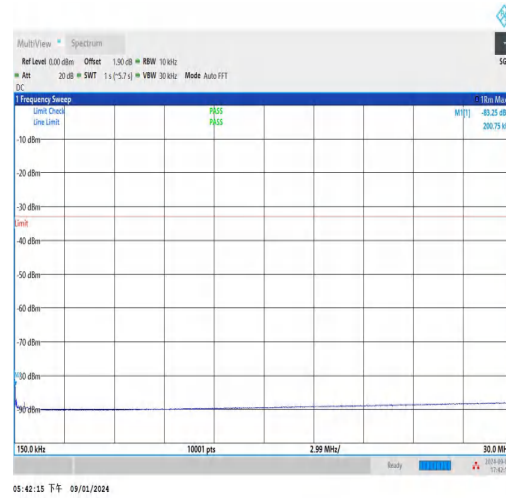
NTNV_N78-3700-3800_PC3_30_10_M_TID2_N/A



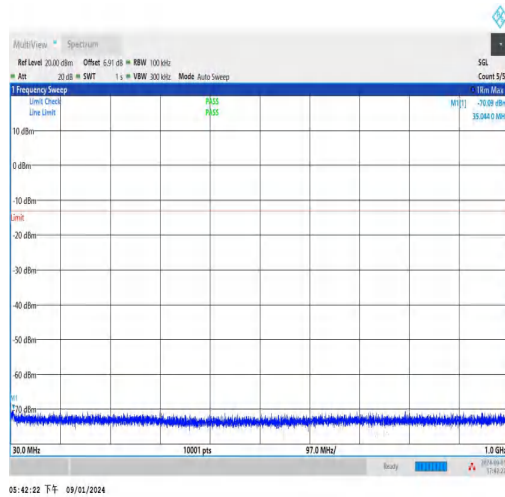
NTNV_N78-3700-3800_PC3_30_10_M_TID2_N/A
18000 40000 #1



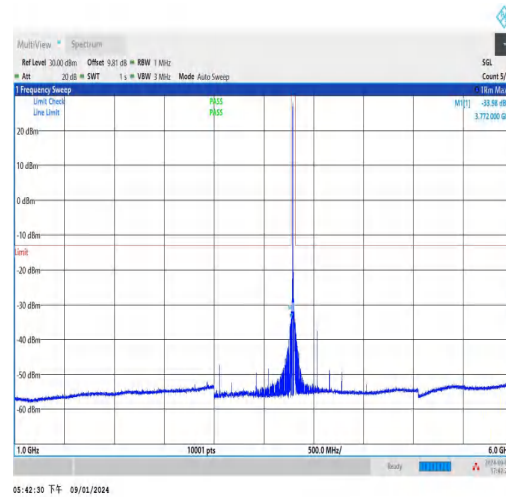
NTNV_N78-3700-3800_PC3_30_10_H_TID1_N/A
_0.009_0.15_#1



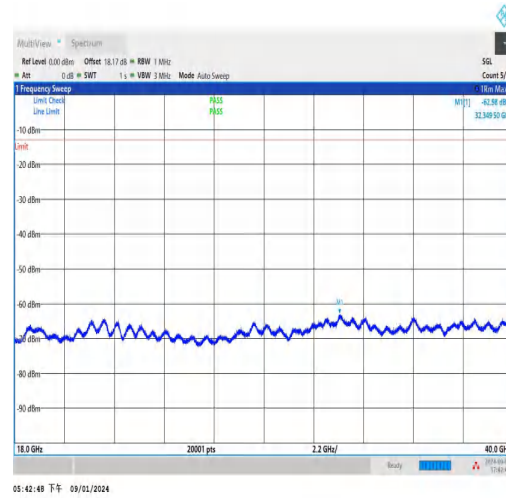
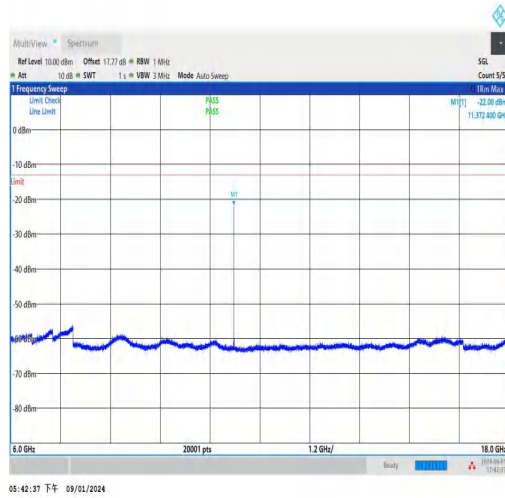
NTNV_N78-3700-3800_PC3_30_10_H_TID1_N/A
_0.15_30_#1



NTNV_N78-3700-3800_PC3_30_10_H_TID1_N/A
_30_1000_#1

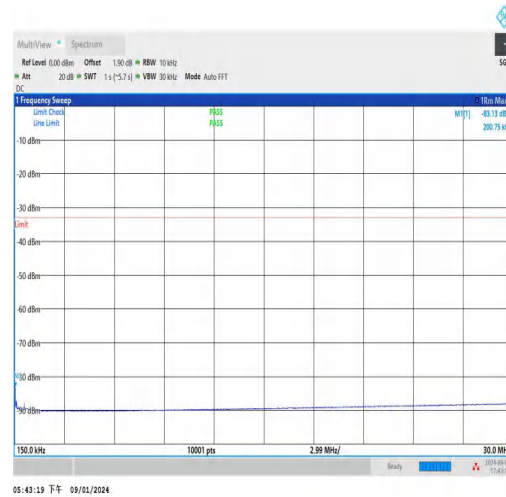
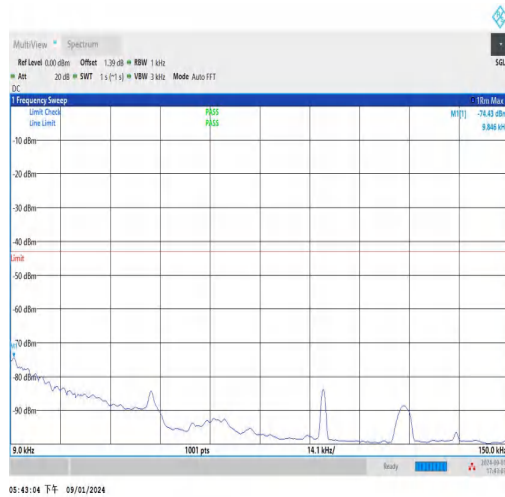


NTNV_N78-3700-3800_PC3_30_10_H_TID1_N/A
_1000_6000_#1



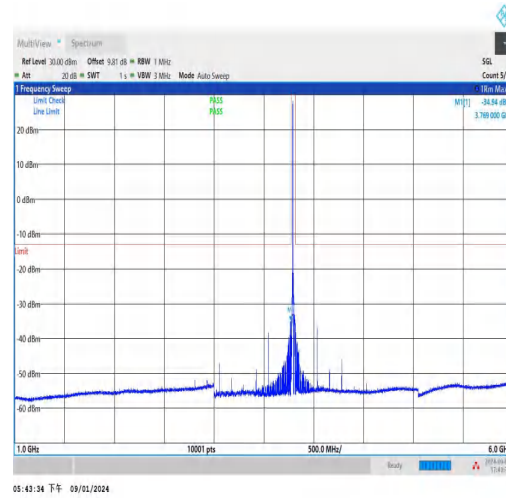
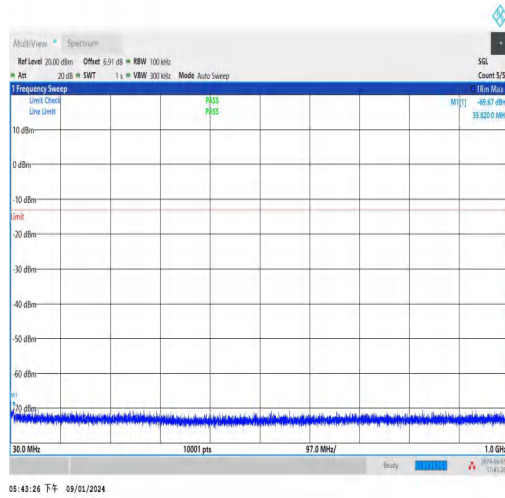
NTNV_N78-3700-3800_PC3_30_10_H_TID1_N/A
_6000_18000_#1

NTNV_N78-3700-3800_PC3_30_10_H_TID1_N/A
_18000_40000_#1



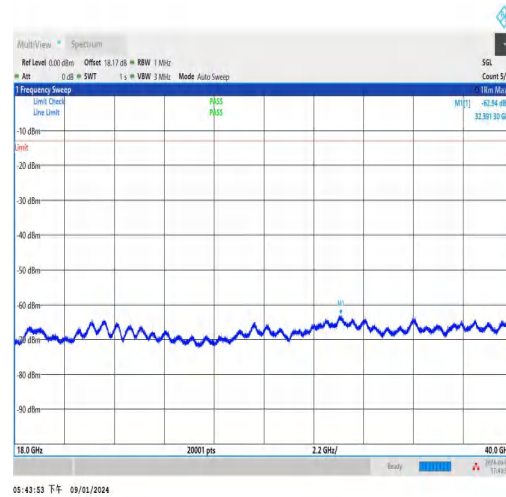
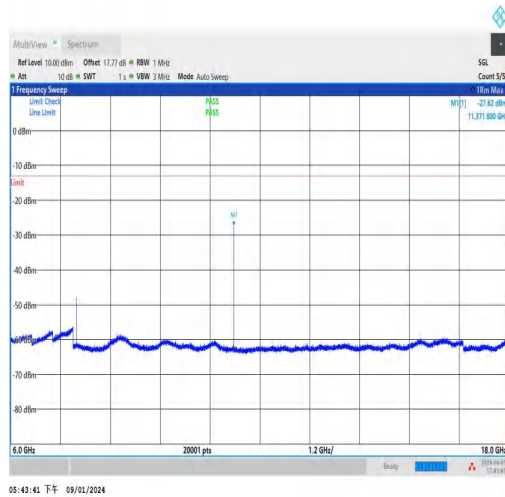
NTNV_N78-3700-3800_PC3_30_10_H_TID2_N/A
_0.009_0.15_#1

NTNV_N78-3700-3800_PC3_30_10_H_TID2_N/A
_0.15_30_#1



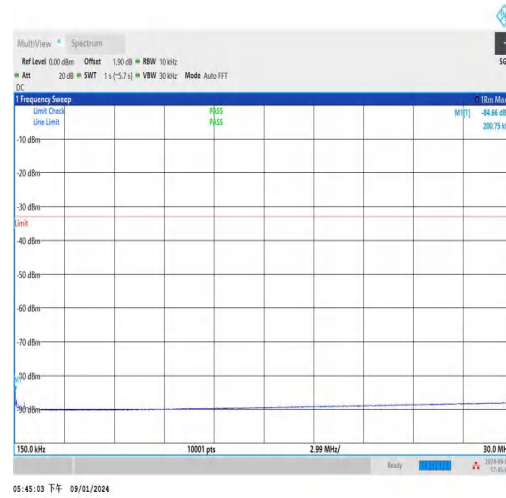
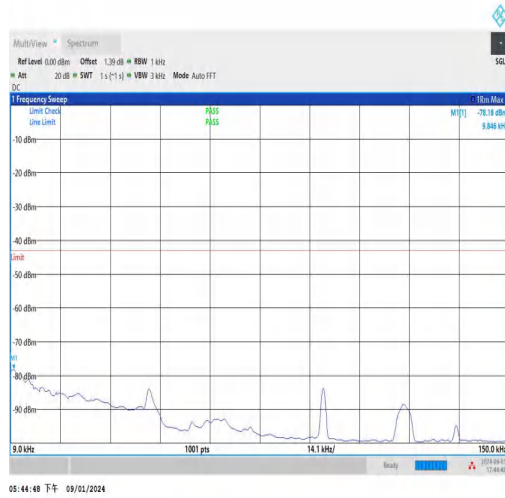
NTNV_N78-3700-3800_PC3_30_10_H_TID2_N/A
_30_1000_#1

NTNV_N78-3700-3800_PC3_30_10_H_TID2_N/A
_1000_6000_#1



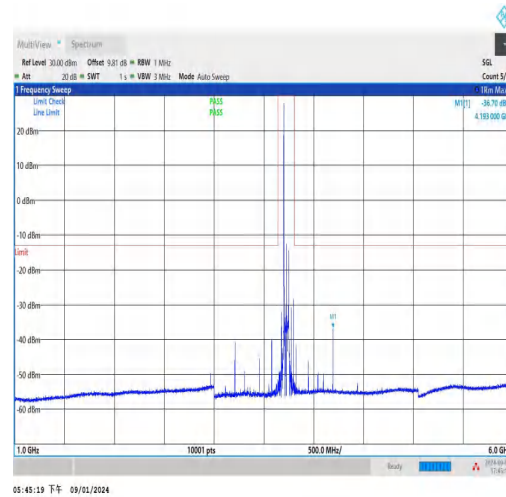
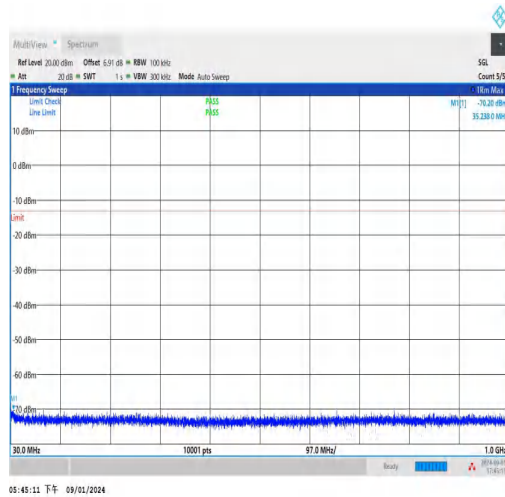
NTNV_N78-3700-3800_PC3_30_10_H_TID2_N/A
_6000_18000_#1

NTNV_N78-3700-3800_PC3_30_10_H_TID2_N/A
_18000_40000_#1



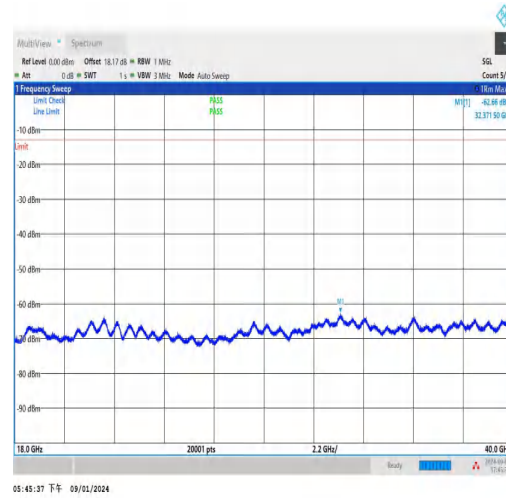
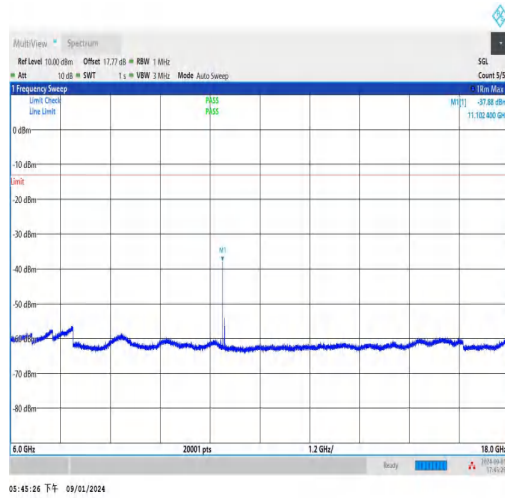
NTNV_N78-3700-3800_PC3_30_50_L_TID1_N/A
_0.009_0.15_#1

NTNV_N78-3700-3800_PC3_30_50_L_TID1_N/A
_0.15_30_#1



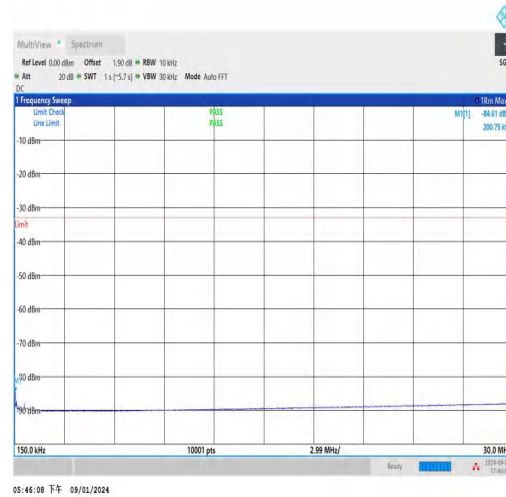
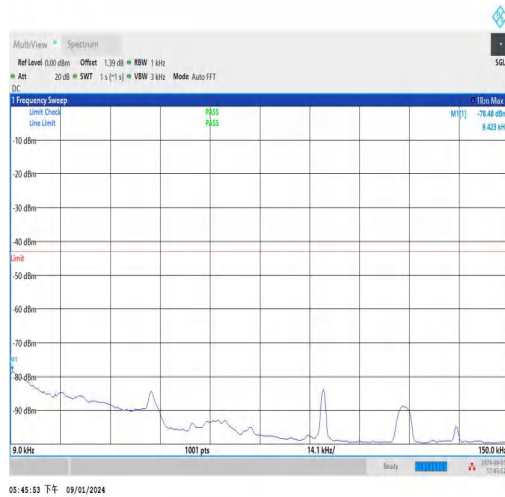
NTNV_N78-3700-3800_PC3_30_50_L_TID1_N/A
_30_1000_#1

NTNV_N78-3700-3800_PC3_30_50_L_TID1_N/A
_1000_6000_#1



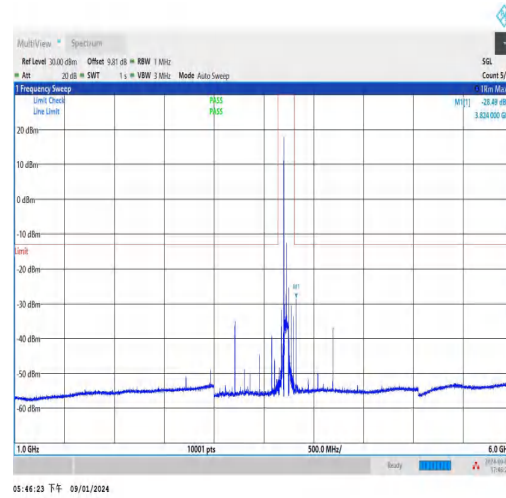
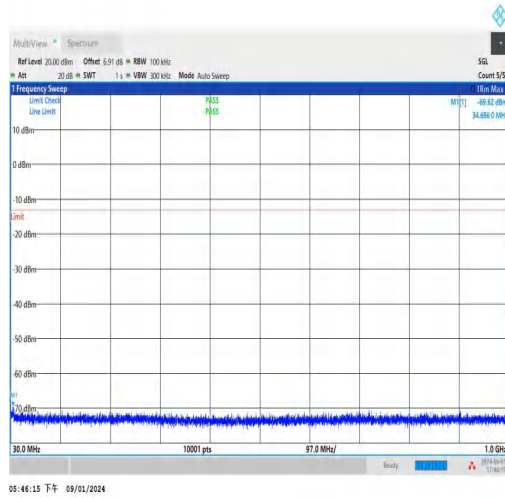
NTNV_N78-3700-3800_PC3_30_50_L_TID1_N/A
_6000_18000_#1

NTNV_N78-3700-3800_PC3_30_50_L_TID1_N/A
_18000_40000_#1



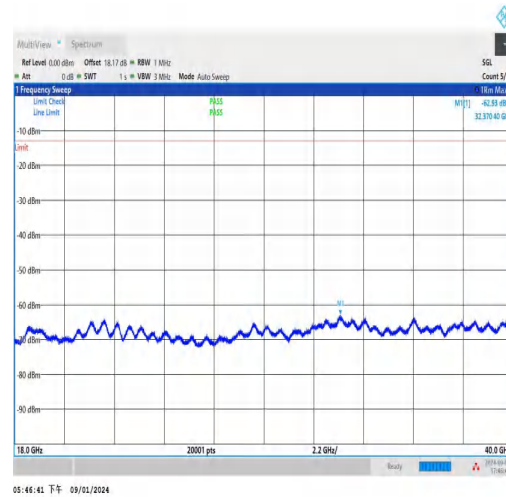
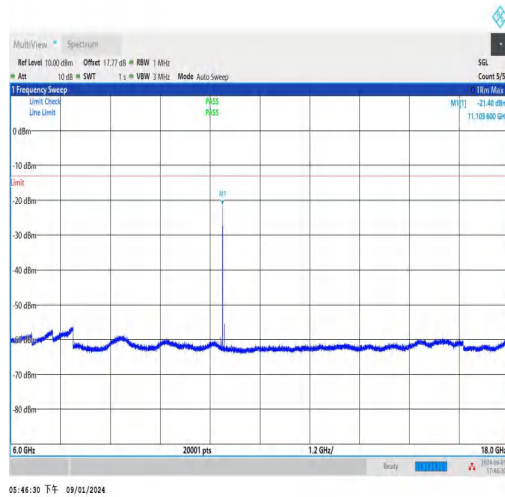
NTNV_N78-3700-3800_PC3_30_50_L_TID2_N/A
_0.009_0.15_#1

NTNV_N78-3700-3800_PC3_30_50_L_TID2_N/A
_0.15_30_#1



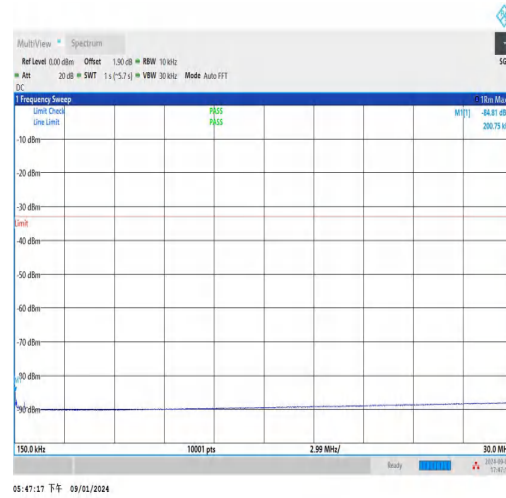
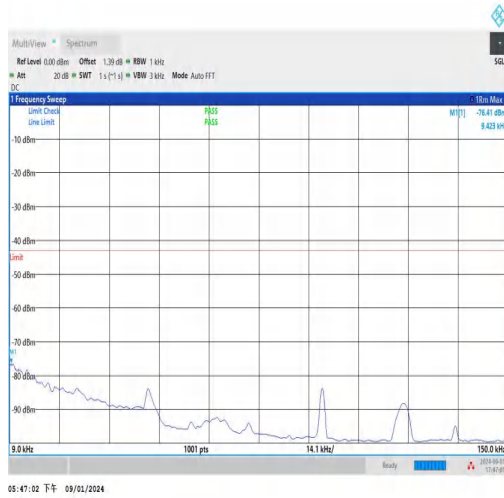
NTNV_N78-3700-3800_PC3_30_50_L_TID2_N/A
_30_1000_#1

NTNV_N78-3700-3800_PC3_30_50_L_TID2_N/A
_1000_6000_#1



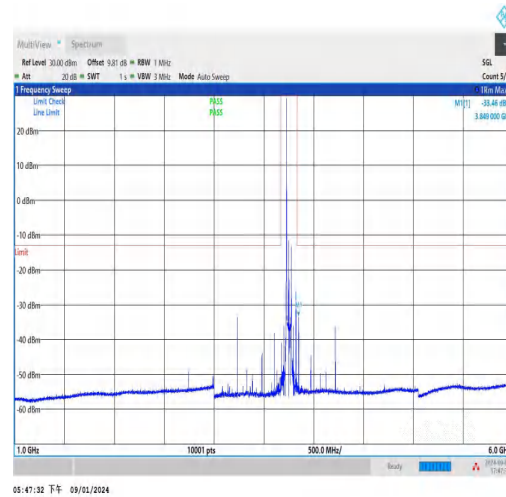
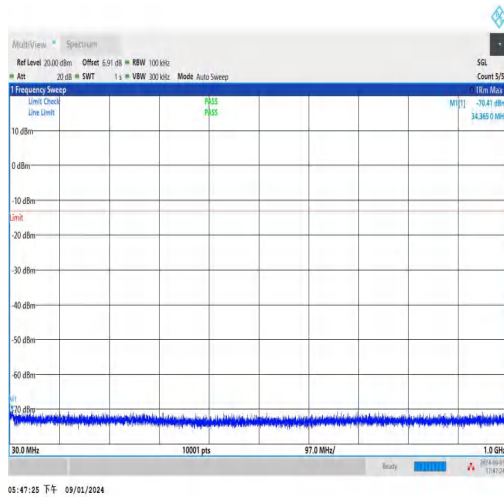
NTNV_N78-3700-3800_PC3_30_50_L_TID2_N/A
_6000_18000_#1

NTNV_N78-3700-3800_PC3_30_50_L_TID2_N/A
_18000_40000_#1



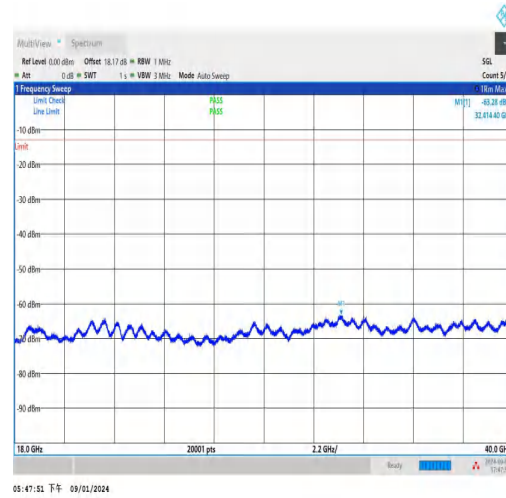
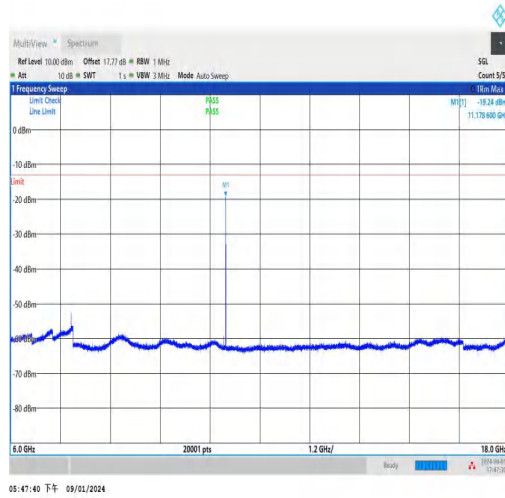
NTNV_N78-3700-3800_PC3_30_50_M_TID1_N/A
_0.009_0.15_#1

NTNV_N78-3700-3800_PC3_30_50_M_TID1_N/A
_0.15_30_#1



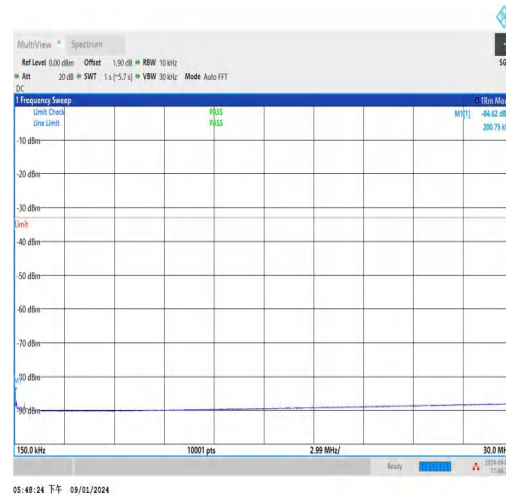
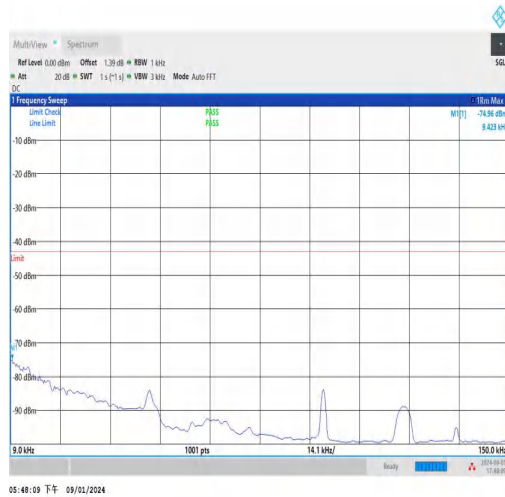
NTNV_N78-3700-3800_PC3_30_50_M_TID1_N/A
_30_1000_#1

NTNV_N78-3700-3800_PC3_30_50_M_TID1_N/A
_1000_6000_#1



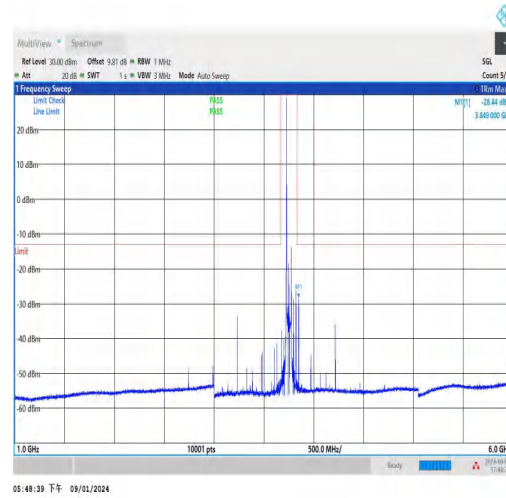
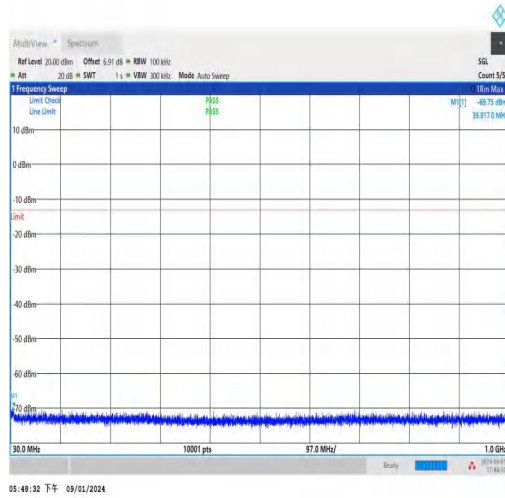
NTNV_N78-3700-3800_PC3_30_50_M_TID1_N/A
_6000_18000_#1

NTNV_N78-3700-3800_PC3_30_50_M_TID1_N/A
_18000_40000_#1



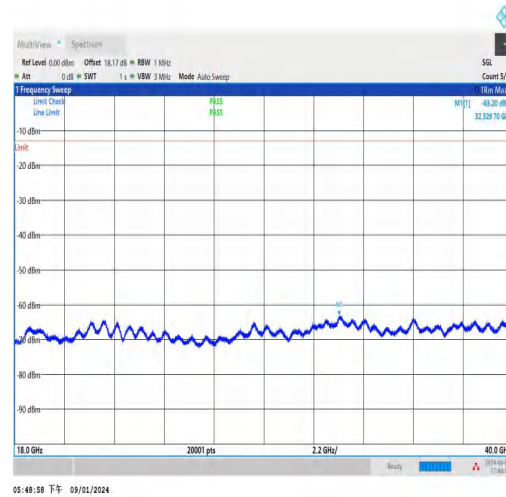
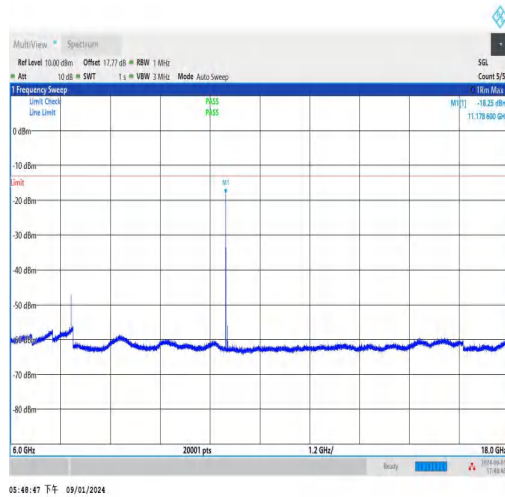
NTNV_N78-3700-3800_PC3_30_50_M_TID2_N/A
_0.009_0.15_#1

NTNV_N78-3700-3800_PC3_30_50_M_TID2_N/A
_0.15_30_#1



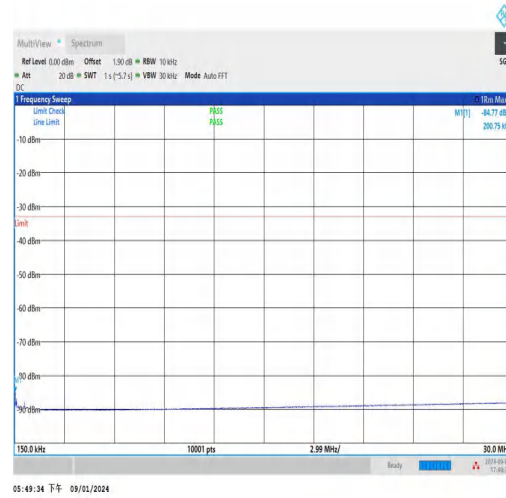
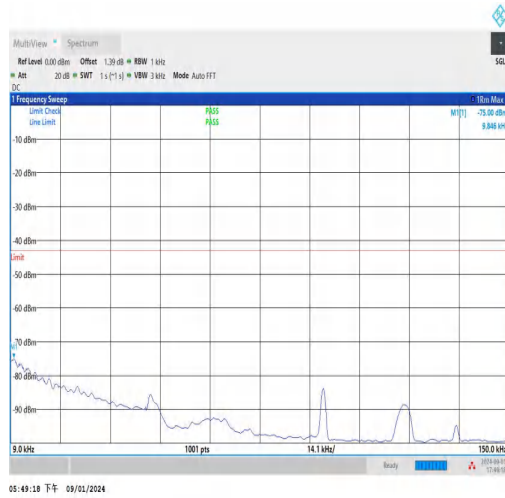
NTNV_N78-3700-3800_PC3_30_50_M_TID2_N/A
_30_1000_#1

NTNV_N78-3700-3800_PC3_30_50_M_TID2_N/A
_1000_6000_#1



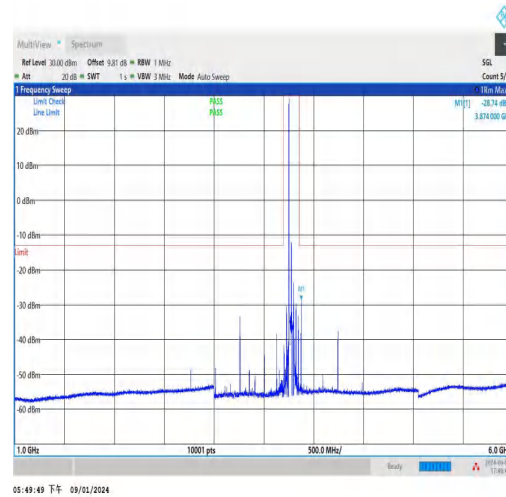
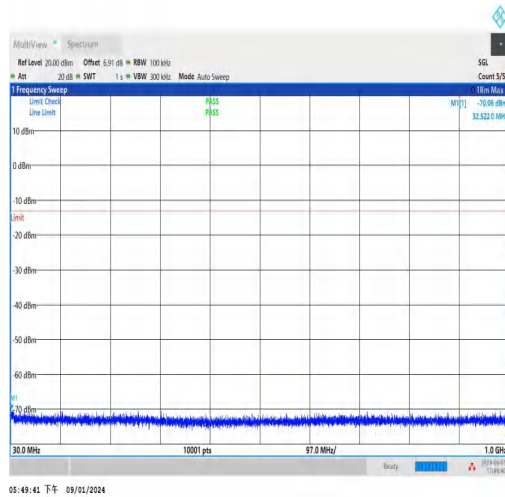
NTNV_N78-3700-3800_PC3_30_50_M_TID2_N/A
_6000_18000_#1

NTNV_N78-3700-3800_PC3_30_50_M_TID2_N/A
_18000_40000_#1



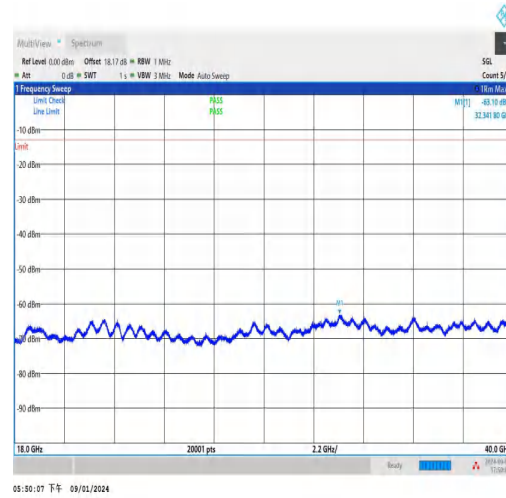
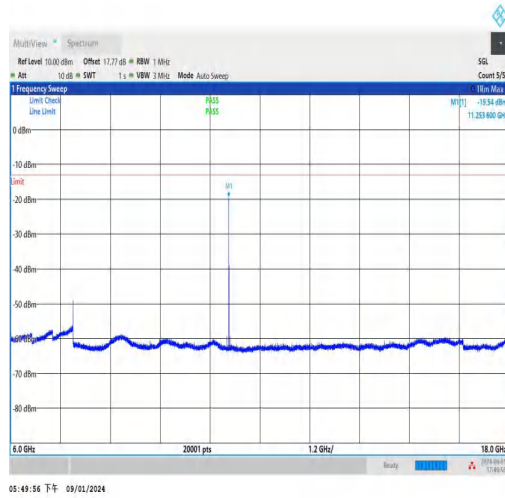
NTNV_N78-3700-3800_PC3_30_50_H_TID1_N/A
_0.009_0.15_#1

NTNV_N78-3700-3800_PC3_30_50_H_TID1_N/A
_0.15_30_#1



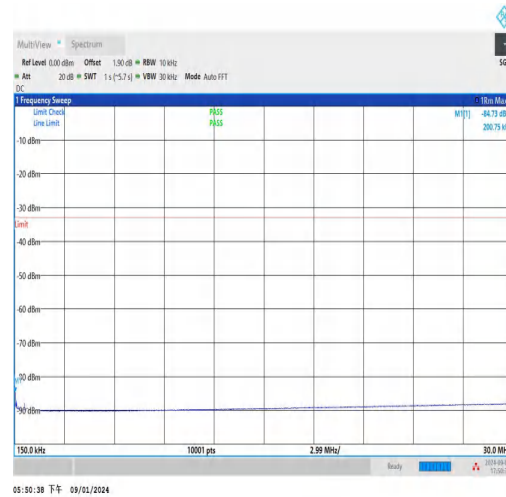
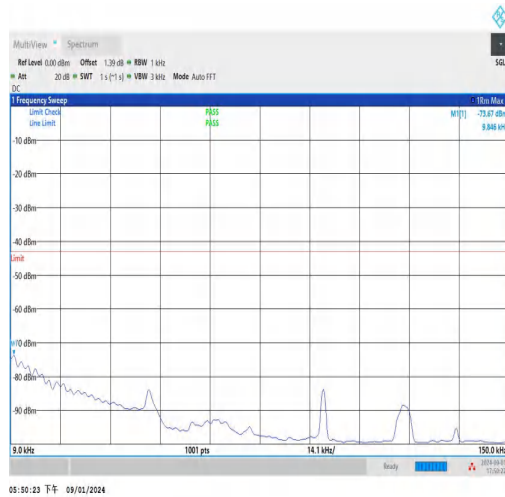
NTNV_N78-3700-3800_PC3_30_50_H_TID1_N/A
_30_1000_#1

NTNV_N78-3700-3800_PC3_30_50_H_TID1_N/A
_1000_6000_#1



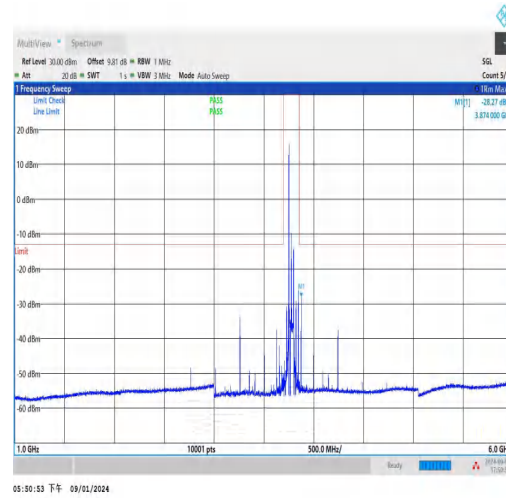
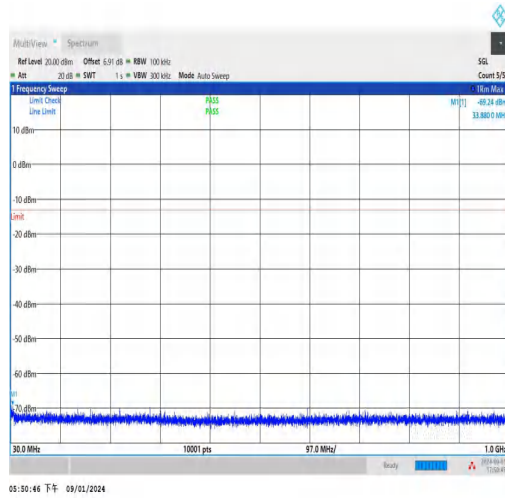
NTNV_N78-3700-3800_PC3_30_50_H_TID1_N/A
_6000_18000_#1

NTNV_N78-3700-3800_PC3_30_50_H_TID1_N/A
_18000_40000_#1



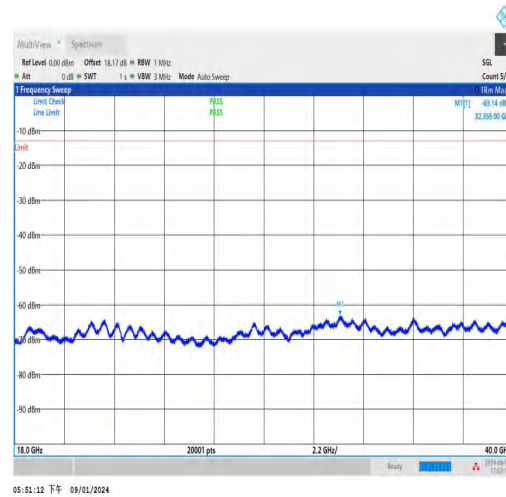
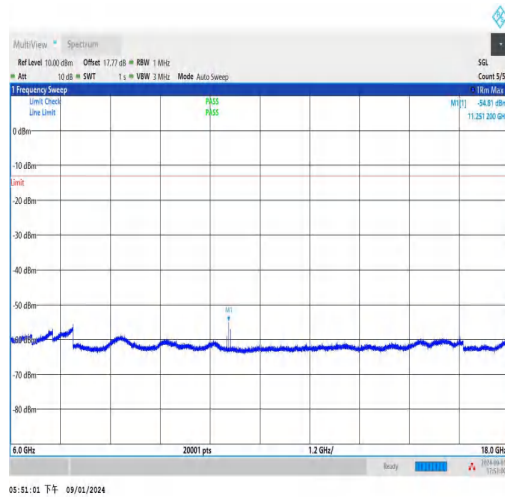
NTNV_N78-3700-3800_PC3_30_50_H_TID2_N/A
_0.009_0.15_#1

NTNV_N78-3700-3800_PC3_30_50_H_TID2_N/A
_0.15_30_#1



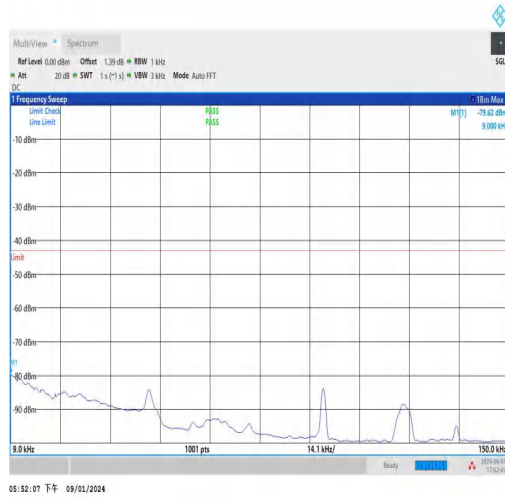
NTNV_N78-3700-3800_PC3_30_50_H_TID2_N/A
_30_1000_#1

NTNV_N78-3700-3800_PC3_30_50_H_TID2_N/A
_1000_6000_#1

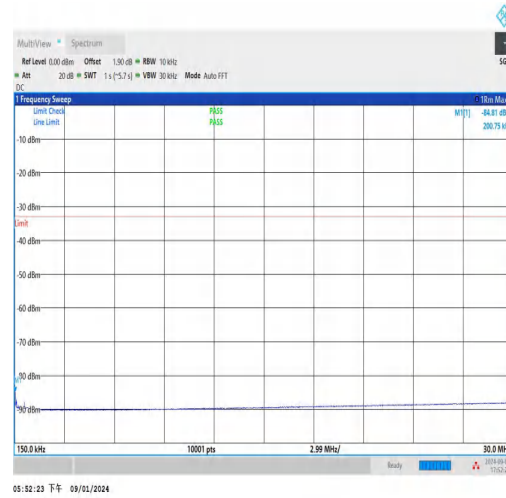


NTNV_N78-3700-3800_PC3_30_50_H_TID2_N/A
_6000_18000_#1

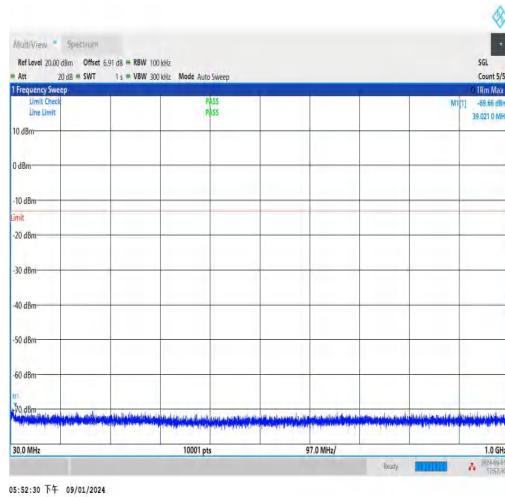
NTNV_N78-3700-3800_PC3_30_50_H_TID2_N/A
_18000_40000_#1



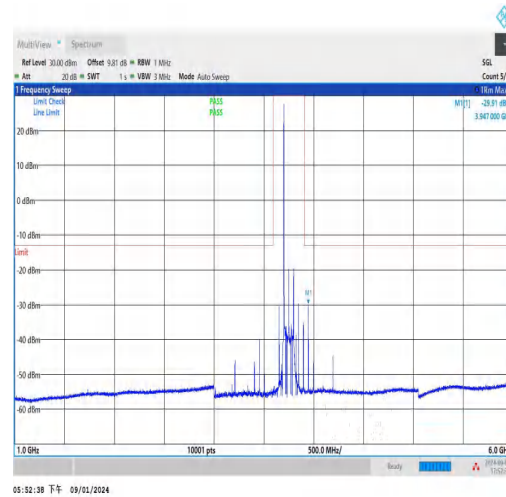
NTNV_N78-3700-3800_PC3_30_100_L_TID1_N/
A_0.009_0.15_#1



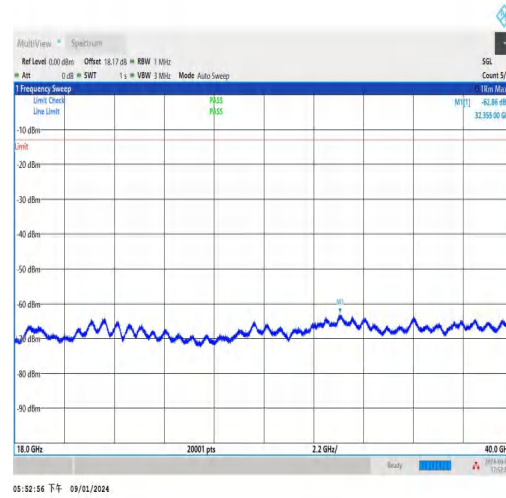
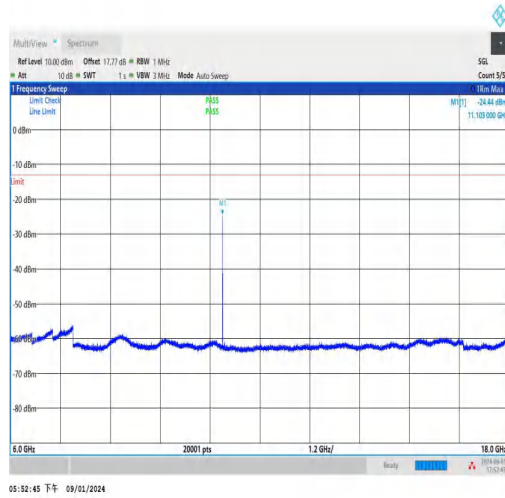
NTNV_N78-3700-3800_PC3_30_100_L_TID1_N/
A_0.15_30_#1



NTNV_N78-3700-3800_PC3_30_100_L_TID1_N/
A_30_1000_#1

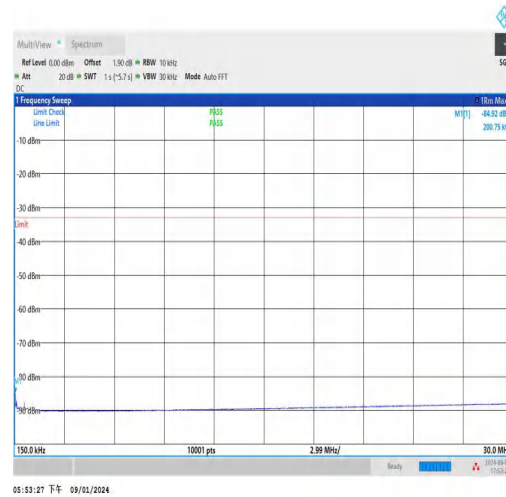
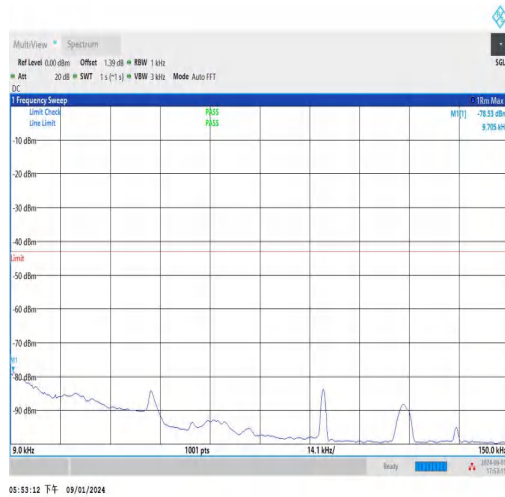


NTNV_N78-3700-3800_PC3_30_100_L_TID1_N/
A_1000_6000_#1



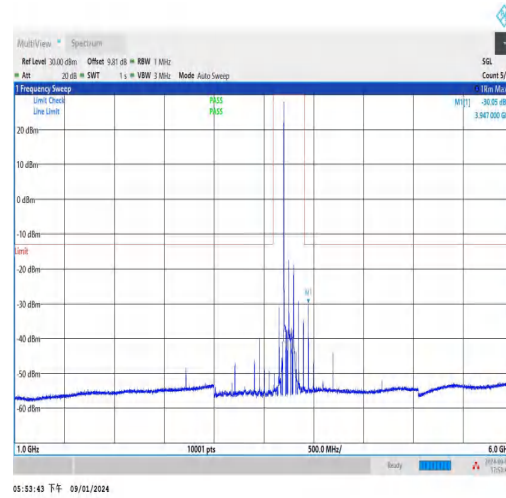
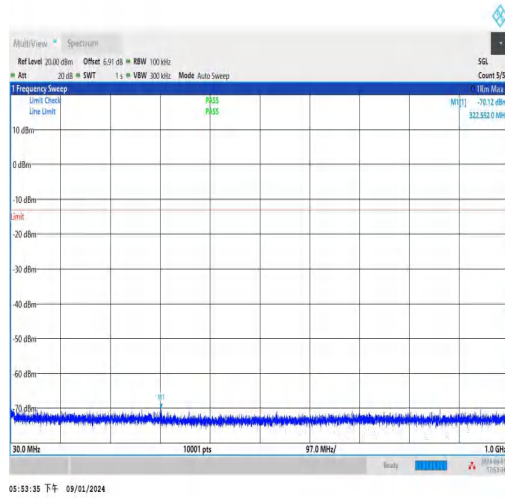
NTNV_N78-3700-3800_PC3_30_100_L_TID1_N/
A_6000_18000_#1

NTNV_N78-3700-3800_PC3_30_100_L_TID1_N/
A_18000_40000_#1



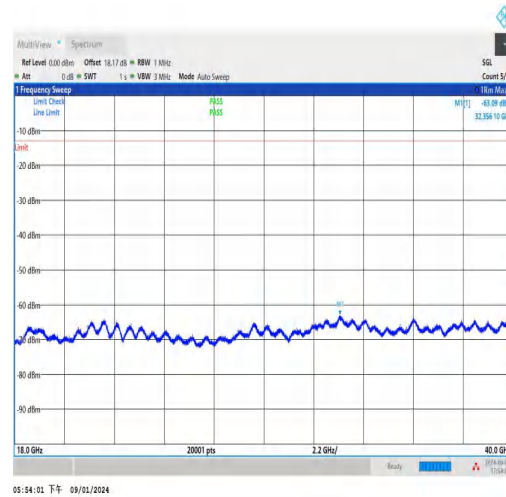
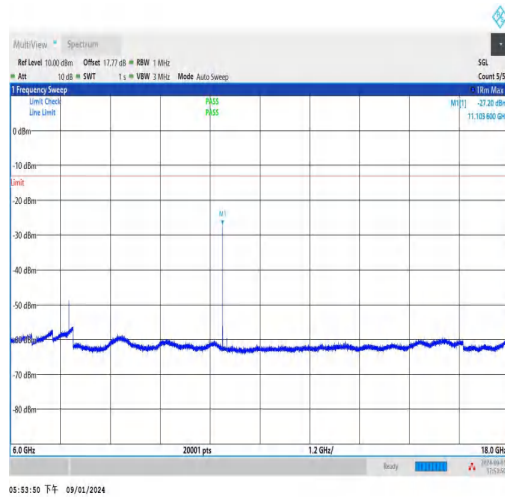
NTNV_N78-3700-3800_PC3_30_100_L_TID2_N/
A_0.009_0.15_#1

NTNV_N78-3700-3800_PC3_30_100_L_TID2_N/
A_0.15_30_#1



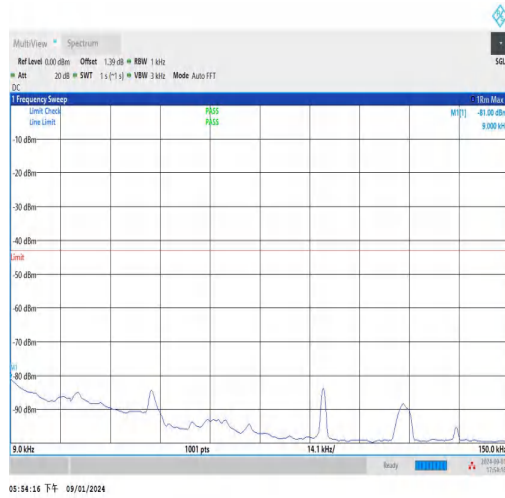
NTNV_N78-3700-3800_PC3_30_100_L_TID2_N/
A_30_1000_#1

NTNV_N78-3700-3800_PC3_30_100_L_TID2_N/
A_1000_6000_#1

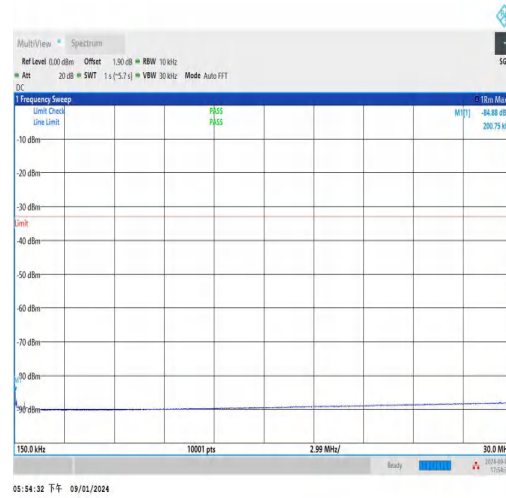


NTNV_N78-3700-3800_PC3_30_100_L_TID2_N/
A_6000_18000_#1

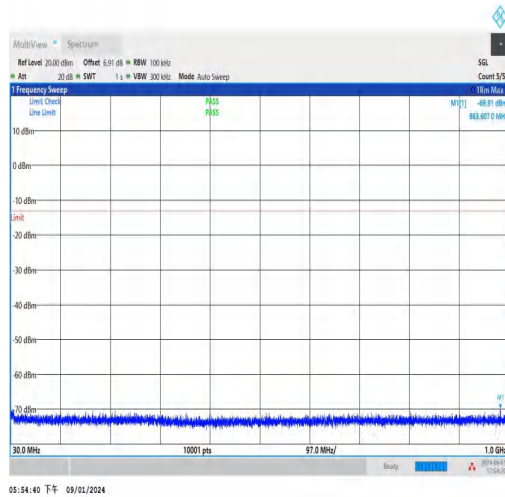
NTNV_N78-3700-3800_PC3_30_100_L_TID2_N/
A_18000_40000_#1



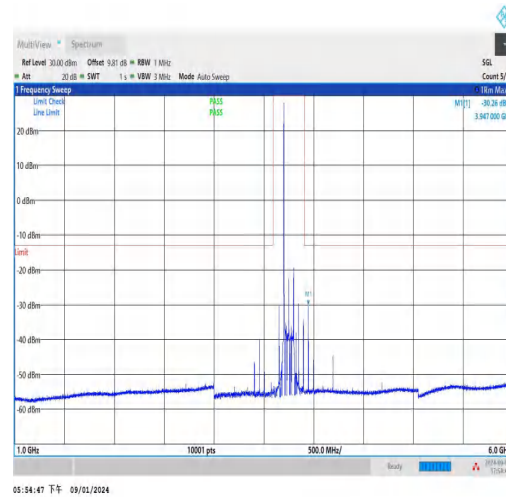
NTNV_N78-3700-3800_PC3_30_100_M_TID1_N/
A_0.009_0.15_#1



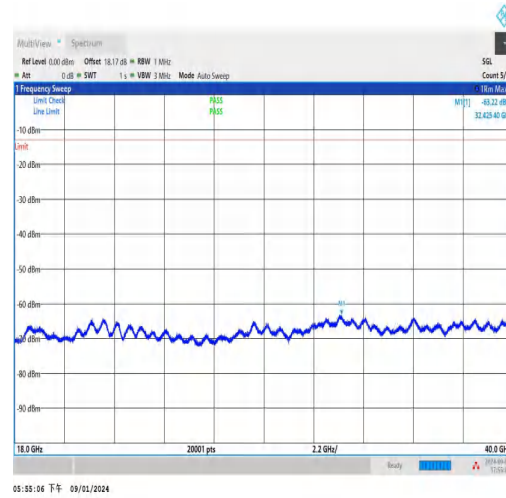
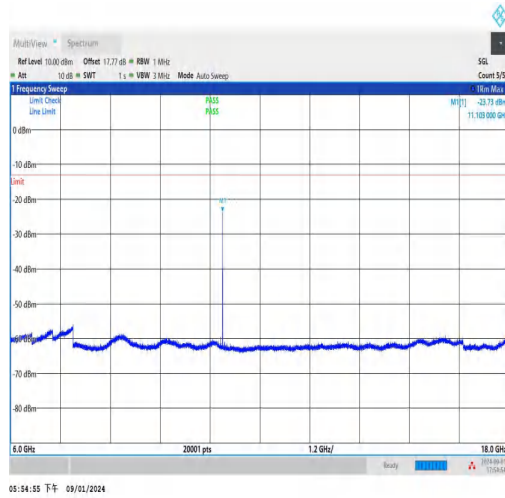
NTNV_N78-3700-3800_PC3_30_100_M_TID1_N/
A_0.15_30_#1



NTNV_N78-3700-3800_PC3_30_100_M_TID1_N/
A_30_1000_#1

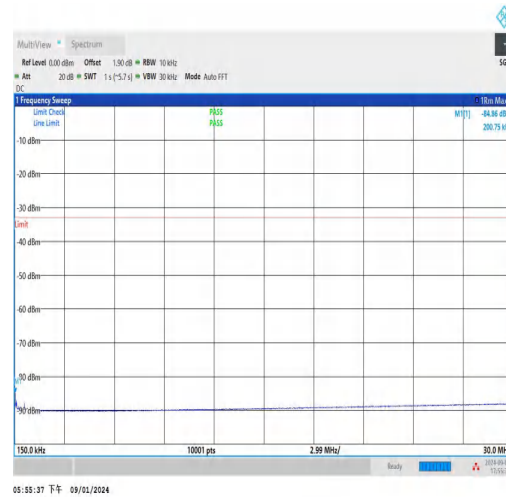
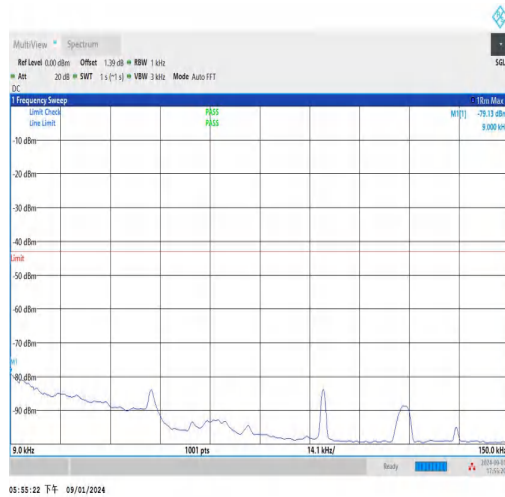


NTNV_N78-3700-3800_PC3_30_100_M_TID1_N/
A_1000_6000_#1



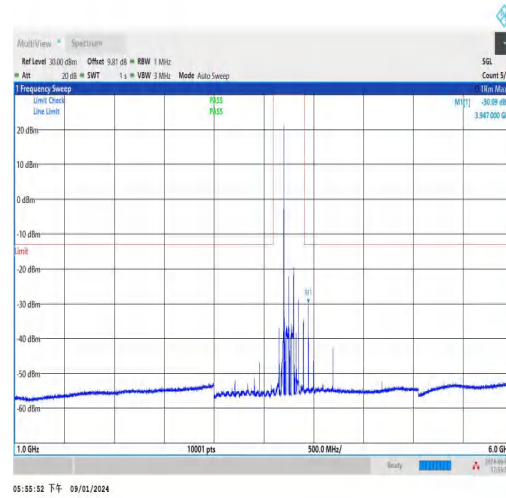
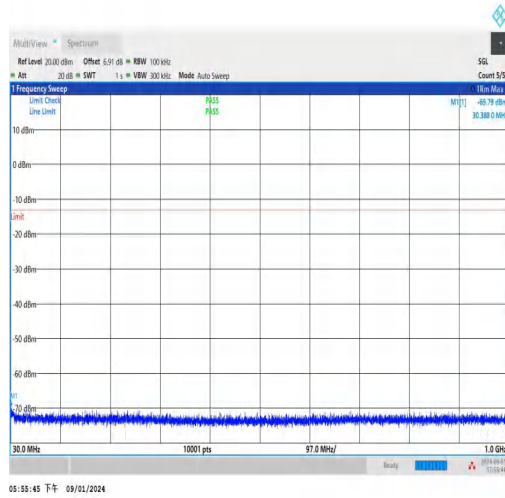
NTNV_N78-3700-3800_PC3_30_100_M_TID1_N/
A_6000_18000_#1

NTNV_N78-3700-3800_PC3_30_100_M_TID1_N/
A_18000_40000_#1



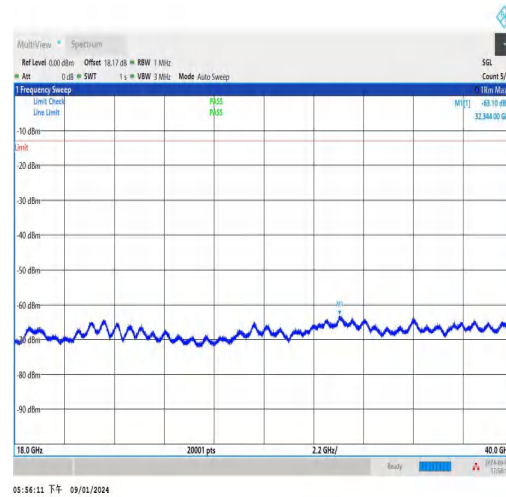
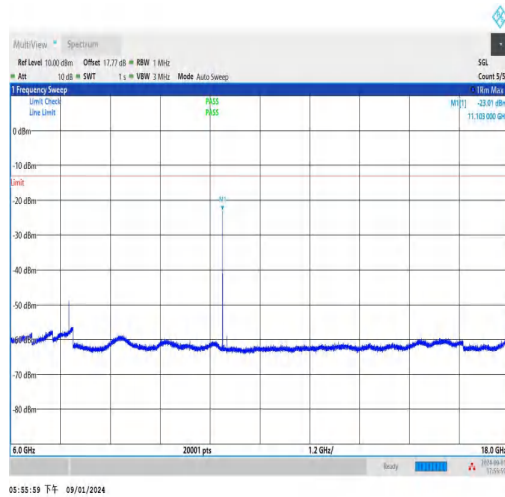
NTNV_N78-3700-3800_PC3_30_100_M_TID2_N/
A_0.009_0.15_#1

NTNV_N78-3700-3800_PC3_30_100_M_TID2_N/
A_0.15_30_#1



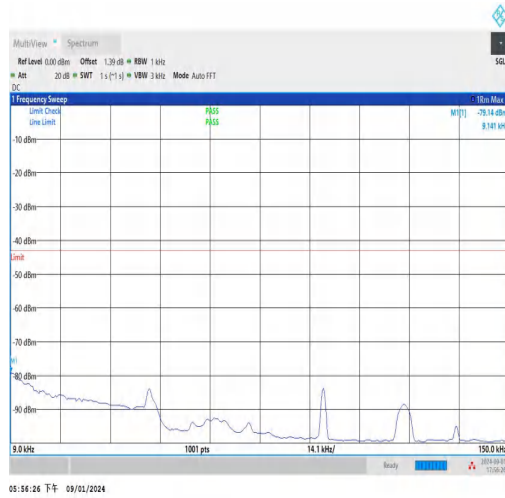
NTNV_N78-3700-3800_PC3_30_100_M_TID2_N/
A_30_1000_#1

NTNV_N78-3700-3800_PC3_30_100_M_TID2_N/
A_1000_6000_#1

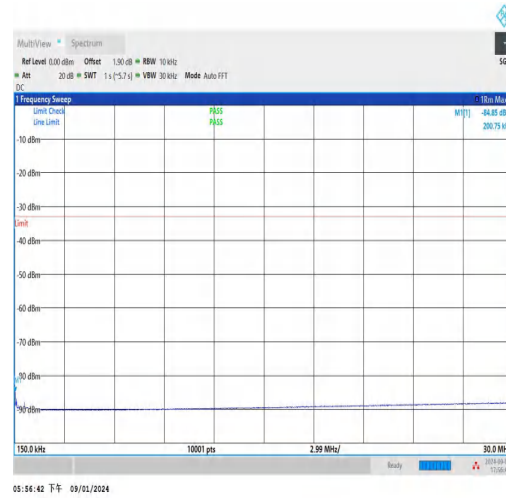


NTNV_N78-3700-3800_PC3_30_100_M_TID2_N/
A_6000_18000_#1

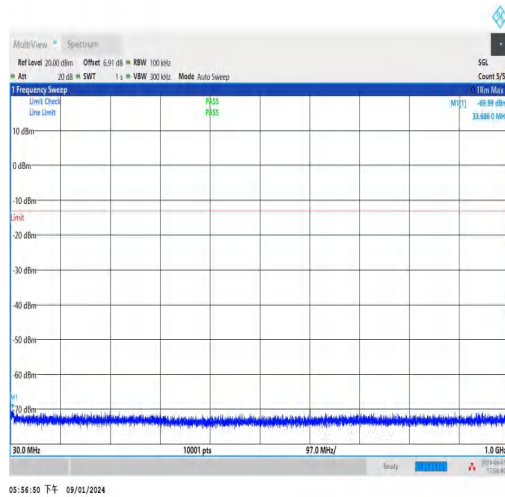
NTNV_N78-3700-3800_PC3_30_100_M_TID2_N/
A_18000_40000_#1



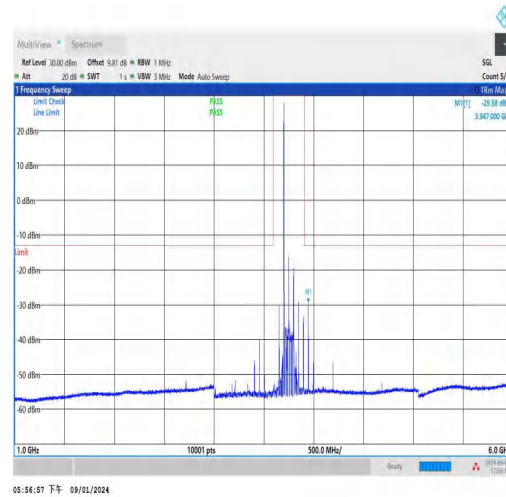
NTNV_N78-3700-3800_PC3_30_100_H_TID1_N/
A_0.009_0.15_#1



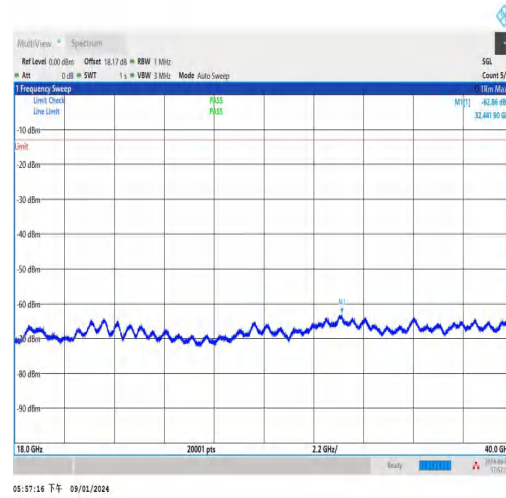
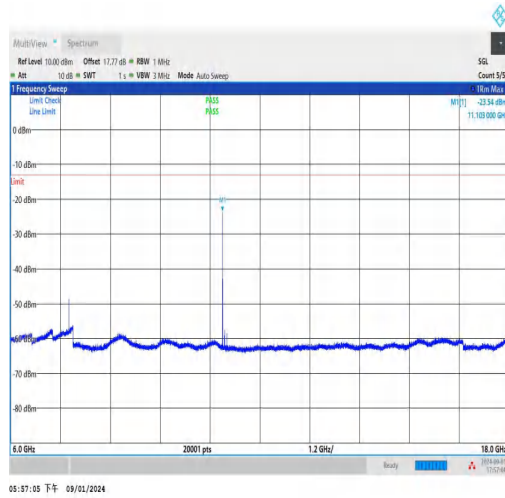
NTNV_N78-3700-3800_PC3_30_100_H_TID1_N/
A_0.15_30_#1



NTNV_N78-3700-3800_PC3_30_100_H_TID1_N/
A_30_1000_#1

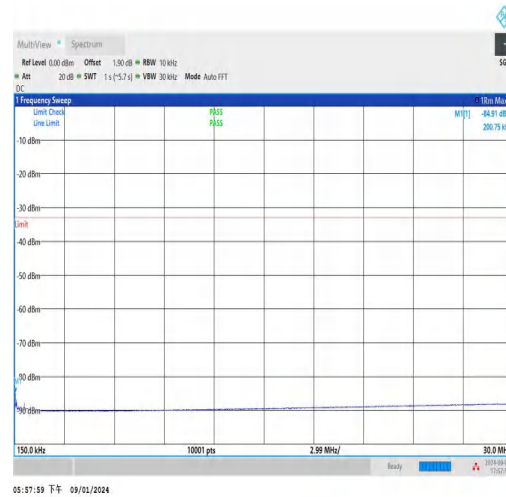
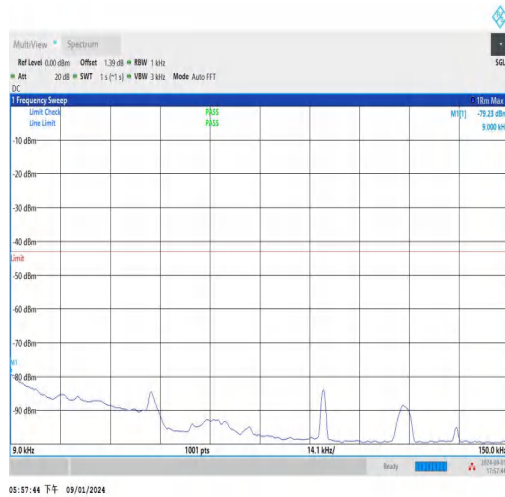


NTNV_N78-3700-3800_PC3_30_100_H_TID1_N/
A_1000_6000_#1



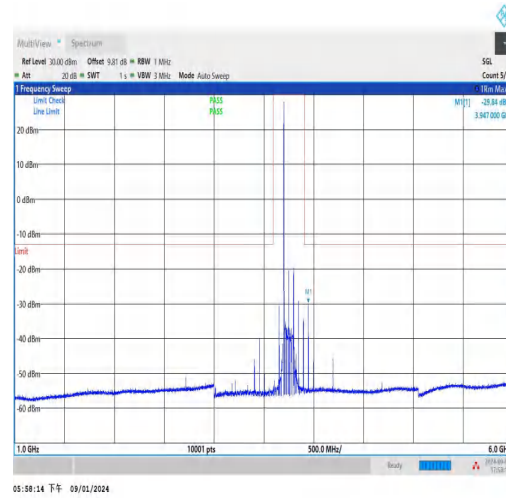
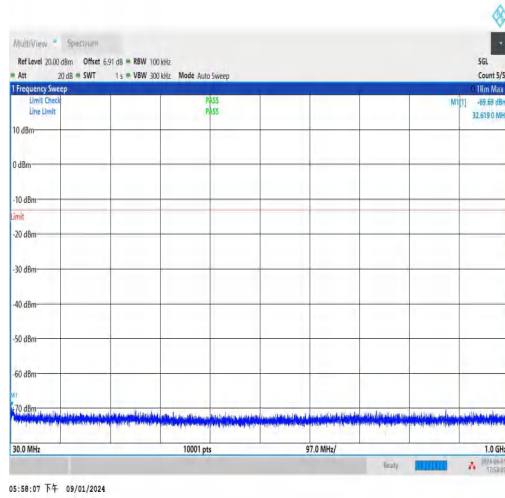
NTNV_N78-3700-3800_PC3_30_100_H_TID1_N/
A_6000_18000_#1

NTNV_N78-3700-3800_PC3_30_100_H_TID1_N/
A_18000_40000_#1



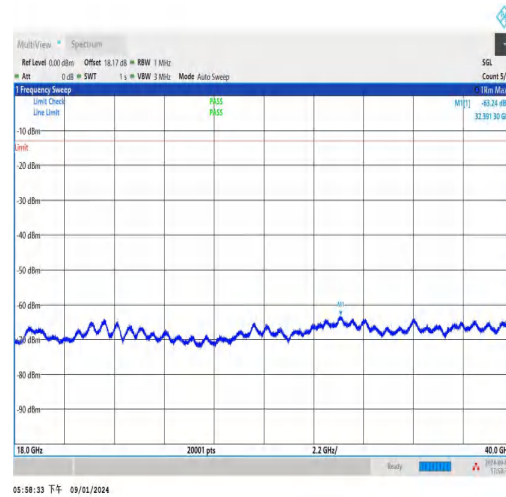
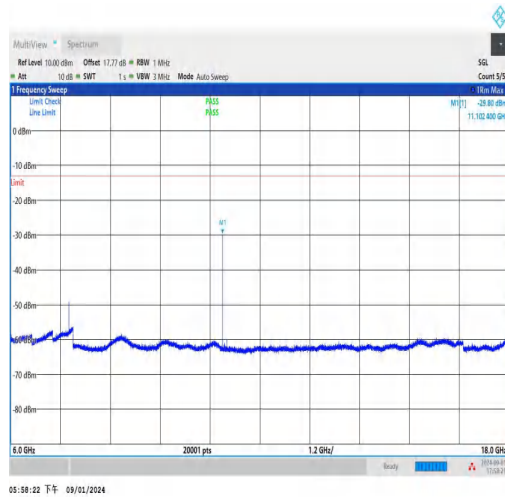
NTNV_N78-3700-3800_PC3_30_100_H_TID2_N/
A_0.009_0.15_#1

NTNV_N78-3700-3800_PC3_30_100_H_TID2_N/
A_0.15_30_#1



NTNV_N78-3700-3800_PC3_30_100_H_TID2_N/
A_30_1000_#1

NTNV_N78-3700-3800_PC3_30_100_H_TID2_N/
A_1000_6000_#1



NTNV_N78-3700-3800_PC3_30_100_H_TID2_N/
A_6000_18000_#1

NTNV_N78-3700-3800_PC3_30_100_H_TID2_N/
A_18000_40000_#1



Appendix F: Frequency Stability

Test Result

Frequency Error VS. Voltage

Voltage										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation (Hz)	Deviation (ppm)	Verdict
N78-3700-3800	30	100	DFT-PI2BPSK	L	Outer_Full	VN	NT	11.00	0.00	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	L	Outer_Full	VL	NT	1.00	0.00	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	L	Outer_Full	VH	NT	14.00	0.00	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	VN	NT	17.00	0.00	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	VL	NT	4.00	0.00	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	VH	NT	17.00	0.00	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Outer_Full	VN	NT	1.00	0.00	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Outer_Full	VL	NT	1.00	0.00	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Outer_Full	VH	NT	18.00	0.00	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_Full	VN	NT	10.00	0.00	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_Full	VL	NT	11.00	0.00	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_Full	VH	NT	8.00	0.00	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Outer_Full	VN	NT	8.00	0.00	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Outer_Full	VL	NT	17.00	0.00	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Outer_Full	VH	NT	14.00	0.00	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	VN	NT	16.00	0.00	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	VL	NT	17.00	0.00	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	VH	NT	7.00	0.00	PASS

Frequency Error VS. Temperature

Temperature										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation (Hz)	Deviation (ppm)	Verdict
N78-3700-3800	30	100	DFT-PI2BPSK	L	Outer_Full	NV	-30	5.000000	0.001333	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	L	Outer_Full	NV	-20	2.000000	0.000533	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	L	Outer_Full	NV	-10	18.000000	0.004800	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	L	Outer_Full	NV	0	9.000000	0.002400	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	L	Outer_Full	NV	10	18.000000	0.004800	PASS



N78-3700-3800	30	100	DFT-PI2BPSK	L	Outer_Full	NV	20	8.000000	0.002133	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	L	Outer_Full	NV	30	9.000000	0.002400	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	L	Outer_Full	NV	40	18.000000	0.004800	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	L	Outer_Full	NV	50	9.000000	0.002400	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	NV	-30	7.000000	0.001867	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	NV	-20	4.000000	0.001067	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	NV	-10	2.000000	0.000533	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	NV	0	11.000000	0.002933	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	NV	10	1.000000	0.000267	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	NV	20	2.000000	0.000533	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	NV	30	18.000000	0.004800	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	NV	40	9.000000	0.002400	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	NV	50	18.000000	0.004800	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Outer_Full	NV	-30	1.000000	0.000267	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Outer_Full	NV	-20	18.000000	0.004800	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Outer_Full	NV	-10	8.000000	0.002133	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Outer_Full	NV	0	8.000000	0.002133	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Outer_Full	NV	10	18.000000	0.004800	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Outer_Full	NV	20	8.000000	0.002133	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Outer_Full	NV	30	18.000000	0.004800	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Outer_Full	NV	40	18.000000	0.004800	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	M	Outer_Full	NV	50	9.000000	0.002400	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_Full	NV	-30	17.000000	0.004533	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_Full	NV	-20	8.000000	0.002133	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_Full	NV	-10	17.000000	0.004533	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_Full	NV	0	18.000000	0.004800	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_Full	NV	10	5.000000	0.001333	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_Full	NV	20	5.000000	0.001333	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_Full	NV	30	3.000000	0.000800	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_Full	NV	40	5.000000	0.001333	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_Full	NV	50	3.000000	0.000800	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Outer_Full	NV	-30	5.000000	0.001333	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Outer_Full	NV	-20	2.000000	0.000533	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Outer_Full	NV	-10	12.000000	0.003200	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Outer_Full	NV	0	12.000000	0.003200	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Outer_Full	NV	10	3.000000	0.000800	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Outer_Full	NV	20	12.000000	0.003200	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Outer_Full	NV	30	2.000000	0.000533	PASS
N78-3700-3800	30	100	DFT-PI2BPSK	H	Outer_Full	NV	40	3.000000	0.000800	PASS



N78-3700-3800	30	100	DFT-PI2BPSK	H	Outer_Full	NV	50	12.000000	0.003200	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	NV	-30	1.000000	0.000267	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	NV	-20	2.000000	0.000533	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	NV	-10	11.000000	0.002933	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	NV	0	2.000000	0.000533	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	NV	10	6.000000	0.001600	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	NV	20	15.000000	0.004000	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	NV	30	6.000000	0.001600	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	NV	40	15.000000	0.004000	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	NV	50	16.000000	0.004267	PASS

-----END-----