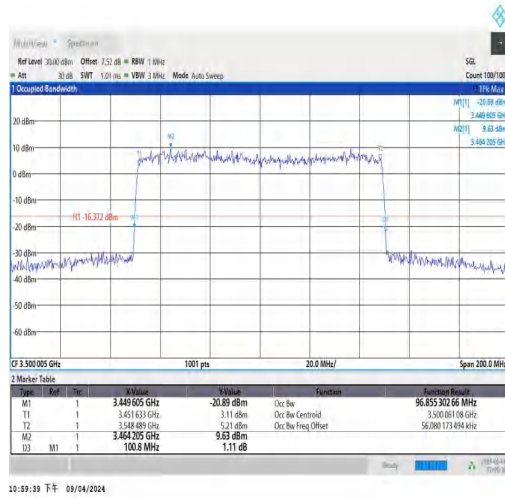




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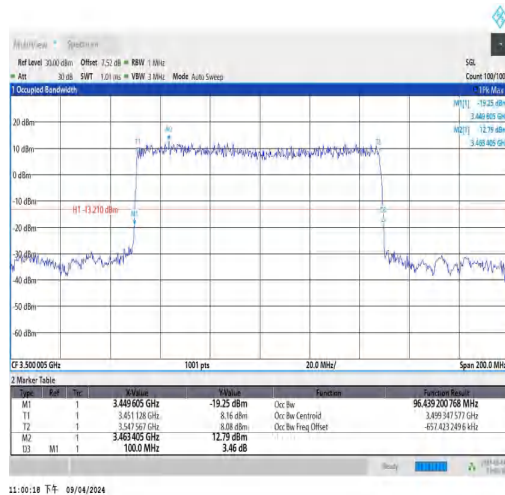
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NTNV_N77-3450-3550_PC3_30_100_M_TID6_A

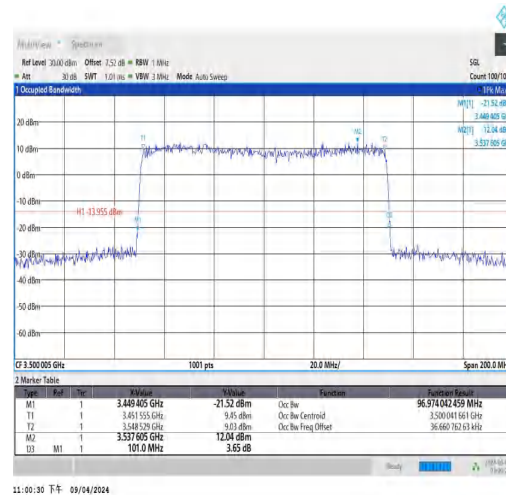
nt1#1

NTNV_N77-3450-3550_PC3_30_100_H_TID1_An

t1#1



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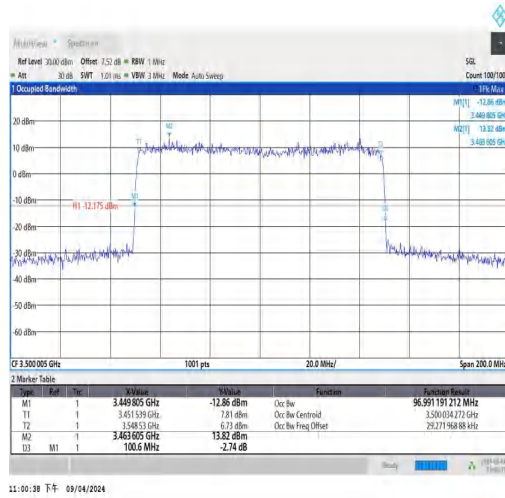
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t1#1

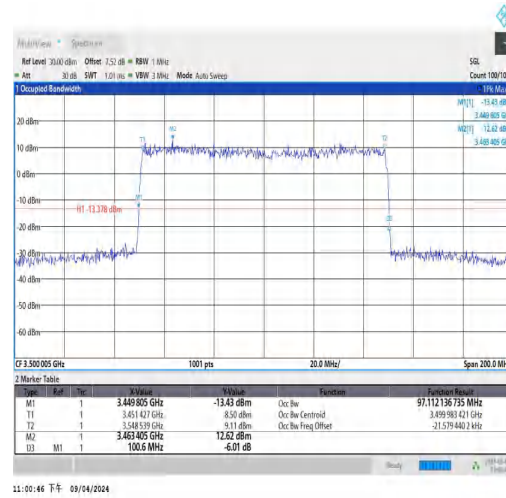
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t1#1



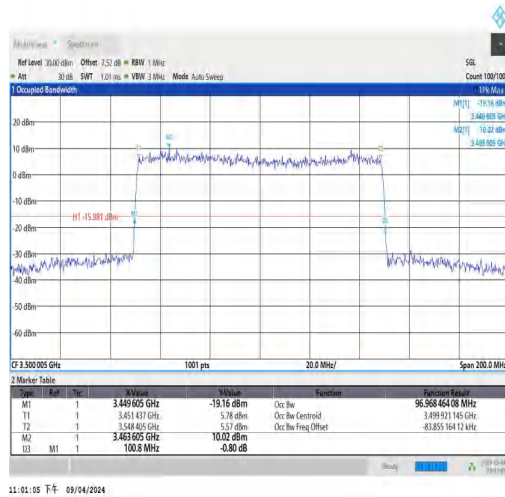
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t1#1



NTNV_N77-3450-3550_PC3_30_100_H_TID5_An

t1#1



NTNV_N77-3450-3550_PC3_30_100_H_TID6_An

t1#1



Appendix D: Band Edge

Test Result

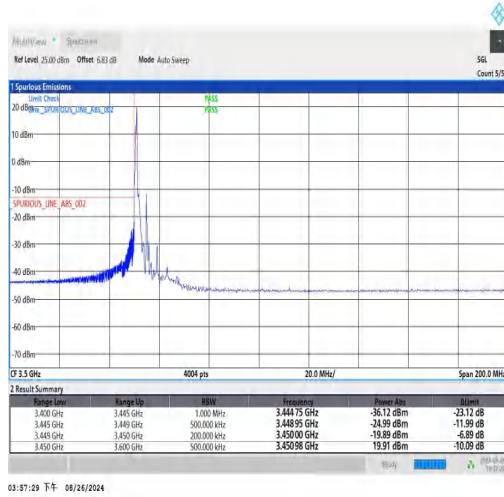
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
N77-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Outer_Full	see graph	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Outer_Full	see graph	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N77-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N77-3450-3550	30	50	DFT-QPSK	L	Outer_Full	see graph	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Outer_Full	see graph	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Outer_Full	see graph	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Outer_Full	see graph	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Outer_Full	see graph	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Outer_Full	see graph	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	L	Outer_Full	see graph	PASS



N77-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N77-3450-3550	30	50	DFT-QPSK	L	Outer_Full	see graph	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Outer_Full	see graph	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Outer_Full	see graph	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Outer_Full	see graph	PASS

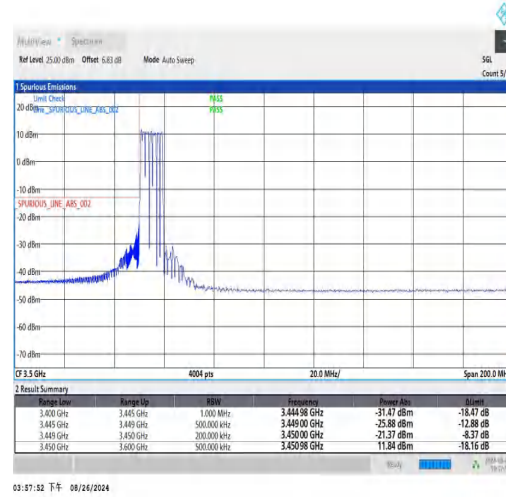


Test Graphs



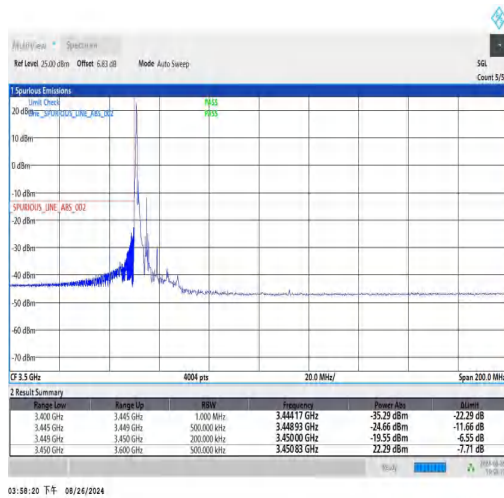
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1#1



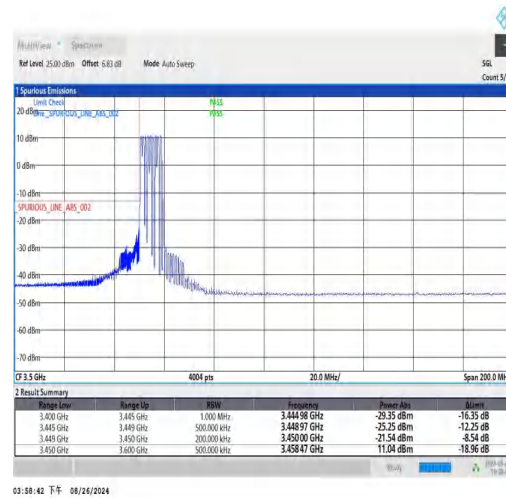
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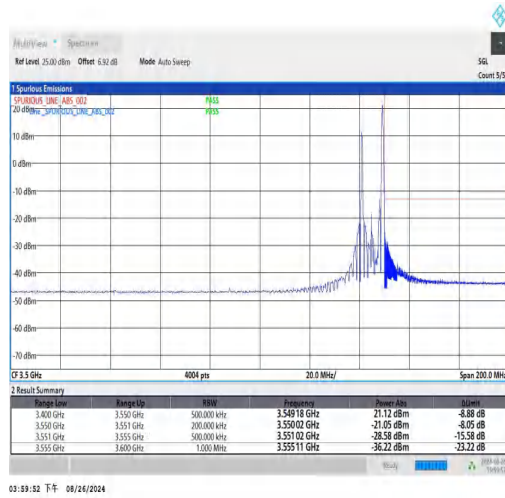
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NTNV_N77-3450-3550_PC2_30_10_L_TID6_RID

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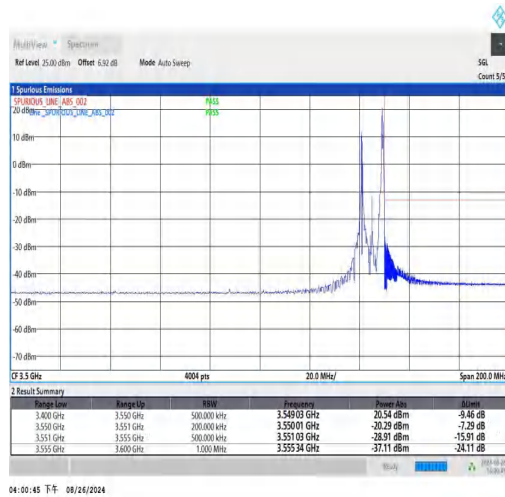
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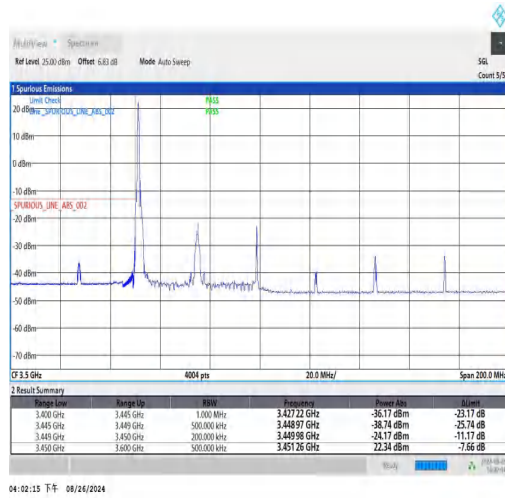
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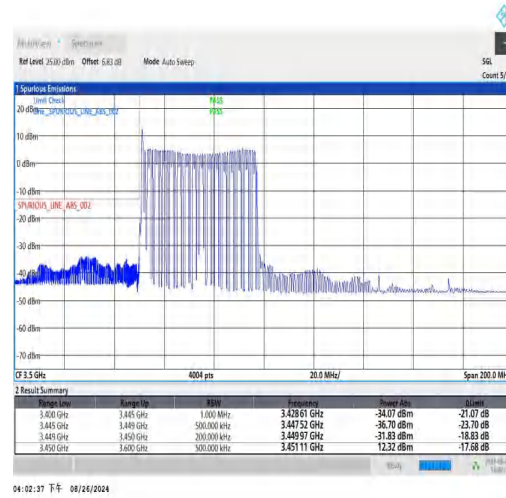
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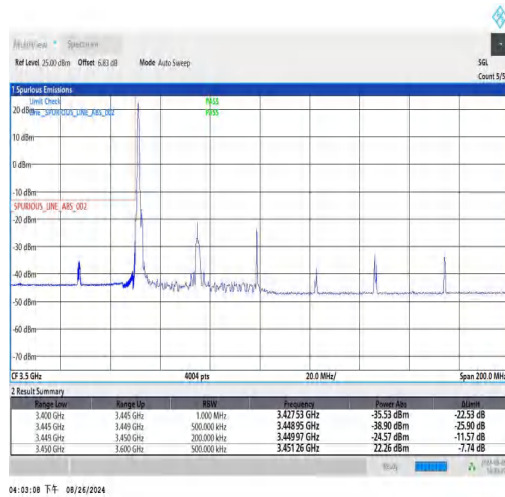
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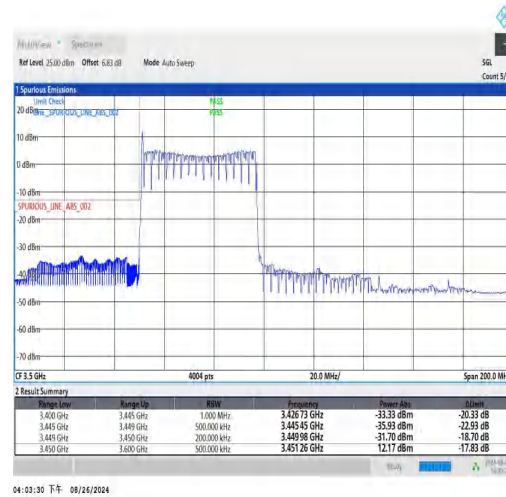
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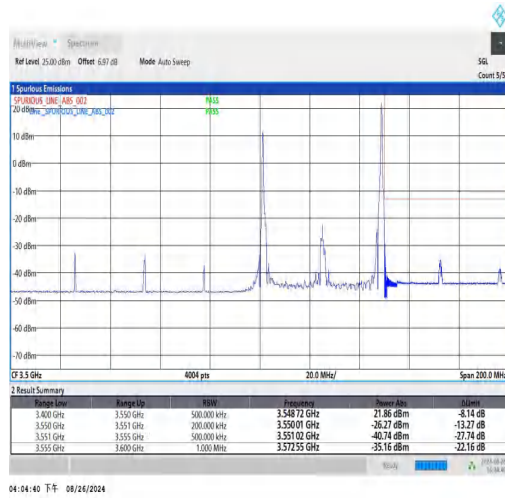
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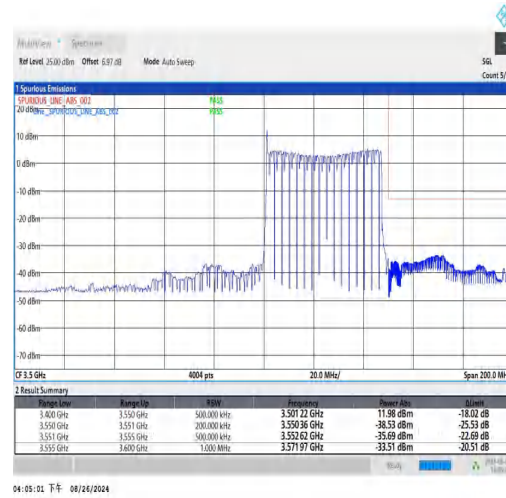
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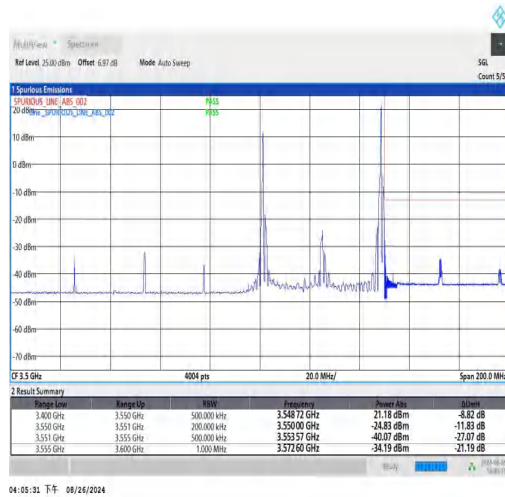
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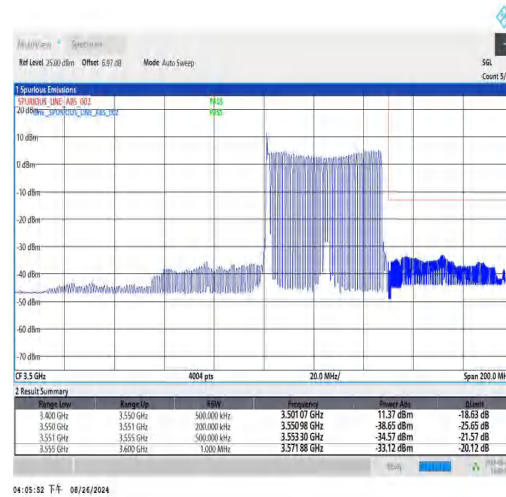
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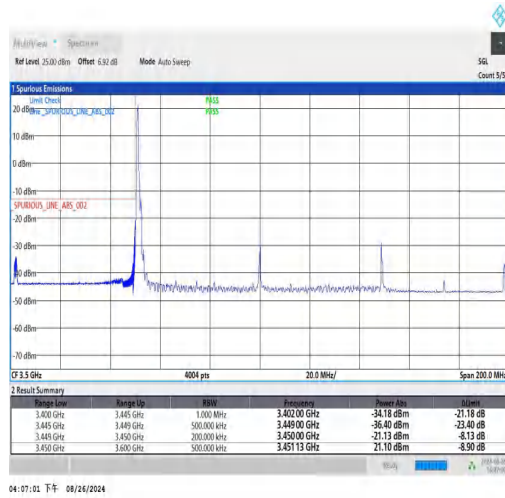
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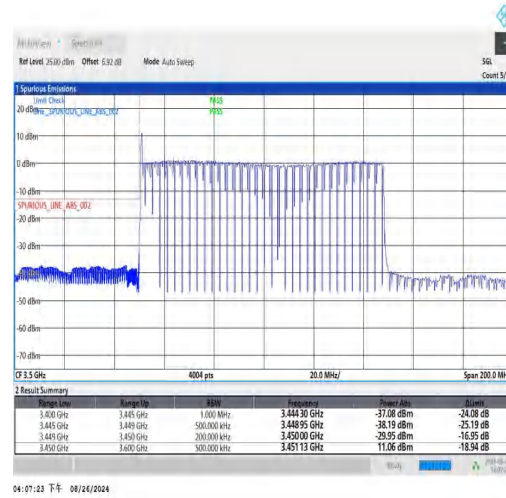
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1#1



NTNV_N77-3450-3550_PC2_30_100_L_TID1_RI

D1#1



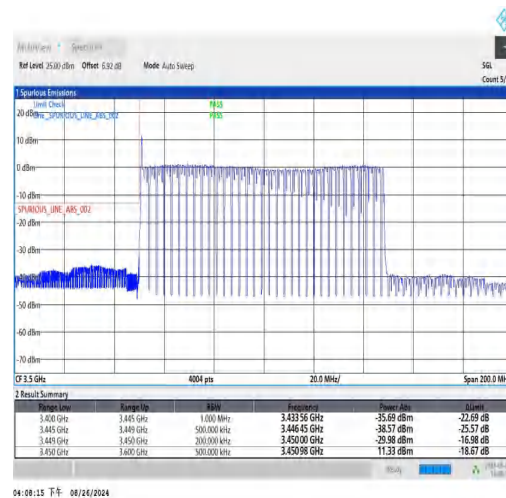
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D1#1



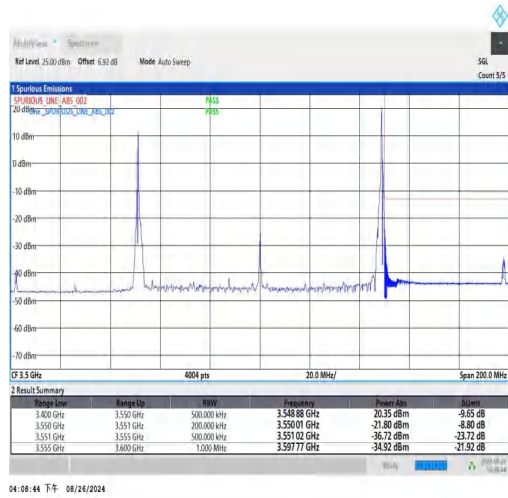
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D1#1



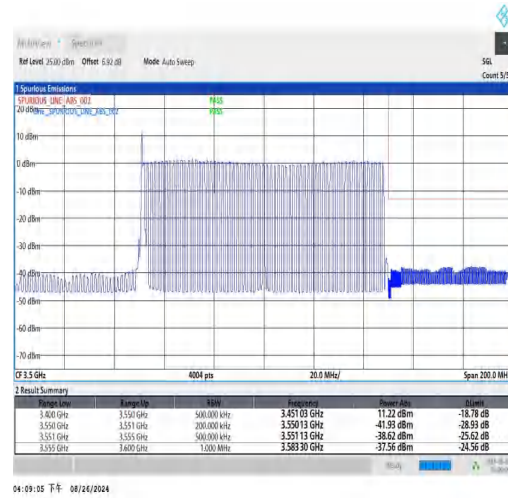
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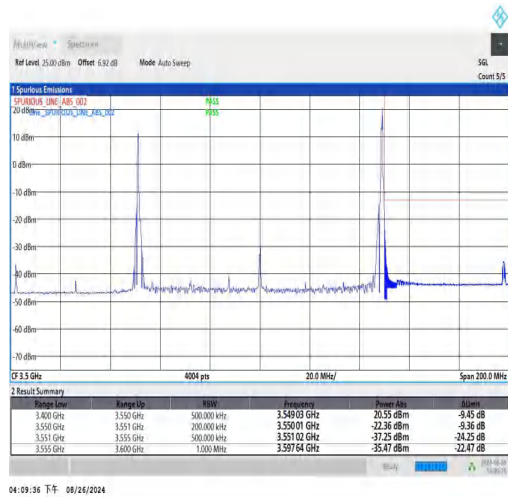
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D1#1



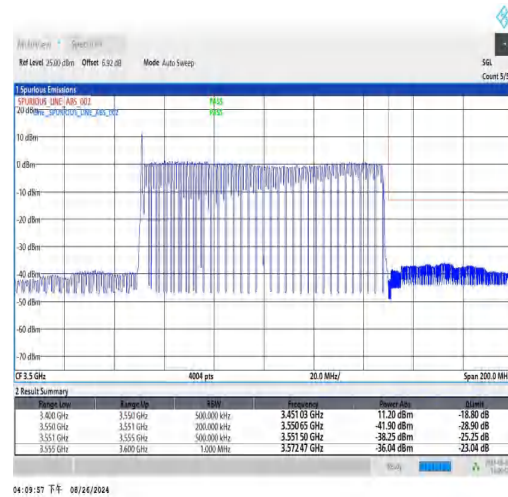
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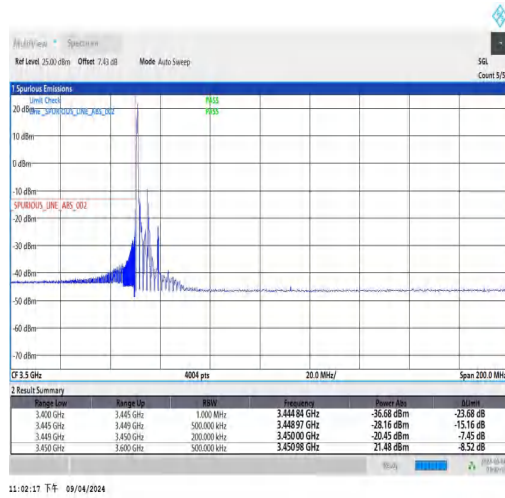
NTNV_N77-3450-3550_PC2_30_100_H_TID5_RI

D1#1



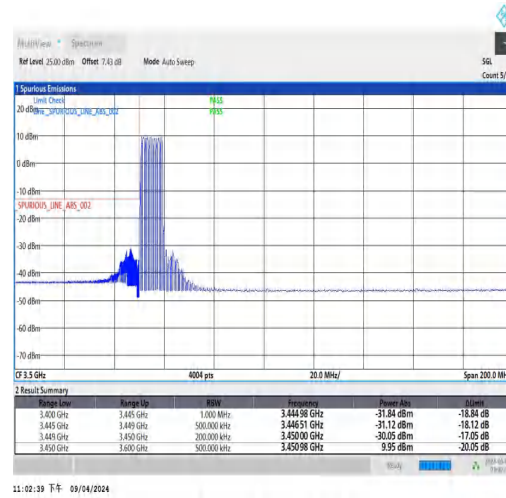
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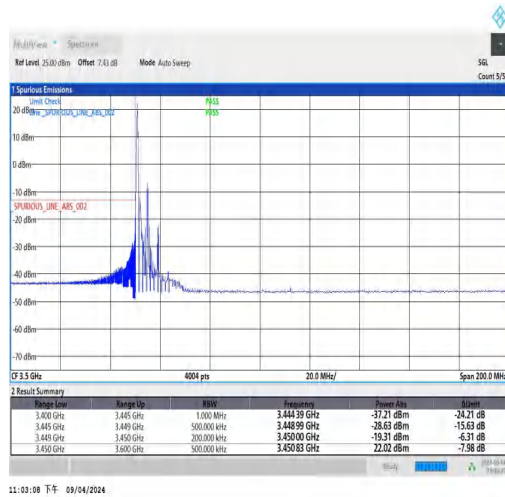
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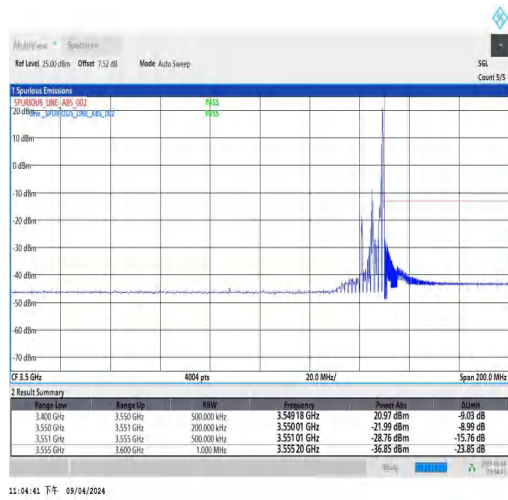
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NTNV_N77-3450-3550_PC3_30_10_L_TID6_RID

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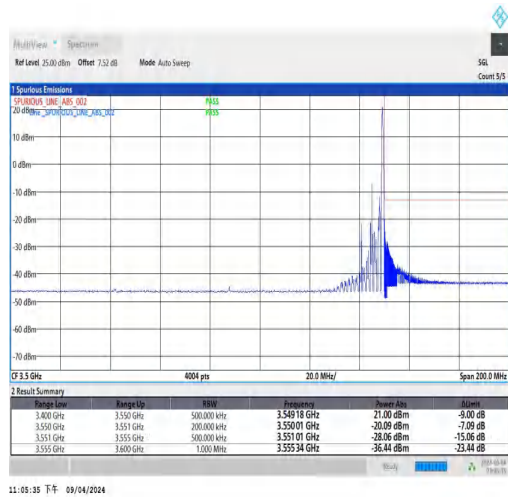
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NTNV_N77-3450-3550_PC3_30_10_H_TID3_RID

1#1



NTNV_N77-3450-3550_PC3_30_10_H_TID5_RID

1#1



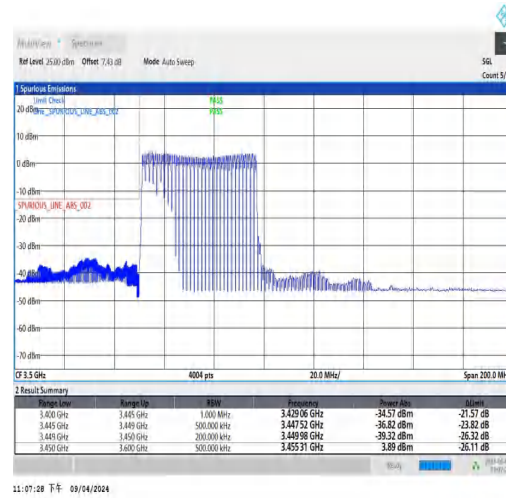
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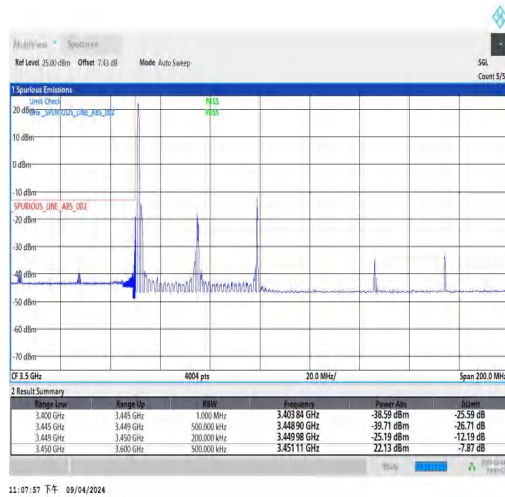
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1#1



NTNV_N77-3450-3550_PC3_30_50_L_TID3_RID

1#1



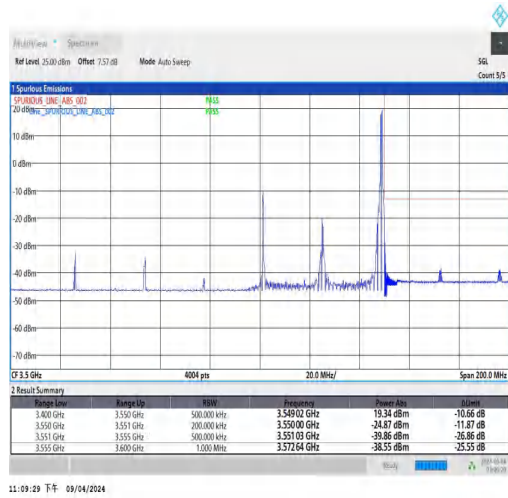
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1#1



NTNV_N77-3450-3550_PC3_30_50_L_TID6_RID

1#1



NTNV_N77-3450-3550_PC3_30_50_H_TID2_RID

1#1



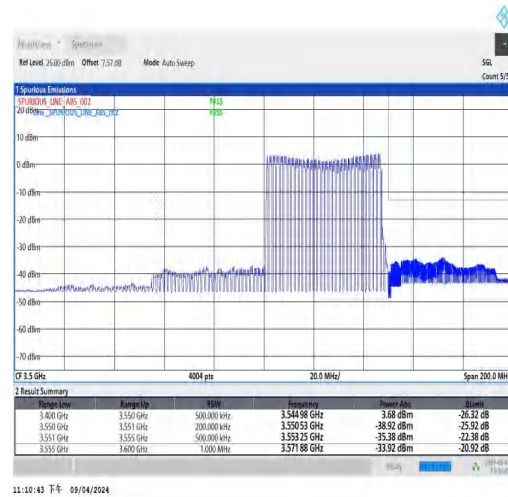
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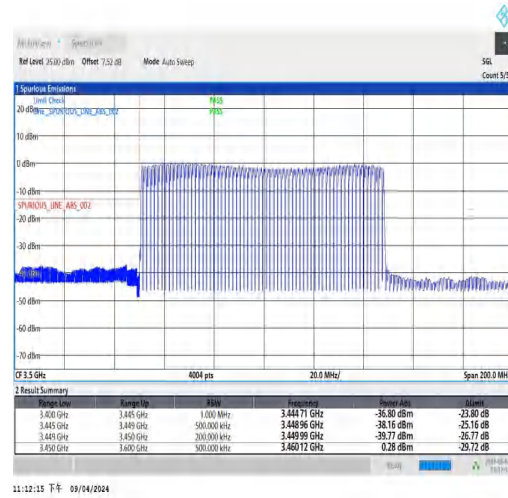
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1#1



NTNV_N77-3450-3550_PC3_30_50_H_TID6_RID

1#1

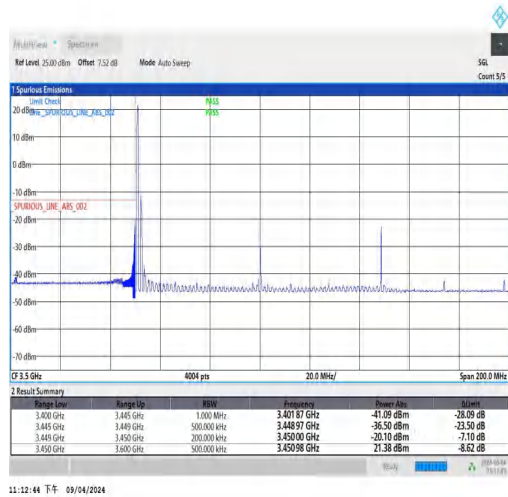


NTNV_N77-3450-3550_PC3_30_100_L_TID1_RI

D1#1

NTNV_N77-3450-3550_PC3_30_100_L_TID3_RI

D1#1



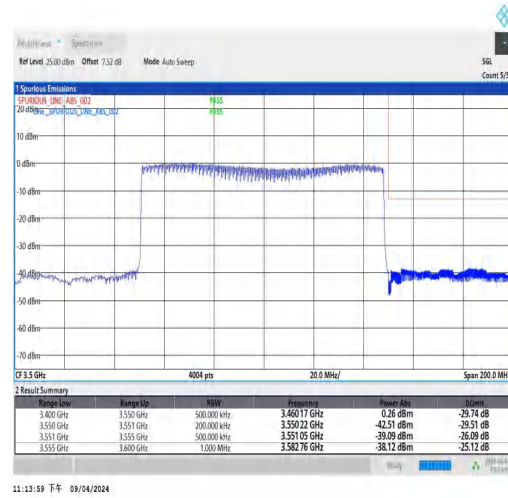
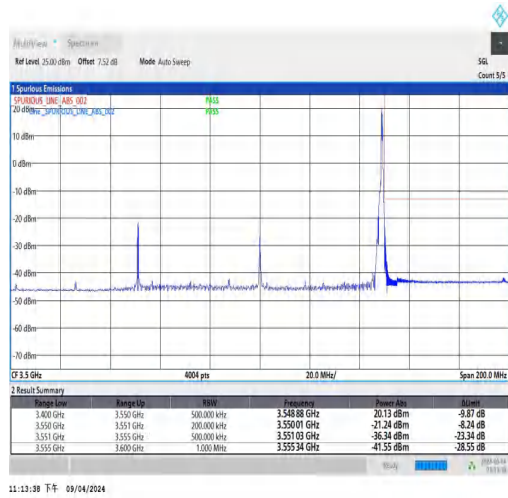
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D1#1



NTNV_N77-3450-3550_PC3_30_100_L_TID6_RI

D1#1

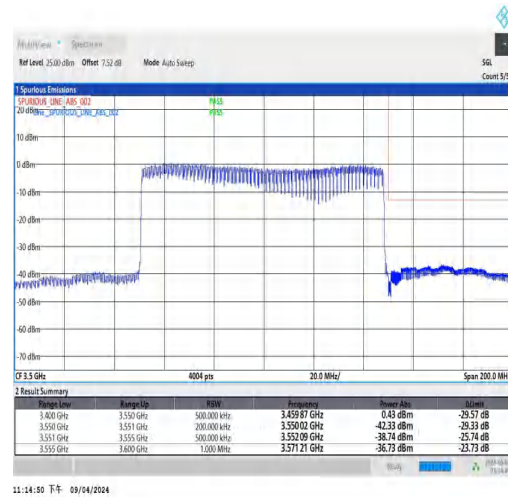
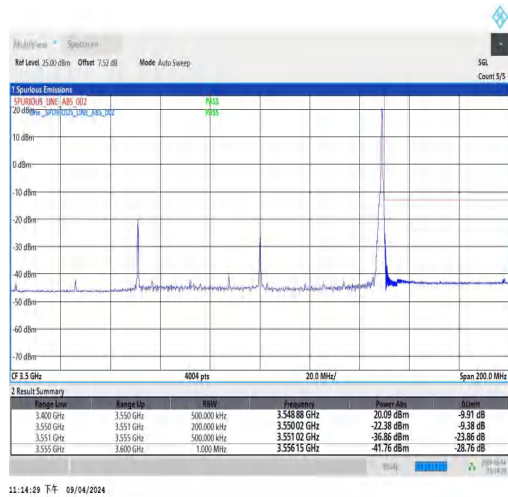


NTNV_N77-3450-3550_PC3_30_100_H_TID2_RI

D1#1

NTNV_N77-3450-3550_PC3_30_100_H_TID3_RI

D1#1



NTNV_N77-3450-3550_PC3_30_100_H_TID5_RI

D1#1

NTNV_N77-3450-3550_PC3_30_100_H_TID6_RI

D1#1



Appendix E: Conducted Spurious Emission

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Start Freq (MHz)	Stop Freq (MHz)	Result (dBm)	Limit (dBm)	Verdict
N77-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	0.009	0.15	-73.84	-43	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	30	1000	-48.82	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	1000	3000	-46.85	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	3000	6000	-33.14	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	6000	26000	-44.85	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	26000	40000	-43.81	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	30	1000	-49.35	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	1000	3000	-47.15	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	3000	6000	-33.10	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	6000	26000	-45.13	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	26000	40000	-43.76	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	30	1000	-49.20	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	1000	3000	-47.09	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	3000	6000	-32.08	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	6000	26000	-44.24	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	26000	40000	-43.76	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Left	30	1000	-49.19	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Left	1000	3000	-47.02	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Left	3000	6000	-33.49	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Left	6000	26000	-44.89	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Left	26000	40000	-44.07	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-49.00	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	1000	3000	-47.07	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	3000	6000	-34.83	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	6000	26000	-44.94	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	26000	40000	-43.88	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Left	30	1000	-49.35	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Left	1000	3000	-47.03	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Left	3000	6000	-36.10	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Left	6000	26000	-44.95	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Left	26000	40000	-43.85	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	30	1000	-49.29	-13	PASS



N77-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	1000	3000	-45.29	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	3000	6000	-30.97	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	6000	26000	-43.90	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	26000	40000	-44.08	-13	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	30	1000	-49.16	-13	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	1000	3000	-44.72	-13	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	3000	6000	-33.81	-13	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	6000	26000	-39.96	-13	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	26000	40000	-44.25	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Left	30	1000	-49.11	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Left	1000	3000	-41.92	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Left	3000	6000	-31.12	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Left	6000	26000	-38.66	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Left	26000	40000	-43.98	-13	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-49.21	-13	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	1000	3000	-46.99	-13	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	3000	6000	-35.78	-13	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	6000	26000	-44.92	-13	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	26000	40000	-43.93	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Left	30	1000	-49.25	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Left	1000	3000	-46.77	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Left	3000	6000	-30.71	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Left	6000	26000	-44.77	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Left	26000	40000	-43.74	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	30	1000	-49.21	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	1000	3000	-46.92	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	3000	6000	-31.25	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	6000	26000	-44.71	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	26000	40000	-43.54	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	30	1000	-49.30	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	1000	3000	-46.78	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	3000	6000	-31.61	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	6000	26000	-44.98	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	26000	40000	-43.92	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Edge_1RB_Left	30	1000	-49.33	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Edge_1RB_Left	1000	3000	-46.71	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Edge_1RB_Left	3000	6000	-31.06	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Edge_1RB_Left	6000	26000	-44.88	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Edge_1RB_Left	26000	40000	-43.96	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-49.08	-13	PASS



N77-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	1000	3000	-46.96	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	3000	6000	-31.45	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	6000	26000	-44.57	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	26000	40000	-43.77	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Left	30	1000	-49.23	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Left	1000	3000	-46.80	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Left	3000	6000	-31.44	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Left	6000	26000	-44.98	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Left	26000	40000	-43.92	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	30	1000	-49.18	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	1000	3000	-45.88	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	3000	6000	-35.17	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	6000	26000	-44.28	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	26000	40000	-43.49	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	30	1000	-49.19	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	1000	3000	-46.24	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	3000	6000	-37.91	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	6000	26000	-44.35	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	L	Edge_1RB_Left	26000	40000	-43.57	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	30	1000	-49.37	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	1000	3000	-46.36	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	3000	6000	-35.40	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	6000	26000	-44.46	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	26000	40000	-43.42	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Left	30	1000	-49.32	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Left	1000	3000	-46.68	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Left	3000	6000	-33.79	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Left	6000	26000	-44.74	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	M	Edge_1RB_Left	26000	40000	-43.03	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-49.37	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	1000	3000	-46.57	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	3000	6000	-36.16	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	6000	26000	-44.42	-13	PASS
N77-3450-3550	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	26000	40000	-43.57	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Left	30	1000	-49.04	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Left	1000	3000	-46.46	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Left	3000	6000	-36.88	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Left	6000	26000	-44.57	-13	PASS
N77-3450-3550	30	10	DFT-QPSK	H	Edge_1RB_Left	26000	40000	-42.85	-13	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	30	1000	-49.28	-13	PASS



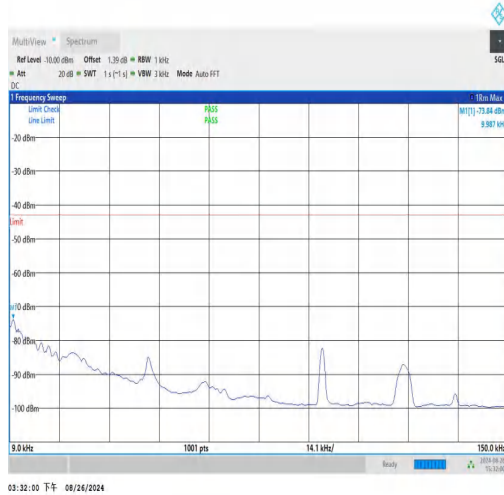
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N77-3450-3550	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	6000	26000	-44.61	-13	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	26000	40000	-43.54	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	30	1000	-49.31	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	1000	3000	-42.71	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	3000	6000	-31.64	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	6000	26000	-44.52	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	L	Edge_1RB_Left	26000	40000	-43.13	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Left	30	1000	-49.30	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Left	1000	3000	-42.30	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Left	3000	6000	-32.22	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Left	6000	26000	-39.55	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	M	Edge_1RB_Left	26000	40000	-43.38	-13	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-49.11	-13	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	1000	3000	-46.58	-13	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	3000	6000	-31.39	-13	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	6000	26000	-44.48	-13	PASS
N77-3450-3550	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	26000	40000	-43.62	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Left	30	1000	-49.22	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Left	1000	3000	-46.20	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Left	3000	6000	-30.79	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Left	6000	26000	-44.60	-13	PASS
N77-3450-3550	30	50	DFT-QPSK	H	Edge_1RB_Left	26000	40000	-43.31	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	30	1000	-49.32	-13	PASS
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N77-3450-3550	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	3000	6000	-31.89	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	6000	26000	-44.54	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	26000	40000	-42.96	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	30	1000	-49.42	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	1000	3000	-46.61	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	3000	6000	-31.35	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	6000	26000	-44.30	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	26000	40000	-43.17	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	30	1000	-49.42	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	1000	3000	-46.50	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	3000	6000	-31.73	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	6000	26000	-44.56	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Edge_1RB_Left	26000	40000	-43.59	-13	PASS
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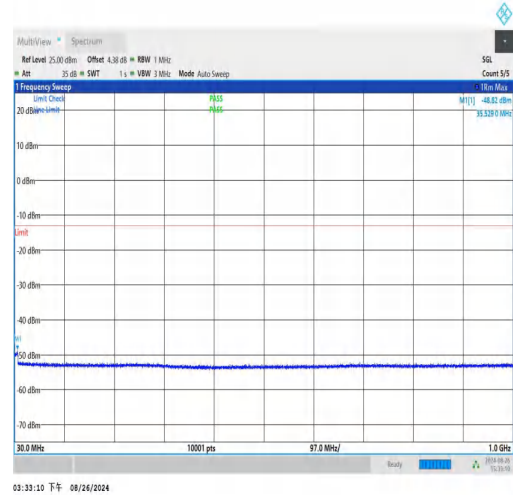
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N77-3450-3550	30	100	DFT-QPSK	M	Edge_1RB_Left	26000	40000	-43.33	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-49.18	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	1000	3000	-46.16	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	3000	6000	-31.25	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	6000	26000	-44.71	-13	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Left	26000	40000	-43.22	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Left	30	1000	-49.21	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Left	1000	3000	-46.46	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Left	3000	6000	-31.49	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Left	6000	26000	-44.37	-13	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Left	26000	40000	-43.63	-13	PASS



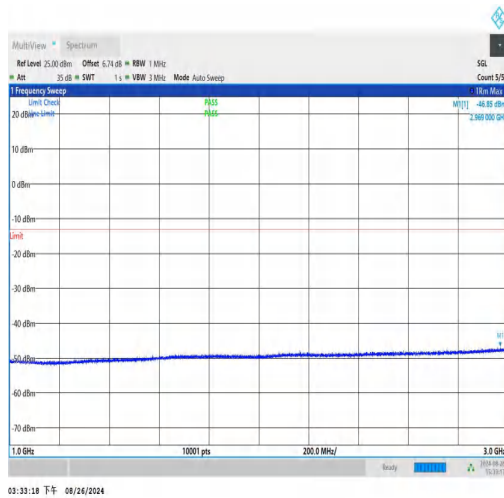
Test Graphs



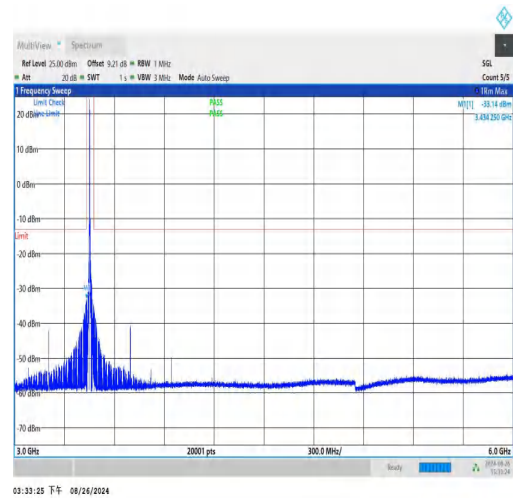
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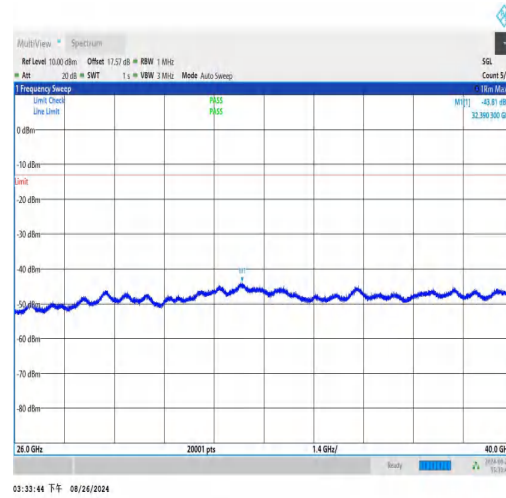
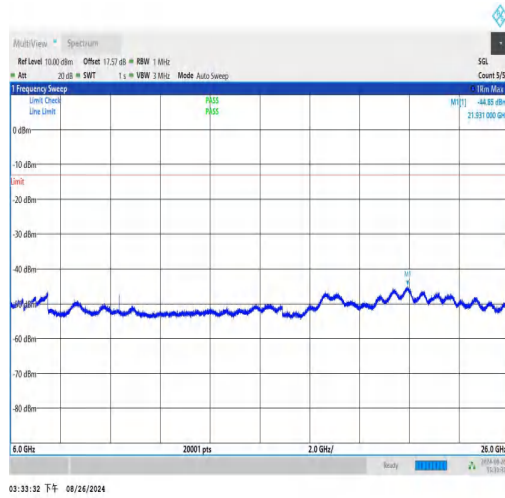
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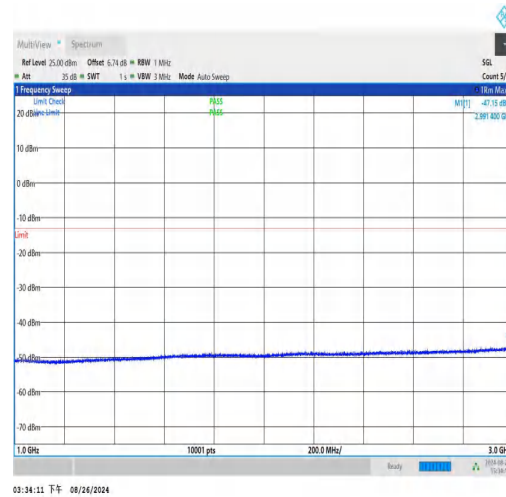
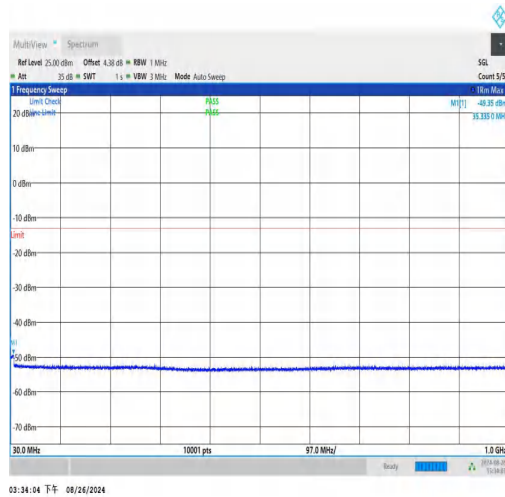


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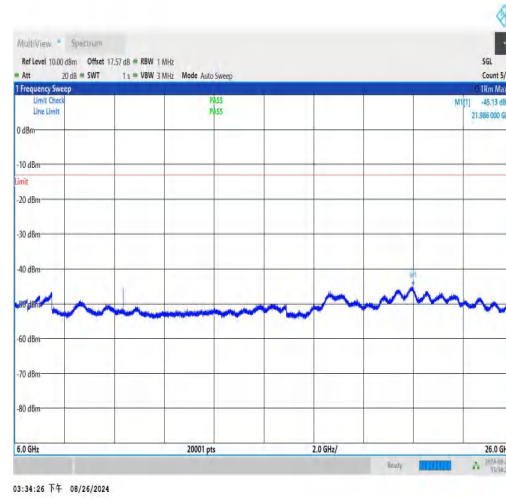
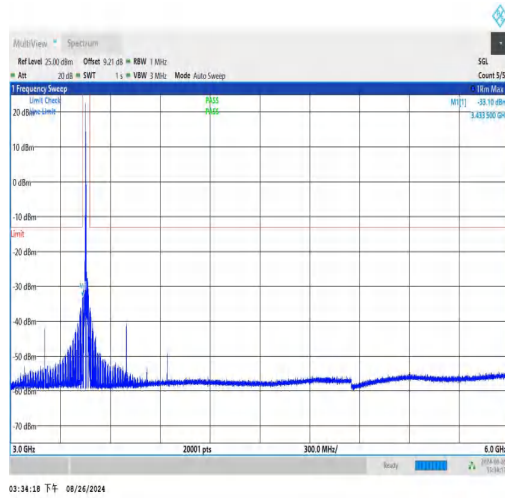
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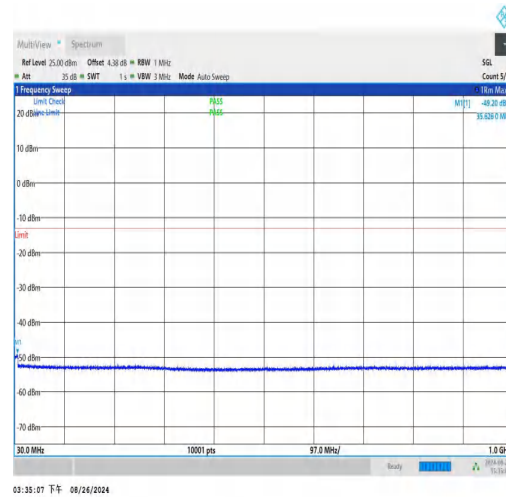
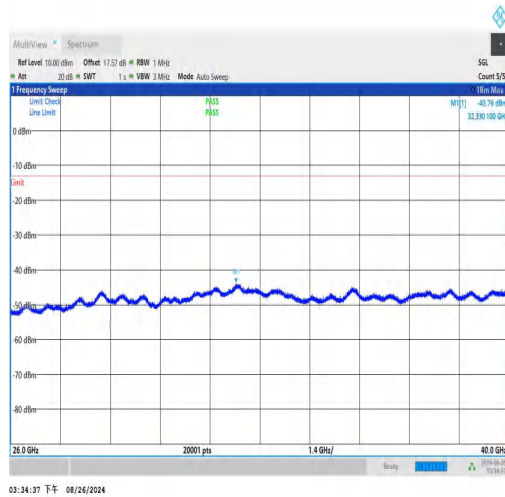
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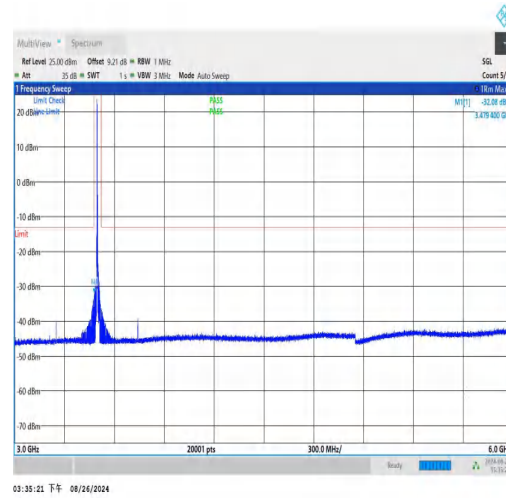
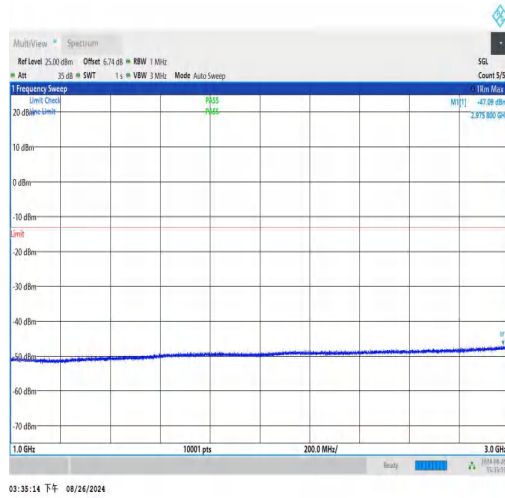
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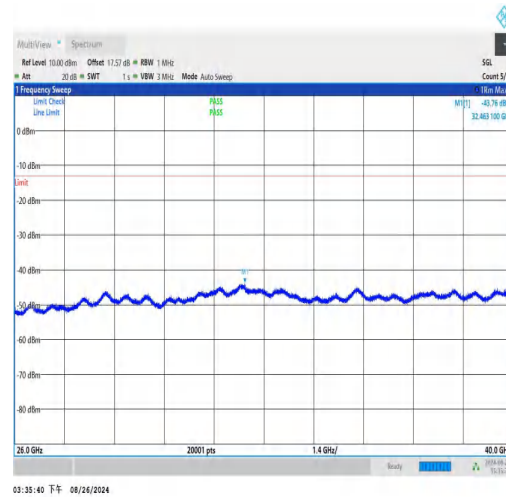
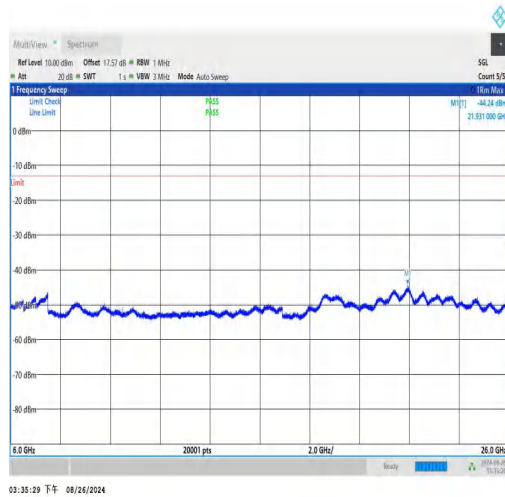
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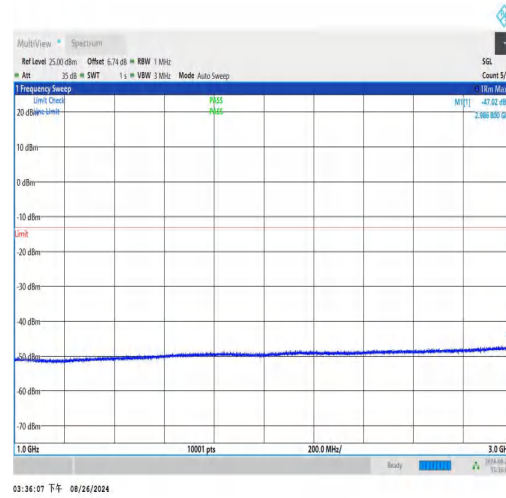
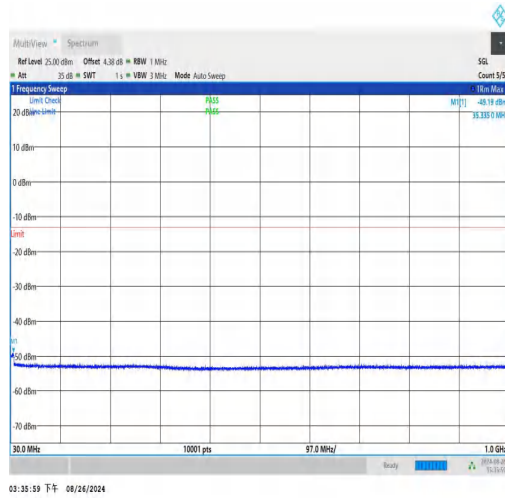
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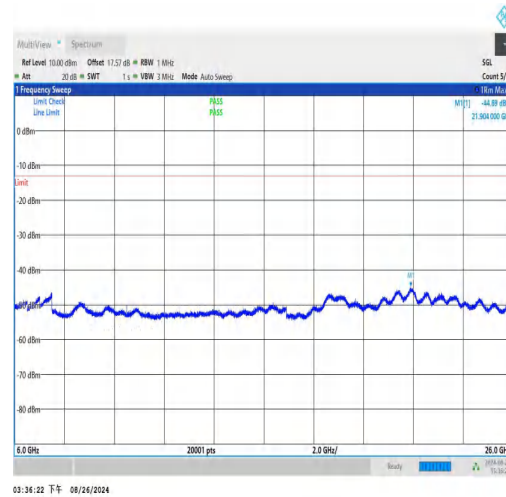
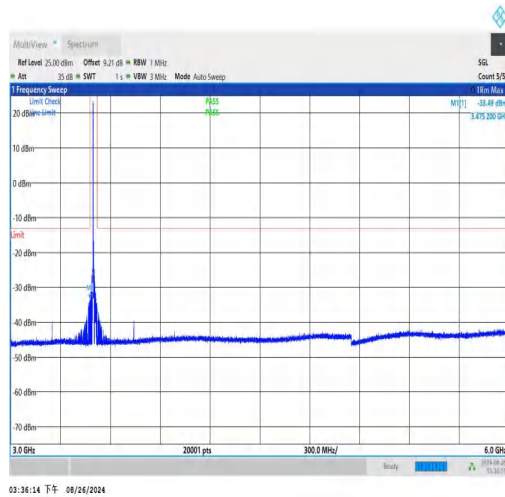
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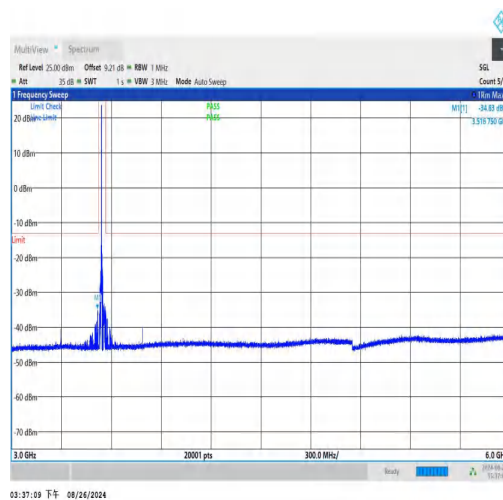
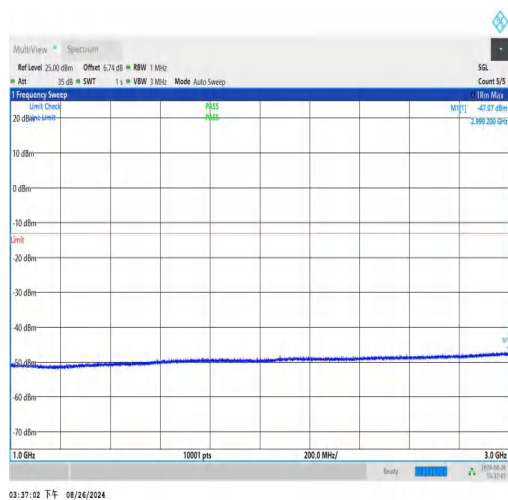
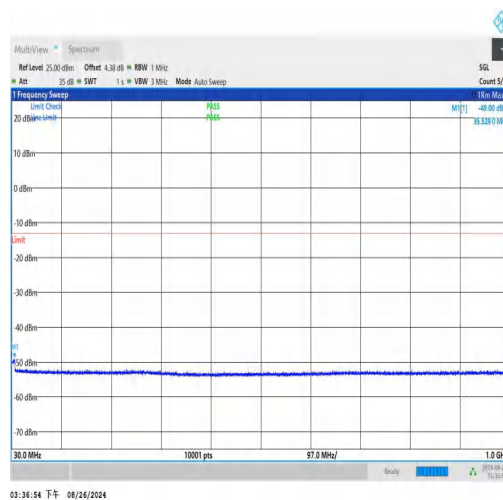
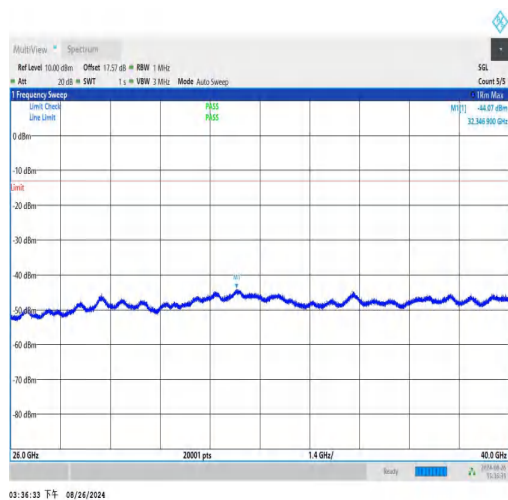


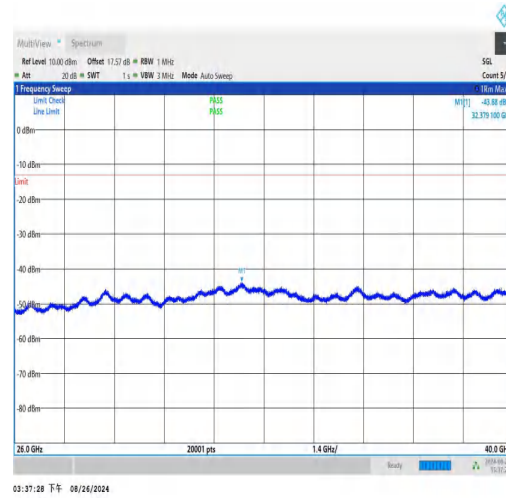
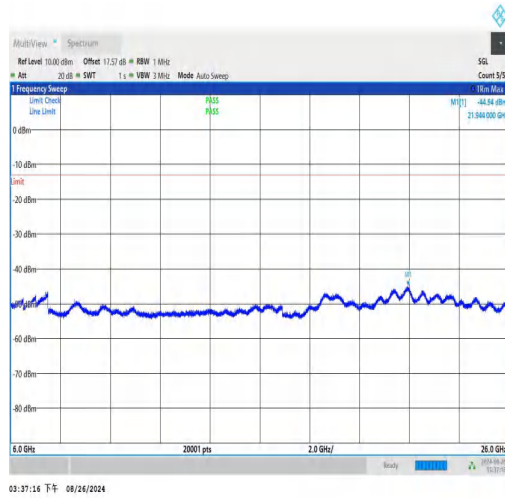
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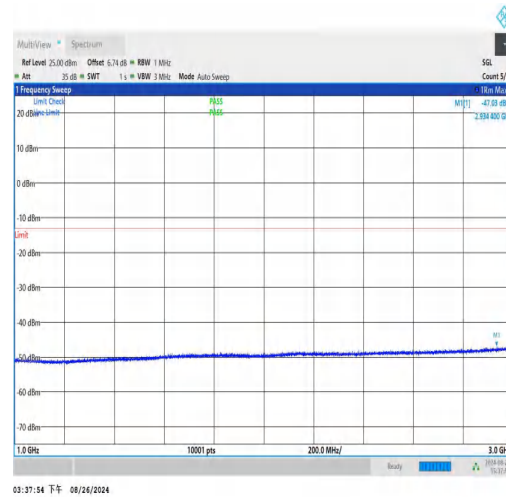
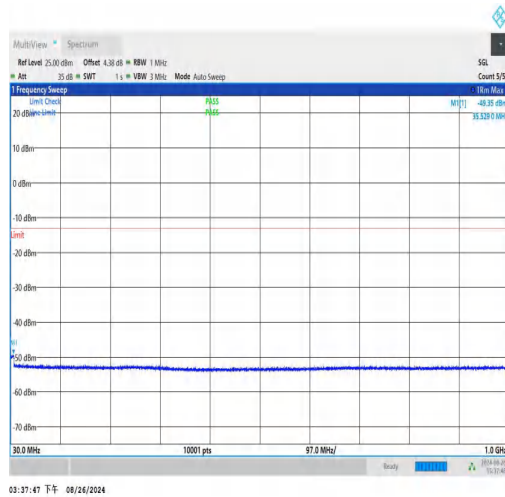
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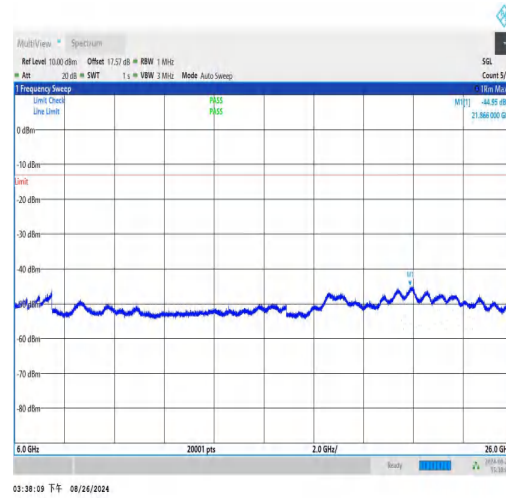
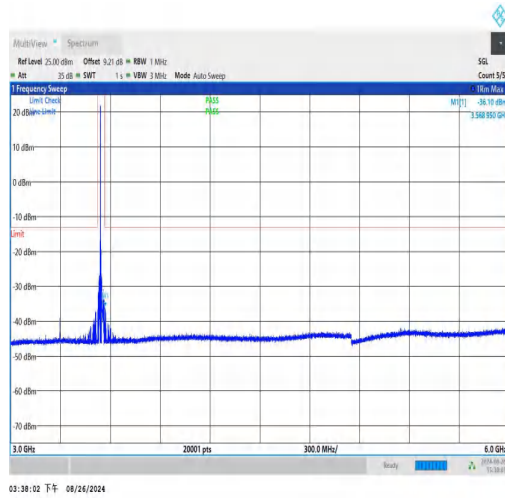
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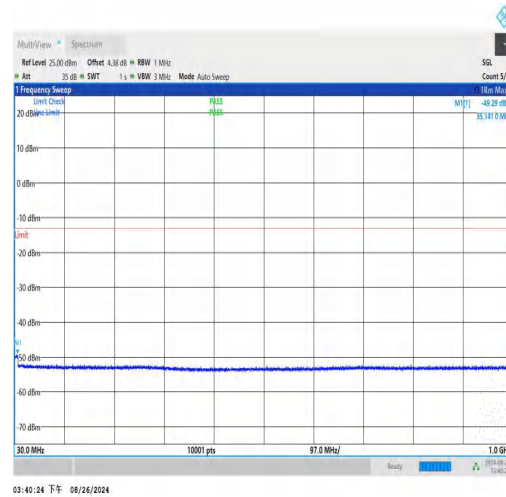
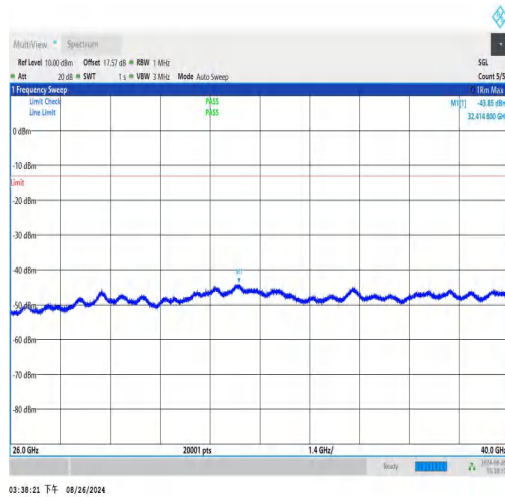
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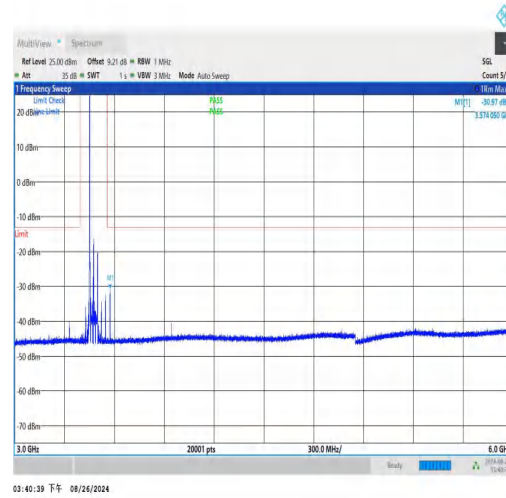
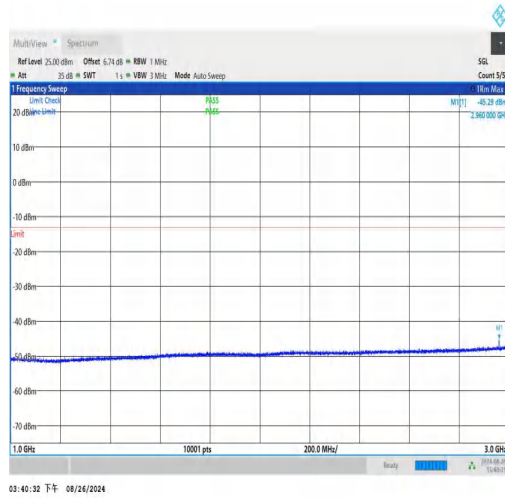
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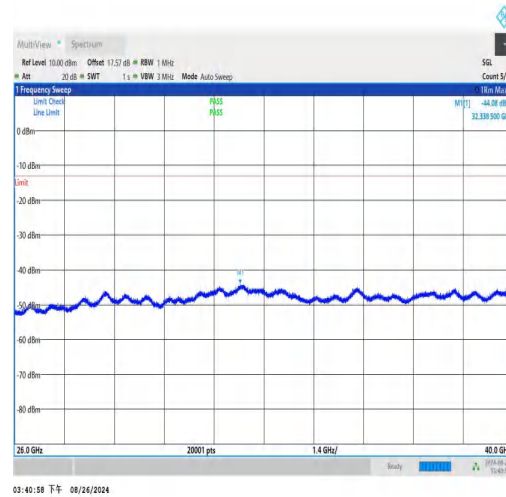
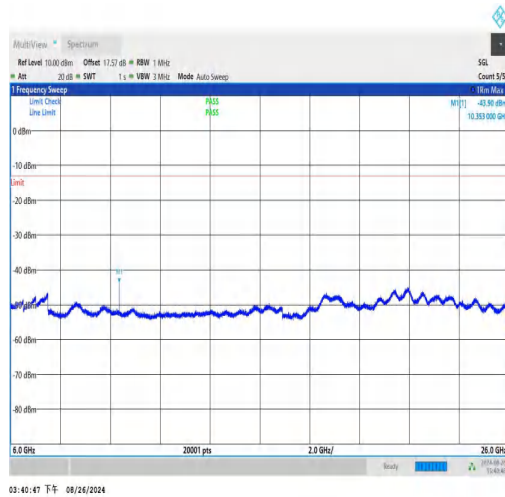
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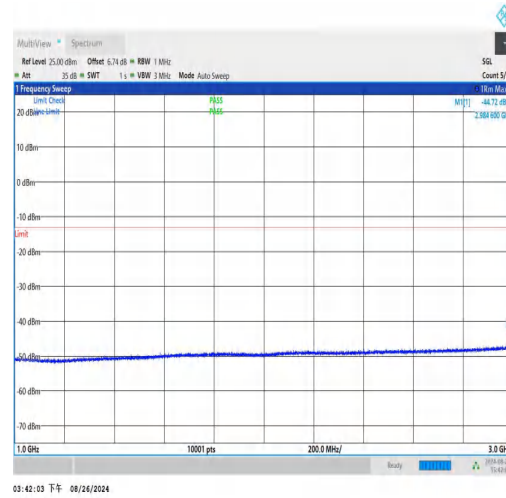
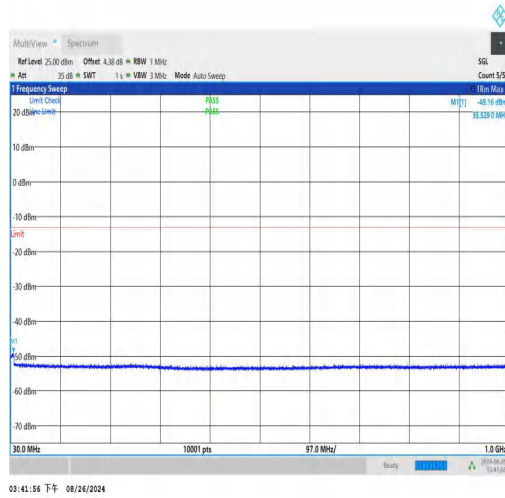
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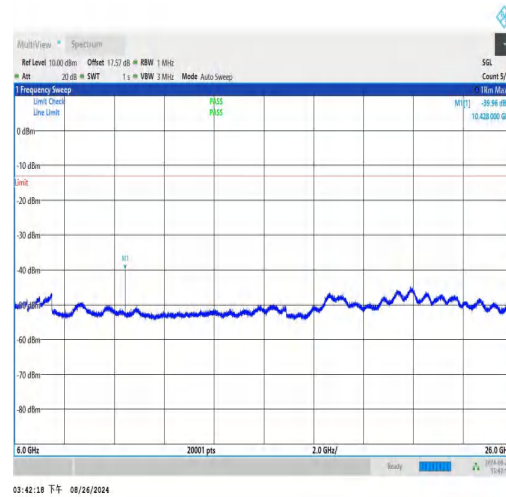
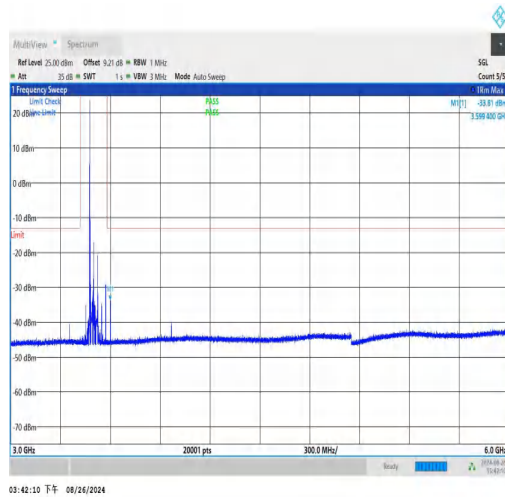
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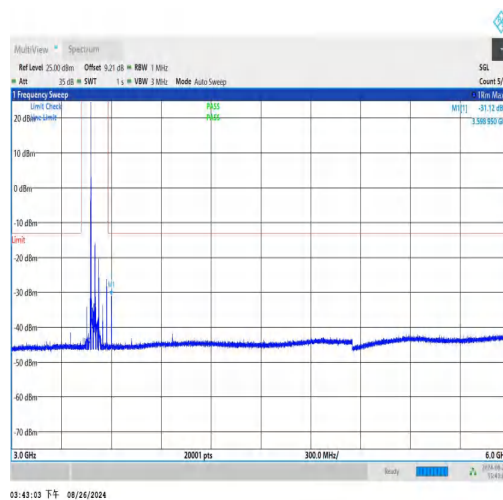
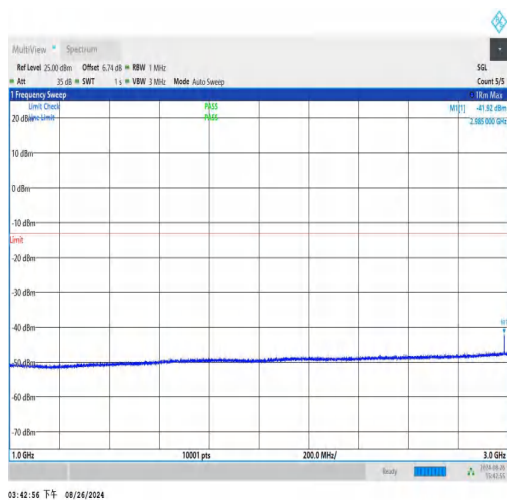
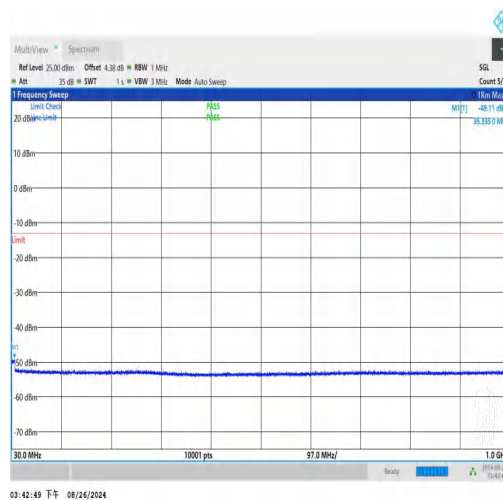
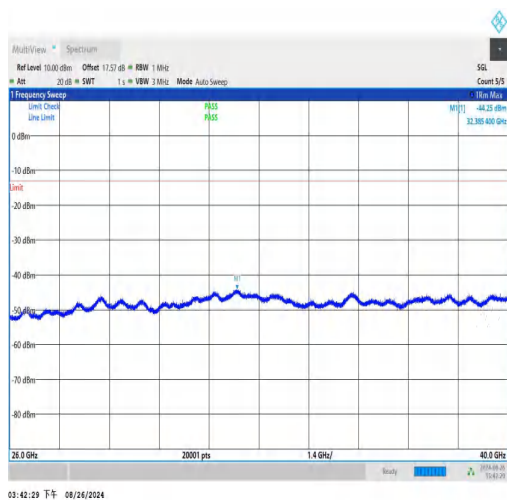


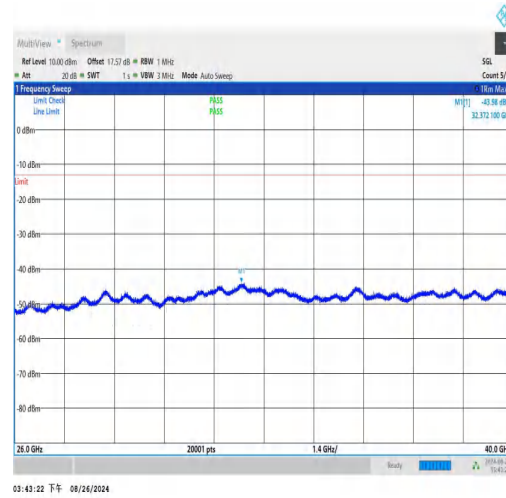
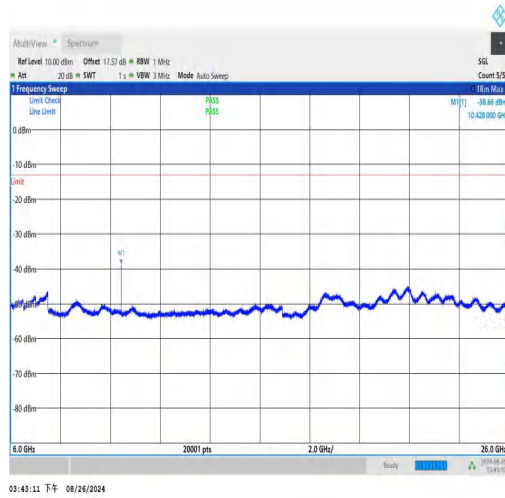
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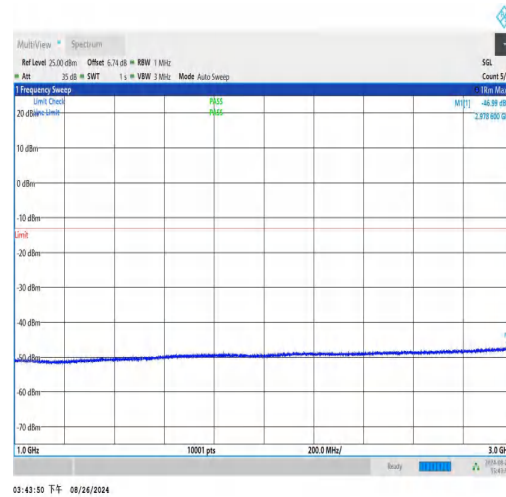
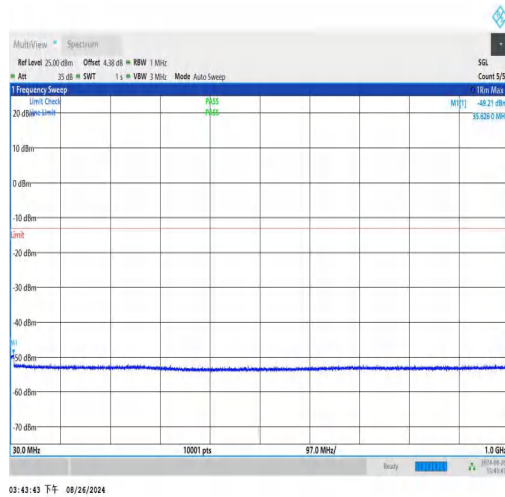
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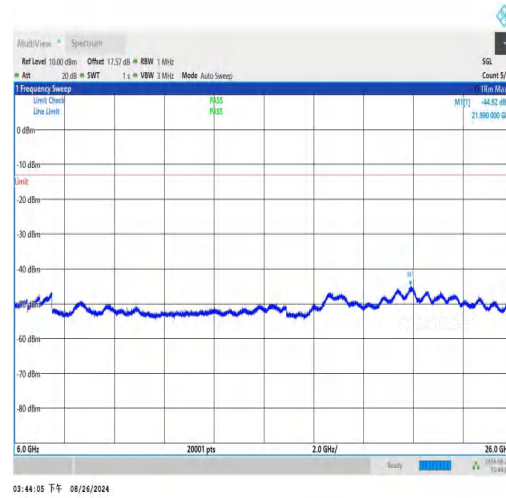
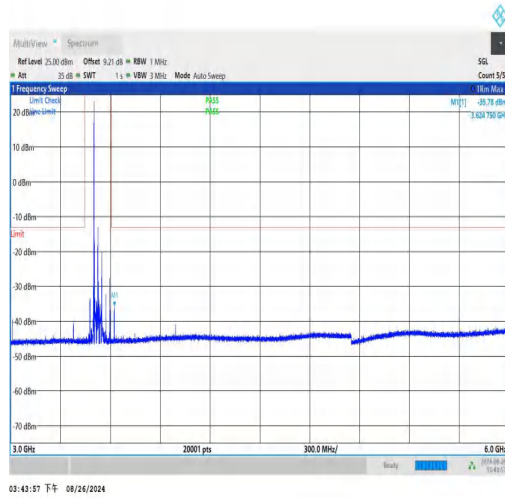
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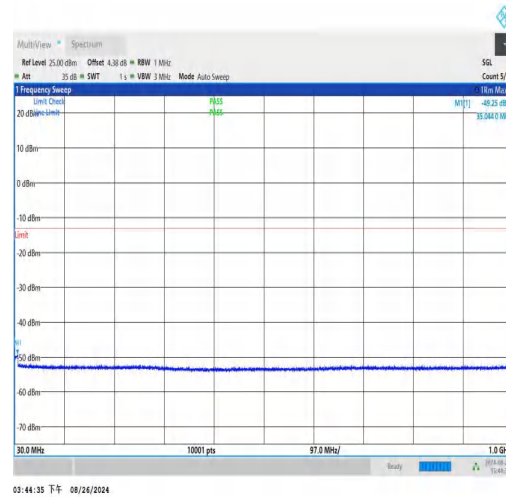
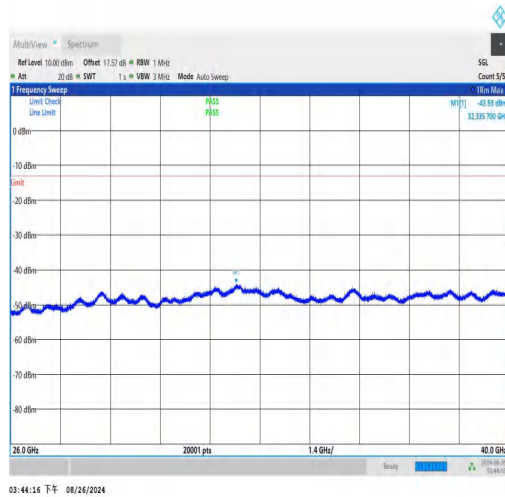
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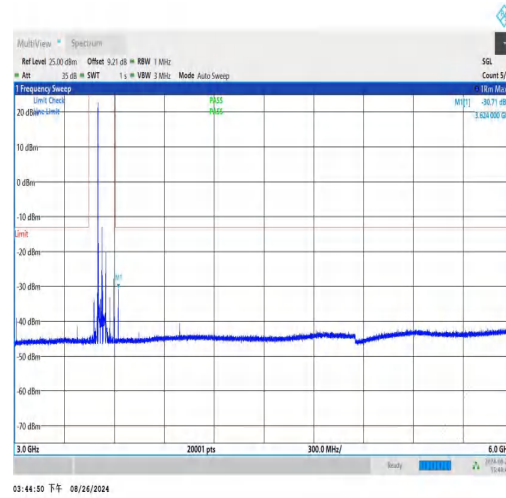
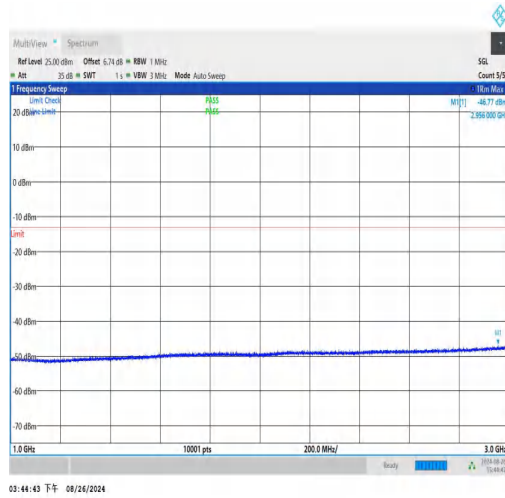
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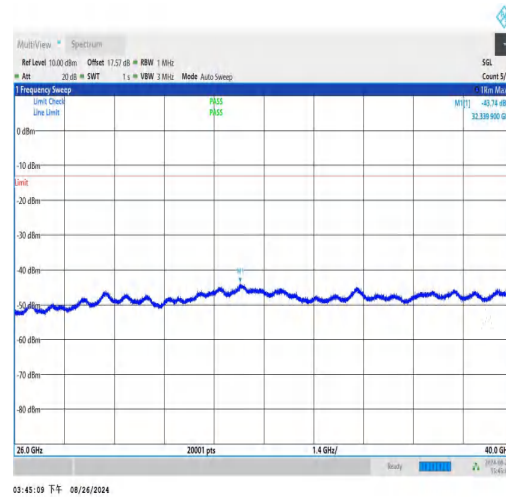
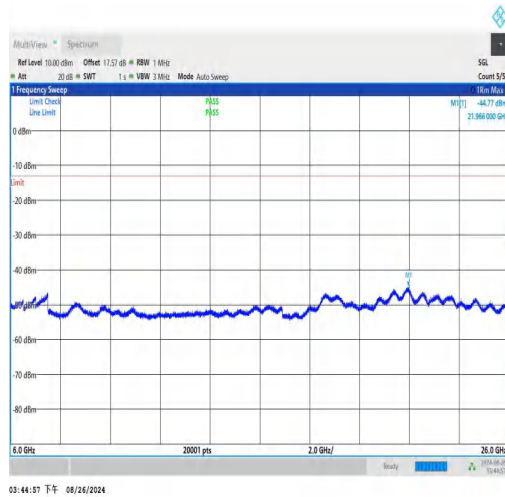
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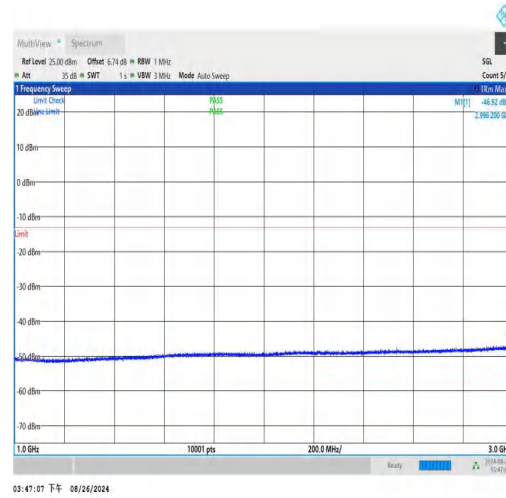
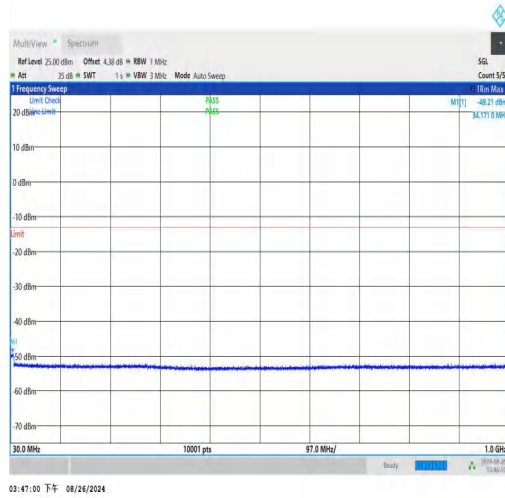
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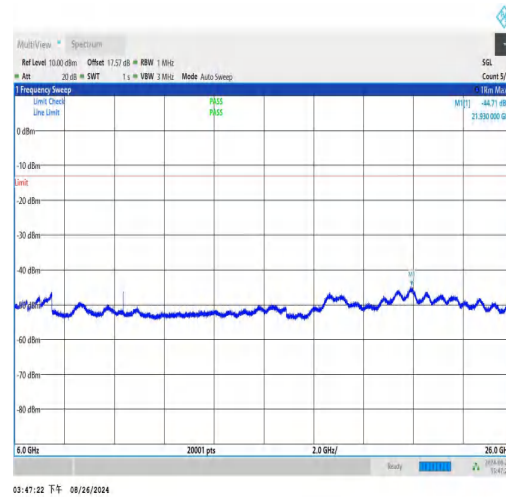
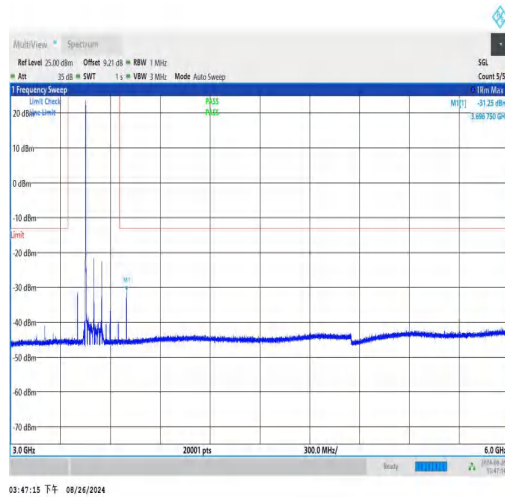
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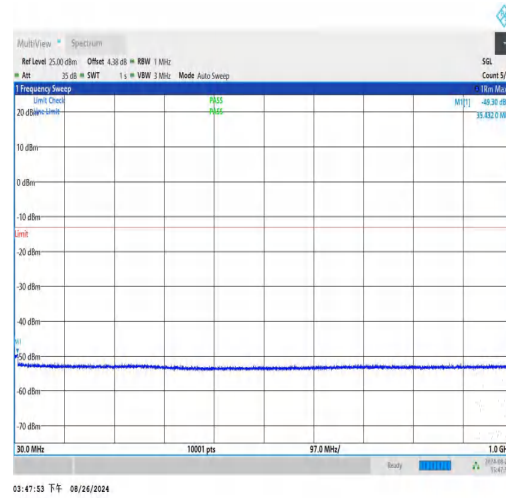
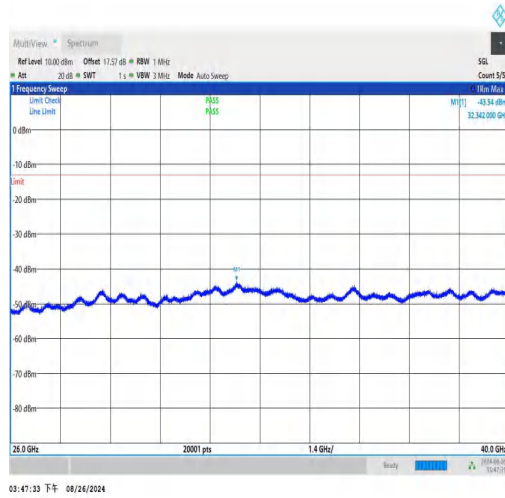
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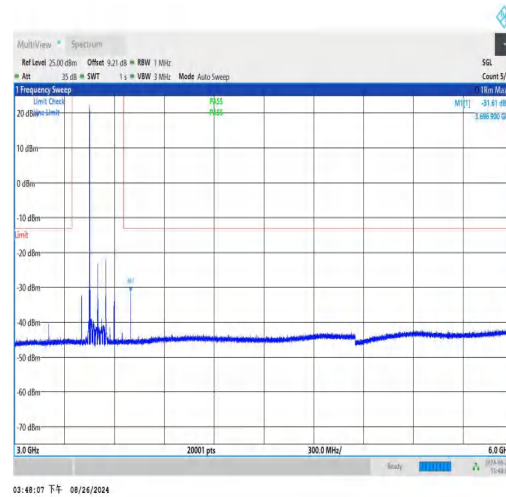
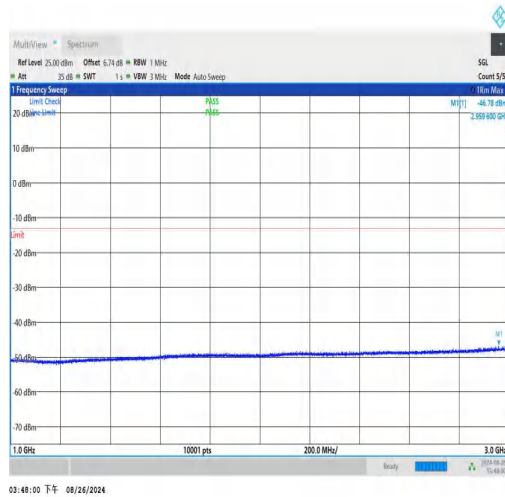
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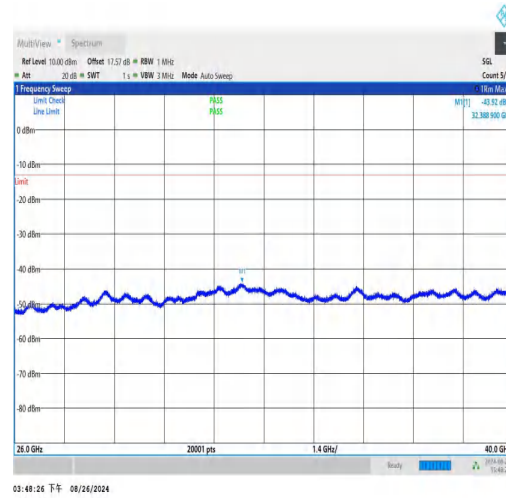
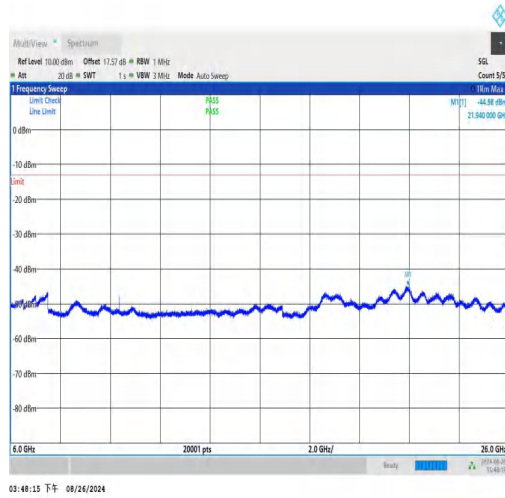
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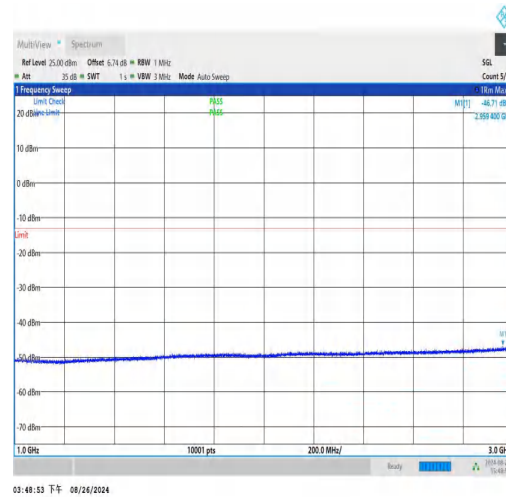
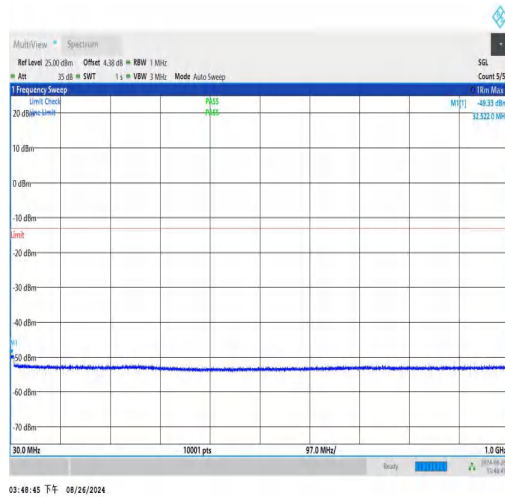
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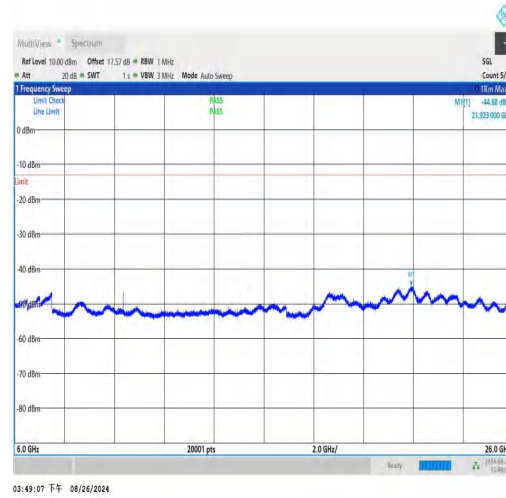
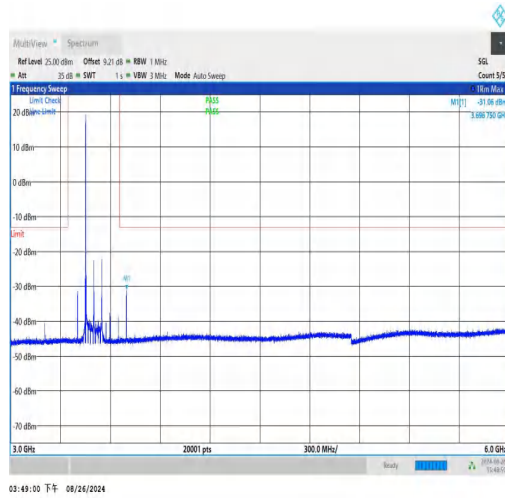
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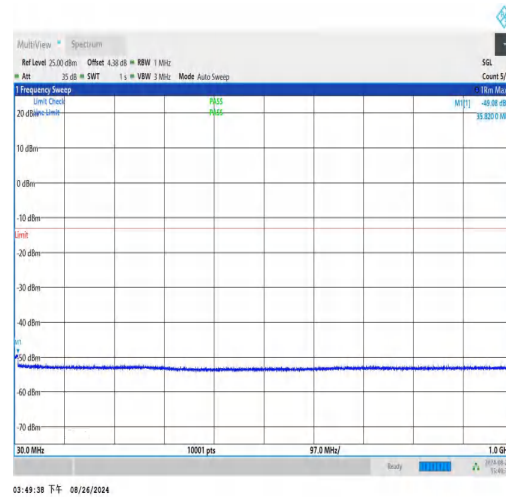
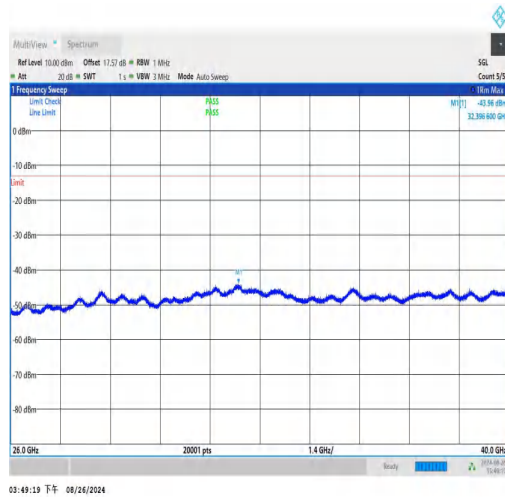
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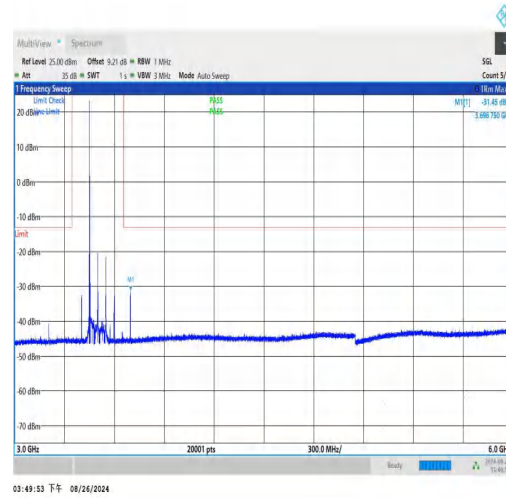
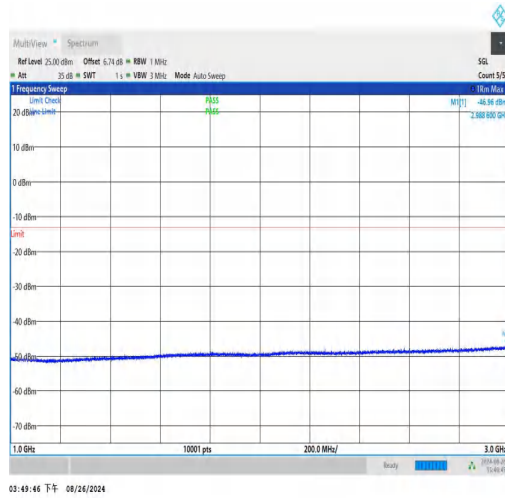
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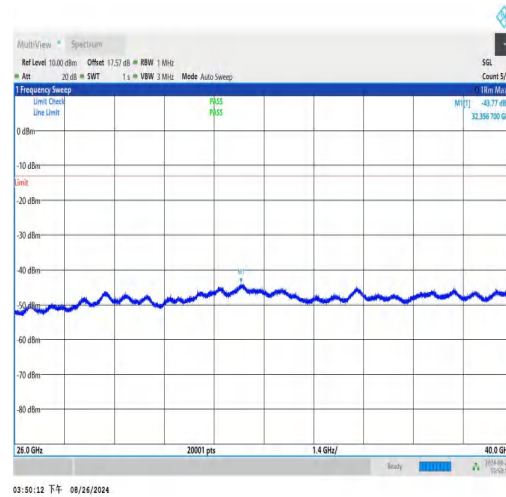
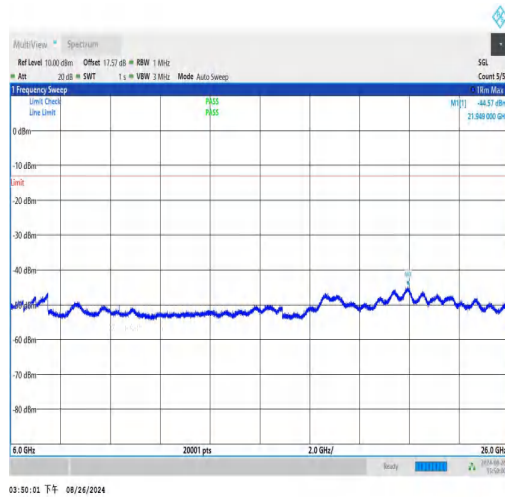
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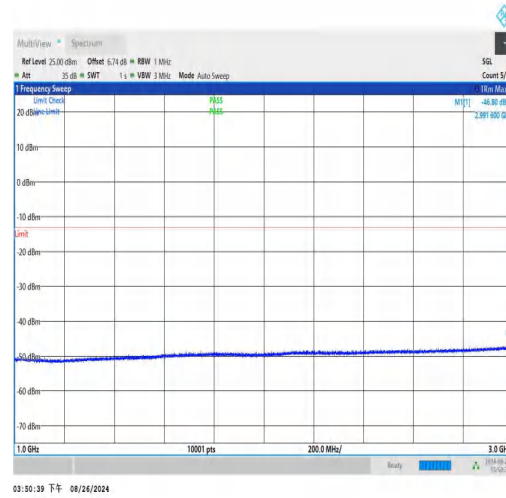
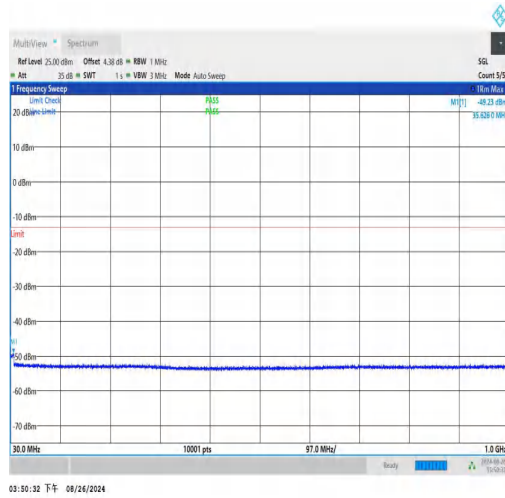
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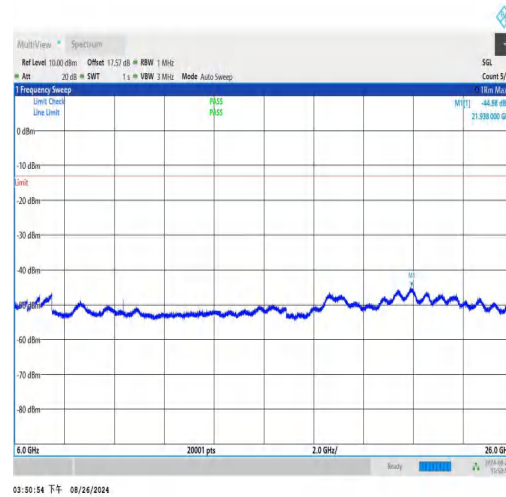
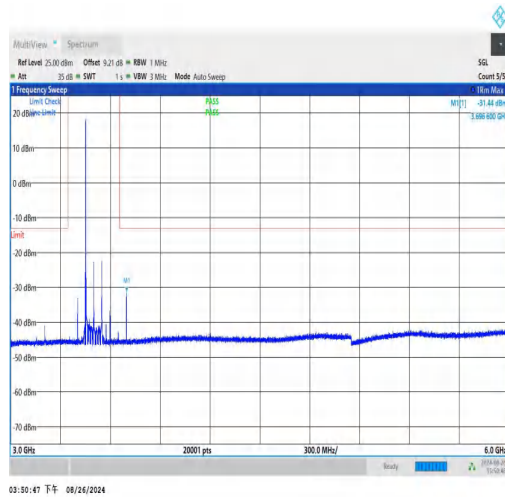
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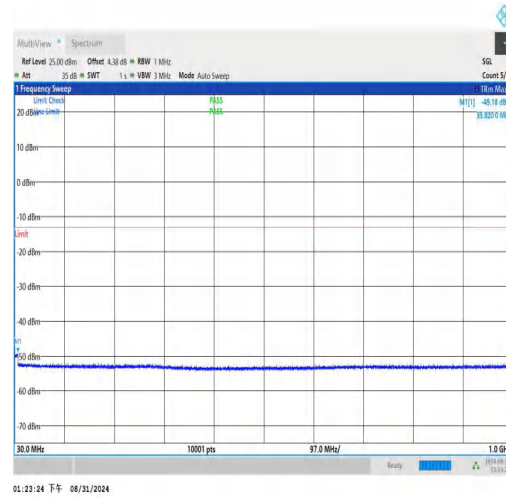
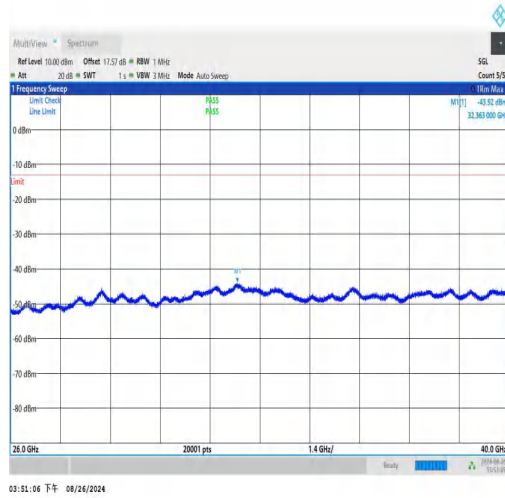
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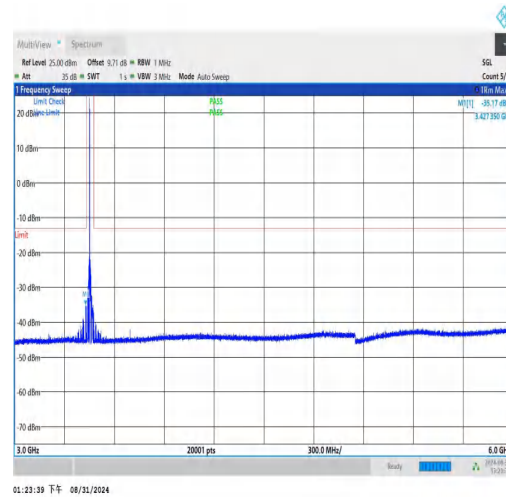
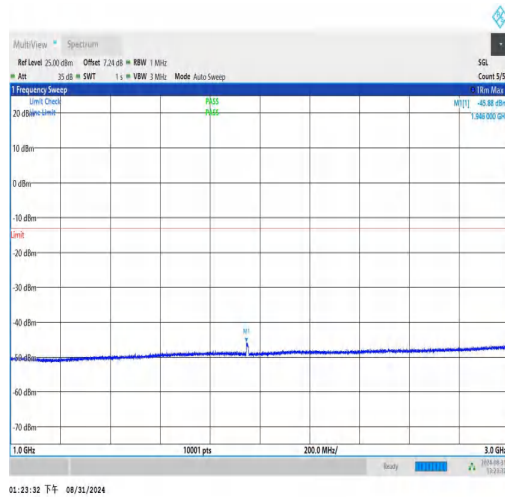
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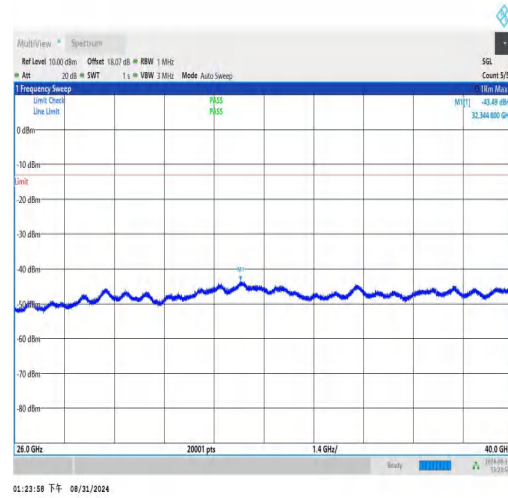
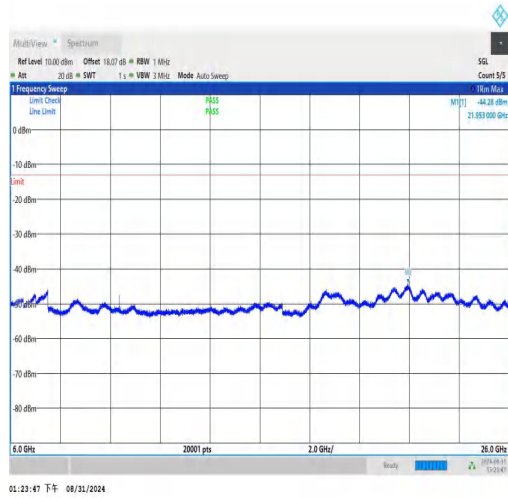
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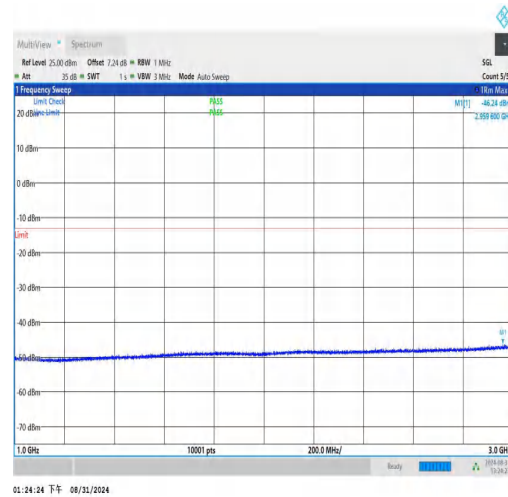
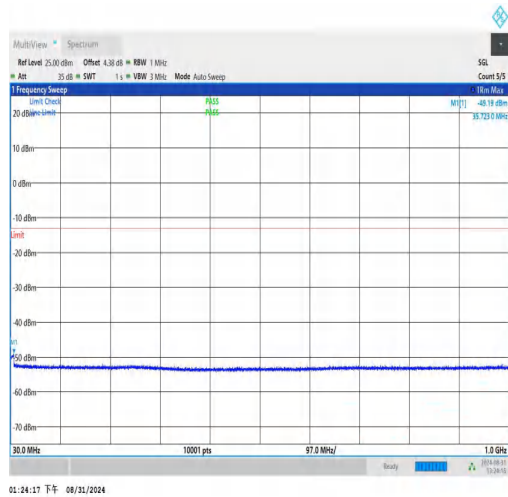
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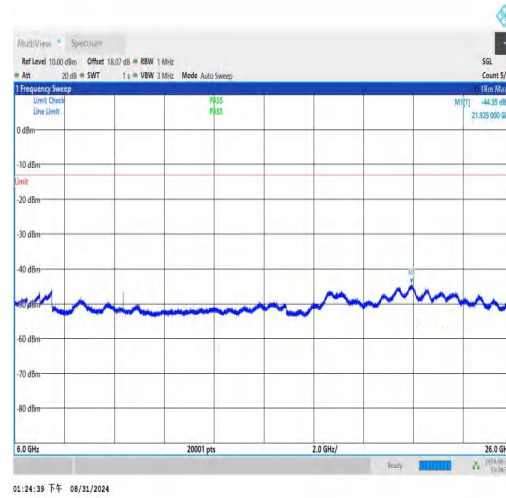
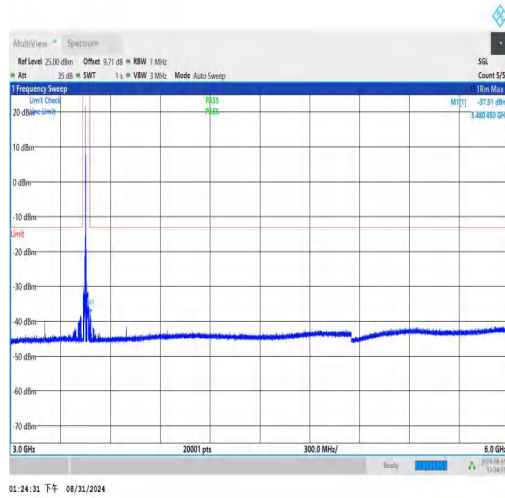
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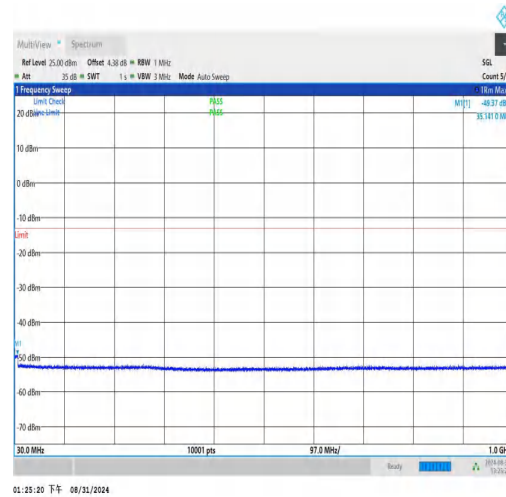
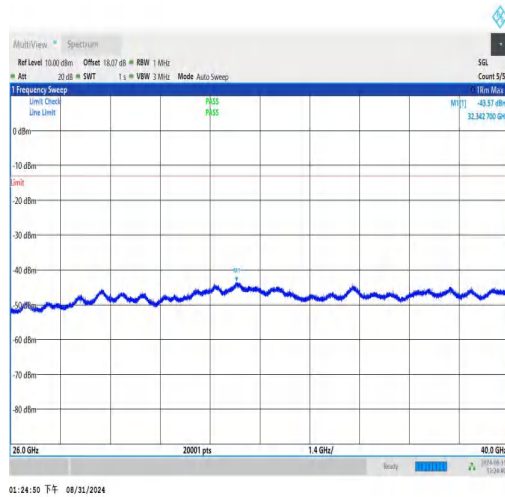
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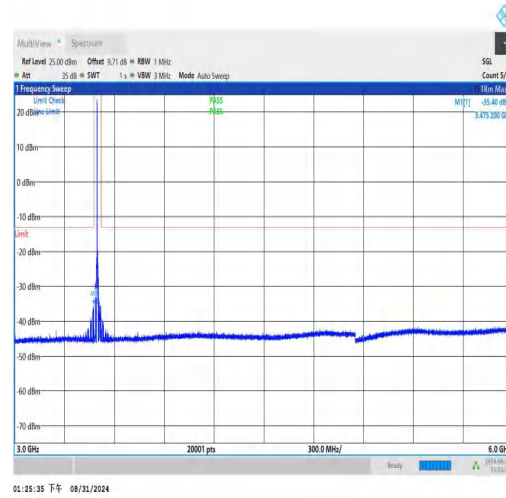
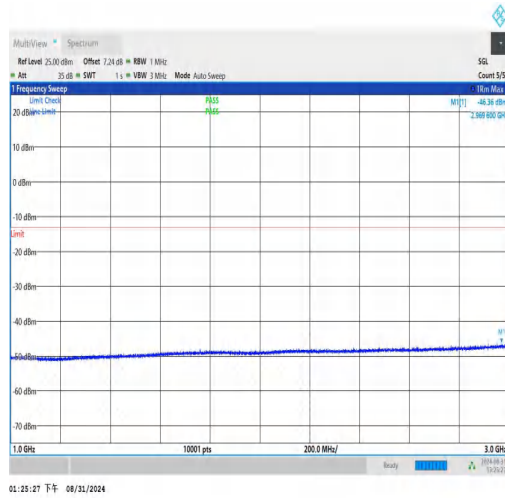
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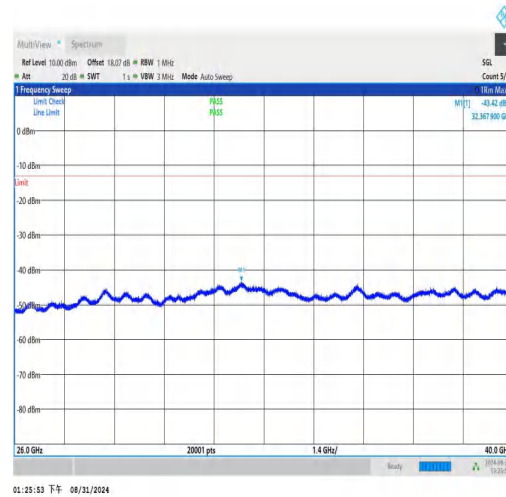
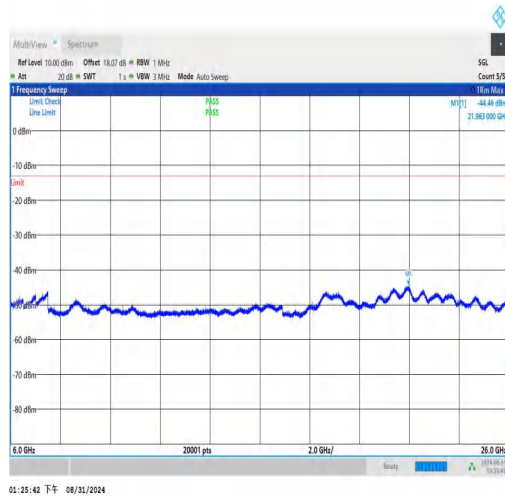
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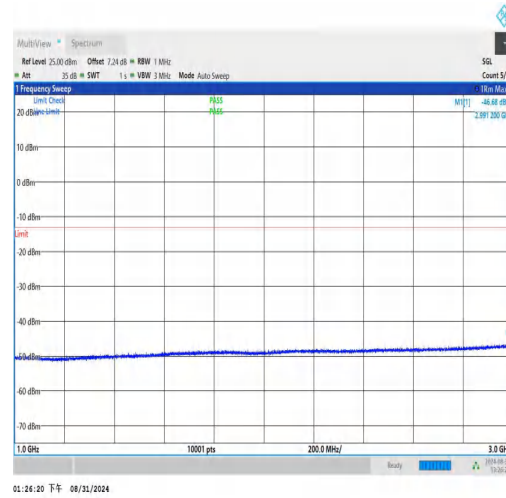
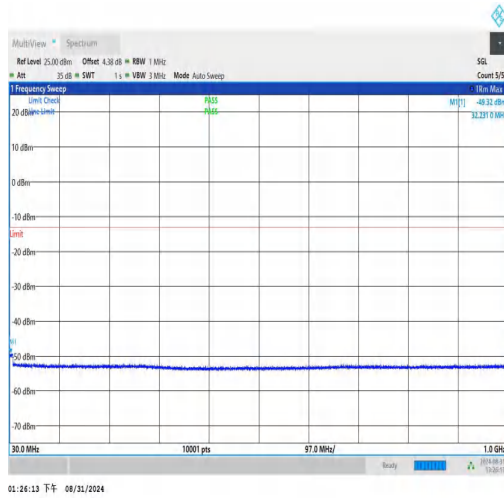
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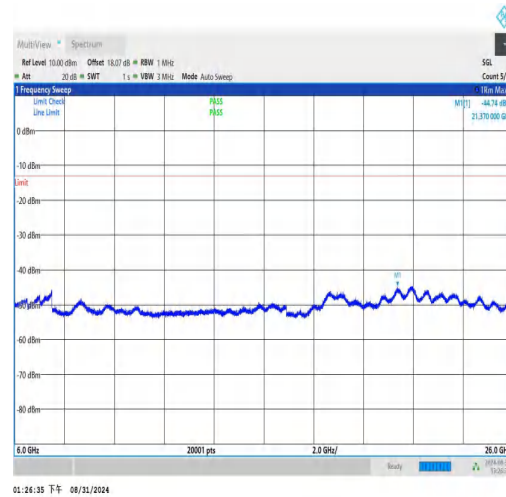
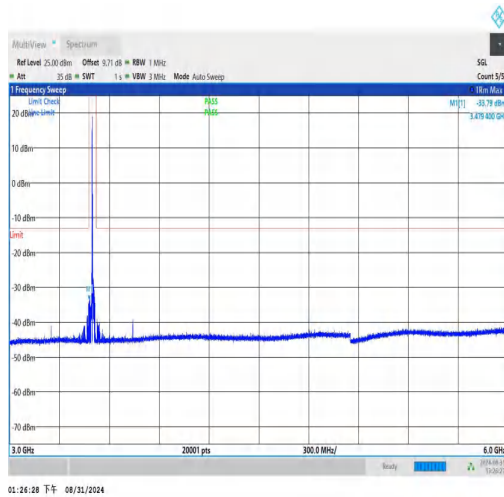
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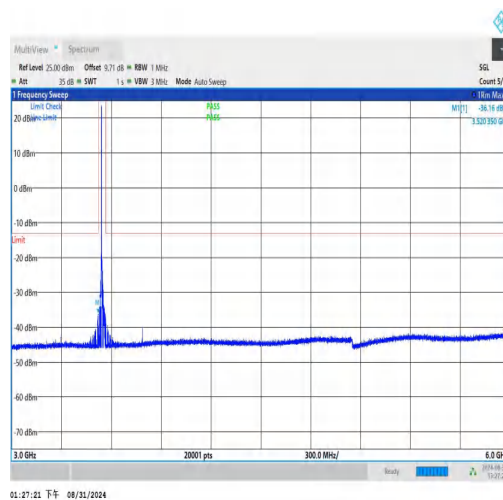
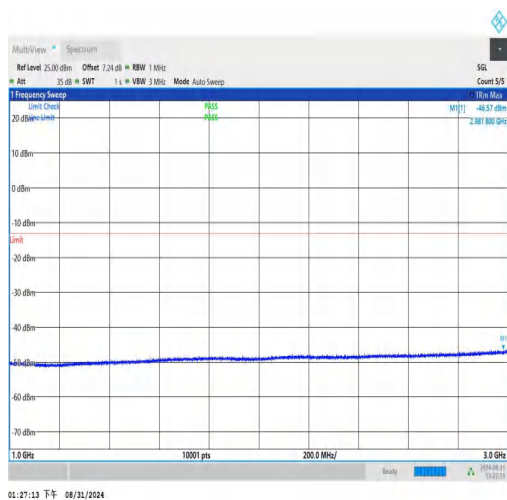
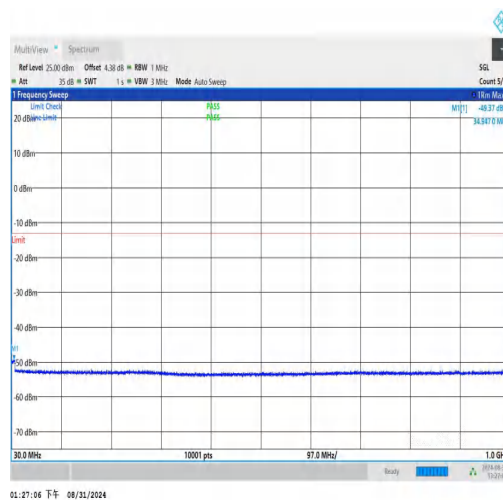
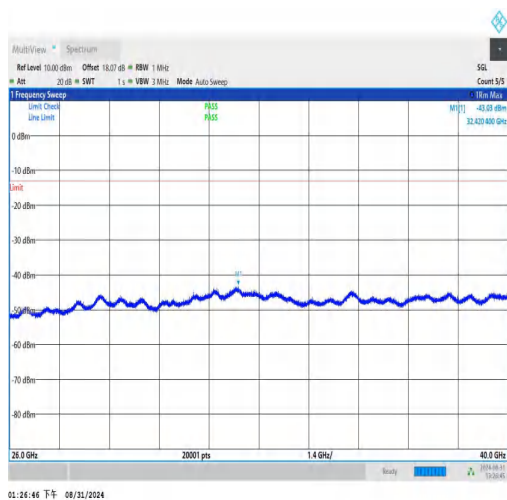


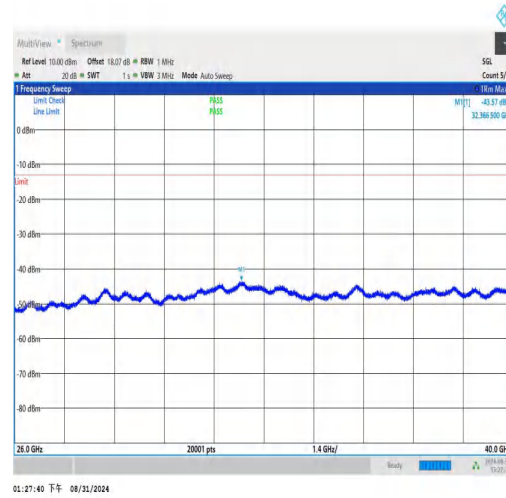
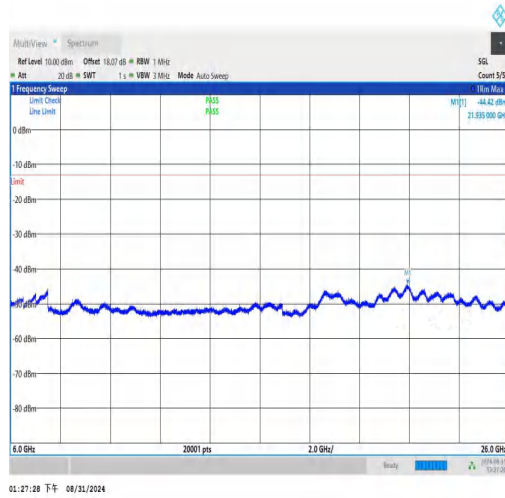
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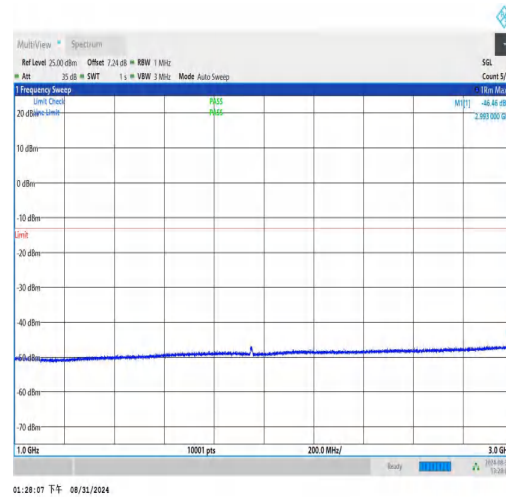
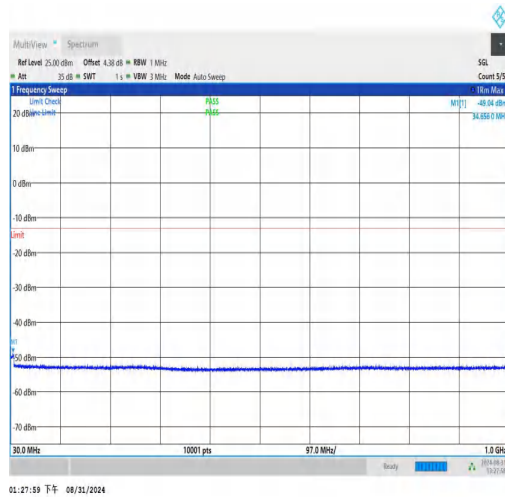
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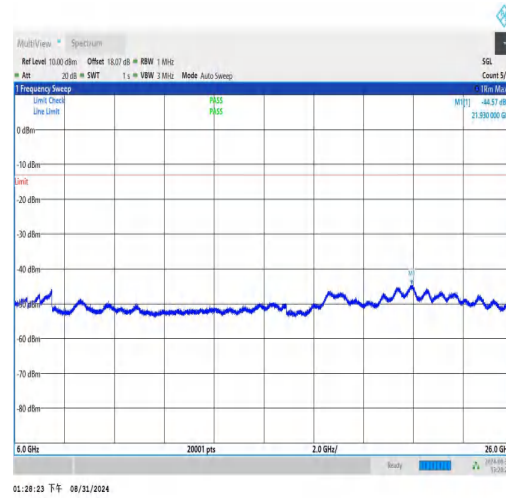
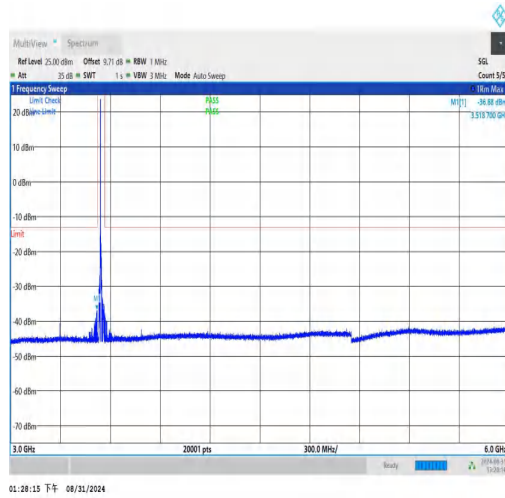
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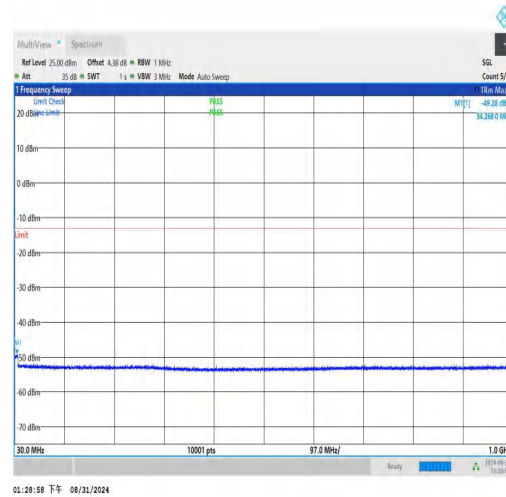
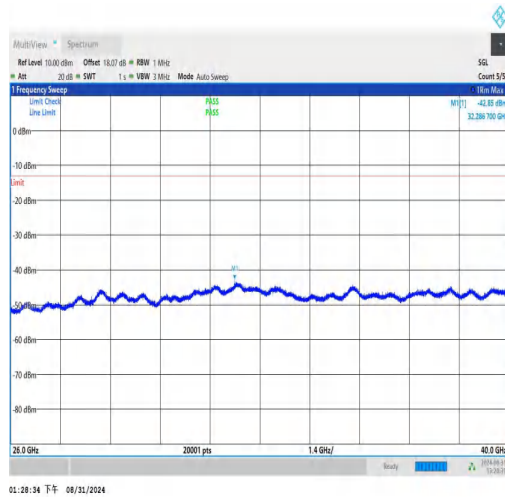
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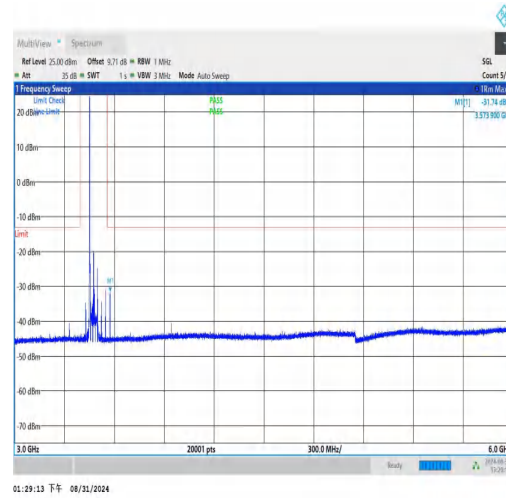
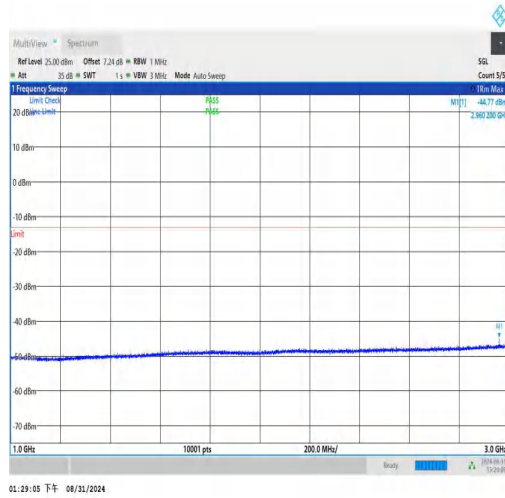
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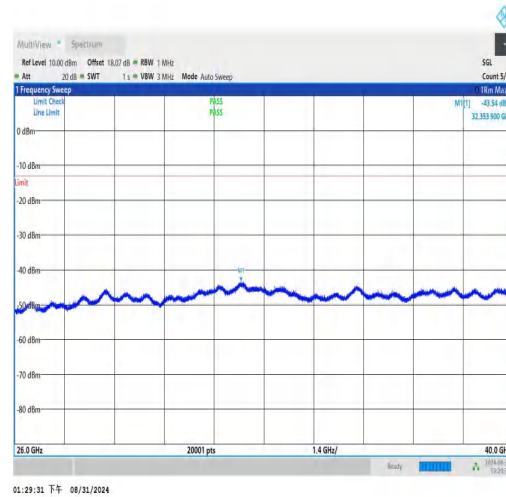
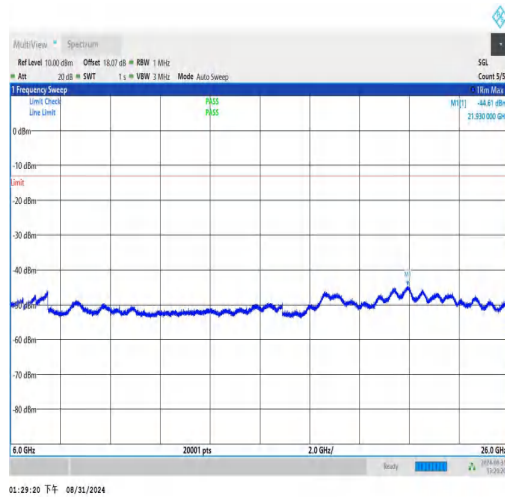
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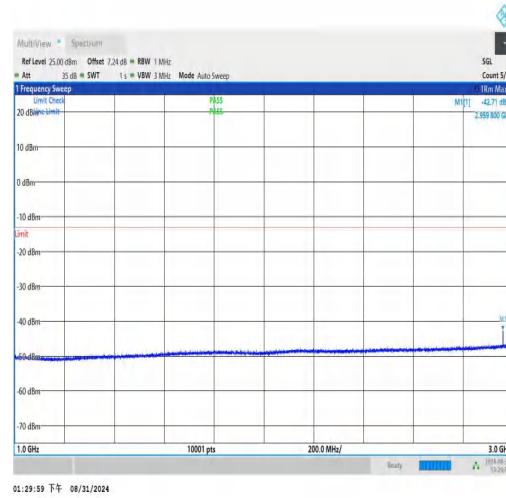
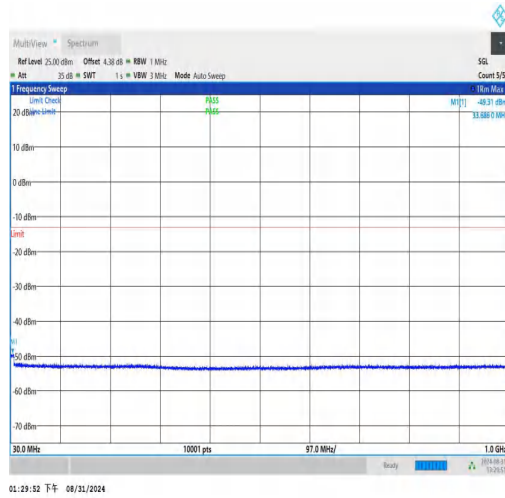
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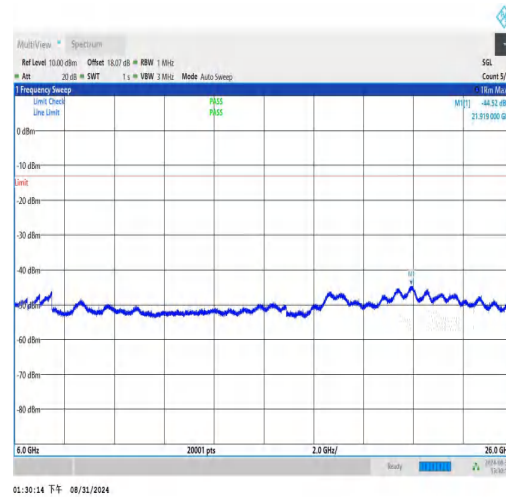
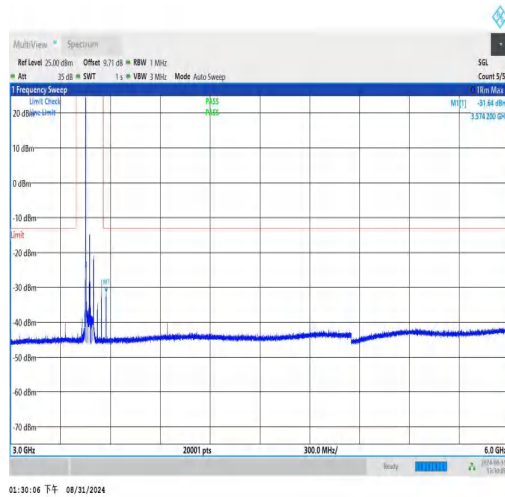
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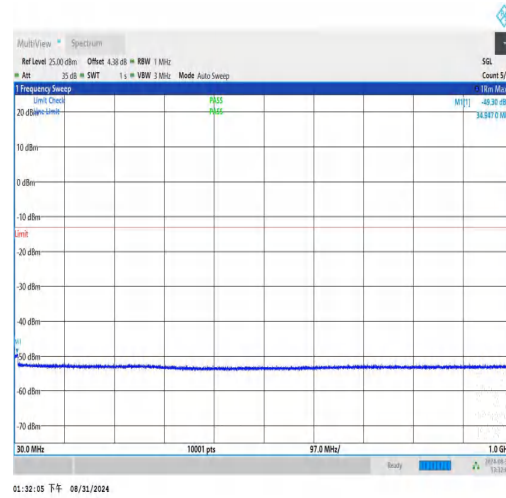
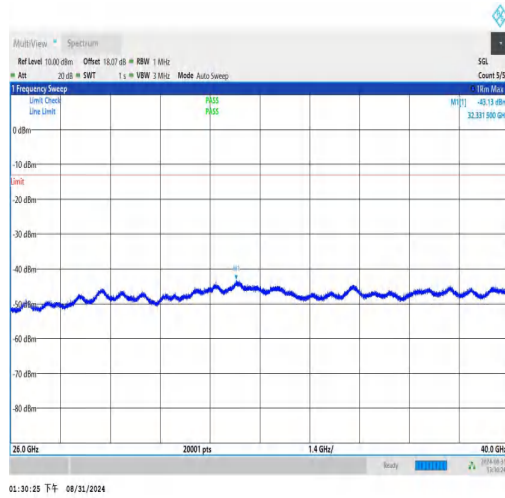
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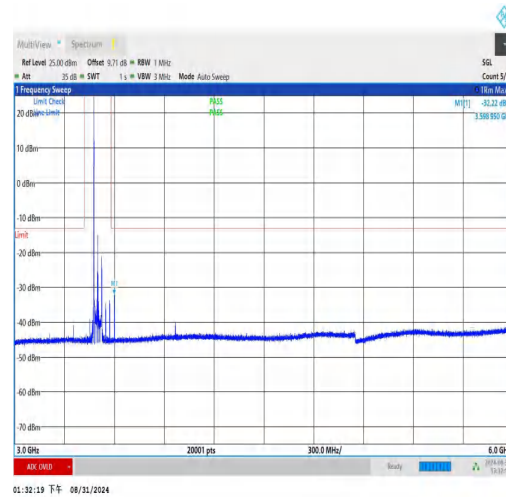
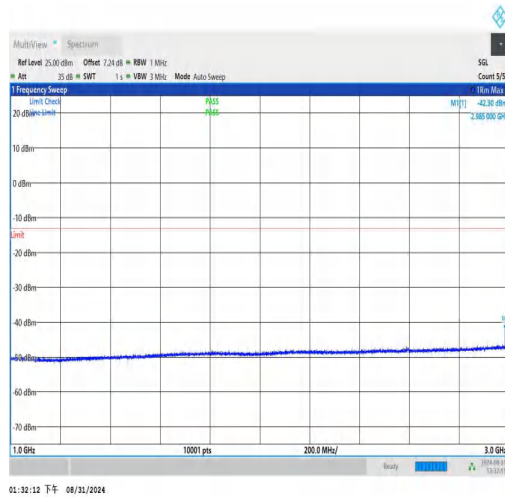
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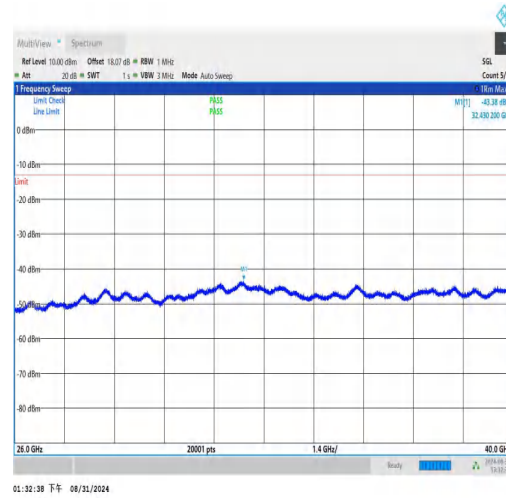
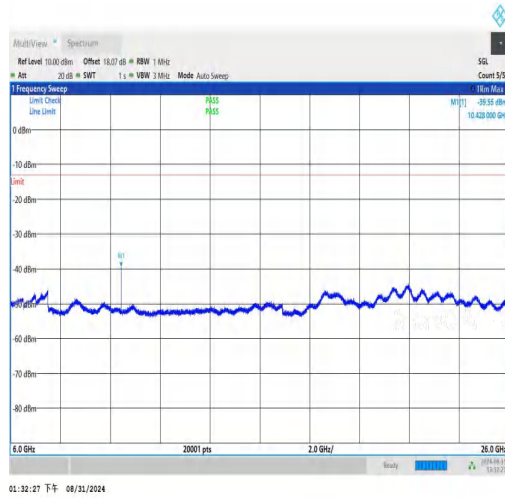
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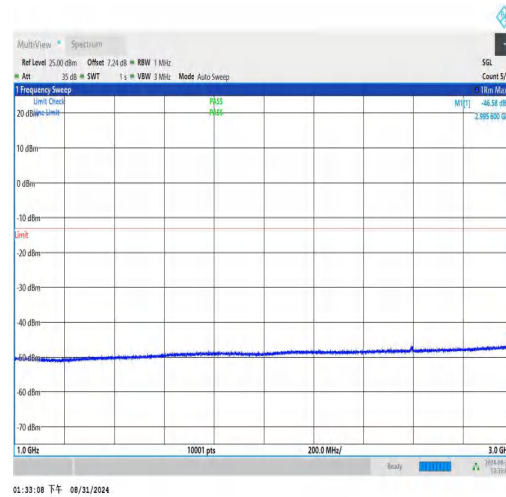
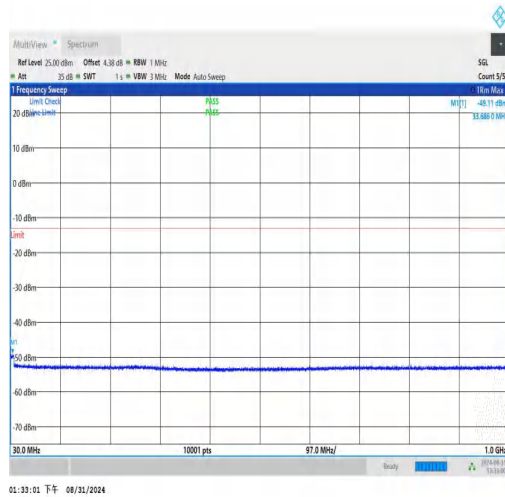
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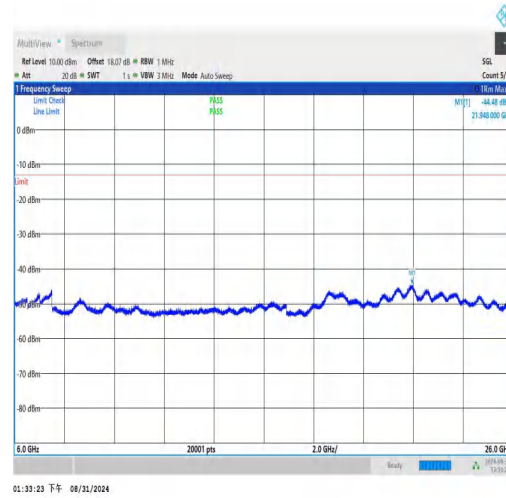
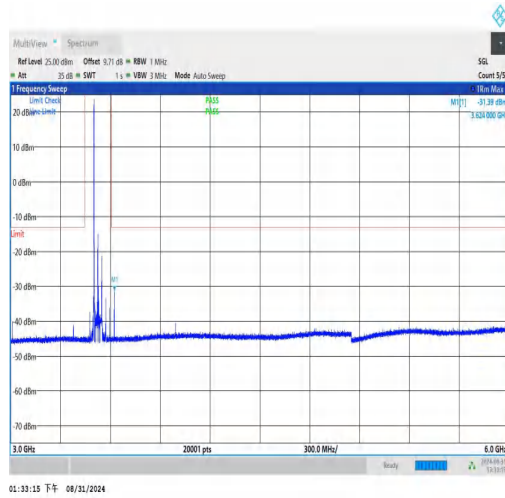
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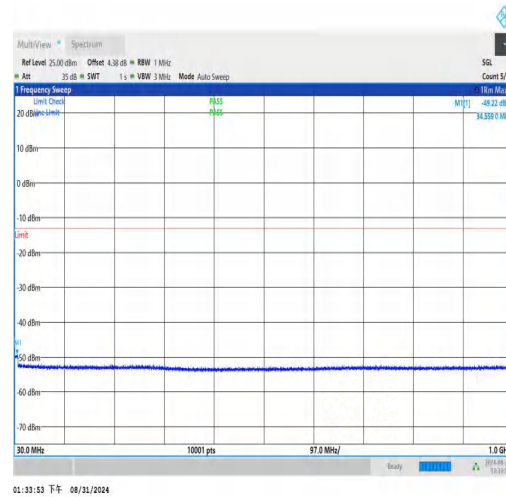
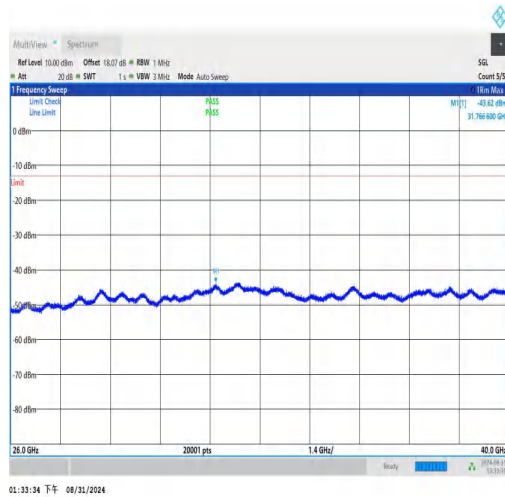
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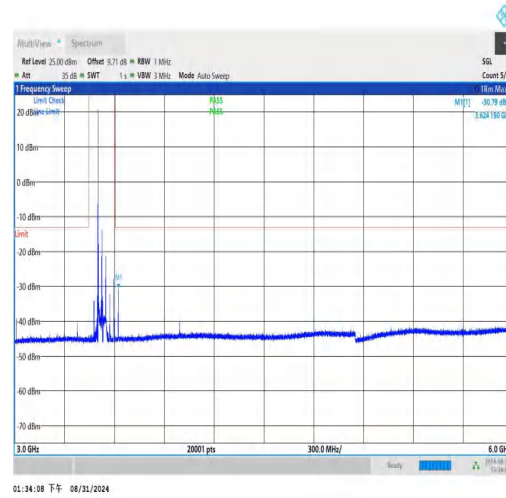
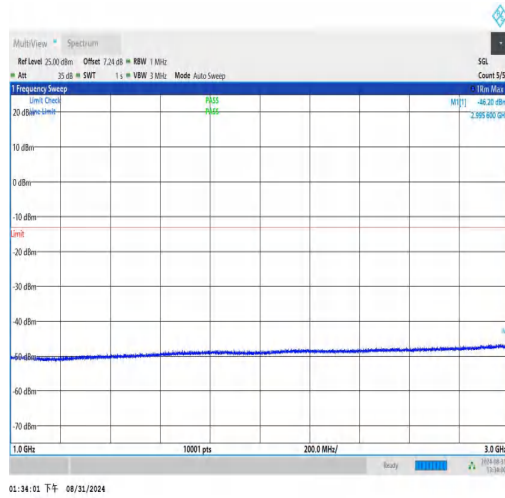
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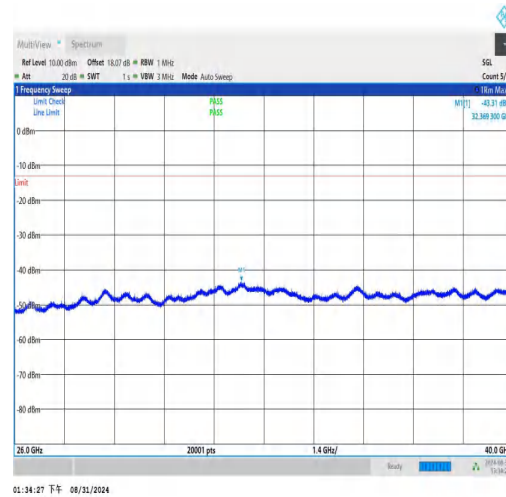
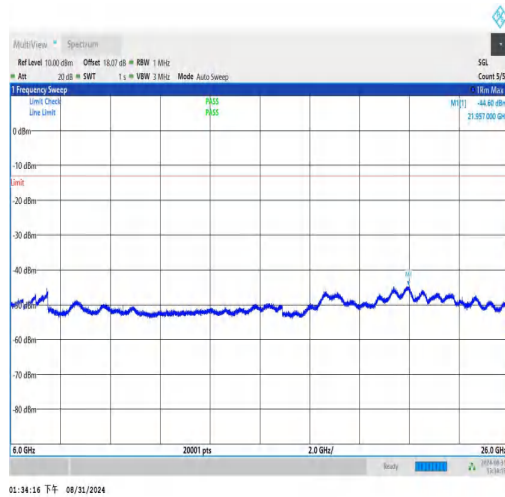
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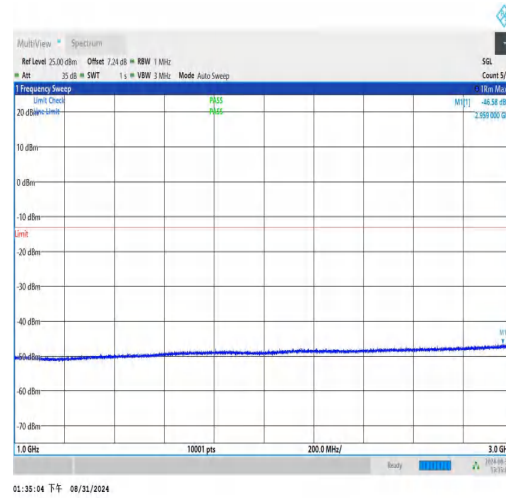
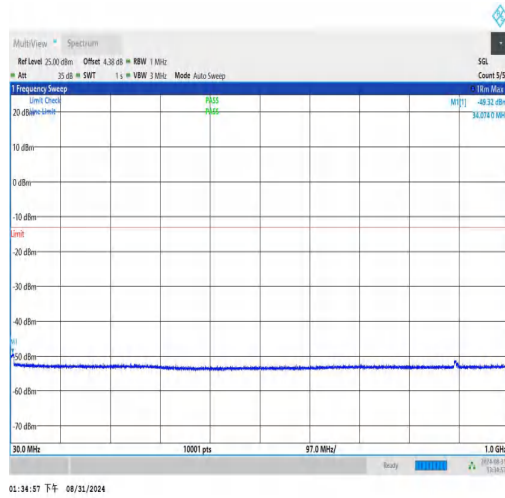
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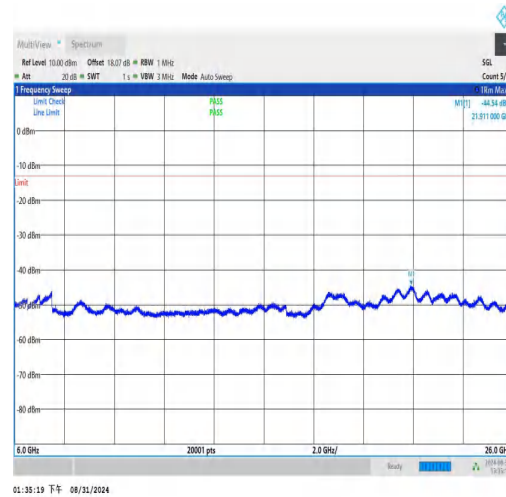
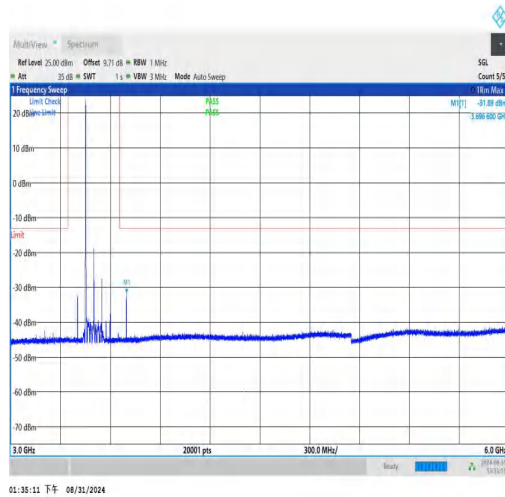
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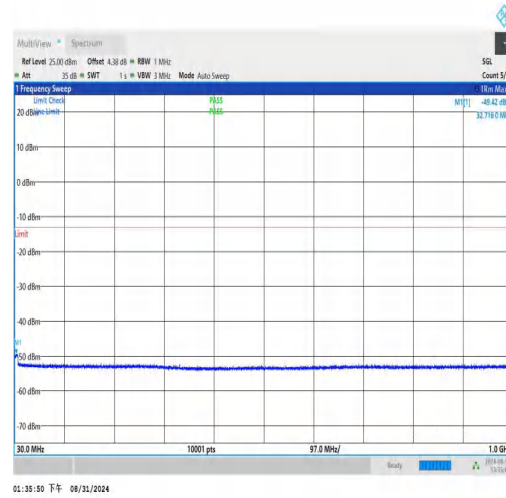
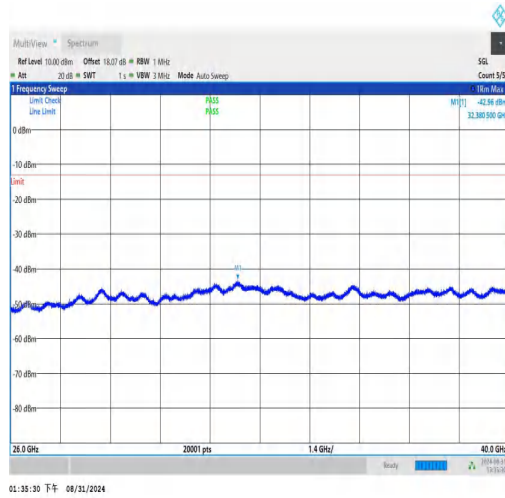
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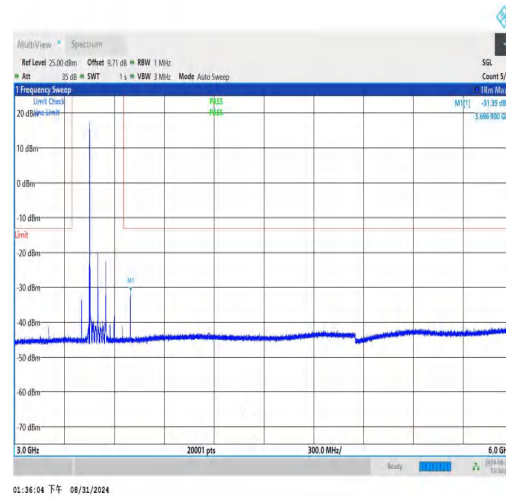
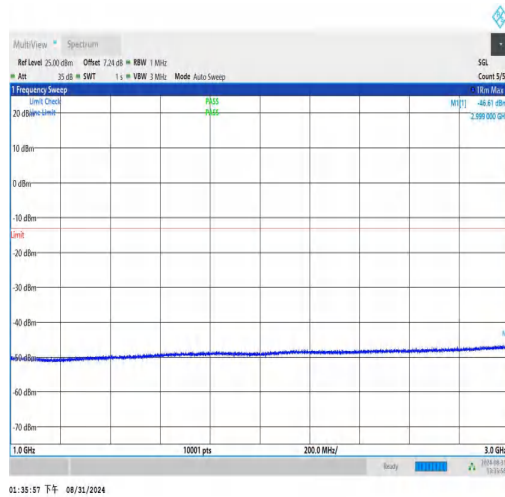
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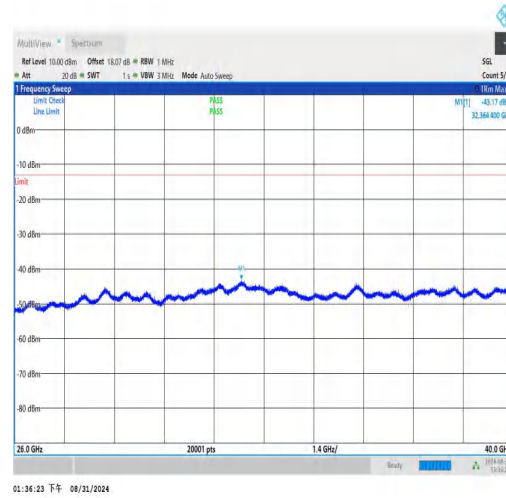
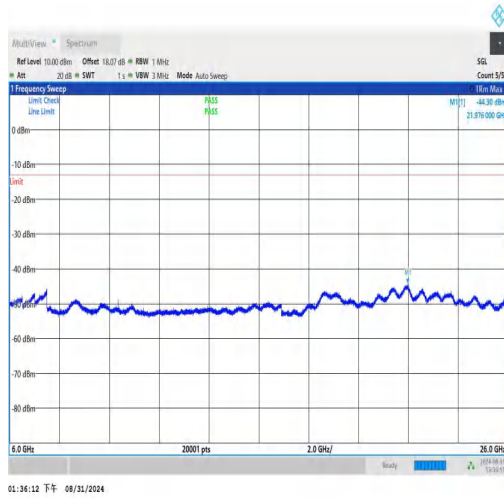
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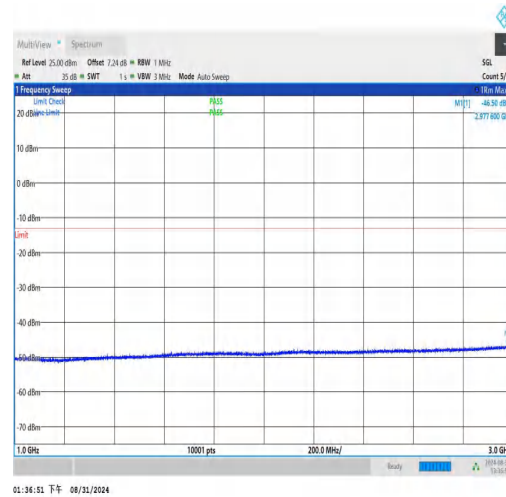
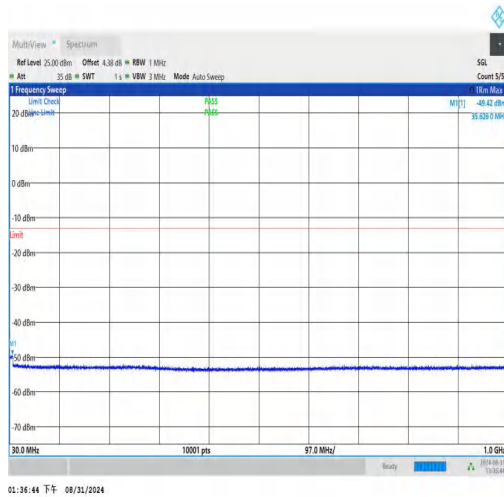
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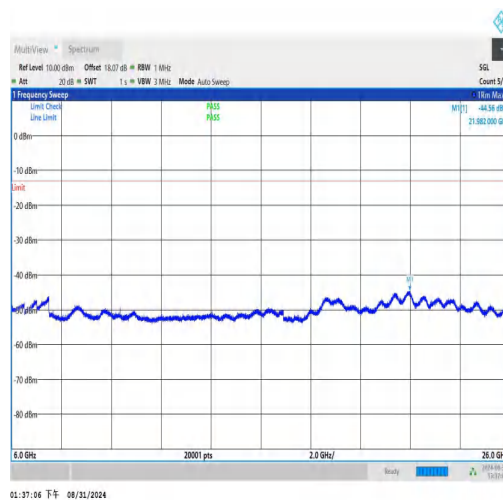


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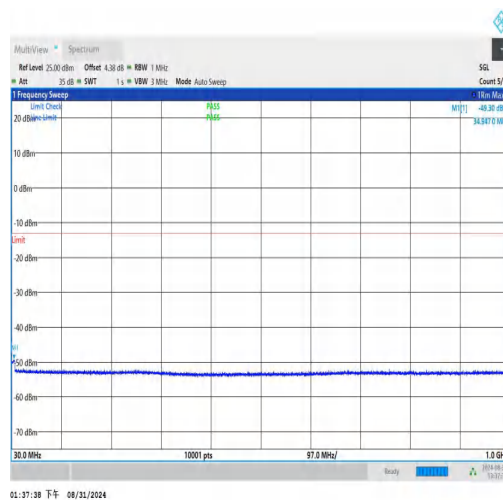
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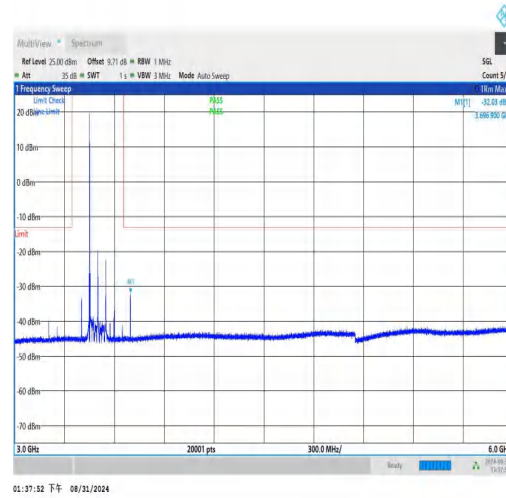
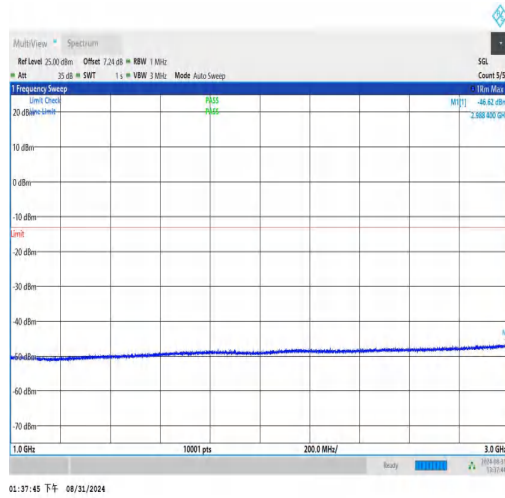
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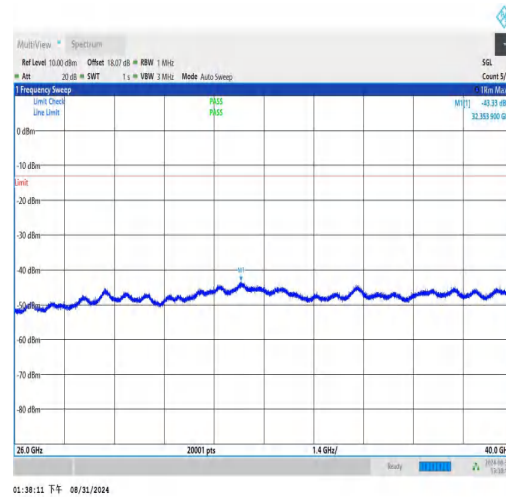
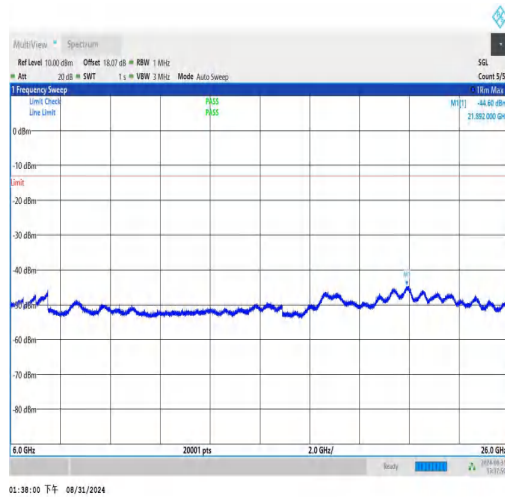


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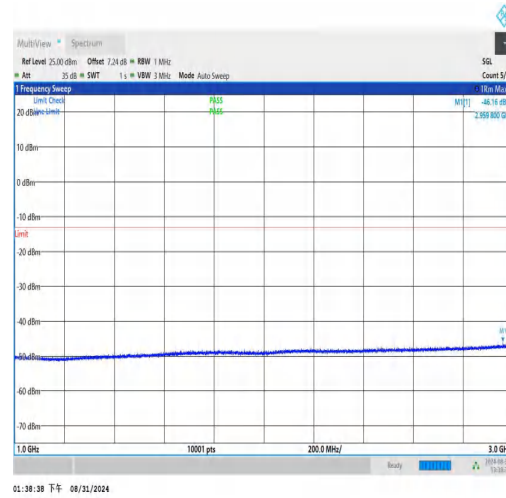
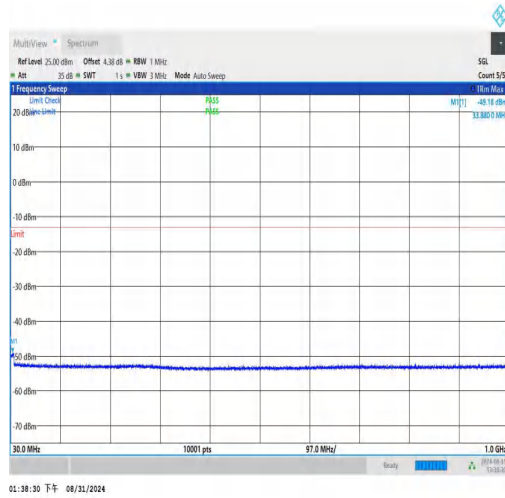
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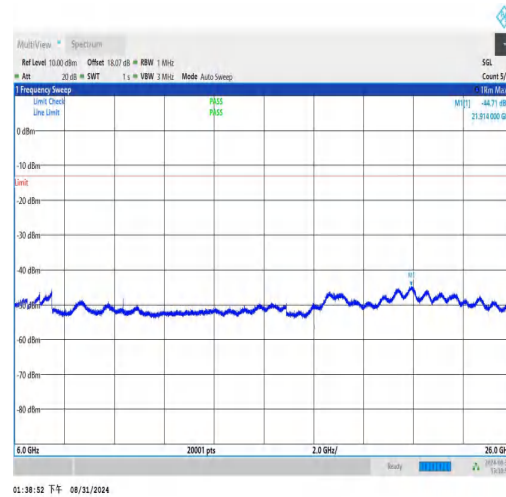
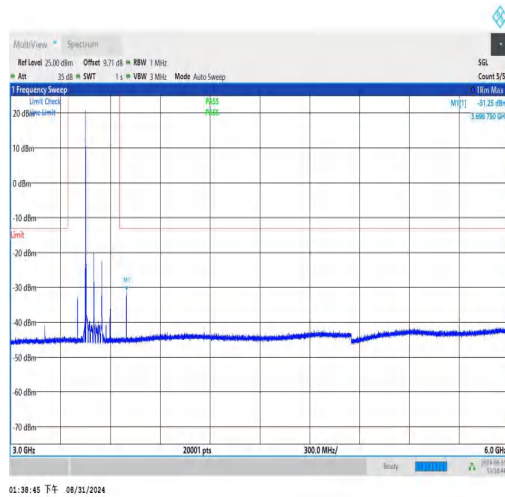
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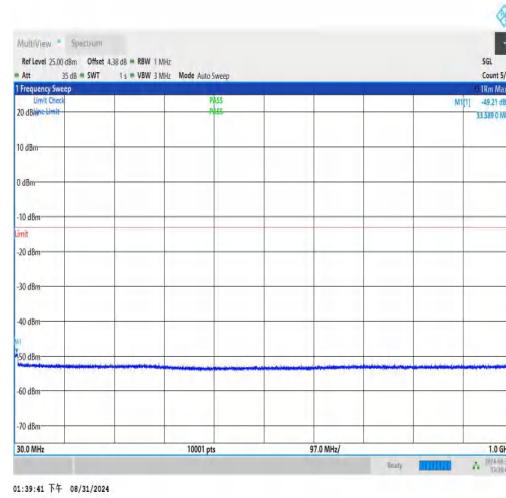
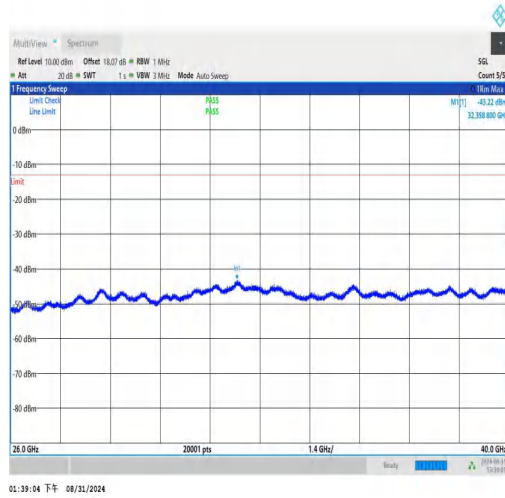
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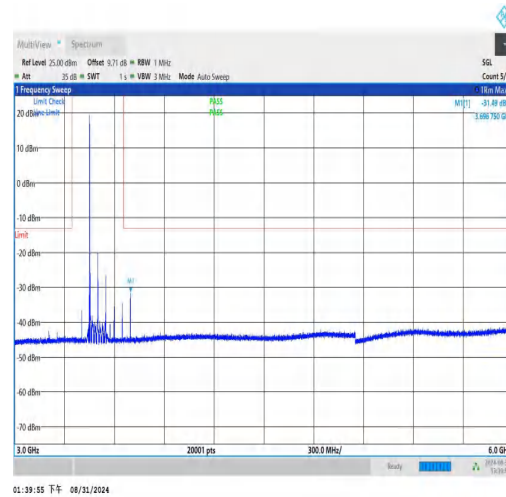
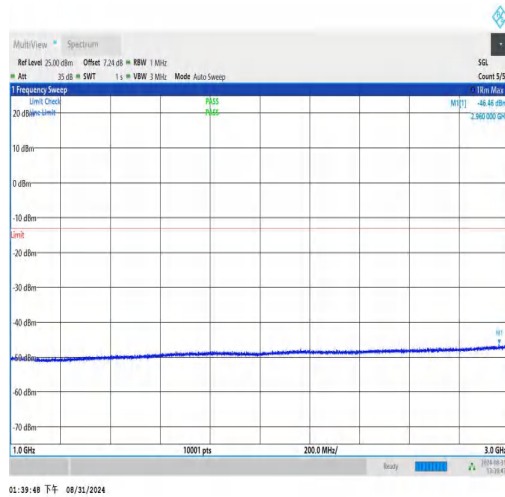
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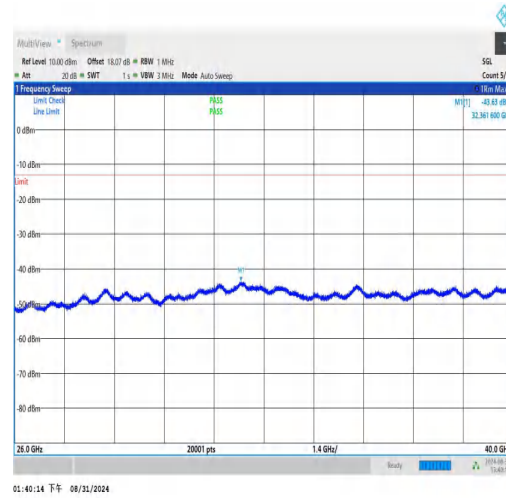
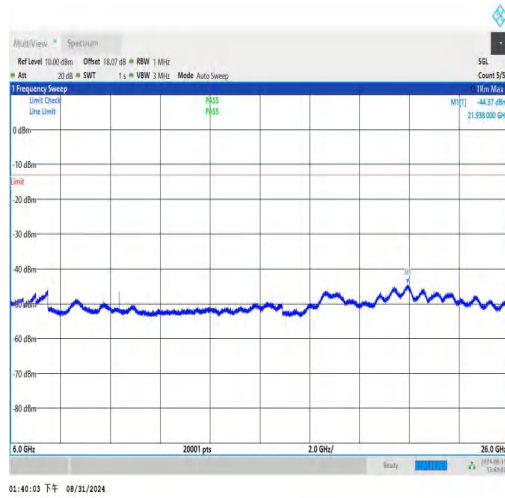
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NTNV_N77-3450-3550_PC3_30_100_H_TID2_N/
A_1000_3000_#1

NTNV_N77-3450-3550_PC3_30_100_H_TID2_N/
A_3000_6000_#1



NTNV_N77-3450-3550_PC3_30_100_H_TID2_N/
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NTNV_N77-3450-3550_PC3_30_100_H_TID2_N/
A_26000_40000_#1



Appendix F: Frequency Stability

Test Result

Frequency Error VS. Voltage

Voltage										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation (Hz)	Deviation (ppm)	Verdict
N77-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	VN	NT	16.00	0.00	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	VL	NT	10.00	0.00	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	VH	NT	12.00	0.00	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Outer_Full	VN	NT	2.00	0.00	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Outer_Full	VL	NT	8.00	0.00	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Outer_Full	VH	NT	14.00	0.00	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	VN	NT	3.00	0.00	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	VL	NT	13.00	0.00	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	VH	NT	3.00	0.00	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	VN	NT	7.00	0.00	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	VL	NT	13.00	0.00	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	VH	NT	14.00	0.00	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	VN	NT	1.00	0.00	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	VL	NT	13.00	0.00	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	VH	NT	19.00	0.01	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Outer_Full	VN	NT	7.00	0.00	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Outer_Full	VL	NT	10.00	0.00	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Outer_Full	VH	NT	16.00	0.00	PASS

Frequency Error VS. Temperature

Temperature										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation (Hz)	Deviation (ppm)	Verdict
N77-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	-30	13.000000	0.003714	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	-20	15.000000	0.004286	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	-10	16.000000	0.004571	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	0	18.000000	0.005143	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	10	9.000000	0.002571	PASS



N77-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	20	3.000000	0.000857	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	30	15.000000	0.004286	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	40	18.000000	0.005143	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	NV	50	11.000000	0.003143	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	-30	16.000000	0.004571	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	-20	10.000000	0.002857	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	-10	12.000000	0.003429	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	0	13.000000	0.003714	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	10	3.000000	0.000857	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	20	16.000000	0.004571	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	30	19.000000	0.005429	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	40	12.000000	0.003429	PASS
N77-3450-3550	30	100	DFT-QPSK	L	Outer_Full	NV	50	15.000000	0.004286	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	-30	16.000000	0.004571	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	-20	6.000000	0.001714	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	-10	16.000000	0.004571	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	0	6.000000	0.001714	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	10	19.000000	0.005429	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	20	12.000000	0.003429	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	30	15.000000	0.004286	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	40	9.000000	0.002571	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	M	Outer_Full	NV	50	6.000000	0.001714	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	-30	16.000000	0.004571	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	-20	17.000000	0.004857	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	-10	19.000000	0.005429	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	0	10.000000	0.002857	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	10	3.000000	0.000857	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	20	13.000000	0.003714	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	30	6.000000	0.001714	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	40	19.000000	0.005429	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	50	3.000000	0.000857	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	-30	10.000000	0.002857	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	-20	3.000000	0.000857	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	-10	6.000000	0.001714	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	0	18.000000	0.005143	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	10	16.000000	0.004571	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	20	6.000000	0.001714	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	30	19.000000	0.005429	PASS
N77-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	40	2.000000	0.000571	PASS



N77-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	NV	50	19.000000	0.005429	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	-30	16.000000	0.004571	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	-20	7.000000	0.002000	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	-10	9.000000	0.002571	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	0	3.000000	0.000857	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	10	19.000000	0.005429	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	20	13.000000	0.003714	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	30	15.000000	0.004286	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	40	6.000000	0.001714	PASS
N77-3450-3550	30	100	DFT-QPSK	H	Outer_Full	NV	50	6.000000	0.001714	PASS

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