



Appendix A: Effective (Isotropic) Radiated Power Output Data

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	EIRP (dBm)	Power Class	Verdict
N2	15	5	DFT-PI2BPSK	L	Edge_1RB_Left	20.24	19.54	PC3	PASS
N2	15	5	DFT-PI2BPSK	L	Edge_1RB_Right	20.18	19.48	PC3	PASS
N2	15	5	DFT-PI2BPSK	L	Outer_Full	20.1	19.4	PC3	PASS
N2	15	5	DFT-PI2BPSK	L	Inner_Full	20.66	19.96	PC3	PASS
N2	15	5	DFT-QPSK	L	Edge_1RB_Right	20.3	19.6	PC3	PASS
N2	15	5	DFT-QPSK	L	Outer_Full	20.29	19.59	PC3	PASS
N2	15	5	DFT-QPSK	L	Inner_Full	20.66	19.96	PC3	PASS
N2	15	5	CP-QPSK	L	Edge_1RB_Left	18.55	17.85	PC3	PASS
N2	15	5	CP-QPSK	L	Edge_1RB_Right	18.5	17.8	PC3	PASS
N2	15	5	CP-QPSK	L	Outer_Full	18.49	17.79	PC3	PASS
N2	15	5	CP-QPSK	L	Inner_Full	19.1	18.4	PC3	PASS
N2	15	5	CP-16QAM	L	Edge_1RB_Left	18.25	17.55	PC3	PASS
N2	15	5	CP-16QAM	L	Edge_1RB_Right	18.19	17.49	PC3	PASS
N2	15	5	CP-16QAM	L	Outer_Full	17.94	17.24	PC3	PASS
N2	15	5	CP-16QAM	L	Inner_Full	18.36	17.66	PC3	PASS
N2	15	5	CP-64QAM	L	Edge_1RB_Left	17.82	17.12	PC3	PASS
N2	15	5	CP-64QAM	L	Edge_1RB_Right	17.95	17.25	PC3	PASS
N2	15	5	CP-64QAM	L	Outer_Full	17.62	16.92	PC3	PASS
N2	15	5	CP-64QAM	L	Inner_Full	18.38	17.68	PC3	PASS
N2	15	5	CP-256QAM	L	Edge_1RB_Left	15.05	14.35	PC3	PASS
N2	15	5	CP-256QAM	L	Edge_1RB_Right	15.16	14.46	PC3	PASS
N2	15	5	CP-256QAM	L	Outer_Full	14.92	14.22	PC3	PASS
N2	15	5	CP-256QAM	L	Inner_Full	14.93	14.23	PC3	PASS
N2	15	5	DFT-PI2BPSK	M	Edge_1RB_Left	20.22	19.52	PC3	PASS
N2	15	5	DFT-PI2BPSK	M	Edge_1RB_Right	19.97	19.27	PC3	PASS
N2	15	5	DFT-PI2BPSK	M	Outer_Full	20	19.3	PC3	PASS
N2	15	5	DFT-PI2BPSK	M	Inner_Full	20.47	19.77	PC3	PASS
N2	15	5	DFT-QPSK	M	Edge_1RB_Left	20.23	19.53	PC3	PASS
N2	15	5	DFT-QPSK	M	Edge_1RB_Right	20	19.3	PC3	PASS
N2	15	5	DFT-QPSK	M	Outer_Full	20.01	19.31	PC3	PASS
N2	15	5	DFT-QPSK	M	Inner_Full	20.41	19.71	PC3	PASS
N2	15	5	CP-QPSK	M	Edge_1RB_Left	18.49	17.79	PC3	PASS



N2	15	5	CP-QPSK	M	Edge_1RB_Right	18.24	17.54	PC3	PASS
N2	15	5	CP-QPSK	M	Outer_Full	18.25	17.55	PC3	PASS
N2	15	5	CP-QPSK	M	Inner_Full	18.91	18.21	PC3	PASS
N2	15	5	CP-16QAM	M	Edge_1RB_Left	18.26	17.56	PC3	PASS
N2	15	5	CP-16QAM	M	Edge_1RB_Right	17.99	17.29	PC3	PASS
N2	15	5	CP-16QAM	M	Outer_Full	17.86	17.16	PC3	PASS
N2	15	5	CP-16QAM	M	Inner_Full	18.29	17.59	PC3	PASS
N2	15	5	CP-64QAM	M	Edge_1RB_Left	18.1	17.4	PC3	PASS
N2	15	5	CP-64QAM	M	Edge_1RB_Right	17.94	17.24	PC3	PASS
N2	15	5	CP-64QAM	M	Outer_Full	17.43	16.73	PC3	PASS
N2	15	5	CP-64QAM	M	Inner_Full	18.25	17.55	PC3	PASS
N2	15	5	CP-256QAM	M	Edge_1RB_Left	15.27	14.57	PC3	PASS
N2	15	5	CP-256QAM	M	Edge_1RB_Right	14.98	14.28	PC3	PASS
N2	15	5	CP-256QAM	M	Outer_Full	14.88	14.18	PC3	PASS
N2	15	5	CP-256QAM	M	Inner_Full	14.89	14.19	PC3	PASS
N2	15	5	DFT-PI2BPSK	H	Edge_1RB_Left	20.26	19.56	PC3	PASS
N2	15	5	DFT-PI2BPSK	H	Edge_1RB_Right	19.44	18.74	PC3	PASS
N2	15	5	DFT-PI2BPSK	H	Outer_Full	19.72	19.02	PC3	PASS
N2	15	5	DFT-PI2BPSK	H	Inner_Full	20.07	19.37	PC3	PASS
N2	15	5	DFT-QPSK	H	Edge_1RB_Left	20.11	19.41	PC3	PASS
N2	15	5	DFT-QPSK	H	Edge_1RB_Right	19.49	18.79	PC3	PASS
N2	15	5	DFT-QPSK	H	Outer_Full	19.64	18.94	PC3	PASS
N2	15	5	DFT-QPSK	H	Inner_Full	19.99	19.29	PC3	PASS
N2	15	5	CP-QPSK	H	Edge_1RB_Left	18.2	17.5	PC3	PASS
N2	15	5	CP-QPSK	H	Edge_1RB_Right	17.62	16.92	PC3	PASS
N2	15	5	CP-QPSK	H	Outer_Full	17.81	17.11	PC3	PASS
N2	15	5	CP-QPSK	H	Inner_Full	18.39	17.69	PC3	PASS
N2	15	5	CP-16QAM	H	Edge_1RB_Left	18.06	17.36	PC3	PASS
N2	15	5	CP-16QAM	H	Edge_1RB_Right	17.45	16.75	PC3	PASS
N2	15	5	CP-16QAM	H	Outer_Full	17.47	16.77	PC3	PASS
N2	15	5	CP-16QAM	H	Inner_Full	17.85	17.15	PC3	PASS
N2	15	5	CP-64QAM	H	Edge_1RB_Left	17.27	16.57	PC3	PASS
N2	15	5	CP-64QAM	H	Edge_1RB_Right	17.01	16.31	PC3	PASS
N2	15	5	CP-64QAM	H	Outer_Full	16.74	16.04	PC3	PASS
N2	15	5	CP-64QAM	H	Inner_Full	17.93	17.23	PC3	PASS
N2	15	5	CP-256QAM	H	Edge_1RB_Left	14.75	14.05	PC3	PASS
N2	15	5	CP-256QAM	H	Edge_1RB_Right	14.19	13.49	PC3	PASS
N2	15	5	CP-256QAM	H	Outer_Full	14.2	13.5	PC3	PASS
N2	15	5	CP-256QAM	H	Inner_Full	14.21	13.51	PC3	PASS
N2	15	10	DFT-PI2BPSK	L	Edge_1RB_Left	20.75	20.05	PC3	PASS



N2	15	10	DFT-PI2BPSK	L	Edge_1RB_Right	20.4	19.7	PC3	PASS
N2	15	10	DFT-PI2BPSK	L	Outer_Full	20.03	19.33	PC3	PASS
N2	15	10	DFT-PI2BPSK	L	Inner_Full	20.35	19.65	PC3	PASS
N2	15	10	DFT-QPSK	L	Edge_1RB_Left	20.54	19.84	PC3	PASS
N2	15	10	DFT-QPSK	L	Edge_1RB_Right	20.4	19.7	PC3	PASS
N2	15	10	DFT-QPSK	L	Outer_Full	20.09	19.39	PC3	PASS
N2	15	10	DFT-QPSK	L	Inner_Full	20.42	19.72	PC3	PASS
N2	15	10	CP-QPSK	L	Edge_1RB_Left	18.89	18.19	PC3	PASS
N2	15	10	CP-QPSK	L	Edge_1RB_Right	18.72	18.02	PC3	PASS
N2	15	10	CP-QPSK	L	Outer_Full	18.34	17.64	PC3	PASS
N2	15	10	CP-QPSK	L	Inner_Full	18.88	18.18	PC3	PASS
N2	15	10	CP-16QAM	L	Edge_1RB_Left	18.57	17.87	PC3	PASS
N2	15	10	CP-16QAM	L	Edge_1RB_Right	18.42	17.72	PC3	PASS
N2	15	10	CP-16QAM	L	Outer_Full	17.84	17.14	PC3	PASS
N2	15	10	CP-16QAM	L	Inner_Full	18.25	17.55	PC3	PASS
N2	15	10	CP-64QAM	L	Edge_1RB_Left	18.46	17.76	PC3	PASS
N2	15	10	CP-64QAM	L	Edge_1RB_Right	18.33	17.63	PC3	PASS
N2	15	10	CP-64QAM	L	Outer_Full	17.73	17.03	PC3	PASS
N2	15	10	CP-64QAM	L	Inner_Full	18.27	17.57	PC3	PASS
N2	15	10	CP-256QAM	L	Edge_1RB_Left	15.65	14.95	PC3	PASS
N2	15	10	CP-256QAM	L	Edge_1RB_Right	15.5	14.8	PC3	PASS
N2	15	10	CP-256QAM	L	Outer_Full	15.11	14.41	PC3	PASS
N2	15	10	CP-256QAM	L	Inner_Full	14.85	14.15	PC3	PASS
N2	15	10	DFT-PI2BPSK	M	Edge_1RB_Left	20.98	20.28	PC3	PASS
N2	15	10	DFT-PI2BPSK	M	Edge_1RB_Right	19.93	19.23	PC3	PASS
N2	15	10	DFT-PI2BPSK	M	Outer_Full	20.07	19.37	PC3	PASS
N2	15	10	DFT-PI2BPSK	M	Inner_Full	20.41	19.71	PC3	PASS
N2	15	10	DFT-QPSK	M	Edge_1RB_Left	20.82	20.12	PC3	PASS
N2	15	10	DFT-QPSK	M	Edge_1RB_Right	20.06	19.36	PC3	PASS
N2	15	10	DFT-QPSK	M	Outer_Full	19.99	19.29	PC3	PASS
N2	15	10	DFT-QPSK	M	Inner_Full	20.37	19.67	PC3	PASS
N2	15	10	CP-QPSK	M	Edge_1RB_Left	19.1	18.4	PC3	PASS
N2	15	10	CP-QPSK	M	Edge_1RB_Right	18.25	17.55	PC3	PASS
N2	15	10	CP-QPSK	M	Outer_Full	18.24	17.54	PC3	PASS
N2	15	10	CP-QPSK	M	Inner_Full	18.91	18.21	PC3	PASS
N2	15	10	CP-16QAM	M	Edge_1RB_Left	18.9	18.2	PC3	PASS
N2	15	10	CP-16QAM	M	Edge_1RB_Right	17.98	17.28	PC3	PASS
N2	15	10	CP-16QAM	M	Outer_Full	17.85	17.15	PC3	PASS
N2	15	10	CP-16QAM	M	Inner_Full	18.22	17.52	PC3	PASS
N2	15	10	CP-64QAM	M	Edge_1RB_Left	18.56	17.86	PC3	PASS



N2	15	10	CP-64QAM	M	Edge_1RB_Right	17.97	17.27	PC3	PASS
N2	15	10	CP-64QAM	M	Outer_Full	17.4	16.7	PC3	PASS
N2	15	10	CP-64QAM	M	Inner_Full	18.21	17.51	PC3	PASS
N2	15	10	CP-256QAM	M	Edge_1RB_Left	15.87	15.17	PC3	PASS
N2	15	10	CP-256QAM	M	Edge_1RB_Right	15.15	14.45	PC3	PASS
N2	15	10	CP-256QAM	M	Outer_Full	15.02	14.32	PC3	PASS
N2	15	10	CP-256QAM	M	Inner_Full	14.96	14.26	PC3	PASS
N2	15	10	DFT-PI2BPSK	H	Edge_1RB_Left	20.48	19.78	PC3	PASS
N2	15	10	DFT-PI2BPSK	H	Edge_1RB_Right	20	19.3	PC3	PASS
N2	15	10	DFT-PI2BPSK	H	Outer_Full	20.34	19.64	PC3	PASS
N2	15	10	DFT-PI2BPSK	H	Inner_Full	20.66	19.96	PC3	PASS
N2	15	10	DFT-QPSK	H	Edge_1RB_Left	20.66	19.96	PC3	PASS
N2	15	10	DFT-QPSK	H	Edge_1RB_Right	19.97	19.27	PC3	PASS
N2	15	10	DFT-QPSK	H	Outer_Full	20.18	19.48	PC3	PASS
N2	15	10	DFT-QPSK	H	Inner_Full	20.52	19.82	PC3	PASS
N2	15	10	CP-QPSK	H	Edge_1RB_Left	19.5	18.8	PC3	PASS
N2	15	10	CP-QPSK	H	Edge_1RB_Right	18.12	17.42	PC3	PASS
N2	15	10	CP-QPSK	H	Outer_Full	18.47	17.77	PC3	PASS
N2	15	10	CP-QPSK	H	Inner_Full	18.91	18.21	PC3	PASS
N2	15	10	CP-16QAM	H	Edge_1RB_Left	19.23	18.53	PC3	PASS
N2	15	10	CP-16QAM	H	Edge_1RB_Right	17.95	17.25	PC3	PASS
N2	15	10	CP-16QAM	H	Outer_Full	18.01	17.31	PC3	PASS
N2	15	10	CP-16QAM	H	Inner_Full	18.34	17.64	PC3	PASS
N2	15	10	CP-64QAM	H	Edge_1RB_Left	18.34	17.64	PC3	PASS
N2	15	10	CP-64QAM	H	Edge_1RB_Right	17.48	16.78	PC3	PASS
N2	15	10	CP-64QAM	H	Outer_Full	17.3	16.6	PC3	PASS
N2	15	10	CP-64QAM	H	Inner_Full	18.45	17.75	PC3	PASS
N2	15	10	CP-256QAM	H	Edge_1RB_Left	15.94	15.24	PC3	PASS
N2	15	10	CP-256QAM	H	Edge_1RB_Right	14.69	13.99	PC3	PASS
N2	15	10	CP-256QAM	H	Outer_Full	14.73	14.03	PC3	PASS
N2	15	10	CP-256QAM	H	Inner_Full	14.67	13.97	PC3	PASS
N2	15	15	DFT-PI2BPSK	L	Edge_1RB_Left	20.99	20.29	PC3	PASS
N2	15	15	DFT-PI2BPSK	L	Edge_1RB_Right	20.85	20.15	PC3	PASS
N2	15	15	DFT-PI2BPSK	L	Outer_Full	20.31	19.61	PC3	PASS
N2	15	15	DFT-PI2BPSK	L	Inner_Full	20.48	19.78	PC3	PASS
N2	15	15	DFT-QPSK	L	Edge_1RB_Left	20.94	20.24	PC3	PASS
N2	15	15	DFT-QPSK	L	Edge_1RB_Right	20.86	20.16	PC3	PASS
N2	15	15	DFT-QPSK	L	Outer_Full	20.22	19.52	PC3	PASS
N2	15	15	DFT-QPSK	L	Inner_Full	20.56	19.86	PC3	PASS
N2	15	15	CP-QPSK	L	Edge_1RB_Left	19.25	18.55	PC3	PASS



N2	15	15	CP-QPSK	L	Edge_1RB_Right	19.11	18.41	PC3	PASS
N2	15	15	CP-QPSK	L	Outer_Full	18.64	17.94	PC3	PASS
N2	15	15	CP-QPSK	L	Inner_Full	19.02	18.32	PC3	PASS
N2	15	15	CP-16QAM	L	Edge_1RB_Left	18.88	18.18	PC3	PASS
N2	15	15	CP-16QAM	L	Edge_1RB_Right	18.86	18.16	PC3	PASS
N2	15	15	CP-16QAM	L	Outer_Full	18.18	17.48	PC3	PASS
N2	15	15	CP-16QAM	L	Inner_Full	18.41	17.71	PC3	PASS
N2	15	15	CP-64QAM	L	Edge_1RB_Left	18.7	18	PC3	PASS
N2	15	15	CP-64QAM	L	Edge_1RB_Right	18.7	18	PC3	PASS
N2	15	15	CP-64QAM	L	Outer_Full	17.97	17.27	PC3	PASS
N2	15	15	CP-64QAM	L	Inner_Full	18.44	17.74	PC3	PASS
N2	15	15	CP-256QAM	L	Edge_1RB_Left	15.98	15.28	PC3	PASS
N2	15	15	CP-256QAM	L	Edge_1RB_Right	16.05	15.35	PC3	PASS
N2	15	15	CP-256QAM	L	Outer_Full	15.37	14.67	PC3	PASS
N2	15	15	CP-256QAM	L	Inner_Full	15.14	14.44	PC3	PASS
N2	15	15	DFT-PI2BPSK	M	Edge_1RB_Left	20.99	20.29	PC3	PASS
N2	15	15	DFT-PI2BPSK	M	Edge_1RB_Right	20.39	19.69	PC3	PASS
N2	15	15	DFT-PI2BPSK	M	Outer_Full	20.12	19.42	PC3	PASS
N2	15	15	DFT-PI2BPSK	M	Inner_Full	20.32	19.62	PC3	PASS
N2	15	15	DFT-QPSK	M	Edge_1RB_Left	20.76	20.06	PC3	PASS
N2	15	15	DFT-QPSK	M	Edge_1RB_Right	20.53	19.83	PC3	PASS
N2	15	15	DFT-QPSK	M	Outer_Full	20.02	19.32	PC3	PASS
N2	15	15	DFT-QPSK	M	Inner_Full	20.3	19.6	PC3	PASS
N2	15	15	CP-QPSK	M	Edge_1RB_Left	19.37	18.67	PC3	PASS
N2	15	15	CP-QPSK	M	Edge_1RB_Right	18.69	17.99	PC3	PASS
N2	15	15	CP-QPSK	M	Outer_Full	18.32	17.62	PC3	PASS
N2	15	15	CP-QPSK	M	Inner_Full	18.68	17.98	PC3	PASS
N2	15	15	CP-16QAM	M	Edge_1RB_Left	19.13	18.43	PC3	PASS
N2	15	15	CP-16QAM	M	Edge_1RB_Right	18.41	17.71	PC3	PASS
N2	15	15	CP-16QAM	M	Outer_Full	17.88	17.18	PC3	PASS
N2	15	15	CP-16QAM	M	Inner_Full	18.13	17.43	PC3	PASS
N2	15	15	CP-64QAM	M	Edge_1RB_Left	18.75	18.05	PC3	PASS
N2	15	15	CP-64QAM	M	Edge_1RB_Right	18.42	17.72	PC3	PASS
N2	15	15	CP-64QAM	M	Outer_Full	17.47	16.77	PC3	PASS
N2	15	15	CP-64QAM	M	Inner_Full	18.12	17.42	PC3	PASS
N2	15	15	CP-256QAM	M	Edge_1RB_Left	16.1	15.4	PC3	PASS
N2	15	15	CP-256QAM	M	Edge_1RB_Right	15.39	14.69	PC3	PASS
N2	15	15	CP-256QAM	M	Outer_Full	15.03	14.33	PC3	PASS
N2	15	15	CP-256QAM	M	Inner_Full	14.78	14.08	PC3	PASS
N2	15	15	DFT-PI2BPSK	H	Edge_1RB_Left	20.79	20.09	PC3	PASS



N2	15	15	DFT-PI2BPSK	H	Edge_1RB_Right	20.36	19.66	PC3	PASS
N2	15	15	DFT-PI2BPSK	H	Outer_Full	20.44	19.74	PC3	PASS
N2	15	15	DFT-PI2BPSK	H	Inner_Full	20.92	20.22	PC3	PASS
N2	15	15	DFT-QPSK	H	Edge_1RB_Left	20.49	19.79	PC3	PASS
N2	15	15	DFT-QPSK	H	Edge_1RB_Right	20.35	19.65	PC3	PASS
N2	15	15	DFT-QPSK	H	Outer_Full	20.55	19.85	PC3	PASS
N2	15	15	DFT-QPSK	H	Inner_Full	20.85	20.15	PC3	PASS
N2	15	15	CP-QPSK	H	Edge_1RB_Left	19.71	19.01	PC3	PASS
N2	15	15	CP-QPSK	H	Edge_1RB_Right	18.58	17.88	PC3	PASS
N2	15	15	CP-QPSK	H	Outer_Full	18.63	17.93	PC3	PASS
N2	15	15	CP-QPSK	H	Inner_Full	19.03	18.33	PC3	PASS
N2	15	15	CP-16QAM	H	Edge_1RB_Left	19.39	18.69	PC3	PASS
N2	15	15	CP-16QAM	H	Edge_1RB_Right	18.24	17.54	PC3	PASS
N2	15	15	CP-16QAM	H	Outer_Full	18.13	17.43	PC3	PASS
N2	15	15	CP-16QAM	H	Inner_Full	18.47	17.77	PC3	PASS
N2	15	15	CP-64QAM	H	Edge_1RB_Left	18.69	17.99	PC3	PASS
N2	15	15	CP-64QAM	H	Edge_1RB_Right	17.71	17.01	PC3	PASS
N2	15	15	CP-64QAM	H	Outer_Full	17.44	16.74	PC3	PASS
N2	15	15	CP-64QAM	H	Inner_Full	18.58	17.88	PC3	PASS
N2	15	15	CP-256QAM	H	Edge_1RB_Left	16.21	15.51	PC3	PASS
N2	15	15	CP-256QAM	H	Edge_1RB_Right	15.1	14.4	PC3	PASS
N2	15	15	CP-256QAM	H	Outer_Full	14.97	14.27	PC3	PASS
N2	15	15	CP-256QAM	H	Inner_Full	14.85	14.15	PC3	PASS
N2	15	20	DFT-PI2BPSK	L	Edge_1RB_Left	20.81	20.11	PC3	PASS
N2	15	20	DFT-PI2BPSK	L	Edge_1RB_Right	20.53	19.83	PC3	PASS
N2	15	20	DFT-PI2BPSK	L	Outer_Full	19.88	19.18	PC3	PASS
N2	15	20	DFT-PI2BPSK	L	Inner_Full	20.05	19.35	PC3	PASS
N2	15	20	DFT-QPSK	L	Edge_1RB_Left	20.68	19.98	PC3	PASS
N2	15	20	DFT-QPSK	L	Edge_1RB_Right	20.4	19.7	PC3	PASS
N2	15	20	DFT-QPSK	L	Outer_Full	19.85	19.15	PC3	PASS
N2	15	20	DFT-QPSK	L	Inner_Full	20.05	19.35	PC3	PASS
N2	15	20	CP-QPSK	L	Edge_1RB_Left	18.83	18.13	PC3	PASS
N2	15	20	CP-QPSK	L	Edge_1RB_Right	18.82	18.12	PC3	PASS
N2	15	20	CP-QPSK	L	Outer_Full	18.33	17.63	PC3	PASS
N2	15	20	CP-QPSK	L	Inner_Full	18.62	17.92	PC3	PASS
N2	15	20	CP-16QAM	L	Edge_1RB_Left	18.65	17.95	PC3	PASS
N2	15	20	CP-16QAM	L	Edge_1RB_Right	18.48	17.78	PC3	PASS
N2	15	20	CP-16QAM	L	Outer_Full	17.79	17.09	PC3	PASS
N2	15	20	CP-16QAM	L	Inner_Full	17.99	17.29	PC3	PASS
N2	15	20	CP-64QAM	L	Edge_1RB_Left	18.22	17.52	PC3	PASS



N2	15	20	CP-64QAM	L	Edge_1RB_Right	18.07	17.37	PC3	PASS
N2	15	20	CP-64QAM	L	Outer_Full	17.51	16.81	PC3	PASS
N2	15	20	CP-64QAM	L	Inner_Full	18.04	17.34	PC3	PASS
N2	15	20	CP-256QAM	L	Edge_1RB_Left	15.66	14.96	PC3	PASS
N2	15	20	CP-256QAM	L	Edge_1RB_Right	15.49	14.79	PC3	PASS
N2	15	20	CP-256QAM	L	Outer_Full	14.83	14.13	PC3	PASS
N2	15	20	CP-256QAM	L	Inner_Full	14.5	13.8	PC3	PASS
N2	15	20	DFT-PI2BPSK	M	Edge_1RB_Left	20.99	20.29	PC3	PASS
N2	15	20	DFT-PI2BPSK	M	Edge_1RB_Right	20.1	19.4	PC3	PASS
N2	15	20	DFT-PI2BPSK	M	Outer_Full	19.73	19.03	PC3	PASS
N2	15	20	DFT-PI2BPSK	M	Inner_Full	19.76	19.06	PC3	PASS
N2	15	20	DFT-QPSK	M	Edge_1RB_Right	20.24	19.54	PC3	PASS
N2	15	20	DFT-QPSK	M	Outer_Full	19.8	19.1	PC3	PASS
N2	15	20	DFT-QPSK	M	Inner_Full	19.82	19.12	PC3	PASS
N2	15	20	CP-QPSK	M	Edge_1RB_Left	19.04	18.34	PC3	PASS
N2	15	20	CP-QPSK	M	Edge_1RB_Right	18.25	17.55	PC3	PASS
N2	15	20	CP-QPSK	M	Outer_Full	17.86	17.16	PC3	PASS
N2	15	20	CP-QPSK	M	Inner_Full	18.18	17.48	PC3	PASS
N2	15	20	CP-16QAM	M	Edge_1RB_Left	18.67	17.97	PC3	PASS
N2	15	20	CP-16QAM	M	Edge_1RB_Right	18.16	17.46	PC3	PASS
N2	15	20	CP-16QAM	M	Outer_Full	17.57	16.87	PC3	PASS
N2	15	20	CP-16QAM	M	Inner_Full	17.65	16.95	PC3	PASS
N2	15	20	CP-64QAM	M	Edge_1RB_Left	18.34	17.64	PC3	PASS
N2	15	20	CP-64QAM	M	Edge_1RB_Right	17.9	17.2	PC3	PASS
N2	15	20	CP-64QAM	M	Outer_Full	17.19	16.49	PC3	PASS
N2	15	20	CP-64QAM	M	Inner_Full	17.67	16.97	PC3	PASS
N2	15	20	CP-256QAM	M	Edge_1RB_Left	15.98	15.28	PC3	PASS
N2	15	20	CP-256QAM	M	Edge_1RB_Right	15.23	14.53	PC3	PASS
N2	15	20	CP-256QAM	M	Outer_Full	14.66	13.96	PC3	PASS
N2	15	20	CP-256QAM	M	Inner_Full	14.2	13.5	PC3	PASS
N2	15	20	DFT-PI2BPSK	H	Edge_1RB_Left	20.9	20.2	PC3	PASS
N2	15	20	DFT-PI2BPSK	H	Edge_1RB_Right	20.12	19.42	PC3	PASS
N2	15	20	DFT-PI2BPSK	H	Outer_Full	20.21	19.51	PC3	PASS
N2	15	20	DFT-PI2BPSK	H	Inner_Full	20.61	19.91	PC3	PASS
N2	15	20	DFT-QPSK	H	Edge_1RB_Left	20.98	20.28	PC3	PASS
N2	15	20	DFT-QPSK	H	Edge_1RB_Right	20.79	20.09	PC3	PASS
N2	15	20	DFT-QPSK	H	Outer_Full	20.49	19.79	PC3	PASS
N2	15	20	DFT-QPSK	H	Inner_Full	20.5	19.8	PC3	PASS
N2	15	20	CP-QPSK	H	Edge_1RB_Left	10.43	9.73	PC3	PASS
N2	15	20	CP-QPSK	H	Edge_1RB_Right	10.24	9.54	PC3	PASS



N2	15	20	CP-QPSK	H	Outer_Full	9.74	9.04	PC3	PASS
N2	15	20	CP-QPSK	H	Inner_Full	1.05	0.35	PC3	PASS
N2	15	20	CP-16QAM	H	Edge_1RB_Left	19.12	18.42	PC3	PASS
N2	15	20	CP-16QAM	H	Edge_1RB_Right	18.02	17.32	PC3	PASS
N2	15	20	CP-16QAM	H	Outer_Full	18.05	17.35	PC3	PASS
N2	15	20	CP-16QAM	H	Inner_Full	18.29	17.59	PC3	PASS
N2	15	20	CP-64QAM	H	Edge_1RB_Left	18.61	17.91	PC3	PASS
N2	15	20	CP-64QAM	H	Edge_1RB_Right	17.51	16.81	PC3	PASS
N2	15	20	CP-64QAM	H	Outer_Full	17.29	16.59	PC3	PASS
N2	15	20	CP-64QAM	H	Inner_Full	18.3	17.6	PC3	PASS
N2	15	20	CP-256QAM	H	Edge_1RB_Left	15.88	15.18	PC3	PASS
N2	15	20	CP-256QAM	H	Edge_1RB_Right	14.88	14.18	PC3	PASS
N2	15	20	CP-256QAM	H	Outer_Full	14.85	14.15	PC3	PASS
N2	15	20	CP-256QAM	H	Inner_Full	14.51	13.81	PC3	PASS
N2	15	25	DFT-PI2BPSK	L	Edge_1RB_Left	19.51	18.81	PC3	PASS
N2	15	25	DFT-PI2BPSK	L	Edge_1RB_Right	19.05	18.35	PC3	PASS
N2	15	25	DFT-PI2BPSK	L	Outer_Full	19.97	19.27	PC3	PASS
N2	15	25	DFT-PI2BPSK	L	Inner_Full	20.15	19.45	PC3	PASS
N2	15	25	DFT-QPSK	L	Edge_1RB_Left	19.32	18.62	PC3	PASS
N2	15	25	DFT-QPSK	L	Edge_1RB_Right	18.91	18.21	PC3	PASS
N2	15	25	DFT-QPSK	L	Outer_Full	19.89	19.19	PC3	PASS
N2	15	25	DFT-QPSK	L	Inner_Full	20.17	19.47	PC3	PASS
N2	15	25	CP-QPSK	L	Edge_1RB_Left	17.45	16.75	PC3	PASS
N2	15	25	CP-QPSK	L	Edge_1RB_Right	17.1	16.4	PC3	PASS
N2	15	25	CP-QPSK	L	Outer_Full	18.17	17.47	PC3	PASS
N2	15	25	CP-QPSK	L	Inner_Full	18.55	17.85	PC3	PASS
N2	15	25	CP-16QAM	L	Edge_1RB_Left	17.16	16.46	PC3	PASS
N2	15	25	CP-16QAM	L	Edge_1RB_Right	16.95	16.25	PC3	PASS
N2	15	25	CP-16QAM	L	Outer_Full	17.81	17.11	PC3	PASS
N2	15	25	CP-16QAM	L	Inner_Full	18.04	17.34	PC3	PASS
N2	15	25	CP-64QAM	L	Edge_1RB_Left	17	16.3	PC3	PASS
N2	15	25	CP-64QAM	L	Edge_1RB_Right	16.43	15.73	PC3	PASS
N2	15	25	CP-64QAM	L	Outer_Full	17.18	16.48	PC3	PASS
N2	15	25	CP-64QAM	L	Inner_Full	17.96	17.26	PC3	PASS
N2	15	25	CP-256QAM	L	Edge_1RB_Left	14.11	13.41	PC3	PASS
N2	15	25	CP-256QAM	L	Edge_1RB_Right	13.79	13.09	PC3	PASS
N2	15	25	CP-256QAM	L	Outer_Full	14.66	13.96	PC3	PASS
N2	15	25	CP-256QAM	L	Inner_Full	14.43	13.73	PC3	PASS
N2	15	25	DFT-PI2BPSK	M	Edge_1RB_Left	19.57	18.87	PC3	PASS
N2	15	25	DFT-PI2BPSK	M	Edge_1RB_Right	18.66	17.96	PC3	PASS



N2	15	25	DFT-PI2BPSK	M	Outer_Full	19.6	18.9	PC3	PASS
N2	15	25	DFT-PI2BPSK	M	Inner_Full	19.81	19.11	PC3	PASS
N2	15	25	DFT-QPSK	M	Edge_1RB_Right	18.81	18.11	PC3	PASS
N2	15	25	DFT-QPSK	M	Outer_Full	19.81	19.11	PC3	PASS
N2	15	25	DFT-QPSK	M	Inner_Full	19.85	19.15	PC3	PASS
N2	15	25	CP-QPSK	M	Edge_1RB_Left	17.52	16.82	PC3	PASS
N2	15	25	CP-QPSK	M	Edge_1RB_Right	16.75	16.05	PC3	PASS
N2	15	25	CP-QPSK	M	Outer_Full	17.7	17	PC3	PASS
N2	15	25	CP-QPSK	M	Inner_Full	18.19	17.49	PC3	PASS
N2	15	25	CP-16QAM	M	Edge_1RB_Left	17.16	16.46	PC3	PASS
N2	15	25	CP-16QAM	M	Edge_1RB_Right	16.4	15.7	PC3	PASS
N2	15	25	CP-16QAM	M	Outer_Full	17.19	16.49	PC3	PASS
N2	15	25	CP-16QAM	M	Inner_Full	17.31	16.61	PC3	PASS
N2	15	25	CP-64QAM	M	Edge_1RB_Left	17.06	16.36	PC3	PASS
N2	15	25	CP-64QAM	M	Edge_1RB_Right	16.4	15.7	PC3	PASS
N2	15	25	CP-64QAM	M	Outer_Full	17.04	16.34	PC3	PASS
N2	15	25	CP-64QAM	M	Inner_Full	17.36	16.66	PC3	PASS
N2	15	25	CP-256QAM	M	Edge_1RB_Left	14.45	13.75	PC3	PASS
N2	15	25	CP-256QAM	M	Edge_1RB_Right	13.68	12.98	PC3	PASS
N2	15	25	CP-256QAM	M	Outer_Full	14.55	13.85	PC3	PASS
N2	15	25	CP-256QAM	M	Inner_Full	14.18	13.48	PC3	PASS
N2	15	25	DFT-PI2BPSK	H	Edge_1RB_Left	19.23	18.53	PC3	PASS
N2	15	25	DFT-PI2BPSK	H	Edge_1RB_Right	18.21	17.51	PC3	PASS
N2	15	25	DFT-PI2BPSK	H	Outer_Full	19.94	19.24	PC3	PASS
N2	15	25	DFT-PI2BPSK	H	Inner_Full	20.26	19.56	PC3	PASS
N2	15	25	DFT-QPSK	H	Edge_1RB_Left	19.25	18.55	PC3	PASS
N2	15	25	DFT-QPSK	H	Edge_1RB_Right	18.38	17.68	PC3	PASS
N2	15	25	DFT-QPSK	H	Outer_Full	19.88	19.18	PC3	PASS
N2	15	25	DFT-QPSK	H	Inner_Full	20.18	19.48	PC3	PASS
N2	15	25	CP-QPSK	H	Edge_1RB_Left	17.5	16.8	PC3	PASS
N2	15	25	CP-QPSK	H	Edge_1RB_Right	16.64	15.94	PC3	PASS
N2	15	25	CP-QPSK	H	Outer_Full	18.14	17.44	PC3	PASS
N2	15	25	CP-QPSK	H	Inner_Full	18.68	17.98	PC3	PASS
N2	15	25	CP-16QAM	H	Edge_1RB_Left	17.19	16.49	PC3	PASS
N2	15	25	CP-16QAM	H	Edge_1RB_Right	16.28	15.58	PC3	PASS
N2	15	25	CP-16QAM	H	Outer_Full	17.62	16.92	PC3	PASS
N2	15	25	CP-16QAM	H	Inner_Full	17.98	17.28	PC3	PASS
N2	15	25	CP-64QAM	H	Edge_1RB_Left	16.91	16.21	PC3	PASS
N2	15	25	CP-64QAM	H	Edge_1RB_Right	16.01	15.31	PC3	PASS
N2	15	25	CP-64QAM	H	Outer_Full	17.17	16.47	PC3	PASS



N2	15	25	CP-64QAM	H	Inner_Full	18.07	17.37	PC3	PASS
N2	15	25	CP-256QAM	H	Edge_1RB_Left	14.11	13.41	PC3	PASS
N2	15	25	CP-256QAM	H	Edge_1RB_Right	13.21	12.51	PC3	PASS
N2	15	25	CP-256QAM	H	Outer_Full	14.62	13.92	PC3	PASS
N2	15	25	CP-256QAM	H	Inner_Full	14.51	13.81	PC3	PASS
N2	15	30	DFT-PI2BPSK	L	Edge_1RB_Left	20.97	20.27	PC3	PASS
N2	15	30	DFT-PI2BPSK	L	Edge_1RB_Right	20.6	19.9	PC3	PASS
N2	15	30	DFT-PI2BPSK	L	Outer_Full	20.21	19.51	PC3	PASS
N2	15	30	DFT-PI2BPSK	L	Inner_Full	20.36	19.66	PC3	PASS
N2	15	30	DFT-QPSK	L	Edge_1RB_Left	20.93	20.23	PC3	PASS
N2	15	30	DFT-QPSK	L	Edge_1RB_Right	20.8	20.1	PC3	PASS
N2	15	30	DFT-QPSK	L	Outer_Full	20.28	19.58	PC3	PASS
N2	15	30	DFT-QPSK	L	Inner_Full	20.42	19.72	PC3	PASS
N2	15	30	CP-QPSK	L	Edge_1RB_Left	18.97	18.27	PC3	PASS
N2	15	30	CP-QPSK	L	Edge_1RB_Right	19.05	18.35	PC3	PASS
N2	15	30	CP-QPSK	L	Outer_Full	18.57	17.87	PC3	PASS
N2	15	30	CP-QPSK	L	Inner_Full	18.89	18.19	PC3	PASS
N2	15	30	CP-16QAM	L	Edge_1RB_Left	19.03	18.33	PC3	PASS
N2	15	30	CP-16QAM	L	Edge_1RB_Right	18.73	18.03	PC3	PASS
N2	15	30	CP-16QAM	L	Outer_Full	18.05	17.35	PC3	PASS
N2	15	30	CP-16QAM	L	Inner_Full	18.23	17.53	PC3	PASS
N2	15	30	CP-64QAM	L	Edge_1RB_Left	18.39	17.69	PC3	PASS
N2	15	30	CP-64QAM	L	Edge_1RB_Right	18.17	17.47	PC3	PASS
N2	15	30	CP-64QAM	L	Outer_Full	17.53	16.83	PC3	PASS
N2	15	30	CP-64QAM	L	Inner_Full	18.32	17.62	PC3	PASS
N2	15	30	CP-256QAM	L	Edge_1RB_Left	15.88	15.18	PC3	PASS
N2	15	30	CP-256QAM	L	Edge_1RB_Right	15.62	14.92	PC3	PASS
N2	15	30	CP-256QAM	L	Outer_Full	15.02	14.32	PC3	PASS
N2	15	30	CP-256QAM	L	Inner_Full	14.73	14.03	PC3	PASS
N2	15	30	DFT-PI2BPSK	M	Edge_1RB_Left	20.9	20.2	PC3	PASS
N2	15	30	DFT-PI2BPSK	M	Edge_1RB_Right	20.3	19.6	PC3	PASS
N2	15	30	DFT-PI2BPSK	M	Outer_Full	19.62	18.92	PC3	PASS
N2	15	30	DFT-PI2BPSK	M	Inner_Full	19.54	18.84	PC3	PASS
N2	15	30	DFT-QPSK	M	Edge_1RB_Left	20.75	20.05	PC3	PASS
N2	15	30	DFT-QPSK	M	Edge_1RB_Right	20.33	19.63	PC3	PASS
N2	15	30	DFT-QPSK	M	Outer_Full	19.56	18.86	PC3	PASS
N2	15	30	DFT-QPSK	M	Inner_Full	19.51	18.81	PC3	PASS
N2	15	30	CP-QPSK	M	Edge_1RB_Left	19.02	18.32	PC3	PASS
N2	15	30	CP-QPSK	M	Edge_1RB_Right	18.57	17.87	PC3	PASS
N2	15	30	CP-QPSK	M	Outer_Full	17.81	17.11	PC3	PASS



N2	15	30	CP-QPSK	M	Inner_Full	18.02	17.32	PC3	PASS
N2	15	30	CP-16QAM	M	Edge_1RB_Left	18.83	18.13	PC3	PASS
N2	15	30	CP-16QAM	M	Edge_1RB_Right	18.35	17.65	PC3	PASS
N2	15	30	CP-16QAM	M	Outer_Full	17.36	16.66	PC3	PASS
N2	15	30	CP-16QAM	M	Inner_Full	17.37	16.67	PC3	PASS
N2	15	30	CP-64QAM	M	Edge_1RB_Left	18.51	17.81	PC3	PASS
N2	15	30	CP-64QAM	M	Edge_1RB_Right	18.16	17.46	PC3	PASS
N2	15	30	CP-64QAM	M	Outer_Full	17.23	16.53	PC3	PASS
N2	15	30	CP-64QAM	M	Inner_Full	17.34	16.64	PC3	PASS
N2	15	30	CP-256QAM	M	Edge_1RB_Left	15.95	15.25	PC3	PASS
N2	15	30	CP-256QAM	M	Edge_1RB_Right	15.5	14.8	PC3	PASS
N2	15	30	CP-256QAM	M	Outer_Full	14.56	13.86	PC3	PASS
N2	15	30	CP-256QAM	M	Inner_Full	14.03	13.33	PC3	PASS
N2	15	30	DFT-PI2BPSK	H	Edge_1RB_Left	20.98	20.28	PC3	PASS
N2	15	30	DFT-PI2BPSK	H	Edge_1RB_Right	19.96	19.26	PC3	PASS
N2	15	30	DFT-PI2BPSK	H	Outer_Full	19.94	19.24	PC3	PASS
N2	15	30	DFT-PI2BPSK	H	Inner_Full	20.27	19.57	PC3	PASS
N2	15	30	DFT-QPSK	H	Edge_1RB_Left	20.98	20.28	PC3	PASS
N2	15	30	DFT-QPSK	H	Edge_1RB_Right	20.03	19.33	PC3	PASS
N2	15	30	DFT-QPSK	H	Outer_Full	19.93	19.23	PC3	PASS
N2	15	30	DFT-QPSK	H	Inner_Full	20.24	19.54	PC3	PASS
N2	15	30	CP-QPSK	H	Edge_1RB_Left	19.3	18.6	PC3	PASS
N2	15	30	CP-QPSK	H	Edge_1RB_Right	18.34	17.64	PC3	PASS
N2	15	30	CP-QPSK	H	Outer_Full	18.23	17.53	PC3	PASS
N2	15	30	CP-QPSK	H	Inner_Full	18.67	17.97	PC3	PASS
N2	15	30	CP-16QAM	H	Edge_1RB_Left	19.01	18.31	PC3	PASS
N2	15	30	CP-16QAM	H	Edge_1RB_Right	17.96	17.26	PC3	PASS
N2	15	30	CP-16QAM	H	Outer_Full	17.81	17.11	PC3	PASS
N2	15	30	CP-16QAM	H	Inner_Full	18.05	17.35	PC3	PASS
N2	15	30	CP-64QAM	H	Edge_1RB_Left	18.54	17.84	PC3	PASS
N2	15	30	CP-64QAM	H	Edge_1RB_Right	17.6	16.9	PC3	PASS
N2	15	30	CP-64QAM	H	Outer_Full	17.29	16.59	PC3	PASS
N2	15	30	CP-64QAM	H	Inner_Full	18.04	17.34	PC3	PASS
N2	15	30	CP-256QAM	H	Edge_1RB_Left	16.01	15.31	PC3	PASS
N2	15	30	CP-256QAM	H	Edge_1RB_Right	15.04	14.34	PC3	PASS
N2	15	30	CP-256QAM	H	Outer_Full	14.75	14.05	PC3	PASS
N2	15	30	CP-256QAM	H	Inner_Full	14.47	13.77	PC3	PASS
N2	15	40	DFT-PI2BPSK	L	Edge_1RB_Left	20.97	20.27	PC3	PASS
N2	15	40	DFT-PI2BPSK	L	Edge_1RB_Right	20.7	20	PC3	PASS
N2	15	40	DFT-PI2BPSK	L	Outer_Full	20.33	19.63	PC3	PASS



N2	15	40	DFT-PI2BPSK	L	Inner_Full	20.36	19.66	PC3	PASS
N2	15	40	DFT-QPSK	L	Edge_1RB_Left	20.99	20.29	PC3	PASS
N2	15	40	DFT-QPSK	L	Edge_1RB_Right	20.9	20.2	PC3	PASS
N2	15	40	DFT-QPSK	L	Outer_Full	20.26	19.56	PC3	PASS
N2	15	40	DFT-QPSK	L	Inner_Full	20.32	19.62	PC3	PASS
N2	15	40	CP-QPSK	L	Edge_1RB_Left	19.66	18.96	PC3	PASS
N2	15	40	CP-QPSK	L	Edge_1RB_Right	19.19	18.49	PC3	PASS
N2	15	40	CP-QPSK	L	Outer_Full	18.59	17.89	PC3	PASS
N2	15	40	CP-QPSK	L	Inner_Full	18.68	17.98	PC3	PASS
N2	15	40	CP-16QAM	L	Edge_1RB_Left	19.36	18.66	PC3	PASS
N2	15	40	CP-16QAM	L	Edge_1RB_Right	18.83	18.13	PC3	PASS
N2	15	40	CP-16QAM	L	Outer_Full	18.05	17.35	PC3	PASS
N2	15	40	CP-16QAM	L	Inner_Full	18.05	17.35	PC3	PASS
N2	15	40	CP-64QAM	L	Edge_1RB_Left	18.73	18.03	PC3	PASS
N2	15	40	CP-64QAM	L	Edge_1RB_Right	18.52	17.82	PC3	PASS
N2	15	40	CP-64QAM	L	Outer_Full	17.69	16.99	PC3	PASS
N2	15	40	CP-64QAM	L	Inner_Full	18.14	17.44	PC3	PASS
N2	15	40	CP-256QAM	L	Edge_1RB_Left	16.47	15.77	PC3	PASS
N2	15	40	CP-256QAM	L	Edge_1RB_Right	15.99	15.29	PC3	PASS
N2	15	40	CP-256QAM	L	Outer_Full	15.24	14.54	PC3	PASS
N2	15	40	CP-256QAM	L	Inner_Full	14.74	14.04	PC3	PASS
N2	15	40	DFT-PI2BPSK	M	Edge_1RB_Left	20.99	20.29	PC3	PASS
N2	15	40	DFT-PI2BPSK	M	Edge_1RB_Right	20.97	20.27	PC3	PASS
N2	15	40	DFT-PI2BPSK	M	Outer_Full	20.25	19.55	PC3	PASS
N2	15	40	DFT-PI2BPSK	M	Inner_Full	19.96	19.26	PC3	PASS
N2	15	40	DFT-QPSK	M	Edge_1RB_Left	20.99	20.29	PC3	PASS
N2	15	40	DFT-QPSK	M	Edge_1RB_Right	20.98	20.28	PC3	PASS
N2	15	40	DFT-QPSK	M	Outer_Full	20.2	19.5	PC3	PASS
N2	15	40	DFT-QPSK	M	Inner_Full	19.92	19.22	PC3	PASS
N2	15	40	CP-QPSK	M	Edge_1RB_Left	20.12	19.42	PC3	PASS
N2	15	40	CP-QPSK	M	Edge_1RB_Right	19.35	18.65	PC3	PASS
N2	15	40	CP-QPSK	M	Outer_Full	18.65	17.95	PC3	PASS
N2	15	40	CP-QPSK	M	Inner_Full	18.47	17.77	PC3	PASS
N2	15	40	CP-16QAM	M	Edge_1RB_Left	19.84	19.14	PC3	PASS
N2	15	40	CP-16QAM	M	Edge_1RB_Right	19.02	18.32	PC3	PASS
N2	15	40	CP-16QAM	M	Outer_Full	18.13	17.43	PC3	PASS
N2	15	40	CP-16QAM	M	Inner_Full	17.86	17.16	PC3	PASS
N2	15	40	CP-64QAM	M	Edge_1RB_Left	19.41	18.71	PC3	PASS
N2	15	40	CP-64QAM	M	Edge_1RB_Right	18.21	17.51	PC3	PASS
N2	15	40	CP-64QAM	M	Outer_Full	17.6	16.9	PC3	PASS



N2	15	40	CP-64QAM	M	Inner_Full	17.8	17.1	PC3	PASS
N2	15	40	CP-256QAM	M	Edge_1RB_Left	16.96	16.26	PC3	PASS
N2	15	40	CP-256QAM	M	Edge_1RB_Right	16.11	15.41	PC3	PASS
N2	15	40	CP-256QAM	M	Outer_Full	15.28	14.58	PC3	PASS
N2	15	40	CP-256QAM	M	Inner_Full	14.51	13.81	PC3	PASS
N2	15	40	DFT-PI2BPSK	H	Edge_1RB_Left	20.96	20.26	PC3	PASS
N2	15	40	DFT-PI2BPSK	H	Edge_1RB_Right	19.96	19.26	PC3	PASS
N2	15	40	DFT-PI2BPSK	H	Outer_Full	19.95	19.25	PC3	PASS
N2	15	40	DFT-PI2BPSK	H	Inner_Full	19.94	19.24	PC3	PASS
N2	15	40	DFT-QPSK	H	Edge_1RB_Left	20.77	20.07	PC3	PASS
N2	15	40	DFT-QPSK	H	Edge_1RB_Right	20.08	19.38	PC3	PASS
N2	15	40	DFT-QPSK	H	Outer_Full	20.13	19.43	PC3	PASS
N2	15	40	DFT-QPSK	H	Inner_Full	19.88	19.18	PC3	PASS
N2	15	40	CP-QPSK	H	Edge_1RB_Left	19.66	18.96	PC3	PASS
N2	15	40	CP-QPSK	H	Edge_1RB_Right	18.18	17.48	PC3	PASS
N2	15	40	CP-QPSK	H	Outer_Full	18.05	17.35	PC3	PASS
N2	15	40	CP-QPSK	H	Inner_Full	18.3	17.6	PC3	PASS
N2	15	40	CP-16QAM	H	Edge_1RB_Left	19.36	18.66	PC3	PASS
N2	15	40	CP-16QAM	H	Edge_1RB_Right	17.84	17.14	PC3	PASS
N2	15	40	CP-16QAM	H	Outer_Full	17.55	16.85	PC3	PASS
N2	15	40	CP-16QAM	H	Inner_Full	17.41	16.71	PC3	PASS
N2	15	40	CP-64QAM	H	Edge_1RB_Left	18.79	18.09	PC3	PASS
N2	15	40	CP-64QAM	H	Edge_1RB_Right	17.55	16.85	PC3	PASS
N2	15	40	CP-64QAM	H	Outer_Full	17.26	16.56	PC3	PASS
N2	15	40	CP-64QAM	H	Inner_Full	17.45	16.75	PC3	PASS
N2	15	40	CP-256QAM	H	Edge_1RB_Left	16.52	15.82	PC3	PASS
N2	15	40	CP-256QAM	H	Edge_1RB_Right	15.07	14.37	PC3	PASS
N2	15	40	CP-256QAM	H	Outer_Full	14.75	14.05	PC3	PASS
N2	15	40	CP-256QAM	H	Inner_Full	14.09	13.39	PC3	PASS



Appendix B: Peak-to-Average Ratio

Peak-to-Average Ratio

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result(dB)	Limit(dB)	Verdict
N2	15	40	DFT-PI2BPSK	L	Outer_Full	9.53	≤13	PASS
N2	15	40	DFT-QPSK	L	Outer_Full	9.27	≤13	PASS
N2	15	40	DFT-PI2BPSK	M	Outer_Full	9.31	≤13	PASS
N2	15	40	DFT-QPSK	M	Outer_Full	9.14	≤13	PASS
N2	15	40	DFT-PI2BPSK	H	Outer_Full	9.47	≤13	PASS
N2	15	40	DFT-QPSK	H	Outer_Full	9.57	≤13	PASS



Appendix C: 26dB Bandwidth and Occupied Bandwidth

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	99% OBW (MHz)	26dB BW (MHz)	Verdict
N2	15	5	DFT-QPSK	L	Outer_Full	4.503	4.900	PASS
N2	15	5	CP-QPSK	L	Outer_Full	4.484	4.910	PASS
N2	15	5	CP-16QAM	L	Outer_Full	4.481	4.910	PASS
N2	15	5	CP-64QAM	L	Outer_Full	4.506	4.930	PASS
N2	15	5	CP-256QAM	L	Outer_Full	4.486	4.890	PASS
N2	15	5	DFT-QPSK	M	Outer_Full	4.496	4.880	PASS
N2	15	5	DFT-PI2BPSK	M	Outer_Full	4.491	4.850	PASS
N2	15	5	CP-QPSK	M	Outer_Full	4.47	4.920	PASS
N2	15	5	CP-16QAM	M	Outer_Full	4.476	4.880	PASS
N2	15	5	DFT-PI2BPSK	L	Outer_Full	4.494	4.860	PASS
N2	15	5	CP-64QAM	M	Outer_Full	4.489	5.390	PASS
N2	15	5	CP-256QAM	M	Outer_Full	4.498	4.920	PASS
N2	15	5	DFT-QPSK	H	Outer_Full	4.503	4.900	PASS
N2	15	5	DFT-PI2BPSK	H	Outer_Full	4.487	4.910	PASS
N2	15	5	CP-QPSK	H	Outer_Full	4.48	4.930	PASS
N2	15	5	CP-16QAM	H	Outer_Full	4.481	4.880	PASS
N2	15	5	CP-64QAM	H	Outer_Full	4.494	6.450	PASS
N2	15	5	CP-256QAM	H	Outer_Full	4.482	4.870	PASS
N2	15	10	DFT-QPSK	L	Outer_Full	8.934	9.580	PASS
N2	15	10	DFT-PI2BPSK	L	Outer_Full	8.963	9.660	PASS
N2	15	10	CP-QPSK	L	Outer_Full	9.289	9.900	PASS
N2	15	10	CP-16QAM	L	Outer_Full	9.276	9.840	PASS
N2	15	20	DFT-QPSK	M	Outer_Full	17.95	18.760	PASS
N2	15	20	DFT-PI2BPSK	M	Outer_Full	17.941	18.840	PASS
N2	15	20	CP-QPSK	M	Outer_Full	18.94	19.760	PASS
N2	15	20	CP-16QAM	M	Outer_Full	18.965	19.720	PASS
N2	15	20	CP-64QAM	M	Outer_Full	18.993	19.720	PASS
N2	15	20	CP-256QAM	M	Outer_Full	18.934	19.880	PASS
N2	15	20	DFT-QPSK	H	Outer_Full	17.936	18.720	PASS
N2	15	20	DFT-PI2BPSK	H	Outer_Full	17.921	18.760	PASS
N2	15	20	CP-QPSK	H	Outer_Full	18.959	19.720	PASS
N2	15	20	CP-16QAM	H	Outer_Full	18.948	19.840	PASS
N2	15	20	CP-64QAM	H	Outer_Full	18.969	21.920	PASS



N2	15	20	CP-256QAM	H	Outer_Full	18.939	19.920	PASS
N2	15	25	DFT-QPSK	L	Outer_Full	22.83	23.900	PASS
N2	15	25	DFT-PI2BPSK	L	Outer_Full	22.878	23.900	PASS
N2	15	25	CP-QPSK	L	Outer_Full	23.698	24.800	PASS
N2	15	25	CP-16QAM	L	Outer_Full	23.671	24.750	PASS
N2	15	25	CP-64QAM	L	Outer_Full	23.673	24.950	PASS
N2	15	25	CP-256QAM	L	Outer_Full	23.68	24.750	PASS
N2	15	25	DFT-QPSK	M	Outer_Full	22.847	23.900	PASS
N2	15	25	CP-QPSK	M	Outer_Full	23.7	24.800	PASS
N2	15	25	CP-16QAM	M	Outer_Full	23.743	24.850	PASS
N2	15	25	CP-64QAM	M	Outer_Full	23.76	28.300	PASS
N2	15	25	CP-256QAM	M	Outer_Full	23.696	24.750	PASS
N2	15	25	DFT-QPSK	H	Outer_Full	22.853	24.050	PASS
N2	15	30	DFT-QPSK	L	Outer_Full	28.618	29.640	PASS
N2	15	30	DFT-PI2BPSK	L	Outer_Full	28.653	29.700	PASS
N2	15	30	CP-QPSK	L	Outer_Full	28.654	29.580	PASS
N2	15	30	CP-16QAM	L	Outer_Full	28.546	29.880	PASS
N2	15	30	CP-64QAM	L	Outer_Full	28.587	30.000	PASS
N2	15	30	CP-256QAM	L	Outer_Full	28.631	29.820	PASS
N2	15	30	DFT-QPSK	M	Outer_Full	28.644	30.000	PASS
N2	15	30	DFT-PI2BPSK	M	Outer_Full	28.68	29.760	PASS
N2	15	30	CP-QPSK	M	Outer_Full	28.681	29.760	PASS
N2	15	30	CP-16QAM	M	Outer_Full	28.6	29.760	PASS
N2	15	30	CP-64QAM	M	Outer_Full	28.588	29.820	PASS
N2	15	30	CP-256QAM	M	Outer_Full	28.621	29.760	PASS
N2	15	30	DFT-QPSK	H	Outer_Full	28.604	29.640	PASS
N2	15	30	DFT-PI2BPSK	H	Outer_Full	28.67	29.700	PASS
N2	15	30	CP-QPSK	H	Outer_Full	28.607	29.640	PASS
N2	15	30	CP-16QAM	H	Outer_Full	28.633	29.760	PASS
N2	15	30	CP-64QAM	H	Outer_Full	28.639	32.820	PASS
N2	15	30	CP-256QAM	H	Outer_Full	28.59	29.820	PASS
N2	15	40	DFT-QPSK	L	Outer_Full	38.675	40.240	PASS
N2	15	40	DFT-PI2BPSK	L	Outer_Full	38.625	40.160	PASS
N2	15	40	CP-QPSK	L	Outer_Full	38.643	40.160	PASS
N2	15	40	CP-16QAM	L	Outer_Full	38.651	40.240	PASS
N2	15	10	CP-64QAM	L	Outer_Full	9.303	9.780	PASS
N2	15	10	CP-256QAM	L	Outer_Full	9.302	9.840	PASS
N2	15	10	DFT-QPSK	M	Outer_Full	8.931	9.440	PASS
N2	15	10	DFT-PI2BPSK	M	Outer_Full	8.952	9.600	PASS
N2	15	10	CP-QPSK	M	Outer_Full	9.288	9.840	PASS



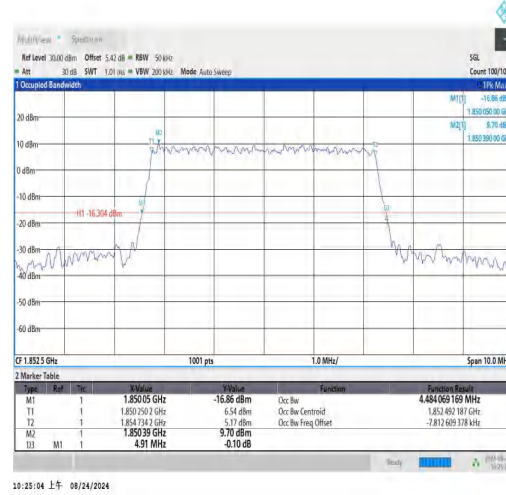
N2	15	10	CP-16QAM	M	Outer_Full	9.301	9.820	PASS
N2	15	10	CP-64QAM	M	Outer_Full	9.317	9.820	PASS
N2	15	10	CP-256QAM	M	Outer_Full	9.291	9.780	PASS
N2	15	10	DFT-QPSK	H	Outer_Full	8.925	9.640	PASS
N2	15	10	DFT-PI2BPSK	H	Outer_Full	8.956	9.660	PASS
N2	15	10	CP-QPSK	H	Outer_Full	9.304	9.840	PASS
N2	15	10	CP-16QAM	H	Outer_Full	9.313	9.920	PASS
N2	15	10	CP-64QAM	H	Outer_Full	9.323	11.560	PASS
N2	15	10	CP-256QAM	H	Outer_Full	9.317	9.920	PASS
N2	15	15	DFT-QPSK	L	Outer_Full	13.472	14.400	PASS
N2	15	15	DFT-PI2BPSK	L	Outer_Full	13.448	14.430	PASS
N2	15	15	CP-QPSK	L	Outer_Full	14.142	15.030	PASS
N2	15	15	CP-16QAM	L	Outer_Full	14.162	14.970	PASS
N2	15	15	CP-64QAM	L	Outer_Full	14.194	15.090	PASS
N2	15	15	CP-256QAM	L	Outer_Full	14.207	14.970	PASS
N2	15	15	DFT-QPSK	M	Outer_Full	13.475	14.430	PASS
N2	15	15	DFT-PI2BPSK	M	Outer_Full	13.46	14.400	PASS
N2	15	15	CP-QPSK	M	Outer_Full	14.127	14.910	PASS
N2	15	15	CP-16QAM	M	Outer_Full	14.177	14.970	PASS
N2	15	15	CP-64QAM	M	Outer_Full	14.182	14.910	PASS
N2	15	15	CP-256QAM	M	Outer_Full	14.136	14.850	PASS
N2	15	15	DFT-QPSK	H	Outer_Full	13.443	14.340	PASS
N2	15	15	DFT-PI2BPSK	H	Outer_Full	13.484	14.400	PASS
N2	15	15	CP-QPSK	H	Outer_Full	14.16	14.910	PASS
N2	15	15	CP-16QAM	H	Outer_Full	14.119	15.030	PASS
N2	15	15	CP-64QAM	H	Outer_Full	14.173	17.370	PASS
N2	15	15	CP-256QAM	H	Outer_Full	14.151	14.970	PASS
N2	15	20	DFT-QPSK	L	Outer_Full	17.933	18.680	PASS
N2	15	20	DFT-PI2BPSK	L	Outer_Full	17.946	18.800	PASS
N2	15	20	CP-QPSK	L	Outer_Full	18.926	19.680	PASS
N2	15	20	CP-16QAM	L	Outer_Full	18.966	19.840	PASS
N2	15	25	DFT-PI2BPSK	H	Outer_Full	22.873	23.800	PASS
N2	15	25	CP-QPSK	H	Outer_Full	23.648	24.750	PASS
N2	15	25	CP-16QAM	H	Outer_Full	23.687	24.800	PASS
N2	15	25	CP-64QAM	H	Outer_Full	23.747	25.750	PASS
N2	15	25	CP-256QAM	H	Outer_Full	23.63	24.750	PASS
N2	15	40	CP-64QAM	L	Outer_Full	38.736	40.160	PASS
N2	15	40	CP-256QAM	L	Outer_Full	38.678	40.240	PASS
N2	15	40	DFT-QPSK	M	Outer_Full	38.663	40.240	PASS
N2	15	40	DFT-PI2BPSK	M	Outer_Full	38.694	40.160	PASS



N2	15	40	CP-QPSK	M	Outer_Full	38.696	40.160	PASS
N2	15	40	CP-16QAM	M	Outer_Full	38.69	40.160	PASS
N2	15	40	CP-64QAM	M	Outer_Full	38.714	40.240	PASS
N2	15	40	CP-256QAM	M	Outer_Full	38.668	40.160	PASS
N2	15	40	DFT-PI2BPSK	H	Outer_Full	38.668	40.240	PASS
N2	15	40	CP-QPSK	H	Outer_Full	38.658	40.160	PASS
N2	15	40	CP-16QAM	H	Outer_Full	38.621	40.160	PASS
N2	15	40	CP-64QAM	H	Outer_Full	38.705	45.600	PASS
N2	15	40	CP-256QAM	H	Outer_Full	38.764	40.160	PASS
N2	15	20	CP-64QAM	L	Outer_Full	18.969	19.800	PASS
N2	15	20	CP-256QAM	L	Outer_Full	18.94	19.800	PASS

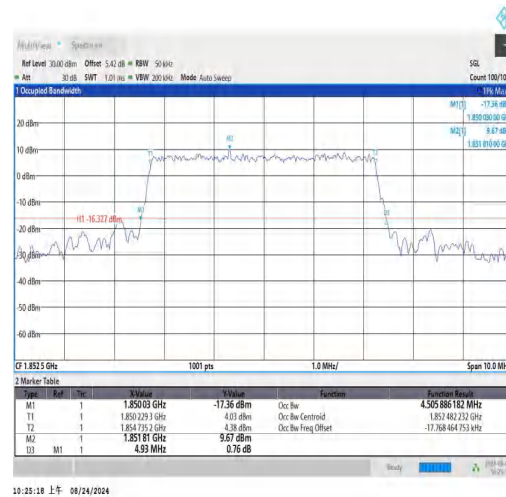
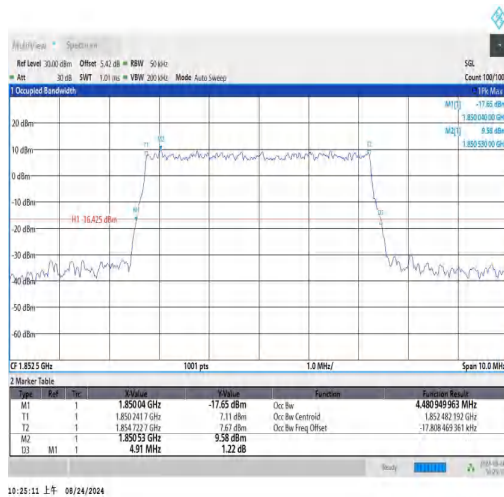


Test Graphs



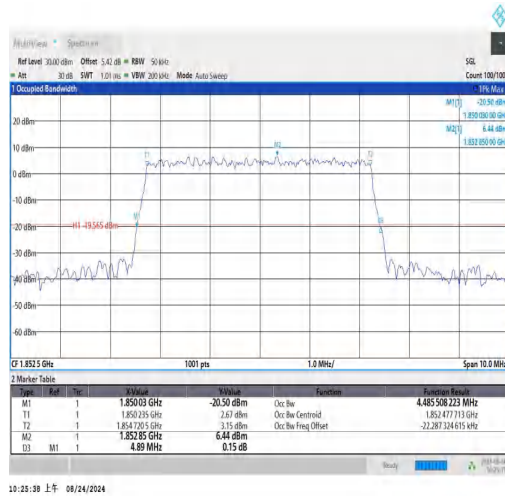
NTNV_N2_PC3_15_5_L_TID1_Ant1#1

NTNV_N2_PC3_15_5_L_TID3_Ant1#1



NTNV_N2_PC3_15_5_L_TID4_Ant1#1

NTNV_N2_PC3_15_5_L_TID5_Ant1#1



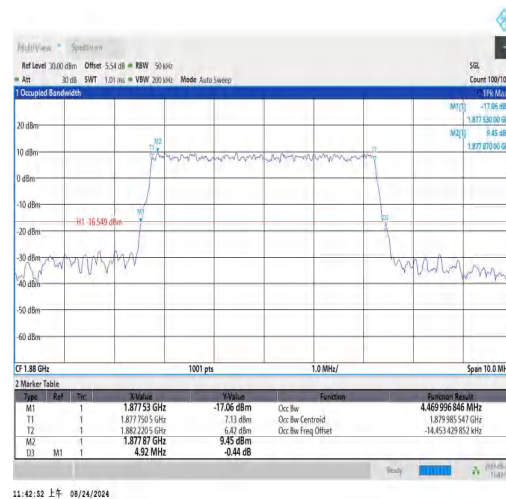
NTNV_N2_PC3_15_5_L_TID6_Ant1#1



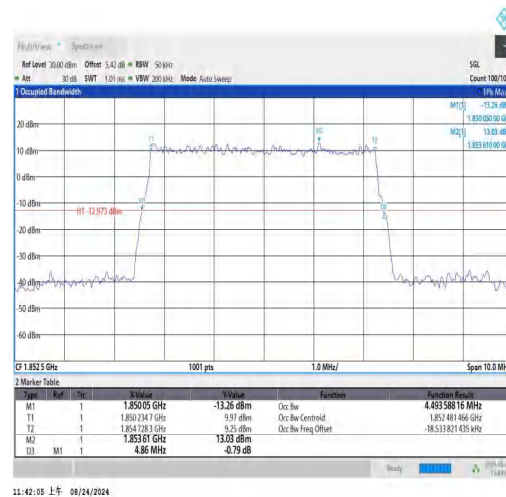
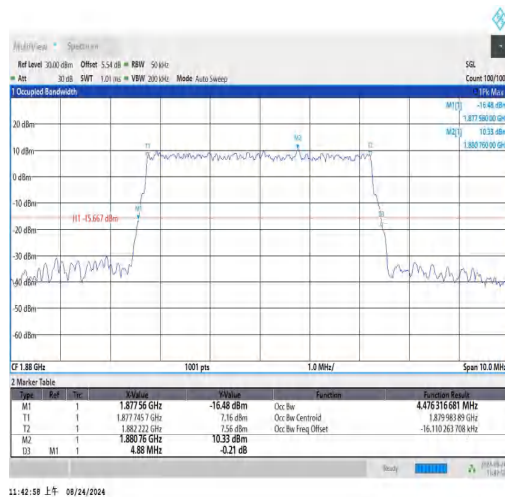
NTNV_N2_PC3_15_5_M_TID1_Ant1#1



NTNV_N2_PC3_15_5_M_TID2_Ant1#1



NTNV_N2_PC3_15_5_M_TID3_Ant1#1

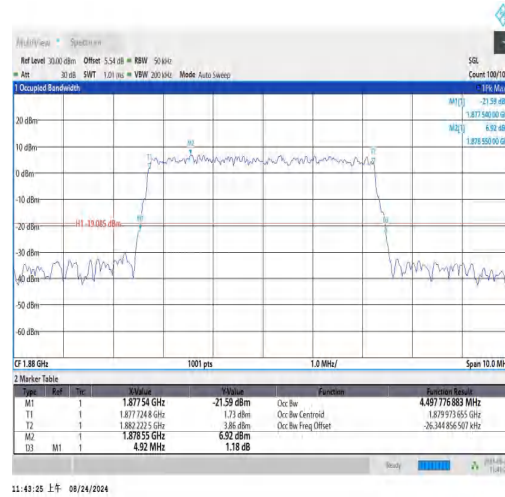




NTNV_N2_PC3_15_5_M_TID4_Ant1#1



NTNV_N2_PC3_15_5_L_TID2_Ant1#1



NTNV_N2_PC3_15_5_M_TID5_Ant1#1



NTNV_N2_PC3_15_5_M_TID6_Ant1#1

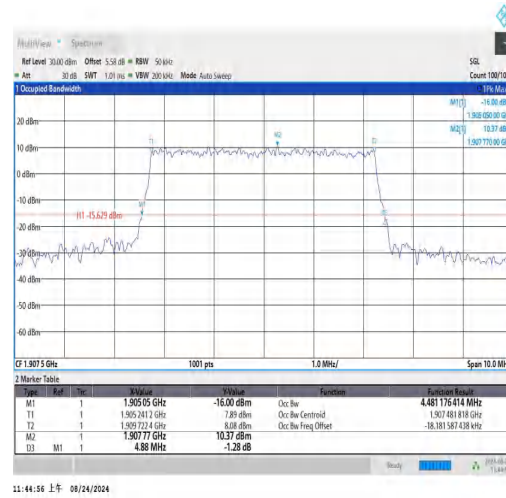


NTNV_N2_PC3_15_5_H_TID1_Ant1#1

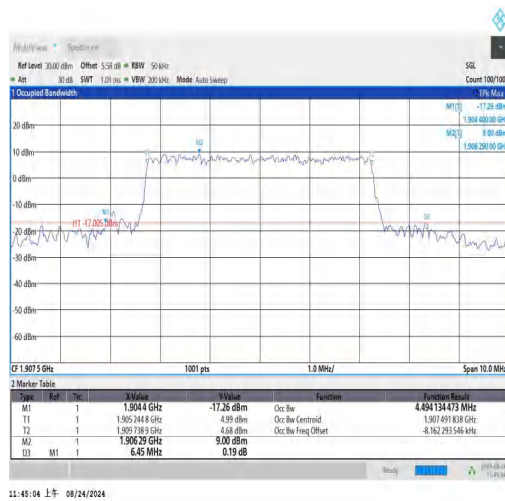
NTNV_N2_PC3_15_5_H_TID2_Ant1#1



NTNV_N2_PC3_15_5_H_TID3_Ant1#1



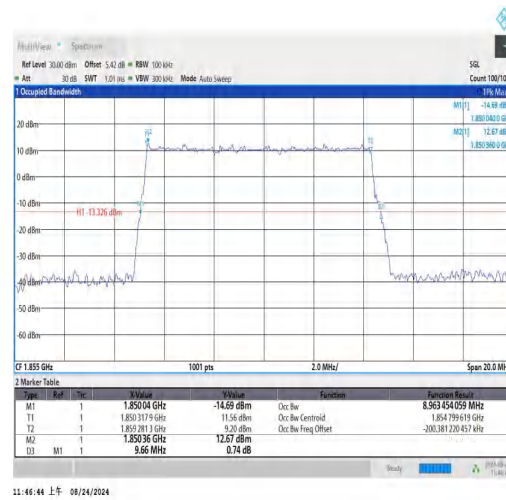
NTNV_N2_PC3_15_5_H_TID4_Ant1#1



NTNV_N2_PC3_15_5_H_TID5_Ant1#1

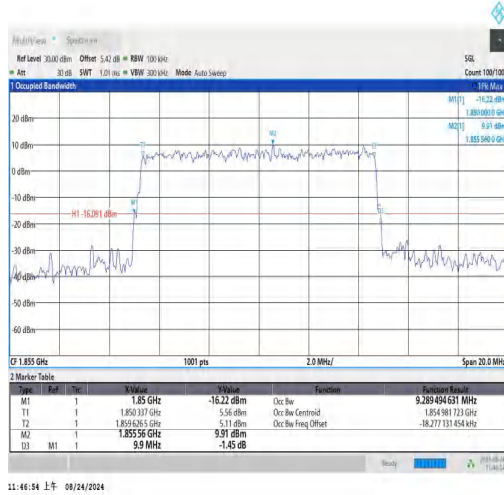


NTNV_N2_PC3_15_5_H_TID6_Ant1#1



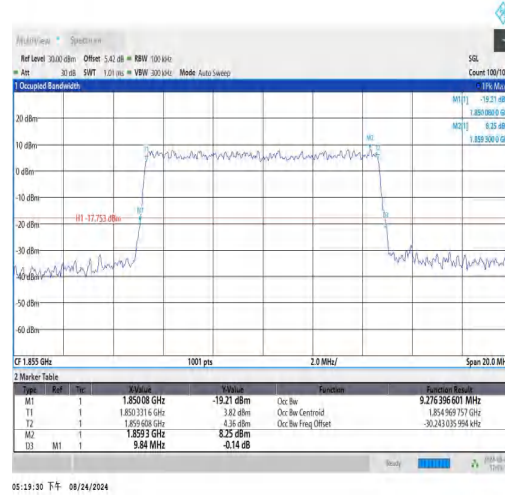


NTNV_N2_PC3_15_10_L_TID1_Ant1#1



11:46:54 上午 08/24/2024

NTNV_N2_PC3_15_10_L_TID2_Ant1#1



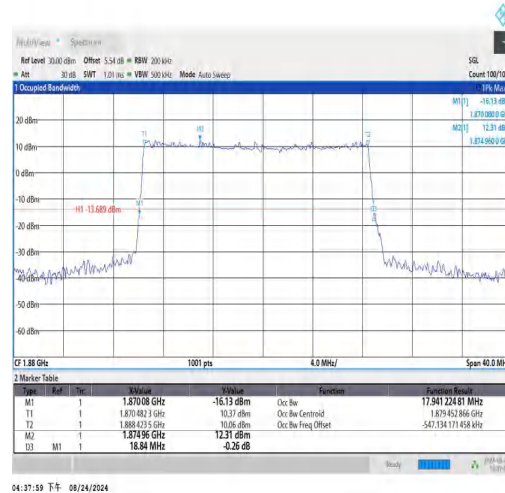
05:19:30 下午 08/24/2024

NTNV_N2_PC3_15_10_L_TID3_Ant1#1



04:37:37 下午 08/24/2024

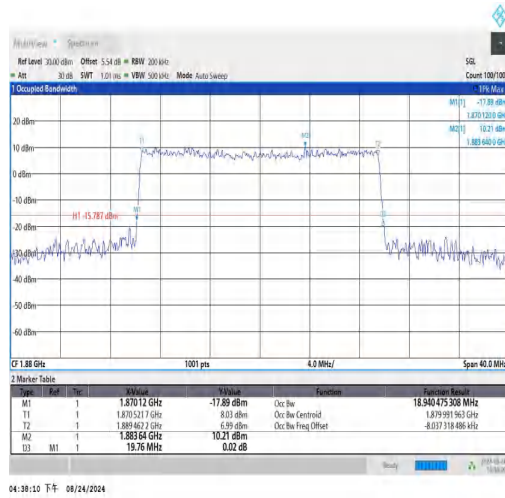
NTNV_N2_PC3_15_10_L_TID4_Ant1#1



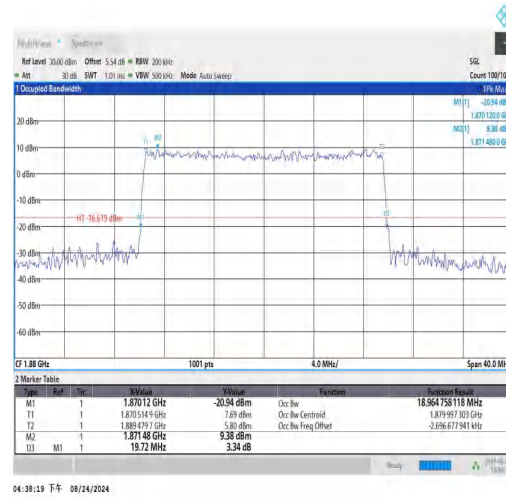
04:37:59 下午 08/24/2024

NTNV_N2_PC3_15_20_M_TID1_Ant1#1

NTNV_N2_PC3_15_20_M_TID2_Ant1#1



NTNV_N2_PC3_15_20_M_TID3_Ant1#1



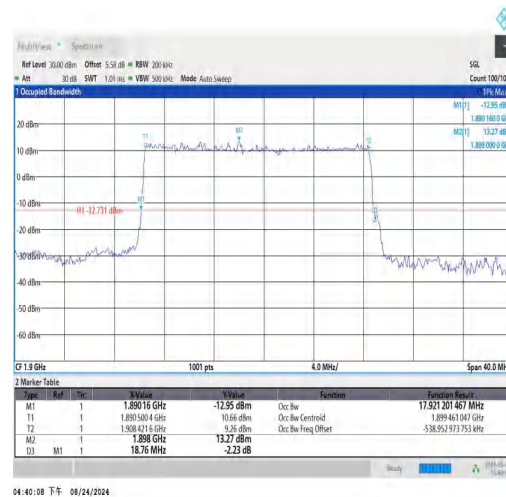
NTNV_N2_PC3_15_20_M_TID4_Ant1#1



NTNV_N2_PC3_15_20_M_TID5_Ant1#1



NTNV_N2_PC3_15_20_M_TID6_Ant1#1



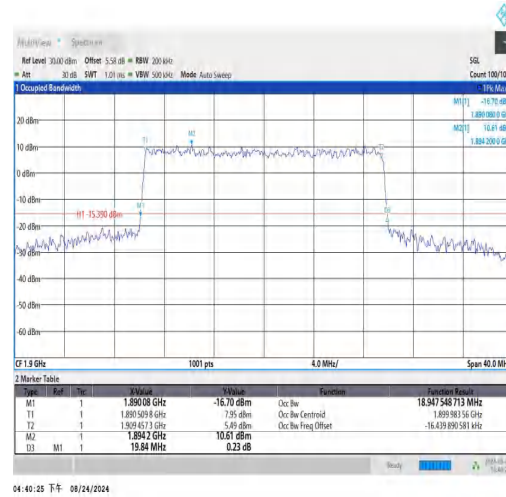


NTNV_N2_PC3_15_20_H_TID1_Ant1#1



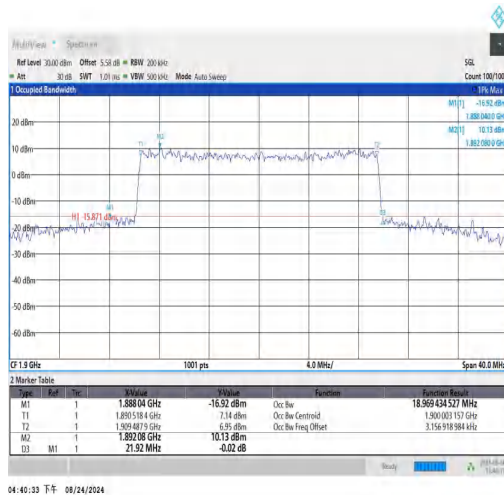
04:40:17 T-F 08/24/2024

NTNV_N2_PC3_15_20_H_TID2_Ant1#1



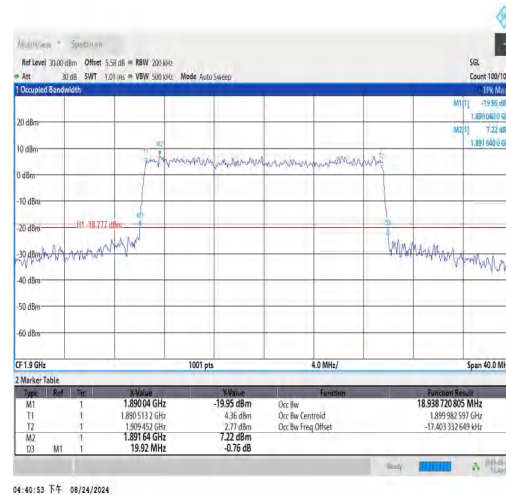
04:40:20 T-F 08/24/2024

NTNV_N2_PC3_15_20_H_TID3_Ant1#1



04:40:33 T-F 08/24/2024

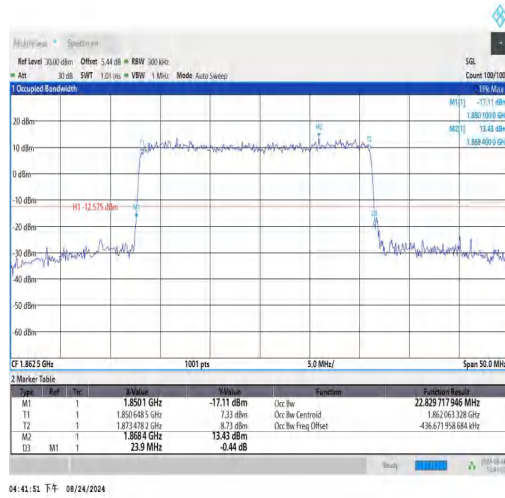
NTNV_N2_PC3_15_20_H_TID4_Ant1#1



04:40:53 T-F 08/24/2024

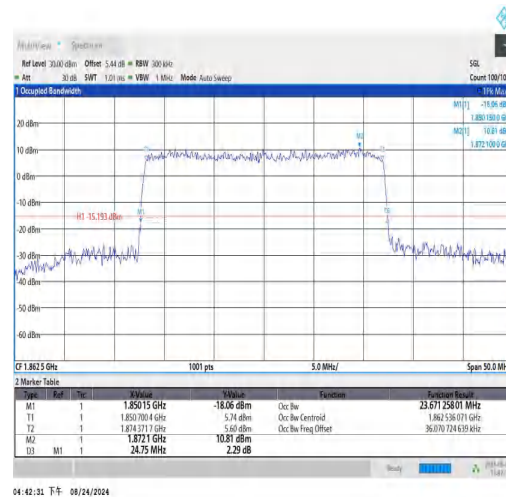
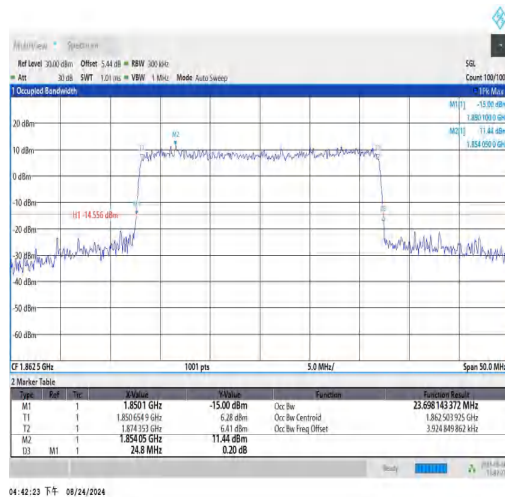
NTNV_N2_PC3_15_20_H_TID5_Ant1#1

NTNV_N2_PC3_15_20_H_TID6_Ant1#1



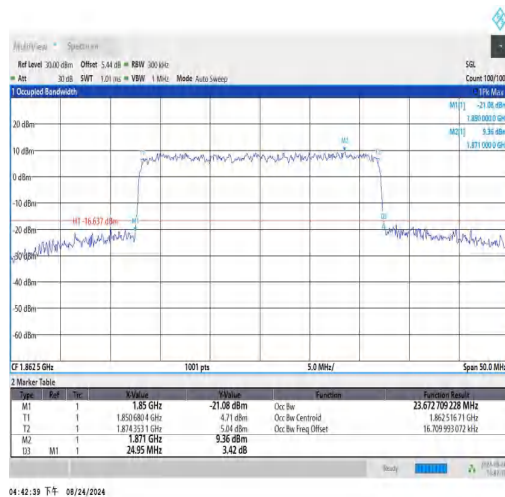
NTNV_N2_PC3_15_25_L_TID1_Ant1#1

NTNV_N2_PC3_15_25_L_TID2_Ant1#1



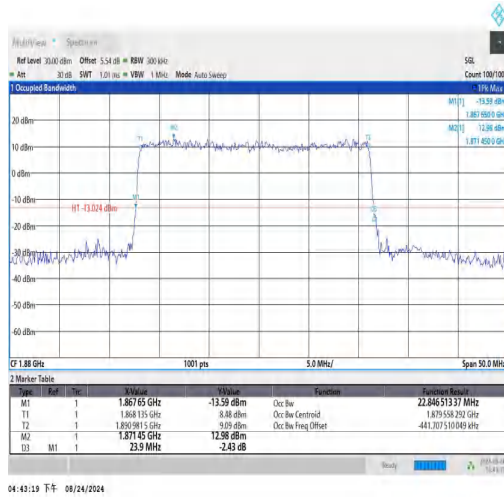
NTNV_N2_PC3_15_25_L_TID3_Ant1#1

NTNV_N2_PC3_15_25_L_TID4_Ant1#1

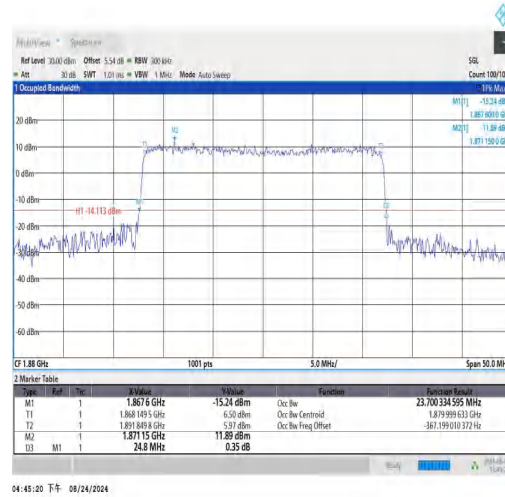




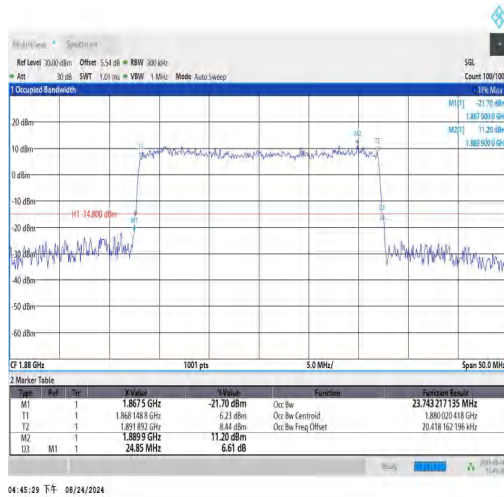
NTNV_N2_PC3_15_25_L_TID5_Ant1#1



NTNV_N2_PC3_15_25_L_TID6_Ant1#1



NTNV_N2_PC3_15_25_M_TID1_Ant1#1

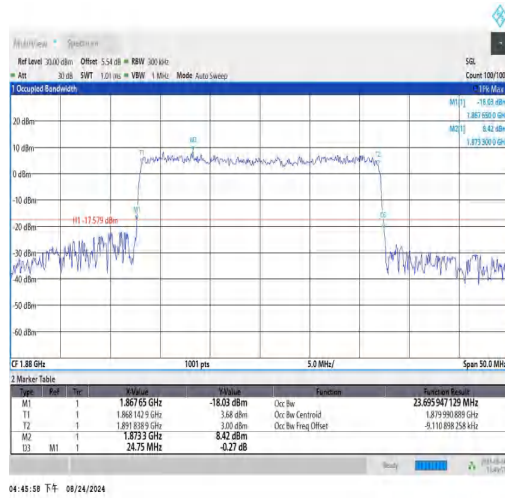


NTNV_N2_PC3_15_25_M_TID3_Ant1#1



NTNV_N2_PC3_15_25_M_TID4_Ant1#1

NTNV_N2_PC3_15_25_M_TID5_Ant1#1



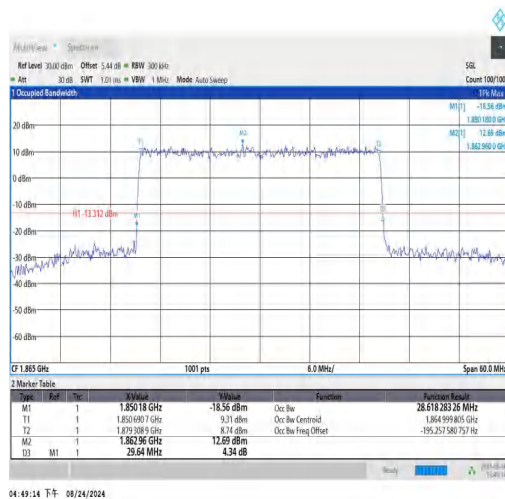
04:45:18 下午 08/24/2024



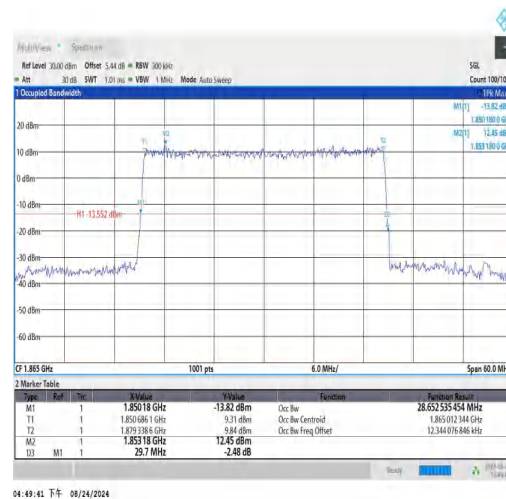
04:46:20 下午 08/24/2024

NTNV_N2_PC3_15_25_M_TID6_Ant1#1

NTNV_N2_PC3_15_25_H_TID1_Ant1#1



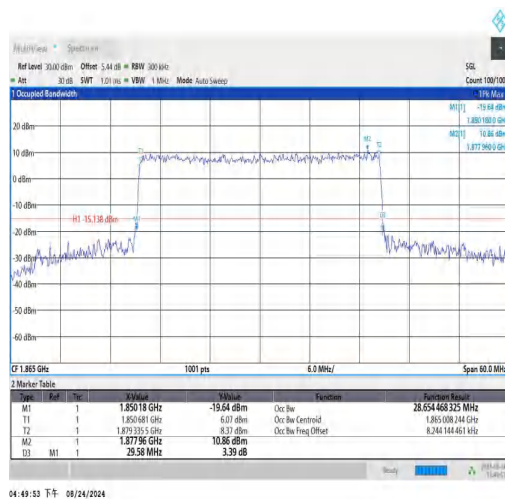
04:49:14 下午 08/24/2024



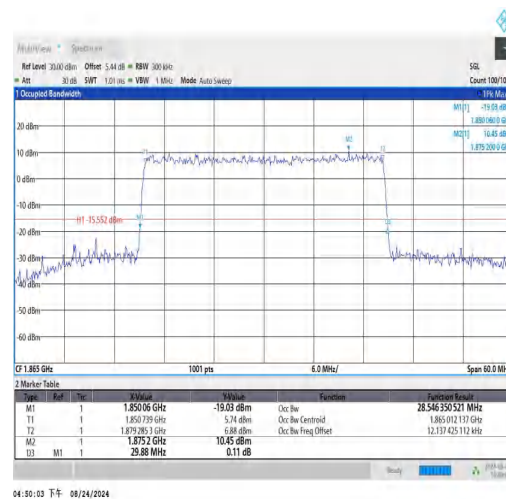
04:49:42 下午 08/24/2024

NTNV_N2_PC3_15_30_L_TID1_Ant1#1

NTNV_N2_PC3_15_30_L_TID2_Ant1#1



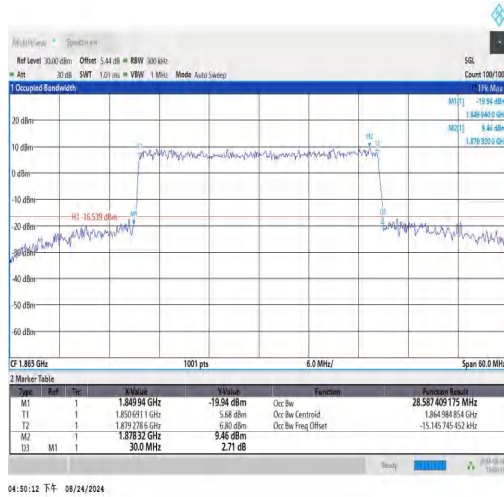
04:49:53 下午 08/24/2024



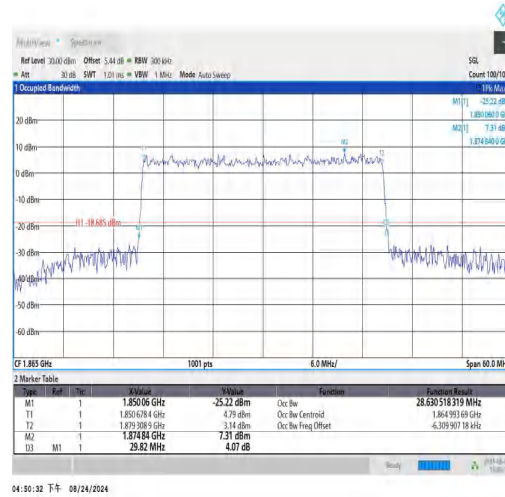
04:50:02 下午 08/24/2024



NTNV_N2_PC3_15_30_L_TID3_Ant1#1



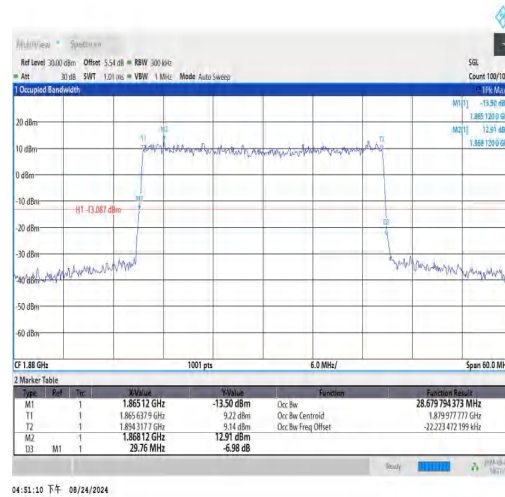
NTNV_N2_PC3_15_30_L_TID4_Ant1#1



NTNV_N2_PC3_15_30_L_TID5_Ant1#1

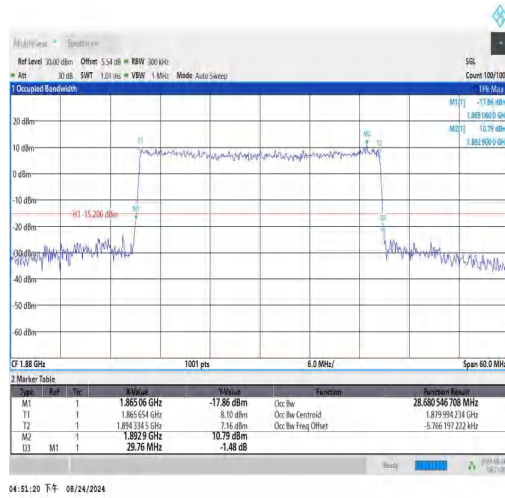


NTNV_N2_PC3_15_30_L_TID6_Ant1#1

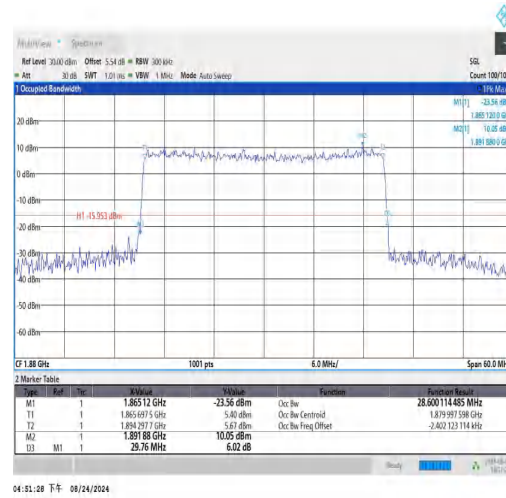


NTNV_N2_PC3_15_30_M_TID1_Ant1#1

NTNV_N2_PC3_15_30_M_TID2_Ant1#1



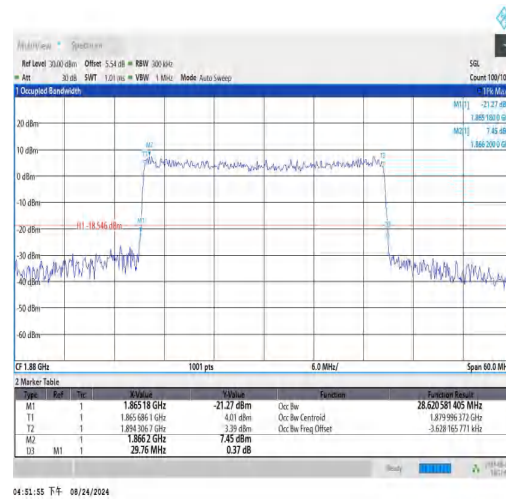
NTNV_N2_PC3_15_30_M_TID3_Ant1#1



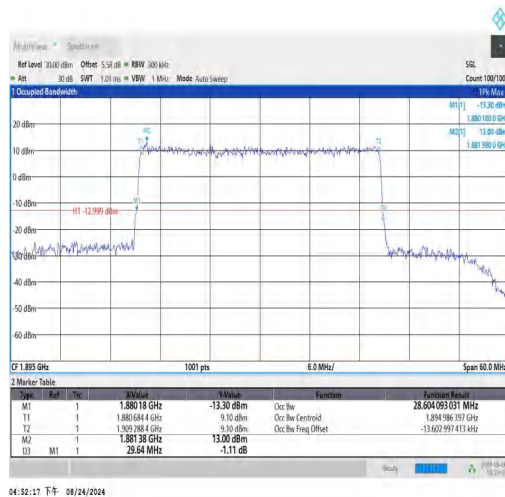
NTNV_N2_PC3_15_30_M_TID4_Ant1#1



NTNV_N2_PC3_15_30_M_TID5_Ant1#1



NTNV_N2_PC3_15_30_M_TID6_Ant1#1



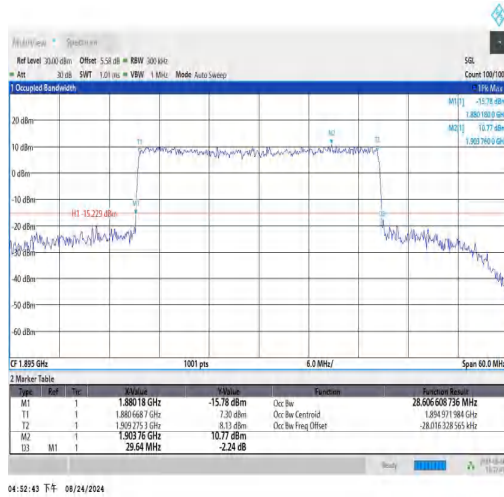
04:52:17 08/24/2024



04:52:32 08/24/2024

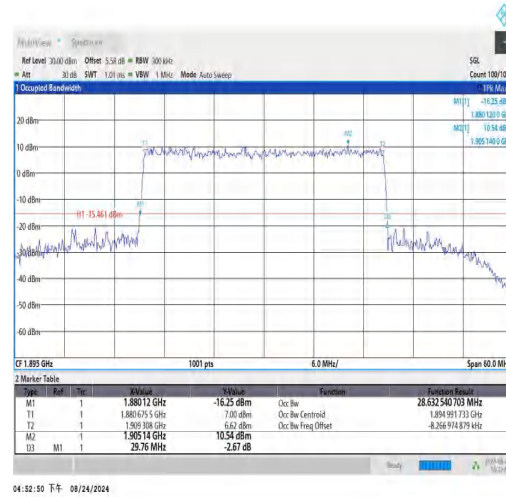


NTNV_N2_PC3_15_30_H_TID1_Ant1#1



04:52:43 T-F 08/24/2024

NTNV_N2_PC3_15_30_H_TID2_Ant1#1



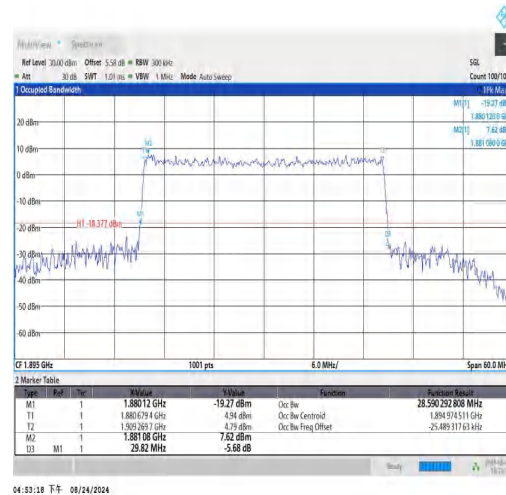
04:52:50 T-F 08/24/2024

NTNV_N2_PC3_15_30_H_TID3_Ant1#1



04:52:58 T-F 08/24/2024

NTNV_N2_PC3_15_30_H_TID4_Ant1#1



04:53:18 T-F 08/24/2024

NTNV_N2_PC3_15_30_H_TID5_Ant1#1

NTNV_N2_PC3_15_30_H_TID6_Ant1#1



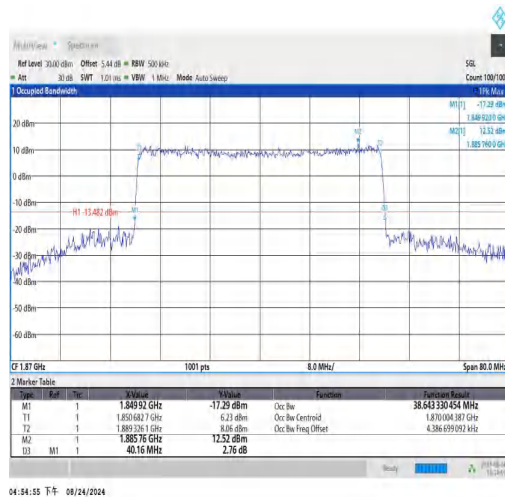
04:34:15 下 08/24/2024



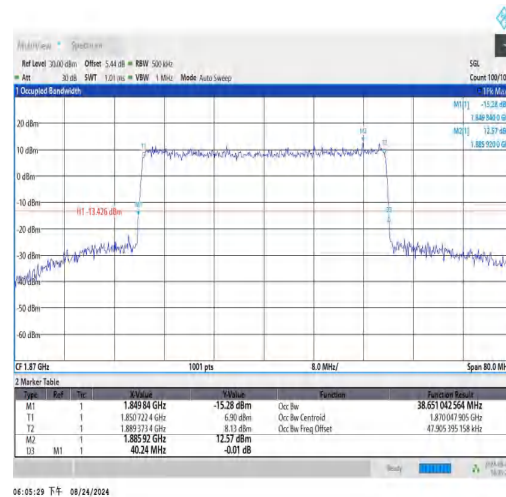
04:34:45 下 08/24/2024

NTNV_N2_PC3_15_40_L_TID1_Ant1#1

NTNV_N2_PC3_15_40_L_TID2_Ant1#1



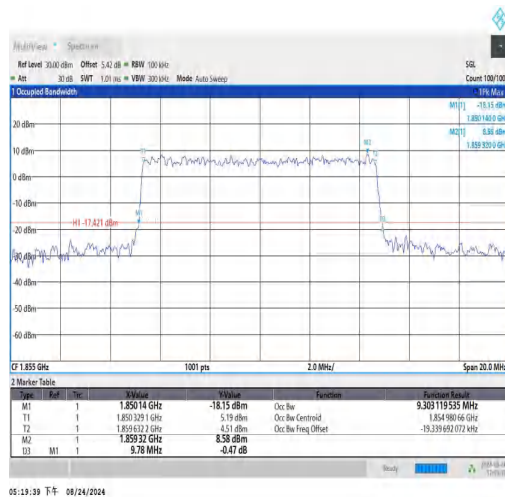
04:34:15 下 08/24/2024



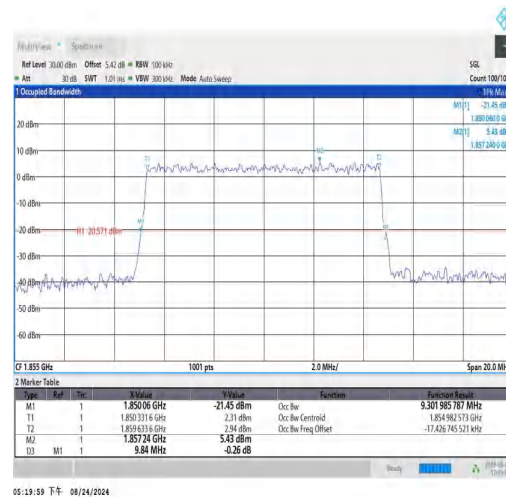
06:05:29 下 08/24/2024

NTNV_N2_PC3_15_40_L_TID3_Ant1#1

NTNV_N2_PC3_15_40_L_TID4_Ant1#1



05:19:39 下 08/24/2024



05:19:59 下 08/24/2024

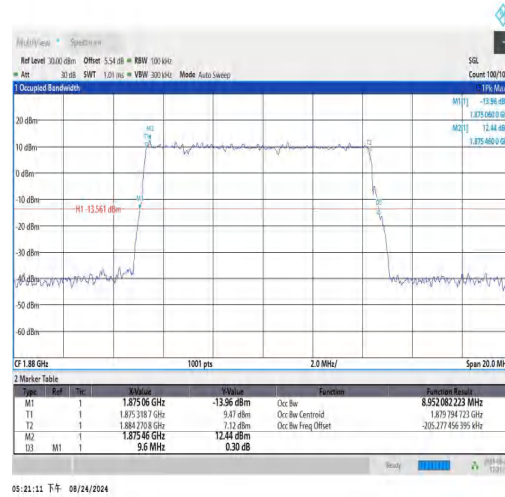


NTNV_N2_PC3_15_10_L_TID5_Ant1#1



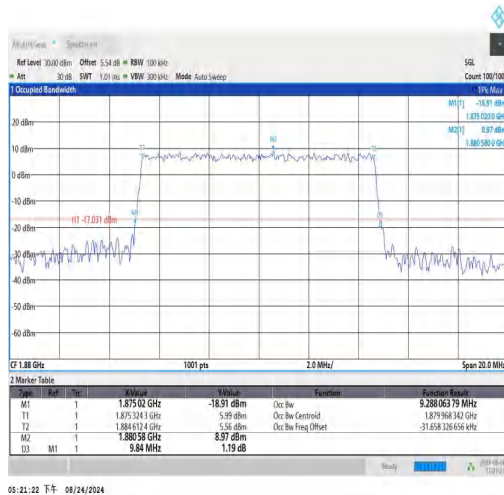
05:20:14 T-F 08/24/2024

NTNV_N2_PC3_15_10_L_TID6_Ant1#1



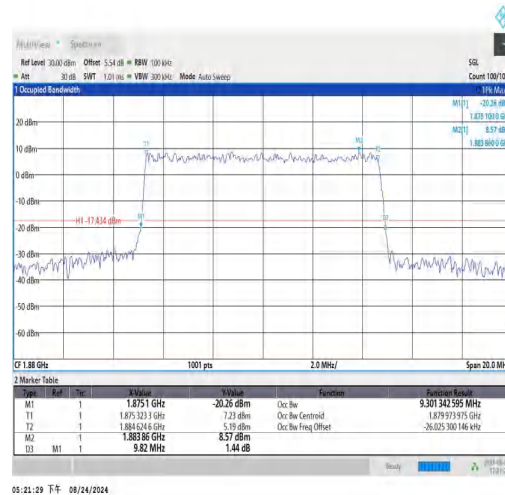
05:21:11 T-F 08/24/2024

NTNV_N2_PC3_15_10_M_TID1_Ant1#1



05:21:22 T-F 08/24/2024

NTNV_N2_PC3_15_10_M_TID2_Ant1#1



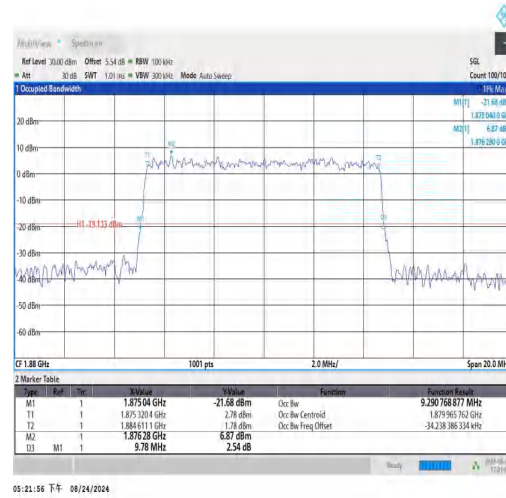
05:21:29 T-F 08/24/2024

NTNV_N2_PC3_15_10_M_TID3_Ant1#1

NTNV_N2_PC3_15_10_M_TID4_Ant1#1



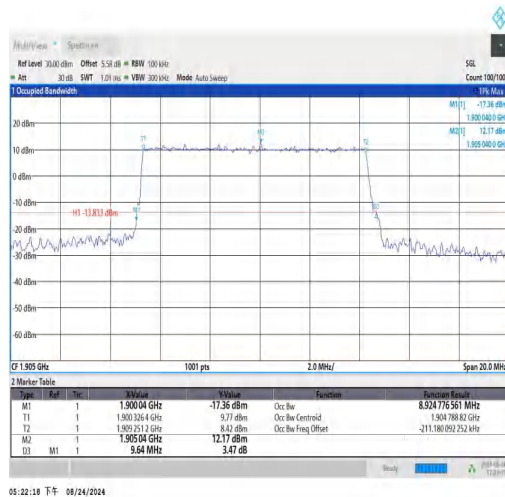
05:21:37 下 08/24/2024



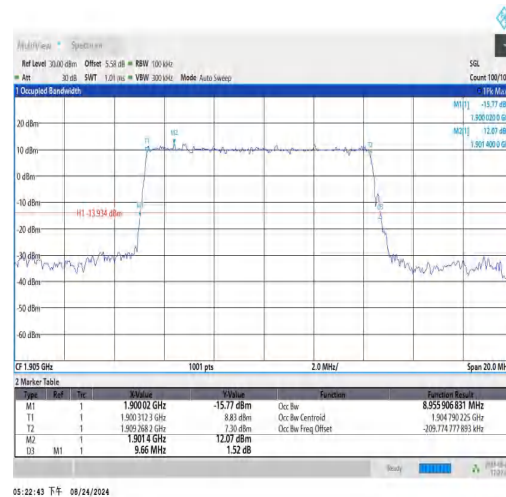
05:21:56 下 08/24/2024

NTVN_N2_PC3_15_10_M_TID5_Ant1#1

NTVN_N2_PC3_15_10_M_TID6_Ant1#1



05:22:19 下 08/24/2024



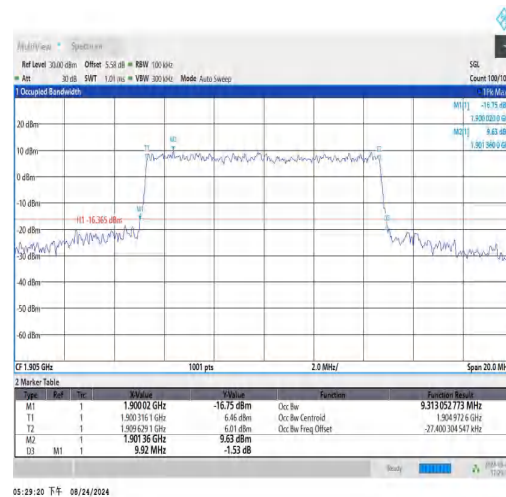
05:22:43 下 08/24/2024

NTVN_N2_PC3_15_10_H_TID1_Ant1#1

NTVN_N2_PC3_15_10_H_TID2_Ant1#1



06:01:14 下 08/24/2024



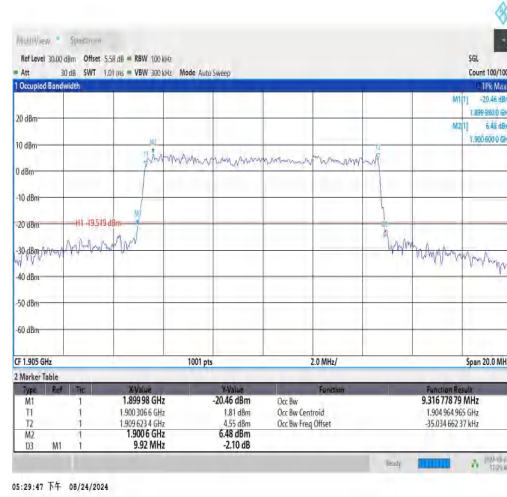
05:29:20 下 08/24/2024



NTNV_N2_PC3_15_10_H_TID3_Ant1#1



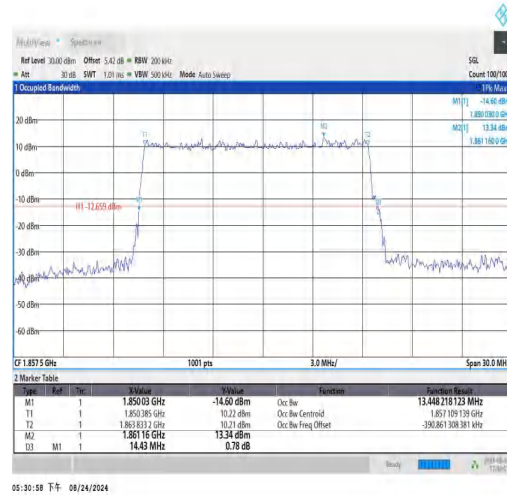
NTNV_N2_PC3_15_10_H_TID4_Ant1#1



NTNV_N2_PC3_15_10_H_TID5_Ant1#1

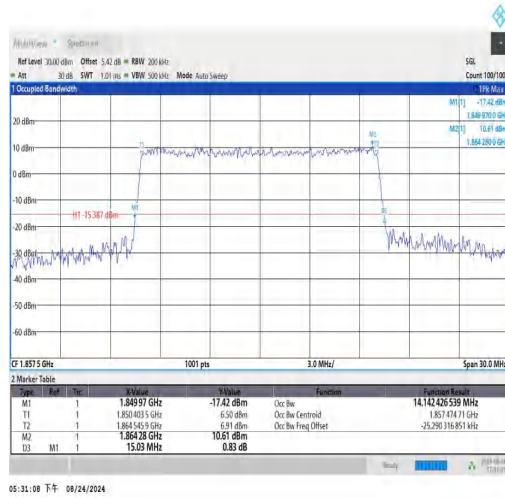


NTNV_N2_PC3_15_10_H_TID6_Ant1#1

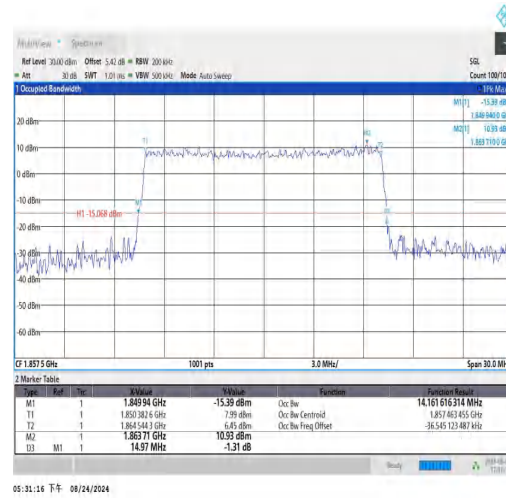


NTNV_N2_PC3_15_15_L_TID1_Ant1#1

NTNV_N2_PC3_15_15_L_TID2_Ant1#1



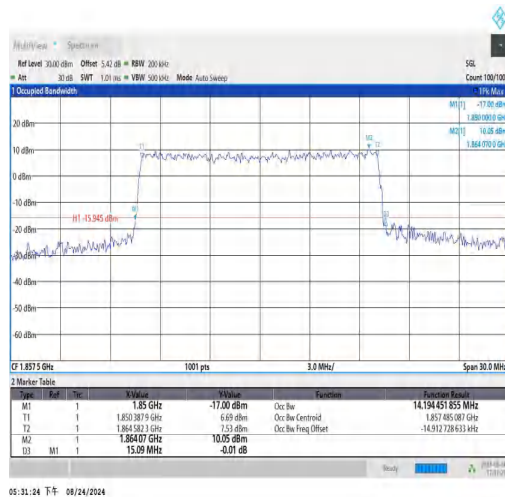
05:31:08 下午 08/24/2024



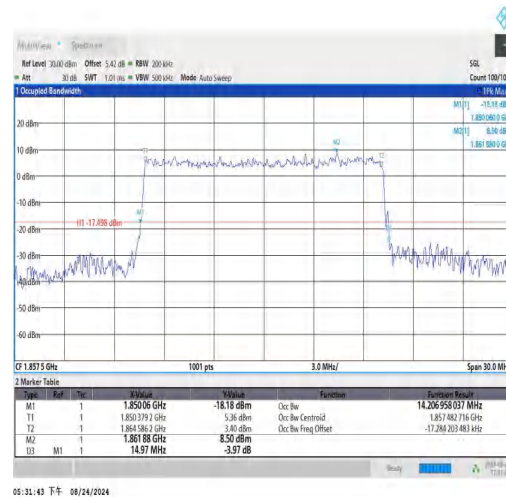
05:31:14 下午 08/24/2024

NTNV_N2_PC3_15_15_L_TID3_Ant1#1

NTNV_N2_PC3_15_15_L_TID4_Ant1#1



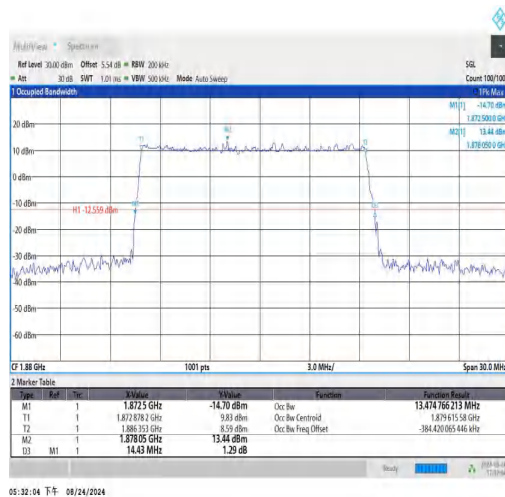
05:31:24 下午 08/24/2024



05:31:43 下午 08/24/2024

NTNV_N2_PC3_15_15_L_TID5_Ant1#1

NTNV_N2_PC3_15_15_L_TID6_Ant1#1



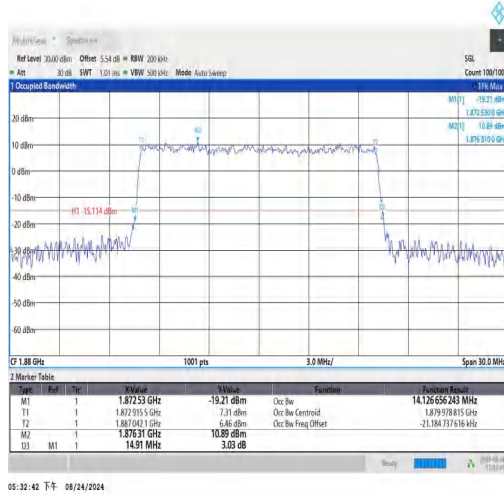
05:32:04 下午 08/24/2024



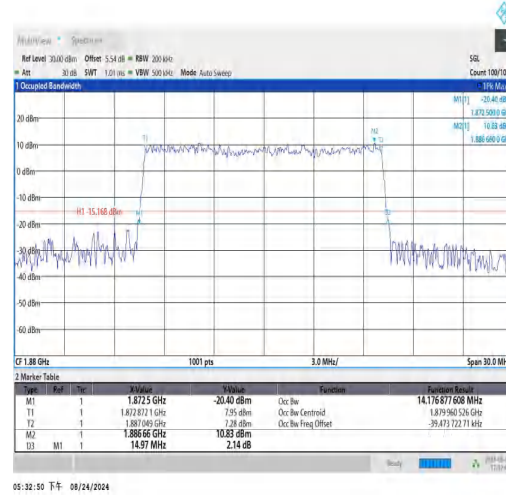
05:32:32 下午 08/24/2024



NTNV_N2_PC3_15_15_M_TID1_Ant1#1



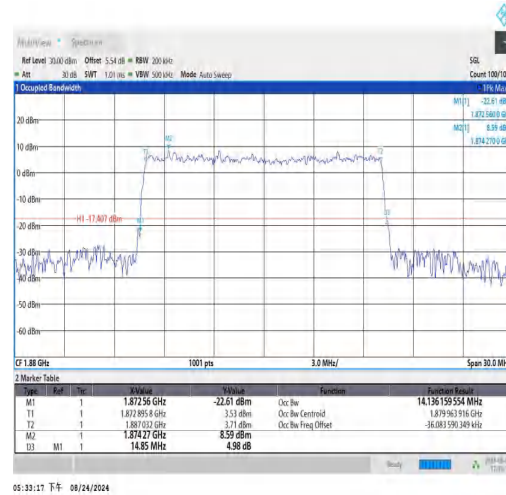
NTNV_N2_PC3_15_15_M_TID2_Ant1#1



NTNV_N2_PC3_15_15_M_TID3_Ant1#1



NTNV_N2_PC3_15_15_M_TID4_Ant1#1



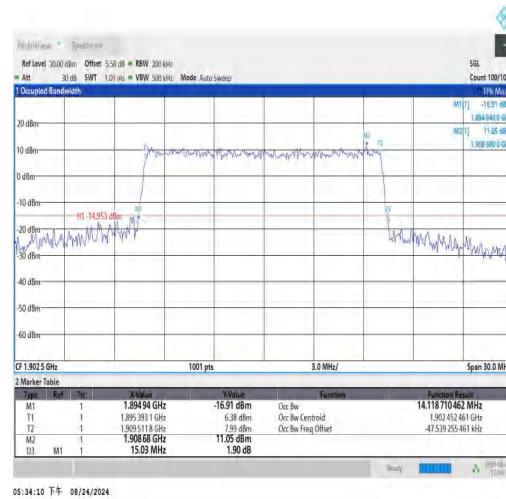
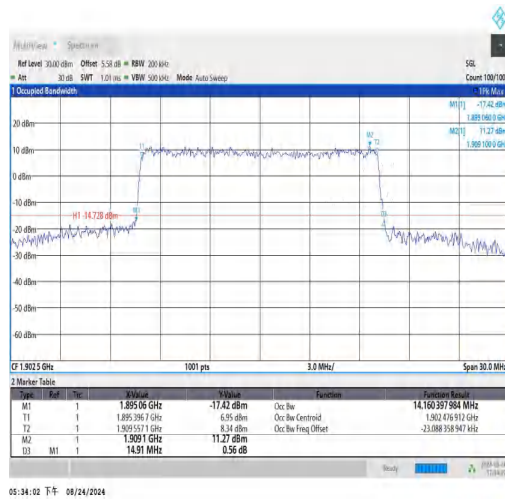
NTNV_N2_PC3_15_15_M_TID5_Ant1#1

NTNV_N2_PC3_15_15_M_TID6_Ant1#1



NTVN_N2_PC3_15_15_H_TID1_Ant1#1

NTVN_N2_PC3_15_15_H_TID2_Ant1#1



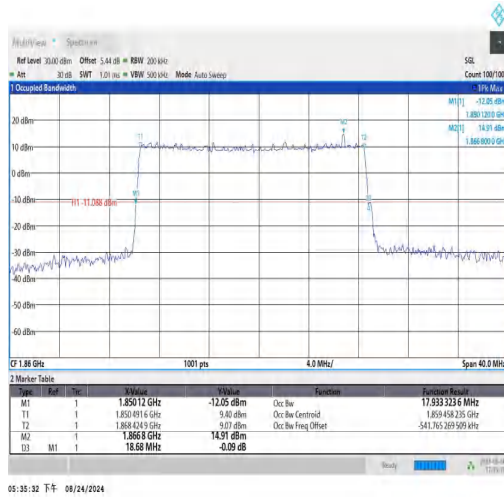
NTVN_N2_PC3_15_15_H_TID3_Ant1#1

NTVN_N2_PC3_15_15_H_TID4_Ant1#1

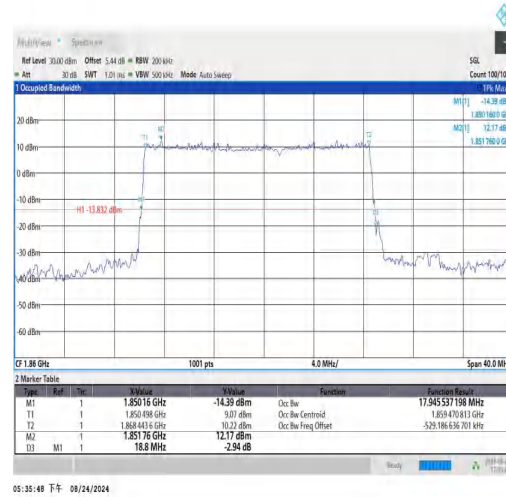




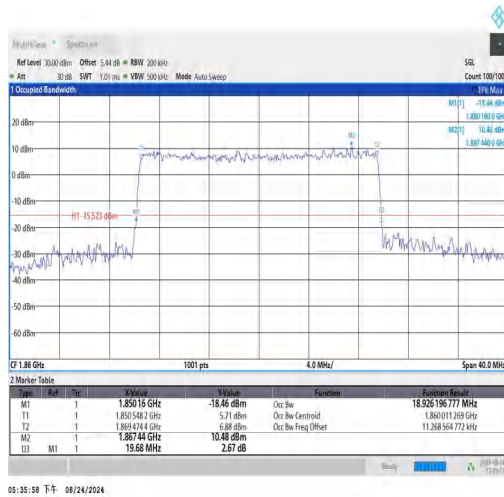
NTNV_N2_PC3_15_15_H_TID5_Ant1#1



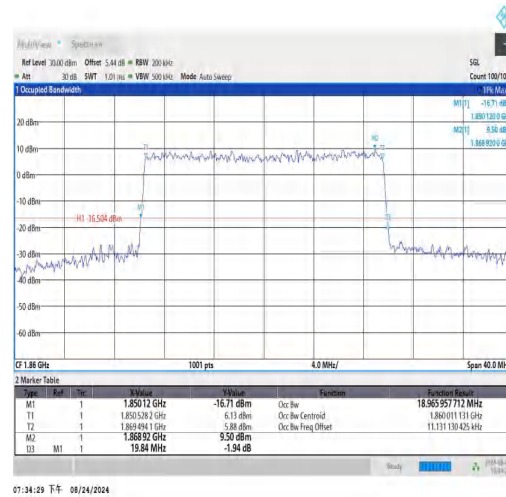
NTNV_N2_PC3_15_15_H_TID6_Ant1#1



NTNV_N2_PC3_15_20_L_TID1_Ant1#1



NTNV_N2_PC3_15_20_L_TID2_Ant1#1



NTNV_N2_PC3_15_20_L_TID3_Ant1#1

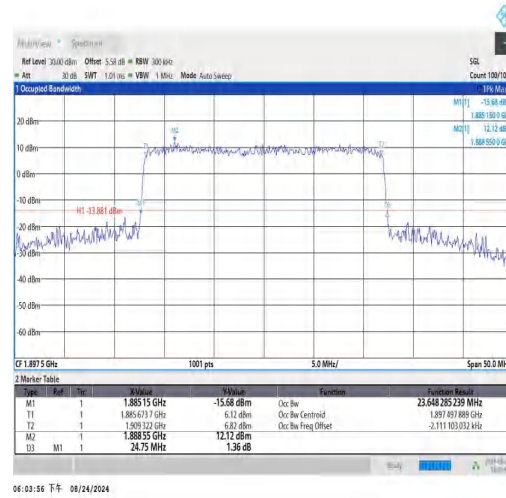


NTNV_N2_PC3_15_20_L_TID4_Ant1#1

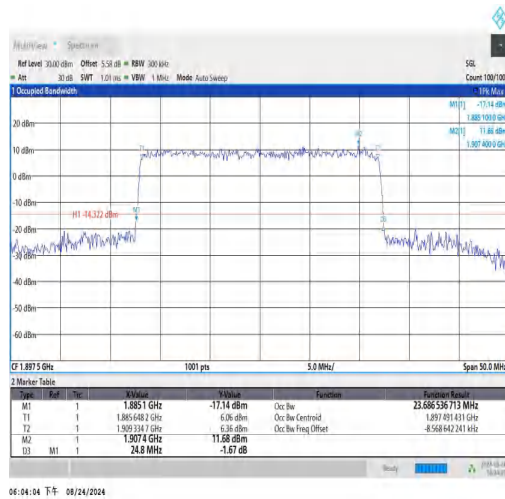




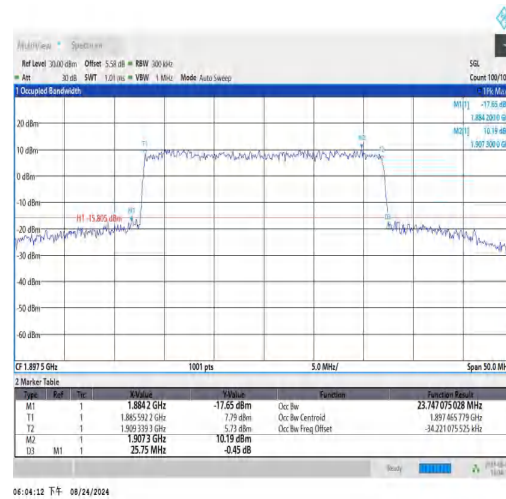
NTNV_N2_PC3_15_25_H_TID2_Ant1#1



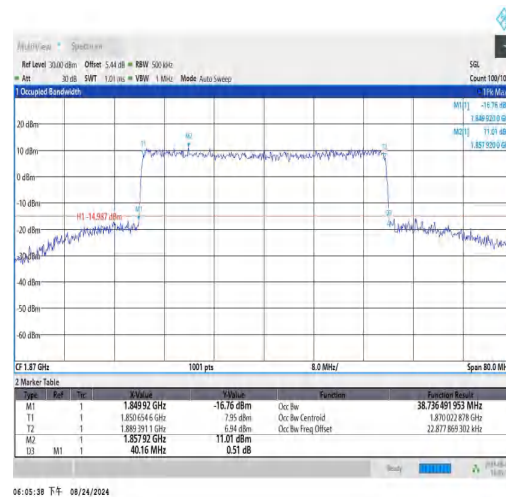
NTNV_N2_PC3_15_25_H_TID3_Ant1#1



NTNV_N2_PC3_15_25_H_TID4_Ant1#1

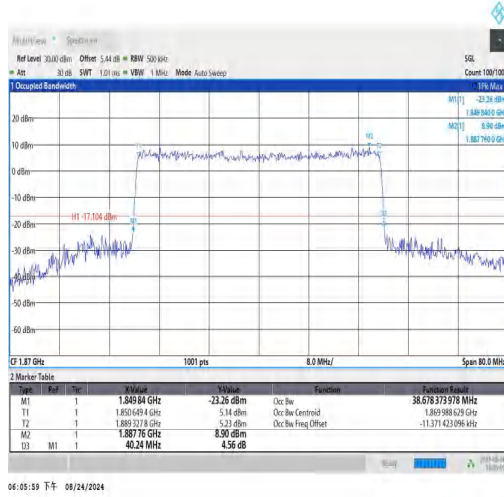


NTNV_N2_PC3_15_25_H_TID5_Ant1#1



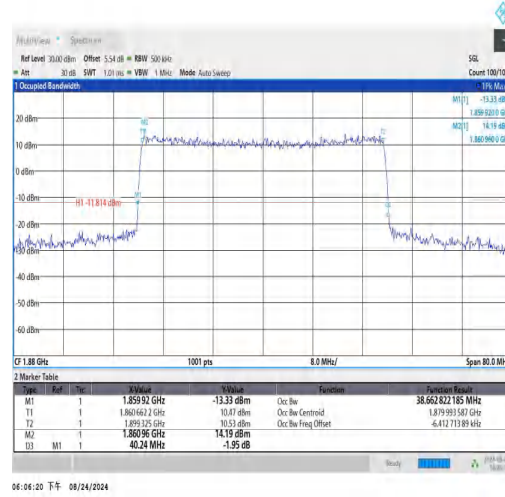


NTNV_N2_PC3_15_25_H_TID6_Ant1#1



06:05:59 T-F 08/24/2024

NTNV_N2_PC3_15_40_L_TID5_Ant1#1



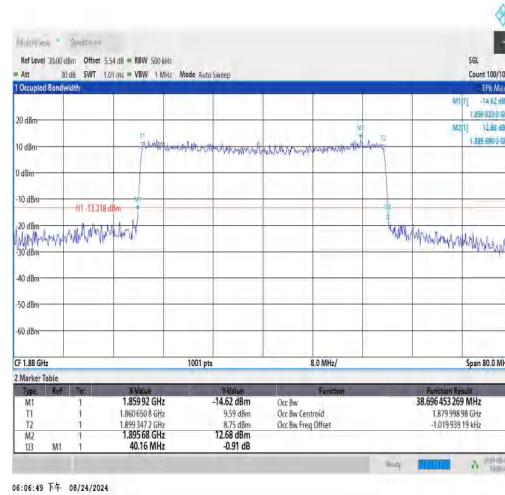
06:06:20 T-F 08/24/2024

NTNV_N2_PC3_15_40_L_TID6_Ant1#1



06:06:39 T-F 08/24/2024

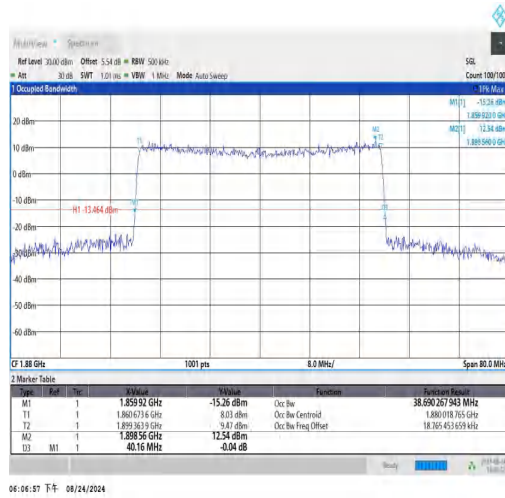
NTNV_N2_PC3_15_40_M_TID1_Ant1#1



06:06:49 T-F 08/24/2024

NTNV_N2_PC3_15_40_M_TID2_Ant1#1

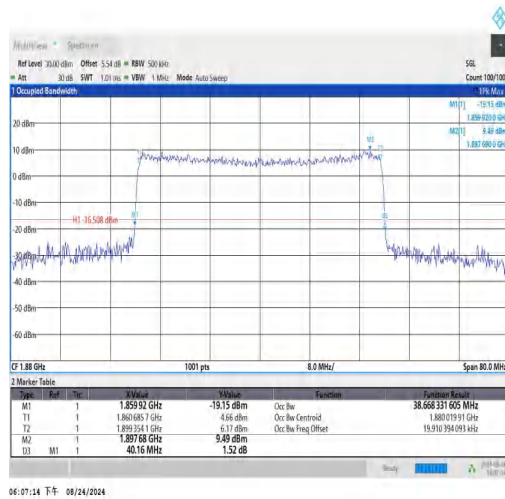
NTNV_N2_PC3_15_40_M_TID3_Ant1#1



NTNV_N2_PC3_15_40_M_TID4_Ant1#1



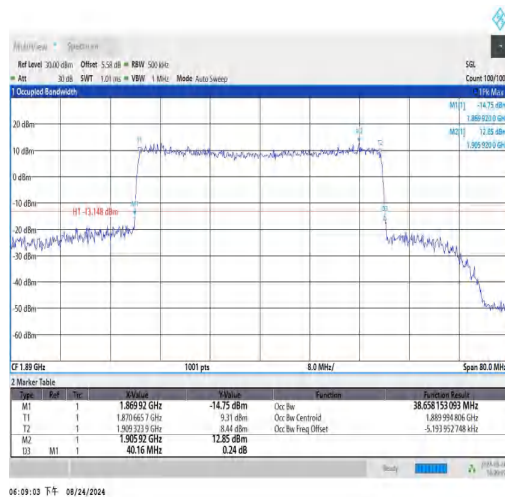
NTNV_N2_PC3_15_40_M_TID5_Ant1#1



NTNV_N2_PC3_15_40_M_TID6_Ant1#1



NTNV_N2_PC3_15_40_H_TID2_Ant1#1



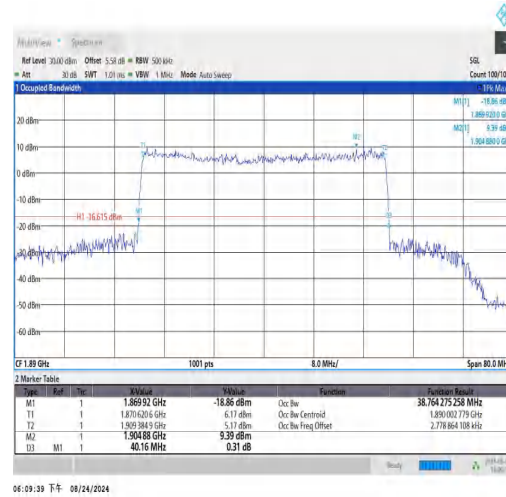


NTNV_N2_PC3_15_40_H_TID3_Ant1#1



06:09:19 下午 08/24/2024

NTNV_N2_PC3_15_40_H_TID4_Ant1#1



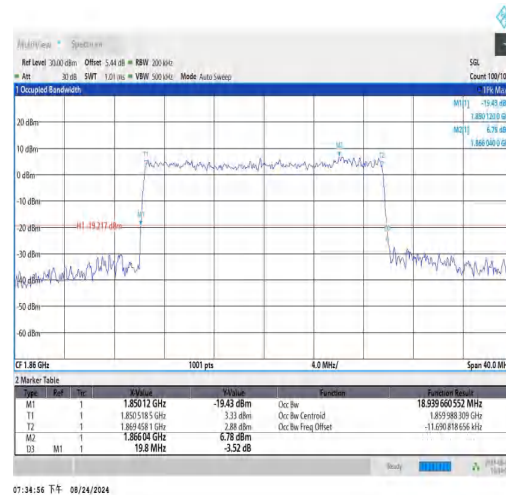
06:09:39 下午 08/24/2024

NTNV_N2_PC3_15_40_H_TID5_Ant1#1



07:34:37 下午 08/24/2024

NTNV_N2_PC3_15_40_H_TID6_Ant1#1



07:34:56 下午 08/24/2024

NTNV_N2_PC3_15_20_L_TID5_Ant1#1



07:34:37 下午 08/24/2024

NTNV_N2_PC3_15_20_L_TID6_Ant1#1



07:34:56 下午 08/24/2024



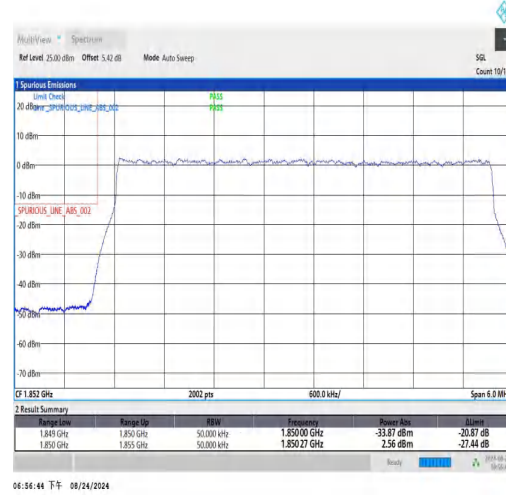
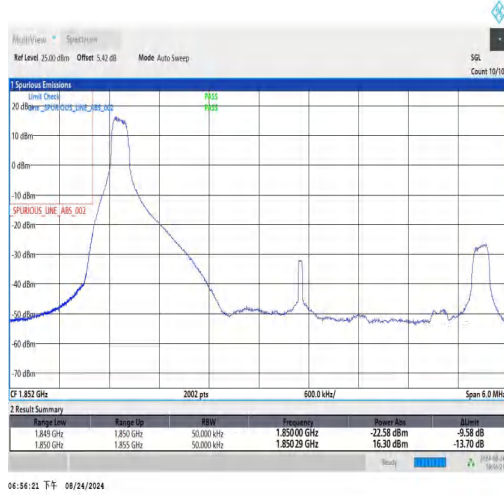
Appendix D: Band Edge

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
N2	15	5	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N2	15	5	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N2	15	5	DFT-QPSK	L	Outer_Full	see graph	PASS
N2	15	5	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N2	15	5	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N2	15	5	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N2	15	5	DFT-QPSK	H	Outer_Full	see graph	PASS
N2	15	20	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N2	15	20	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N2	15	20	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N2	15	20	DFT-QPSK	L	Outer_Full	see graph	PASS
N2	15	20	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N2	15	20	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N2	15	20	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N2	15	20	DFT-QPSK	H	Outer_Full	see graph	PASS
N2	15	40	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N2	15	40	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N2	15	40	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N2	15	40	DFT-QPSK	L	Outer_Full	see graph	PASS
N2	15	40	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N2	15	40	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N2	15	40	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N2	15	40	DFT-QPSK	H	Outer_Full	see graph	PASS

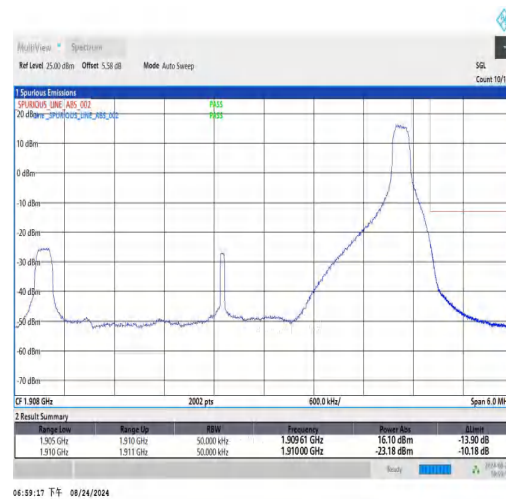


Test Graphs



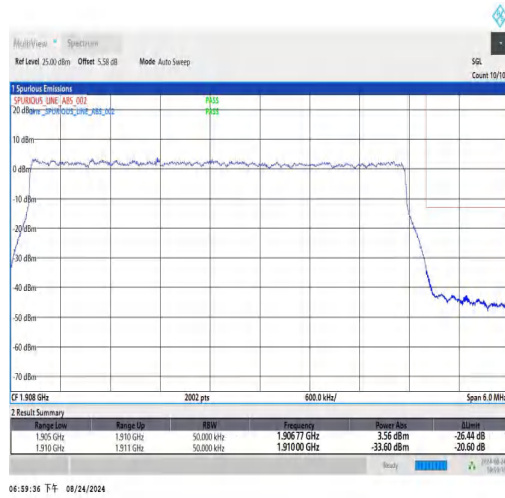
NTNV_N2_PC3_15_5_L_TID1_RID1#1

NTNV_N2_PC3_15_5_L_TID3_RID1#1

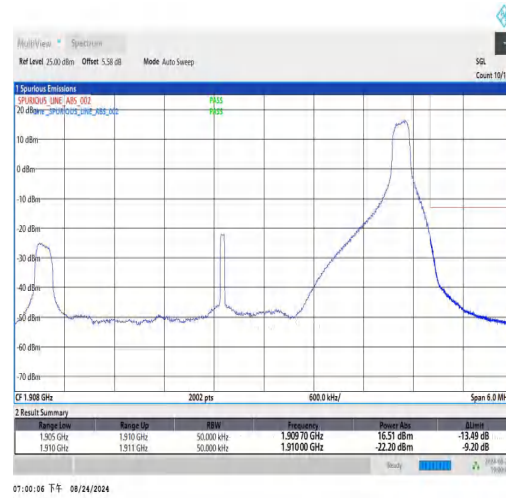


NTNV_N2_PC3_15_5_L_TID6_RID1#1

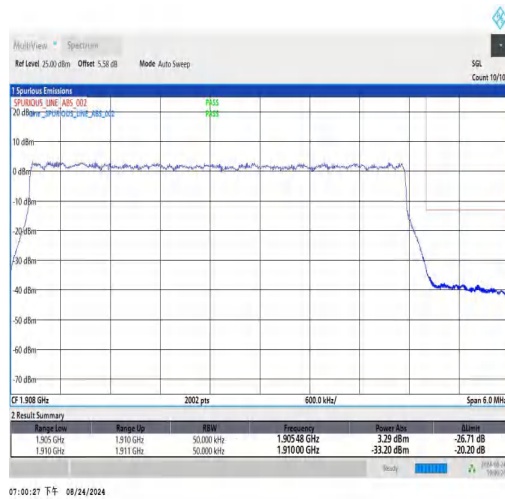
NTNV_N2_PC3_15_5_H_TID2_RID1#1



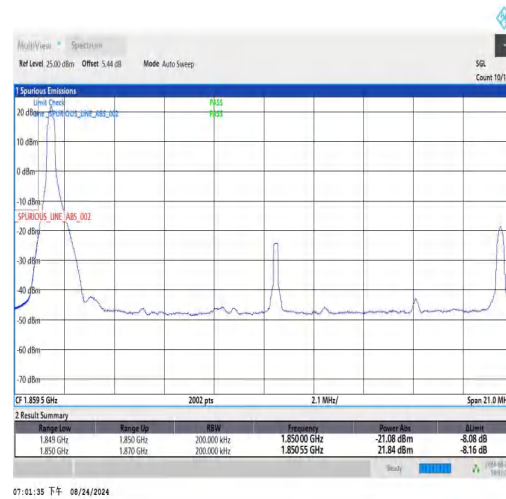
NTNV_N2_PC3_15_5_H_TID3_RID1#1



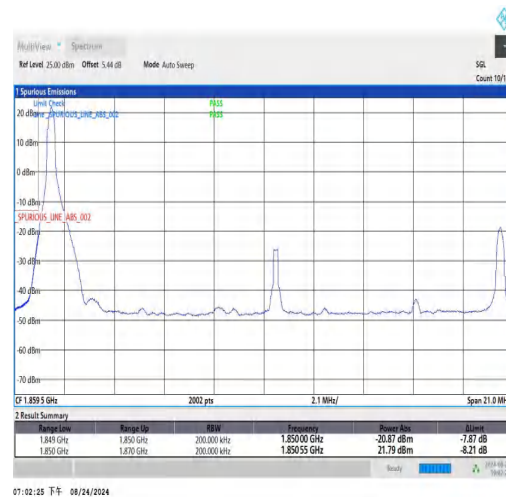
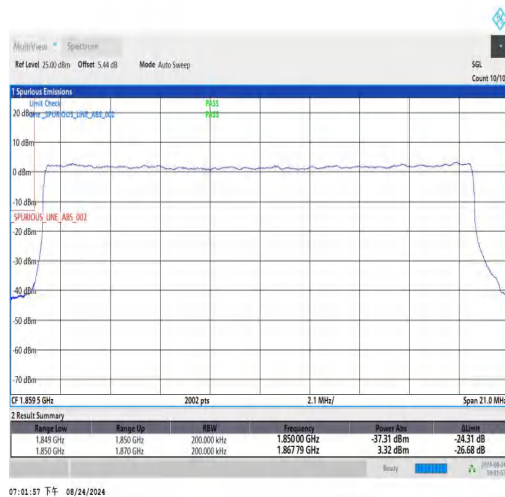
NTNV_N2_PC3_15_5_H_TID5_RID1#1



NTNV_N2_PC3_15_5_H_TID6_RID1#1

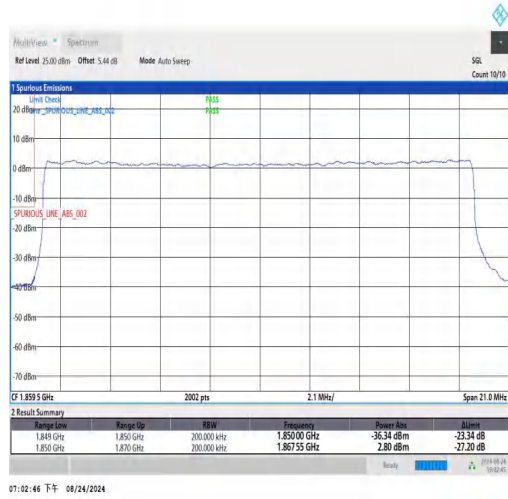


NTNV_N2_PC3_15_20_L_TID1_RID1#1

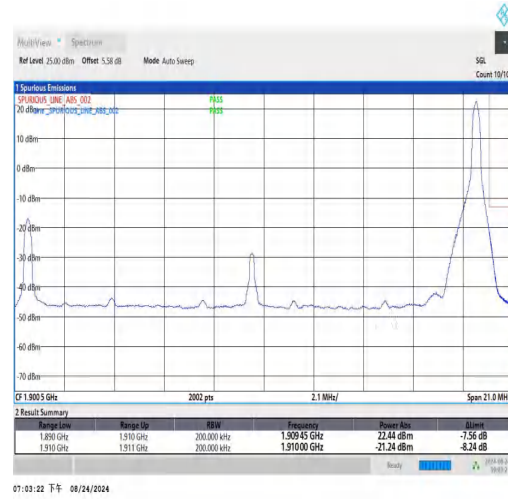




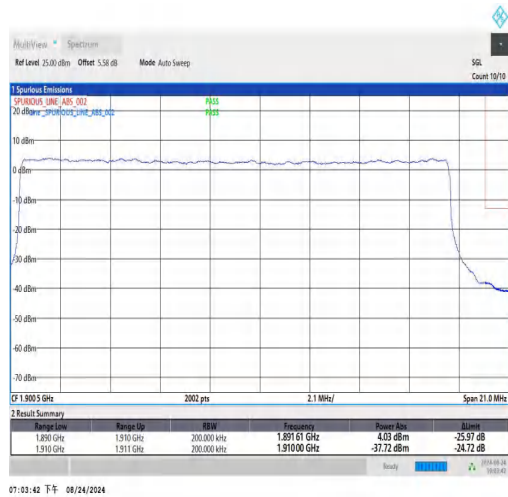
NTNV_N2_PC3_15_20_L_TID3_RID1#1



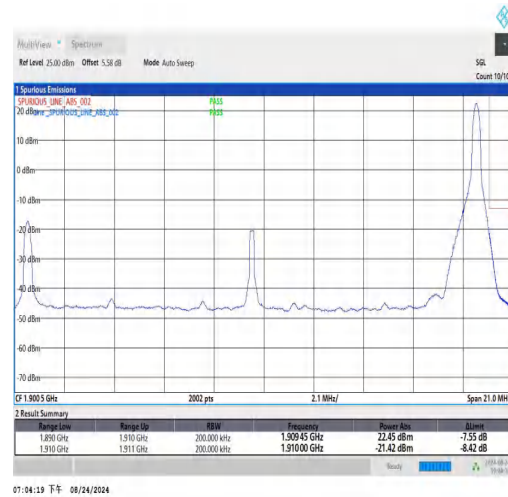
NTNV_N2_PC3_15_20_L_TID4_RID1#1



NTNV_N2_PC3_15_20_L_TID6_RID1#1

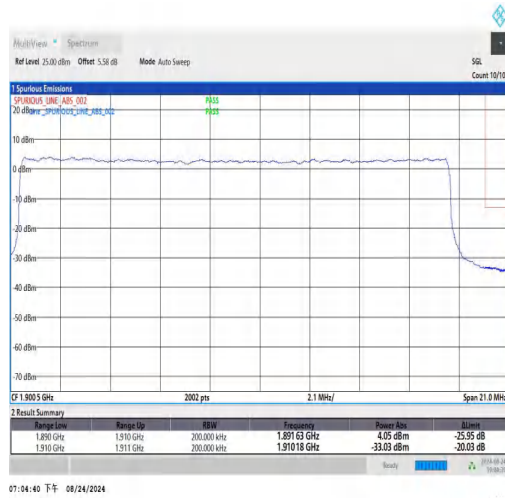


NTNV_N2_PC3_15_20_H_TID2_RID1#1

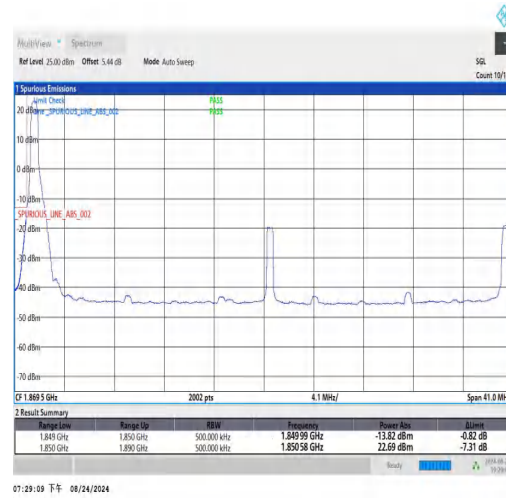


NTNV_N2_PC3_15_20_H_TID3_RID1#1

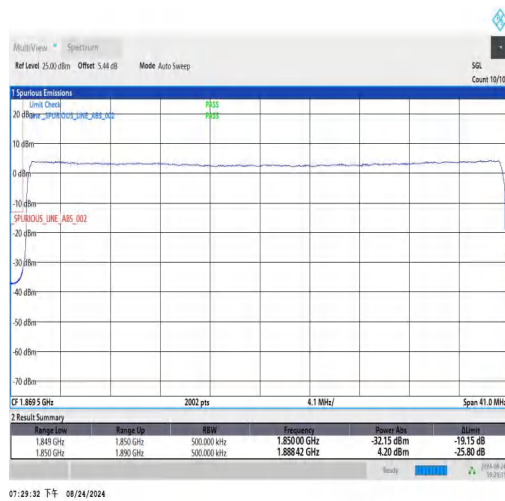
NTNV_N2_PC3_15_20_H_TID5_RID1#1



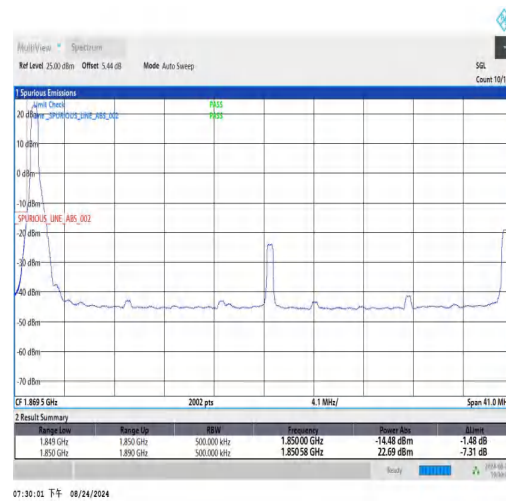
NTNV_N2_PC3_15_20_H_TID6_RID1#1



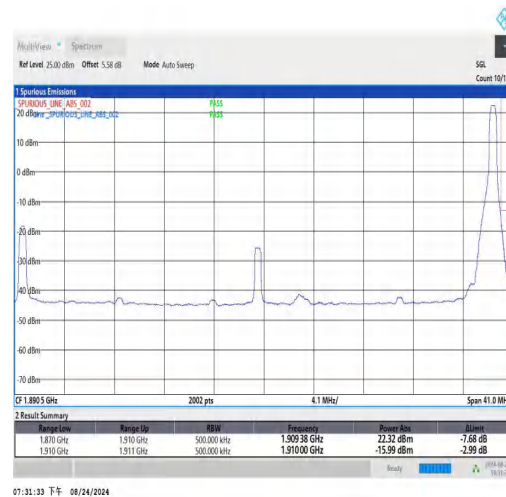
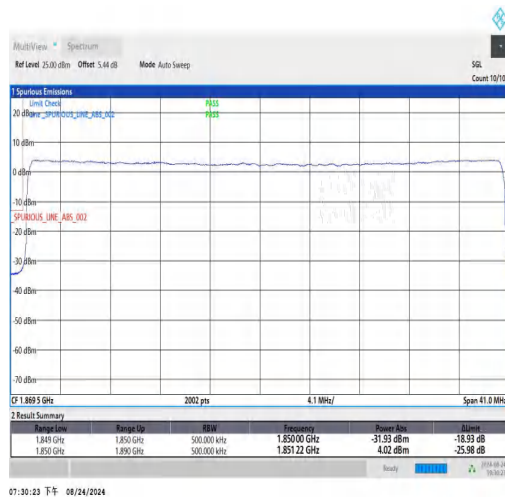
NTNV_N2_PC3_15_40_L_TID1_RID1#1



NTNV_N2_PC3_15_40_L_TID3_RID1#1

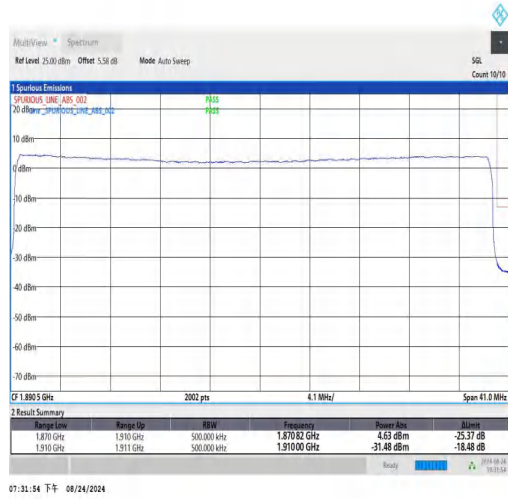


NTNV_N2_PC3_15_40_L_TID4_RID1#1





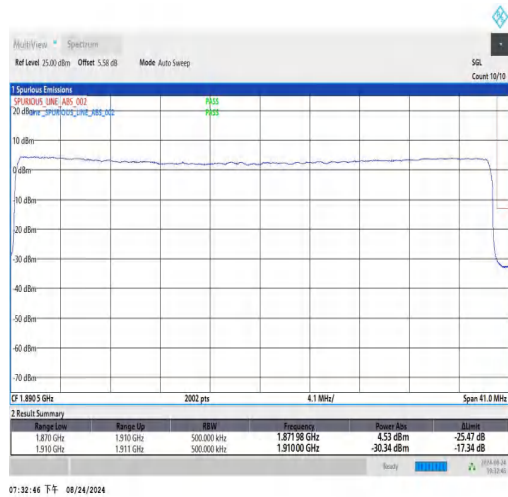
NTNV_N2_PC3_15_40_L_TID6_RID1#1



NTNV_N2_PC3_15_40_H_TID2_RID1#1



NTNV_N2_PC3_15_40_H_TID3_RID1#1



NTNV_N2_PC3_15_40_H_TID5_RID1#1

NTNV_N2_PC3_15_40_H_TID6_RID1#1



Appendix E: Conducted Spurious Emission

Test Result

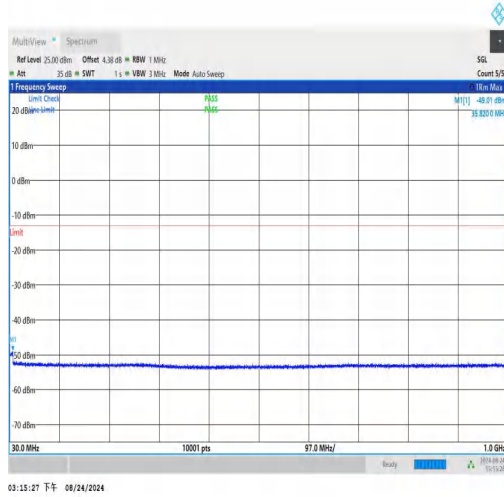
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Start Freq (MHz)	Stop Freq (MHz)	Result (dBm)	Limit (dBm)	Verdict
N2	15	5	DFT-PI2BPSK	L	Edge_1RB_Left	30	1000	-49.01	-13	PASS
N2	15	5	DFT-PI2BPSK	L	Edge_1RB_Left	1000	3000	-41.52	-13	PASS
N2	15	5	DFT-PI2BPSK	L	Edge_1RB_Left	3000	12000	-46.89	-13	PASS
N2	15	5	DFT-PI2BPSK	L	Edge_1RB_Left	12000	20000	-46.67	-13	PASS
N2	15	5	DFT-PI2BPSK	M	Edge_1RB_Left	30	1000	-49.35	-13	PASS
N2	15	5	DFT-PI2BPSK	M	Edge_1RB_Left	1000	3000	-40.71	-13	PASS
N2	15	5	DFT-PI2BPSK	M	Edge_1RB_Left	3000	12000	-47.02	-13	PASS
N2	15	5	DFT-PI2BPSK	M	Edge_1RB_Left	12000	20000	-46.73	-13	PASS
N2	15	5	DFT-QPSK	M	Edge_1RB_Left	30	1000	-48.90	-13	PASS
N2	15	5	DFT-QPSK	M	Edge_1RB_Left	1000	3000	-41.68	-13	PASS
N2	15	5	DFT-QPSK	M	Edge_1RB_Left	3000	12000	-46.56	-13	PASS
N2	15	5	DFT-QPSK	M	Edge_1RB_Left	12000	20000	-46.61	-13	PASS
N2	15	5	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-48.95	-13	PASS
N2	15	5	DFT-PI2BPSK	H	Edge_1RB_Left	1000	3000	-40.85	-13	PASS
N2	15	5	DFT-PI2BPSK	H	Edge_1RB_Left	3000	12000	-46.45	-13	PASS
N2	15	5	DFT-PI2BPSK	H	Edge_1RB_Left	12000	20000	-46.16	-13	PASS
N2	15	5	DFT-QPSK	H	Edge_1RB_Left	30	1000	-48.96	-13	PASS
N2	15	5	DFT-QPSK	H	Edge_1RB_Left	1000	3000	-41.61	-13	PASS
N2	15	5	DFT-QPSK	H	Edge_1RB_Left	3000	12000	-46.29	-13	PASS
N2	15	5	DFT-QPSK	H	Edge_1RB_Left	12000	20000	-46.62	-13	PASS
N2	15	20	DFT-PI2BPSK	L	Edge_1RB_Left	30	1000	-49.39	-13	PASS
N2	15	20	DFT-PI2BPSK	L	Edge_1RB_Left	1000	3000	-37.62	-13	PASS
N2	15	20	DFT-PI2BPSK	L	Edge_1RB_Left	3000	12000	-46.69	-13	PASS
N2	15	20	DFT-PI2BPSK	L	Edge_1RB_Left	12000	20000	-46.48	-13	PASS
N2	15	20	DFT-QPSK	L	Edge_1RB_Left	30	1000	-49.10	-13	PASS
N2	15	20	DFT-QPSK	L	Edge_1RB_Left	1000	3000	-37.86	-13	PASS
N2	15	20	DFT-QPSK	L	Edge_1RB_Left	3000	12000	-46.61	-13	PASS
N2	15	20	DFT-QPSK	L	Edge_1RB_Left	12000	20000	-46.42	-13	PASS
N2	15	20	DFT-PI2BPSK	M	Edge_1RB_Left	30	1000	-49.28	-13	PASS
N2	15	20	DFT-PI2BPSK	M	Edge_1RB_Left	1000	3000	-46.95	-13	PASS
N2	15	20	DFT-PI2BPSK	M	Edge_1RB_Left	3000	12000	-46.86	-13	PASS
N2	15	20	DFT-PI2BPSK	M	Edge_1RB_Left	12000	20000	-46.70	-13	PASS



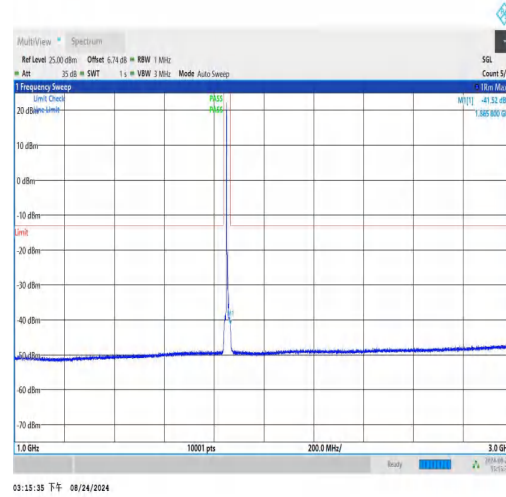
N2	15	20	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-49.08	-13	PASS
N2	15	20	DFT-PI2BPSK	H	Edge_1RB_Left	1000	3000	-47.03	-13	PASS
N2	15	20	DFT-PI2BPSK	H	Edge_1RB_Left	3000	12000	-46.73	-13	PASS
N2	15	20	DFT-PI2BPSK	H	Edge_1RB_Left	12000	20000	-46.67	-13	PASS
N2	15	20	DFT-QPSK	H	Edge_1RB_Left	30	1000	-49.27	-13	PASS
N2	15	20	DFT-QPSK	H	Edge_1RB_Left	1000	3000	-44.22	-13	PASS
N2	15	20	DFT-QPSK	H	Edge_1RB_Left	3000	12000	-46.46	-13	PASS
N2	15	20	DFT-QPSK	H	Edge_1RB_Left	12000	20000	-46.71	-13	PASS
N2	15	40	DFT-PI2BPSK	L	Edge_1RB_Left	30	1000	-49.12	-13	PASS
N2	15	40	DFT-PI2BPSK	L	Edge_1RB_Left	1000	3000	-47.05	-13	PASS
N2	15	40	DFT-PI2BPSK	L	Edge_1RB_Left	3000	12000	-46.65	-13	PASS
N2	15	40	DFT-PI2BPSK	L	Edge_1RB_Left	12000	20000	-46.72	-13	PASS
N2	15	40	DFT-QPSK	L	Edge_1RB_Left	30	1000	-49.38	-13	PASS
N2	15	40	DFT-QPSK	L	Edge_1RB_Left	1000	3000	-46.81	-13	PASS
N2	15	40	DFT-QPSK	L	Edge_1RB_Left	3000	12000	-46.71	-13	PASS
N2	15	40	DFT-QPSK	L	Edge_1RB_Left	12000	20000	-46.42	-13	PASS
N2	15	40	DFT-PI2BPSK	M	Edge_1RB_Left	30	1000	-49.36	-13	PASS
N2	15	40	DFT-PI2BPSK	M	Edge_1RB_Left	1000	3000	-46.94	-13	PASS
N2	15	40	DFT-PI2BPSK	M	Edge_1RB_Left	3000	12000	-46.93	-13	PASS
N2	15	40	DFT-PI2BPSK	M	Edge_1RB_Left	12000	20000	-46.80	-13	PASS
N2	15	40	DFT-QPSK	M	Edge_1RB_Left	30	1000	-48.99	-13	PASS
N2	15	40	DFT-QPSK	M	Edge_1RB_Left	1000	3000	-47.07	-13	PASS
N2	15	40	DFT-QPSK	M	Edge_1RB_Left	3000	12000	-46.65	-13	PASS
N2	15	40	DFT-QPSK	M	Edge_1RB_Left	12000	20000	-46.66	-13	PASS
N2	15	40	DFT-PI2BPSK	H	Edge_1RB_Left	30	1000	-48.93	-13	PASS
N2	15	40	DFT-PI2BPSK	H	Edge_1RB_Left	1000	3000	-47.03	-13	PASS
N2	15	40	DFT-PI2BPSK	H	Edge_1RB_Left	3000	12000	-46.40	-13	PASS
N2	15	40	DFT-PI2BPSK	H	Edge_1RB_Left	12000	20000	-46.76	-13	PASS
N2	15	40	DFT-QPSK	H	Edge_1RB_Left	30	1000	-49.18	-13	PASS
N2	15	40	DFT-QPSK	H	Edge_1RB_Left	1000	3000	-46.98	-13	PASS
N2	15	40	DFT-QPSK	H	Edge_1RB_Left	3000	12000	-46.43	-13	PASS
N2	15	40	DFT-QPSK	H	Edge_1RB_Left	12000	20000	-46.60	-13	PASS



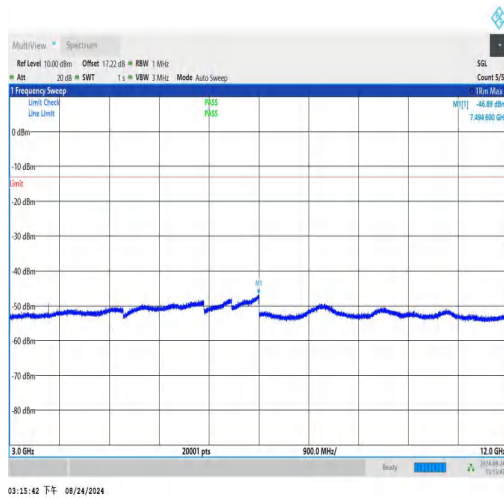
Test Graphs



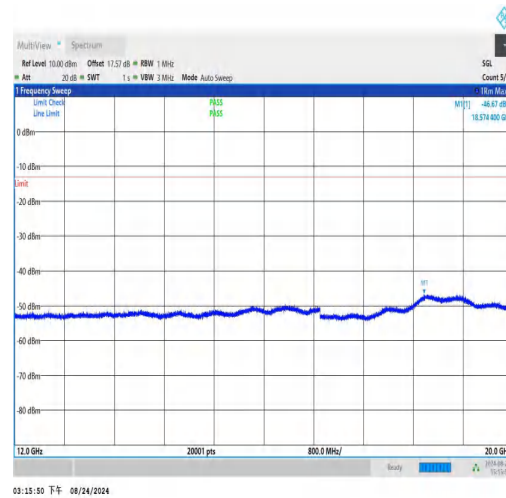
NTNV_N2_PC3_15_5_L_TID1_N/A_30_1000_#1



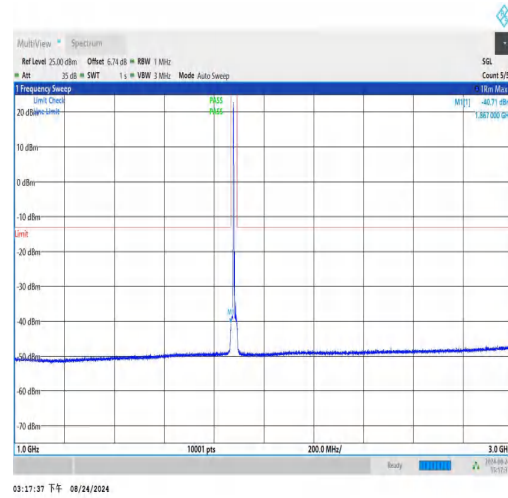
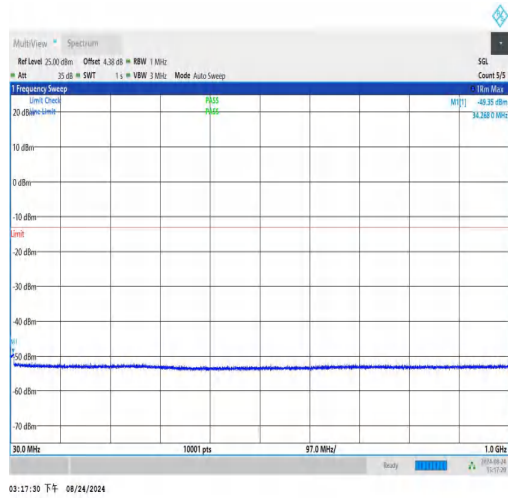
NTNV_N2_PC3_15_5_L_TID1_N/A_1000_3000_#1



NTNV_N2_PC3_15_5_L_TID1_N/A_3000_12000_#1

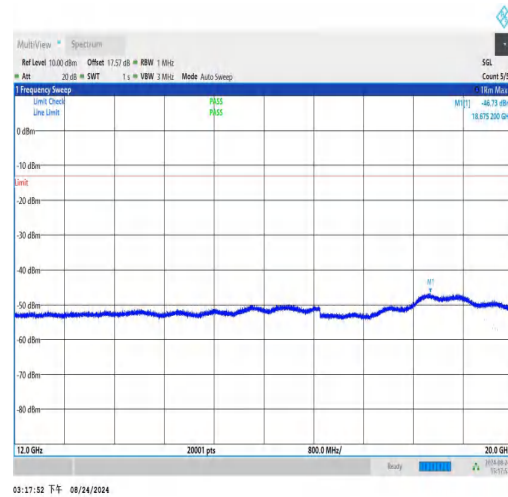
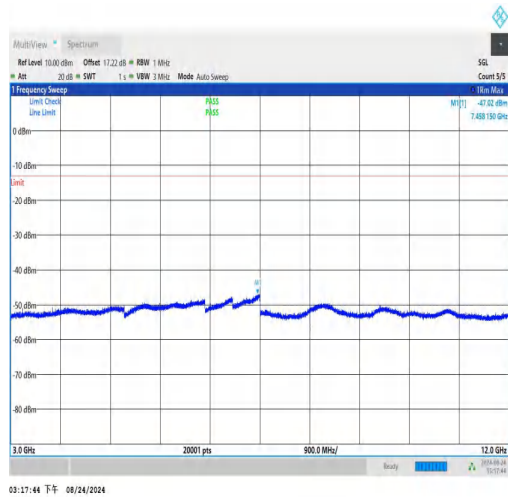


NTNV_N2_PC3_15_5_L_TID1_N/A_12000_20000_#1



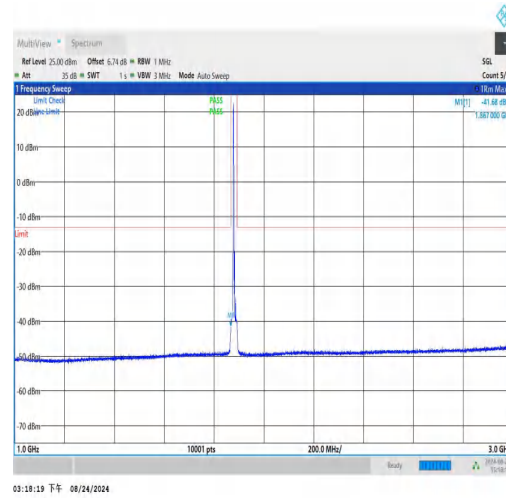
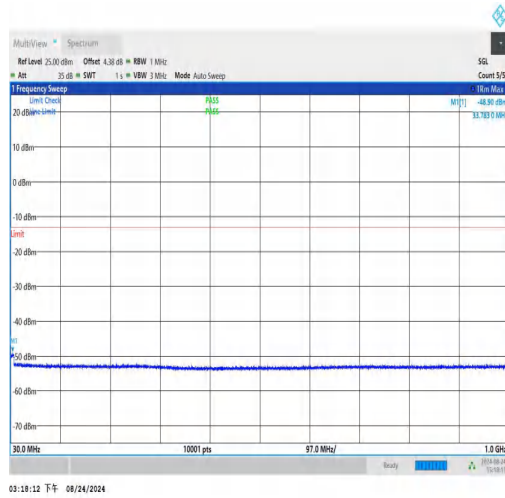
NTNV_N2_PC3_15_5_M_TID1_N/A_30_1000_#1

NTNV_N2_PC3_15_5_M_TID1_N/A_1000_3000_#1



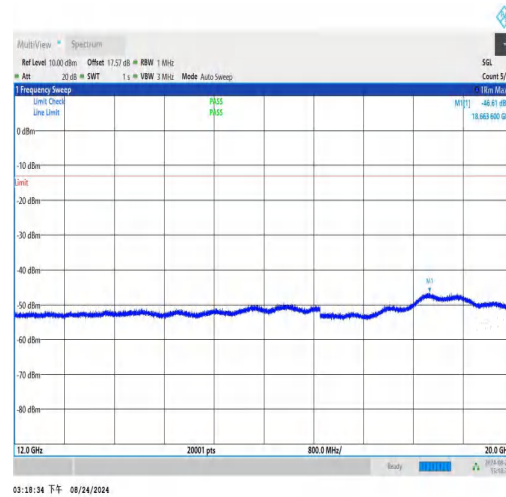
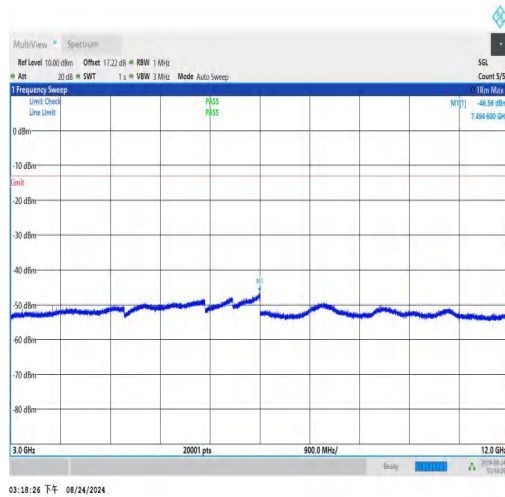
NTNV_N2_PC3_15_5_M_TID1_N/A_3000_12000_#1

NTNV_N2_PC3_15_5_M_TID1_N/A_12000_20000_#1



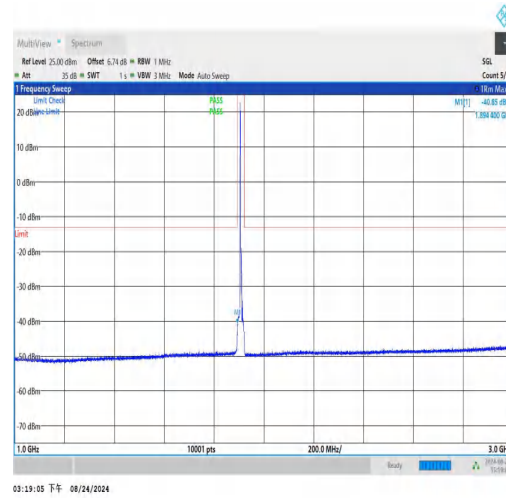
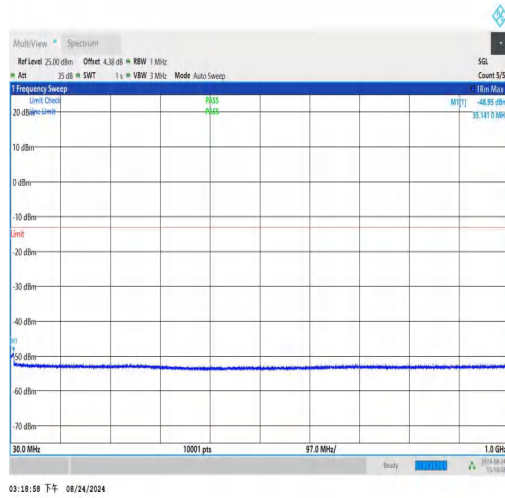
NTNV_N2_PC3_15_5_M_TID2_N/A_30_1000_#1

NTNV_N2_PC3_15_5_M_TID2_N/A_1000_3000_#1



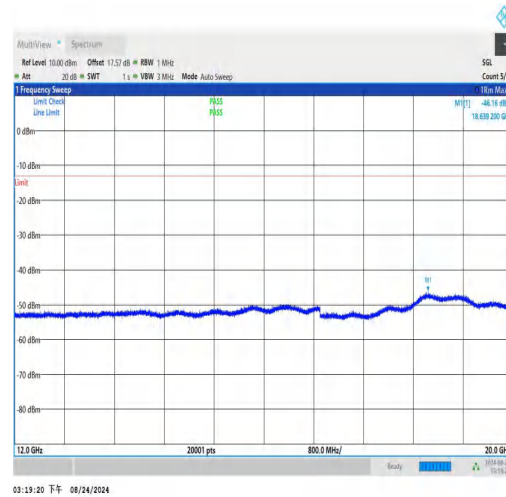
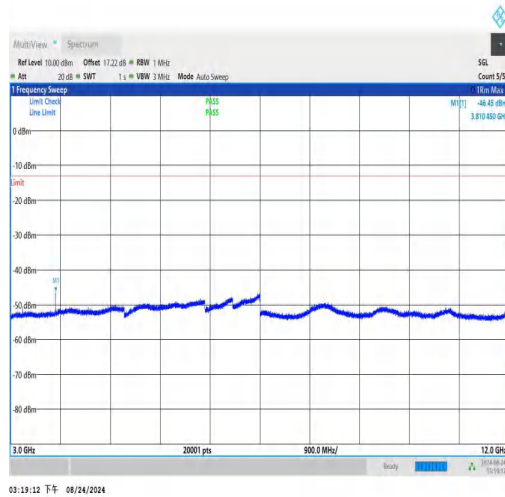
NTNV_N2_PC3_15_5_M_TID2_N/A_3000_12000_#1

NTNV_N2_PC3_15_5_M_TID2_N/A_12000_20000_#1



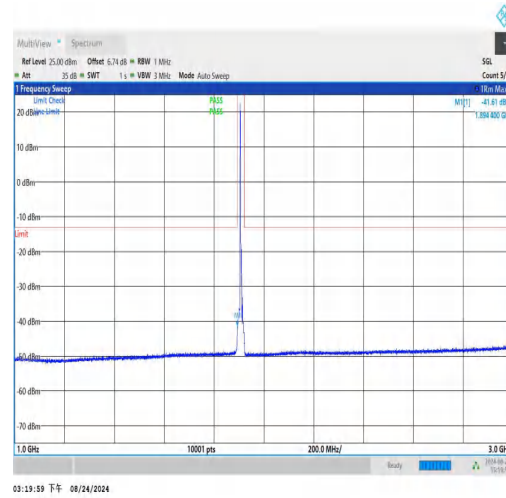
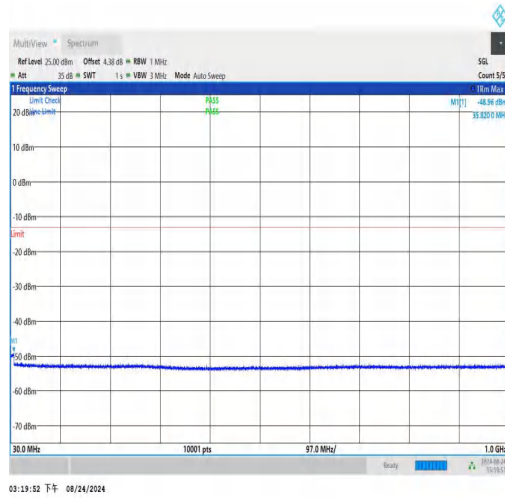
NTNV_N2_PC3_15_5_H_TID1_N/A_30_1000_#1

NTNV_N2_PC3_15_5_H_TID1_N/A_1000_3000_#1



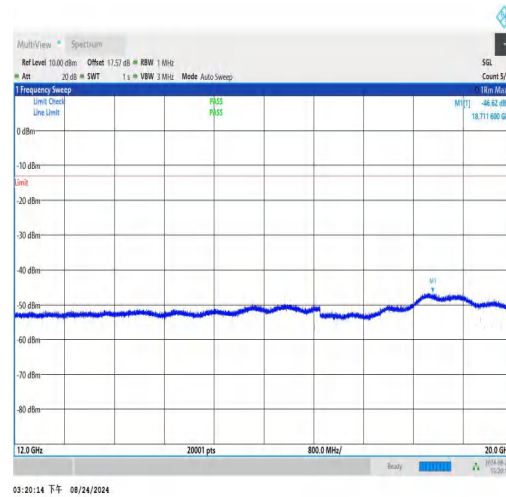
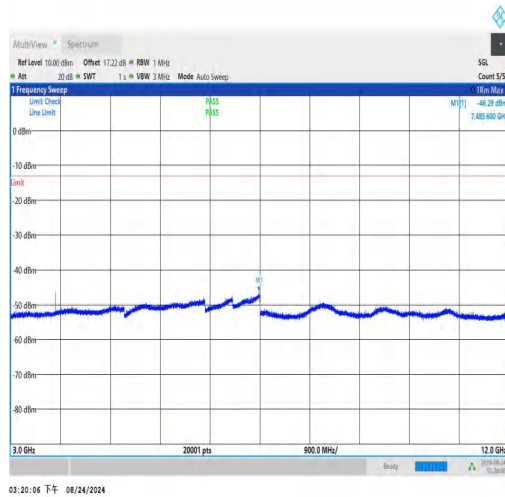
NTNV_N2_PC3_15_5_H_TID1_N/A_3000_12000_#1

NTNV_N2_PC3_15_5_H_TID1_N/A_12000_20000_#1



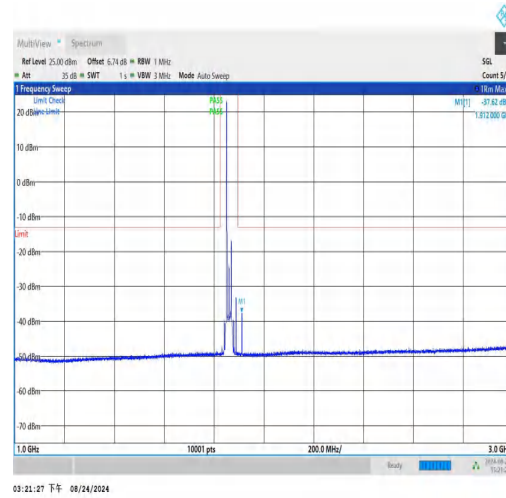
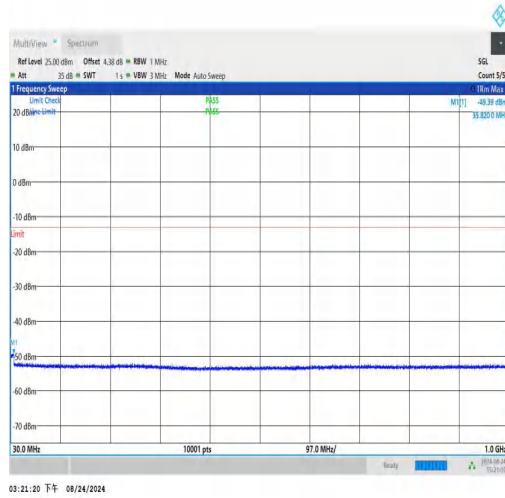
NTNV_N2_PC3_15_5_H_TID2_N/A_30_1000_#1

NTNV_N2_PC3_15_5_H_TID2_N/A_1000_3000_#1



NTNV_N2_PC3_15_5_H_TID2_N/A_3000_12000_#1

NTNV_N2_PC3_15_5_H_TID2_N/A_12000_20000_#1

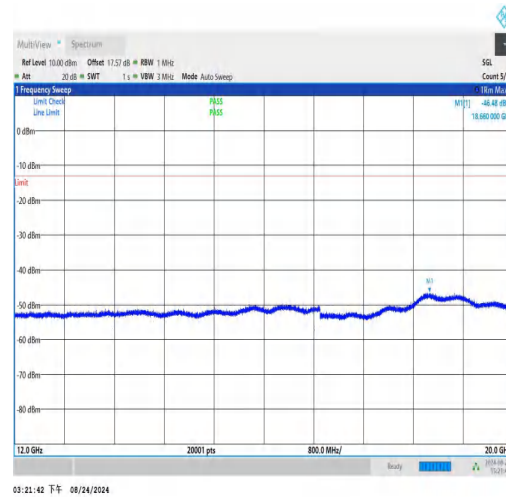
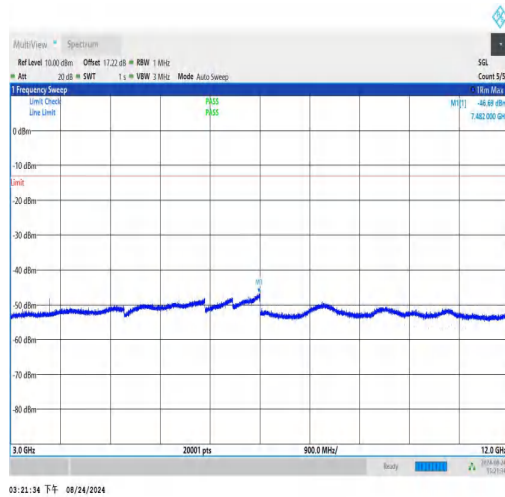


NTNV_N2_PC3_15_20_L_TID1_N/A_30_1000_#

1

NTNV_N2_PC3_15_20_L_TID1_N/A_1000_3000

_#1

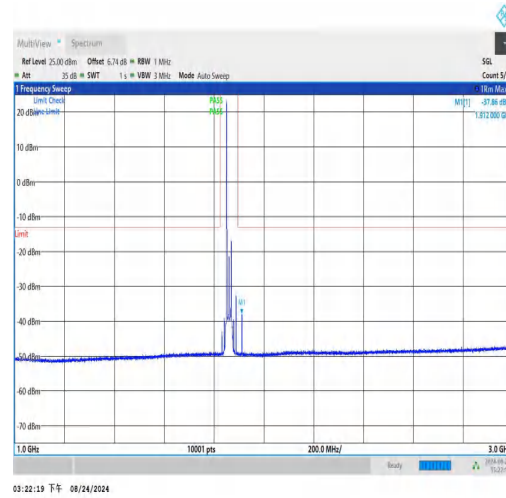
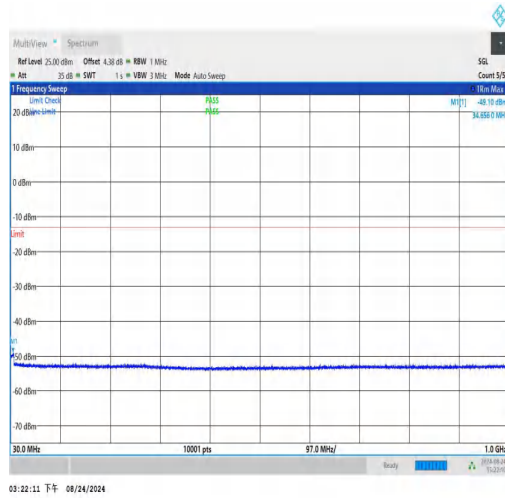


NTNV_N2_PC3_15_20_L_TID1_N/A_3000_1200

0_#1

NTNV_N2_PC3_15_20_L_TID1_N/A_12000_200

00_#1

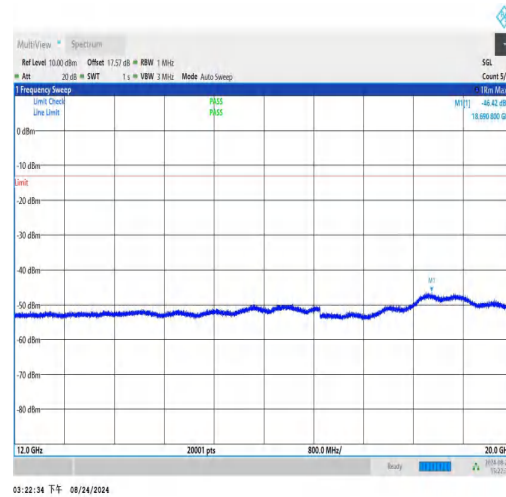
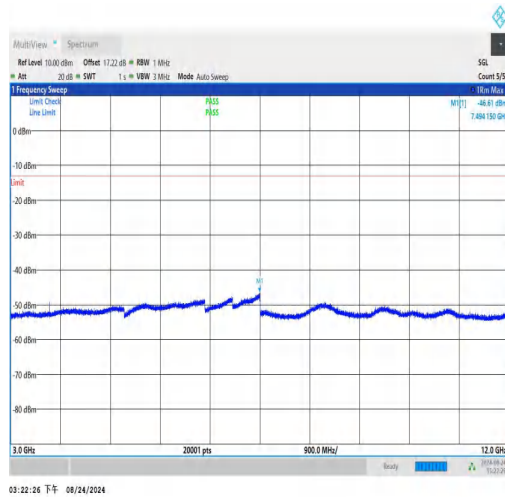


NTNV_N2_PC3_15_20_L_TID2_N/A_30_1000_#

NTNV_N2_PC3_15_20_L_TID2_N/A_1000_3000

1

_#1

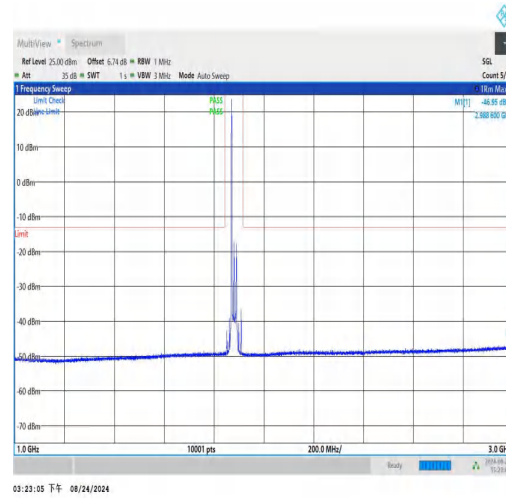
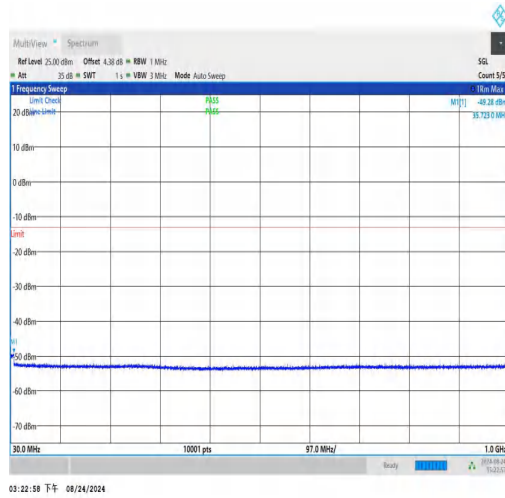


NTNV_N2_PC3_15_20_L_TID2_N/A_3000_1200

NTNV_N2_PC3_15_20_L_TID2_N/A_12000_200

0_#1

00_#1

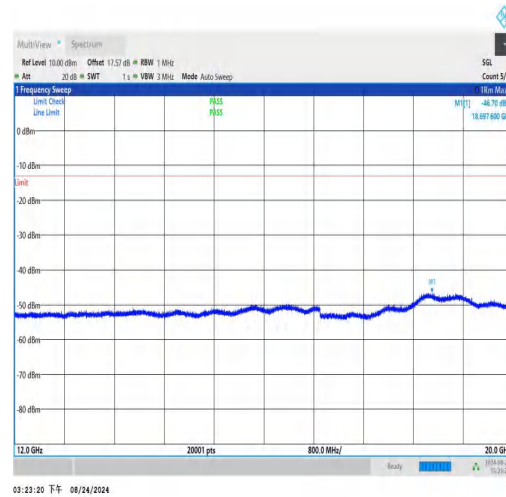
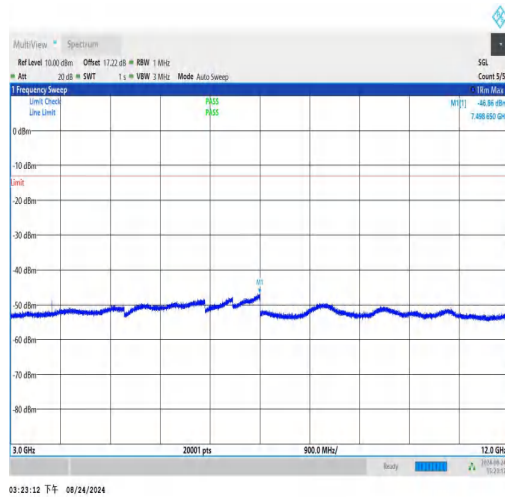


NTNV_N2_PC3_15_20_M_TID1_N/A_30_1000_#

1

NTNV_N2_PC3_15_20_M_TID1_N/A_1000_3000

_#1

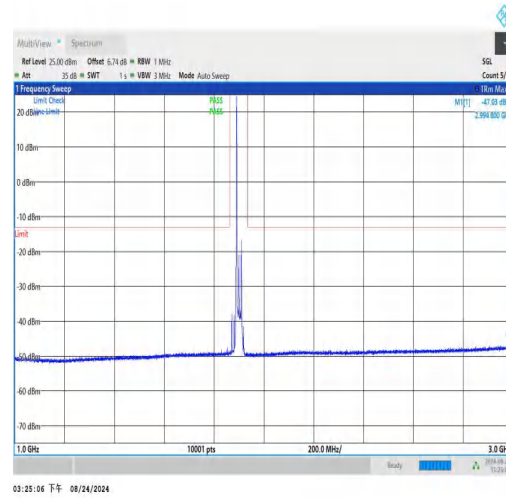
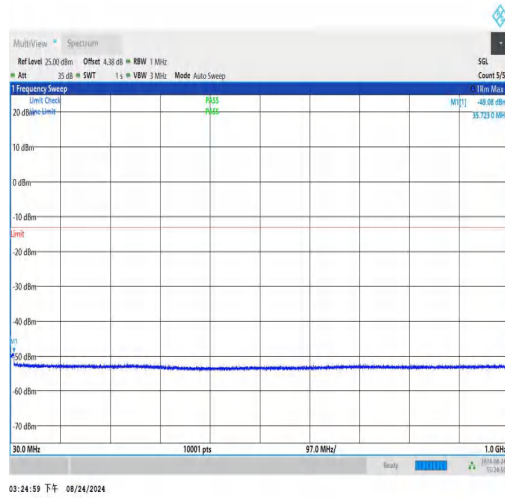


NTNV_N2_PC3_15_20_M_TID1_N/A_3000_1200

0_#1

NTNV_N2_PC3_15_20_M_TID1_N/A_12000_200

00_#1

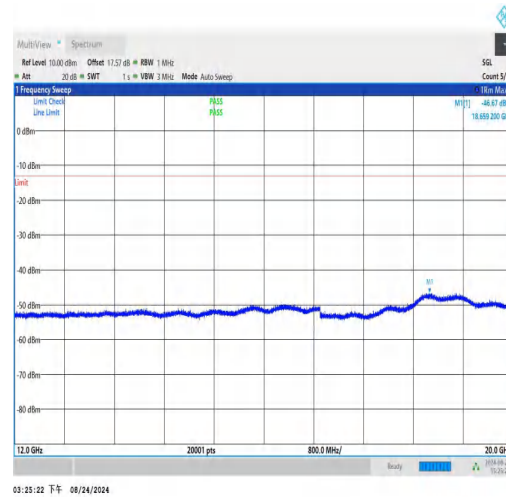
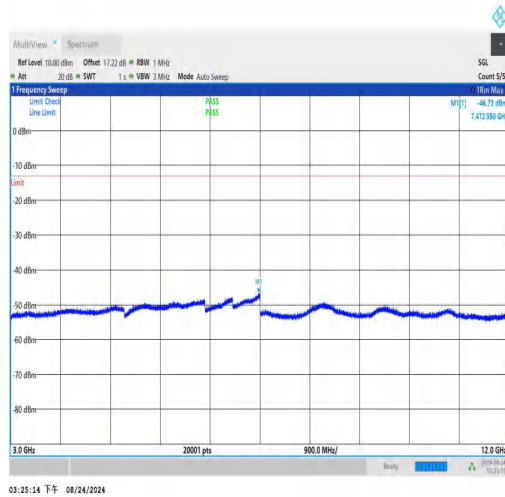


NTNV_N2_PC3_15_20_H_TID1_N/A_30_1000_#

1

NTNV_N2_PC3_15_20_H_TID1_N/A_1000_3000

_#1

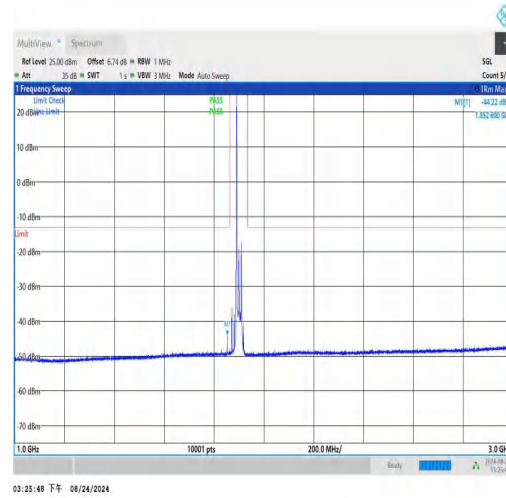
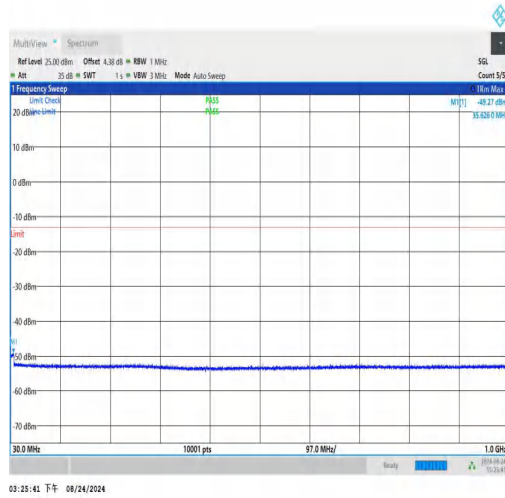


NTNV_N2_PC3_15_20_H_TID1_N/A_3000_1200

0_#1

NTNV_N2_PC3_15_20_H_TID1_N/A_12000_200

00_#1

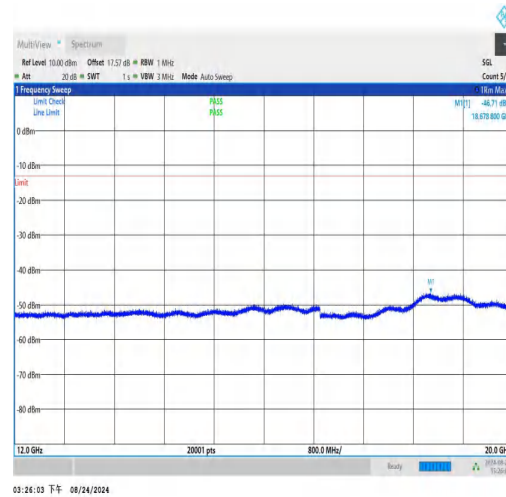
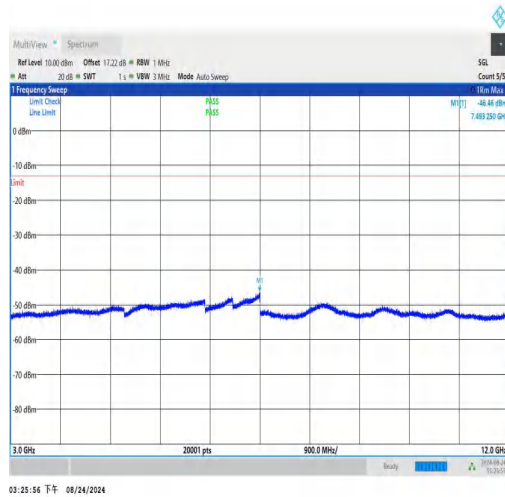


NTNV_N2_PC3_15_20_H_TID2_N/A_30_1000_#

1

NTNV_N2_PC3_15_20_H_TID2_N/A_1000_3000

_#1

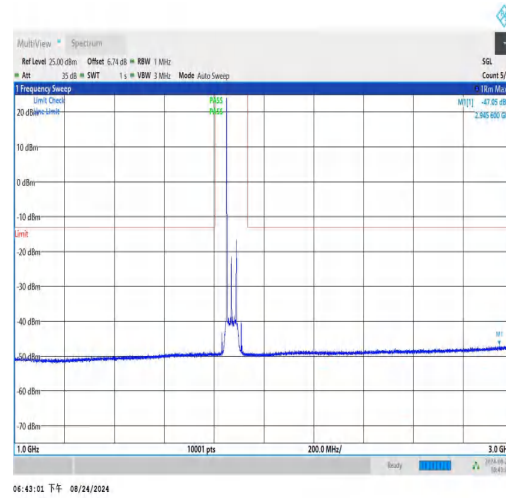
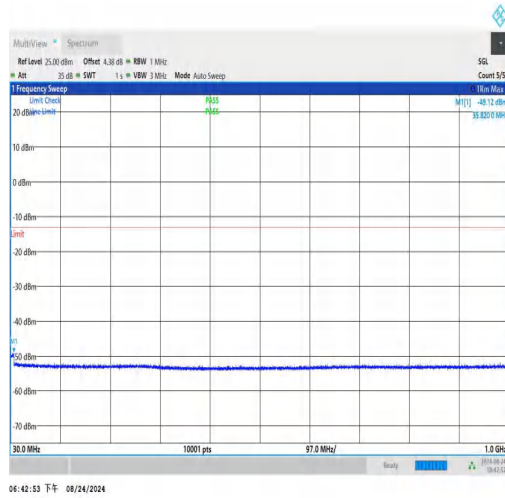


NTNV_N2_PC3_15_20_H_TID2_N/A_3000_1200

0_#1

NTNV_N2_PC3_15_20_H_TID2_N/A_12000_200

00_#1

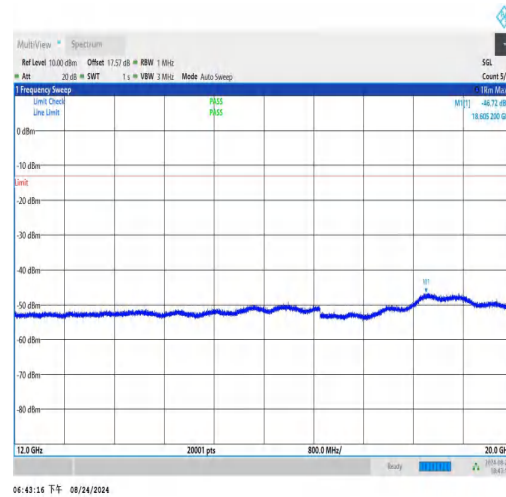
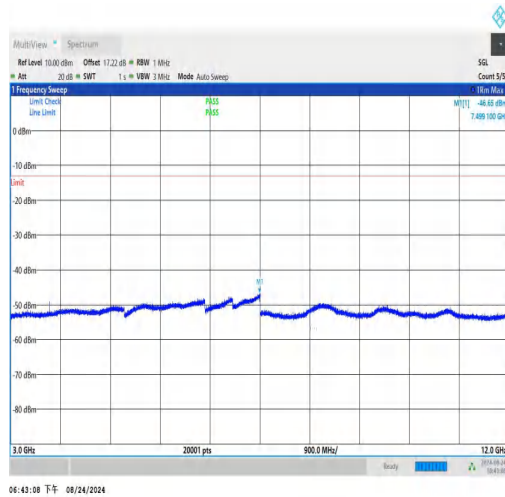


NTNV_N2_PC3_15_40_L_TID1_N/A_30_1000_#

NTNV_N2_PC3_15_40_L_TID1_N/A_1000_3000

1

_#1

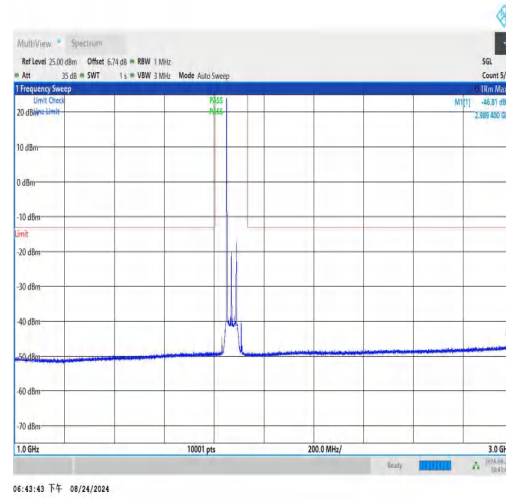
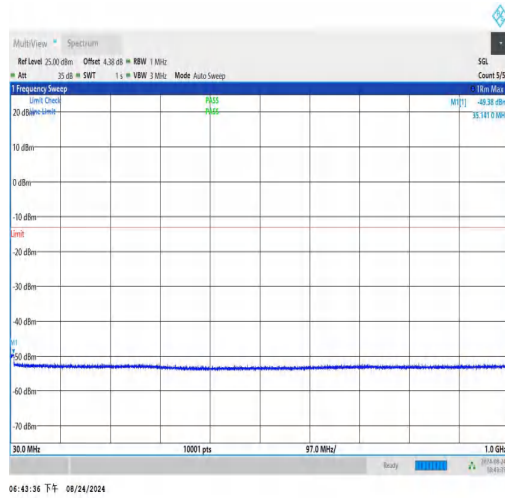


NTNV_N2_PC3_15_40_L_TID1_N/A_3000_1200

NTNV_N2_PC3_15_40_L_TID1_N/A_12000_200

0_#1

00_#1

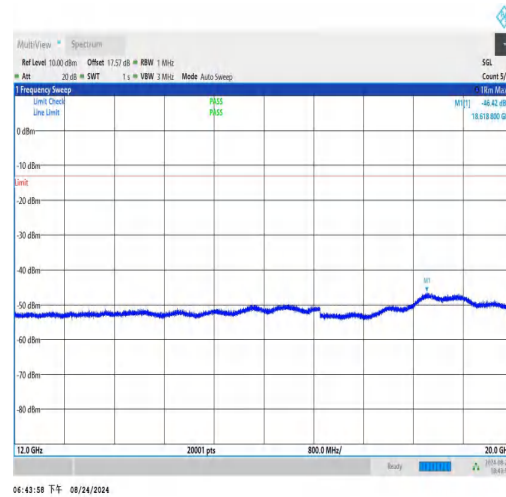
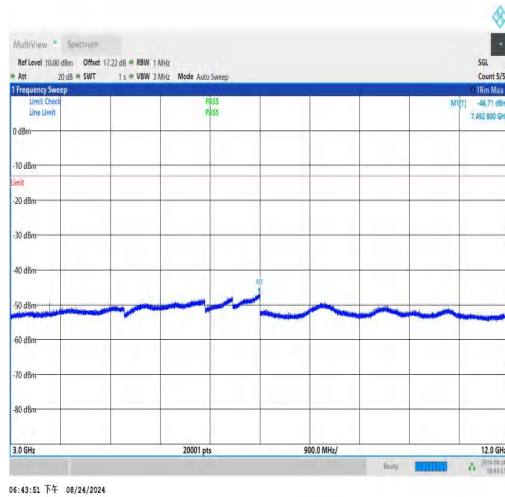


NTNV_N2_PC3_15_40_L_TID2_N/A_30_1000_#

1

NTNV_N2_PC3_15_40_L_TID2_N/A_1000_3000

_#1

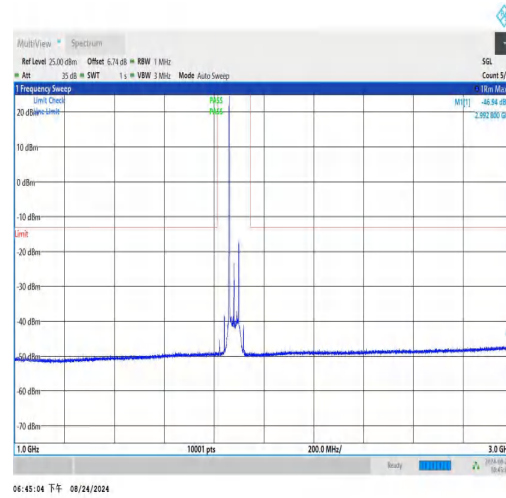
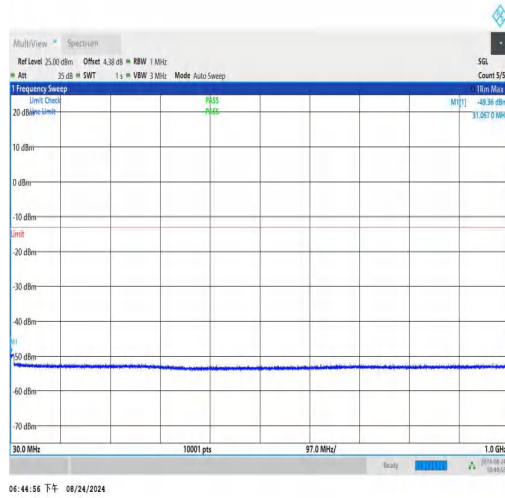


NTNV_N2_PC3_15_40_L_TID2_N/A_3000_1200

0_#1

NTNV_N2_PC3_15_40_L_TID2_N/A_12000_200

00_#1

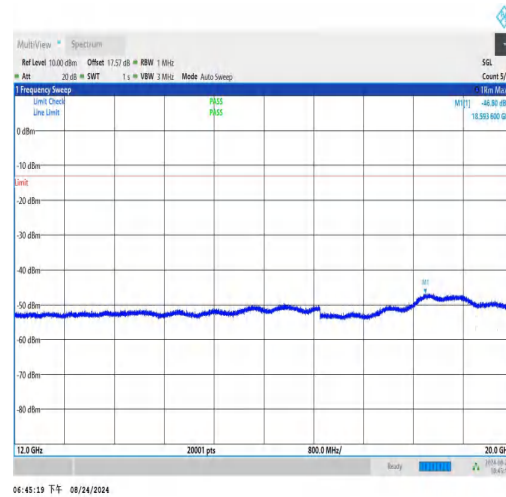
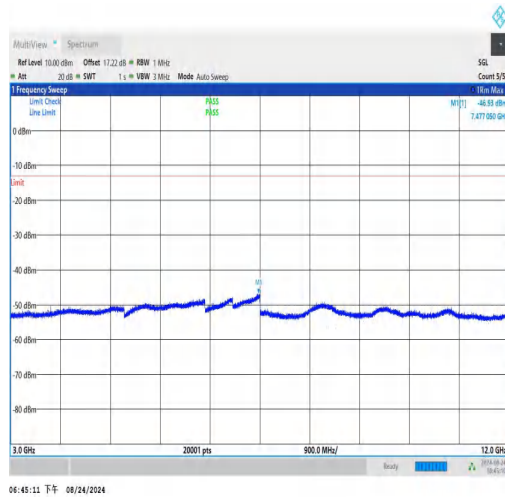


NTNV_N2_PC3_15_40_M_TID1_N/A_30_1000_#

1

NTNV_N2_PC3_15_40_M_TID1_N/A_1000_3000

_#1

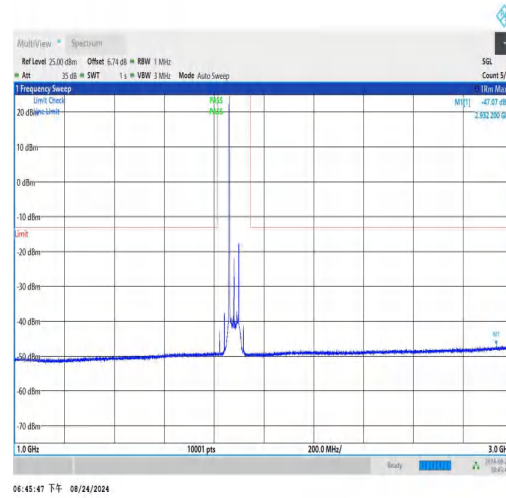
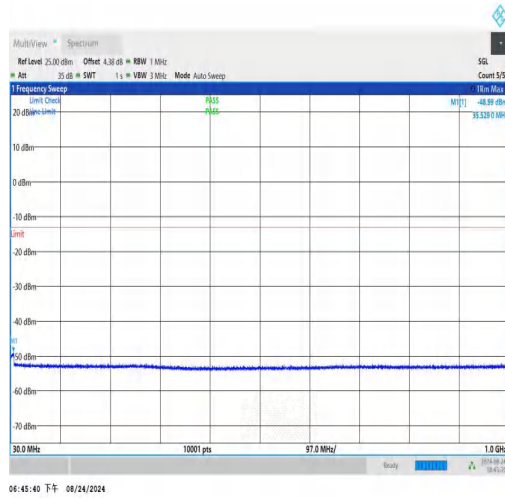


NTNV_N2_PC3_15_40_M_TID1_N/A_3000_1200

0_#1

NTNV_N2_PC3_15_40_M_TID1_N/A_12000_200

00_#1

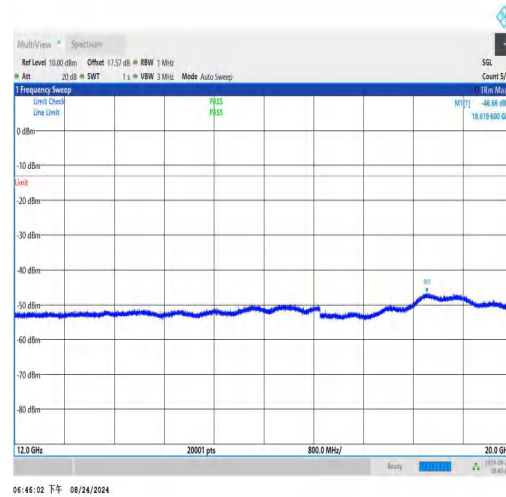
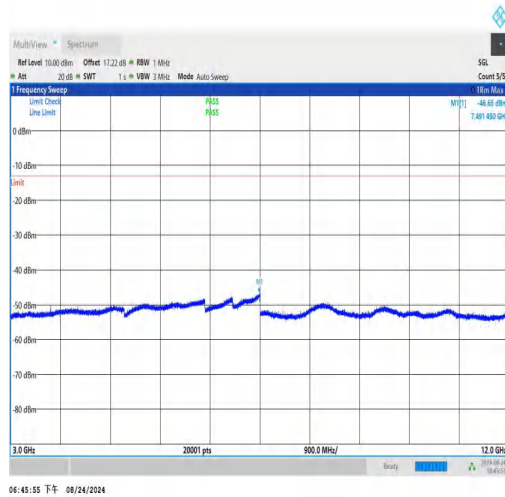


NTNV_N2_PC3_15_40_M_TID2_N/A_30_1000_#

1

NTNV_N2_PC3_15_40_M_TID2_N/A_1000_3000

_#1

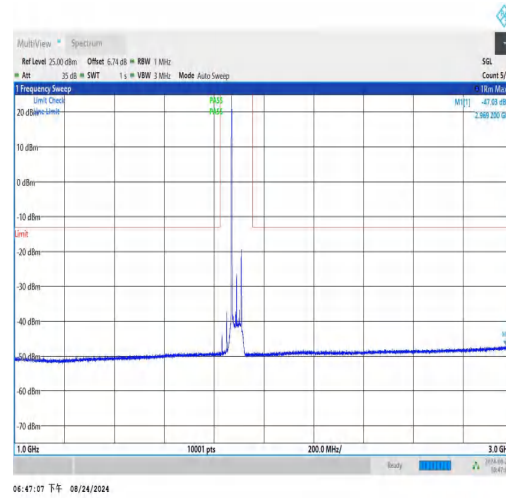
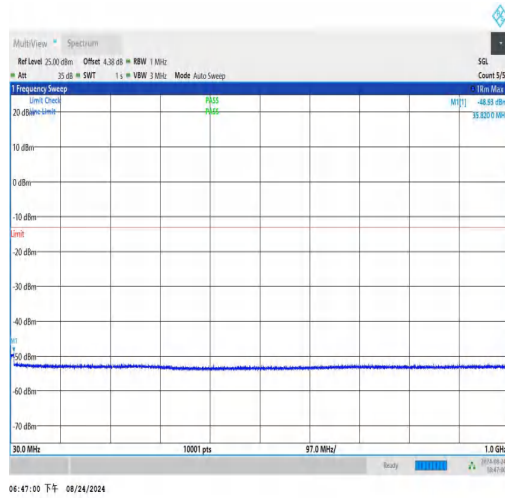


NTNV_N2_PC3_15_40_M_TID2_N/A_3000_1200

0_#1

NTNV_N2_PC3_15_40_M_TID2_N/A_12000_200

00_#1

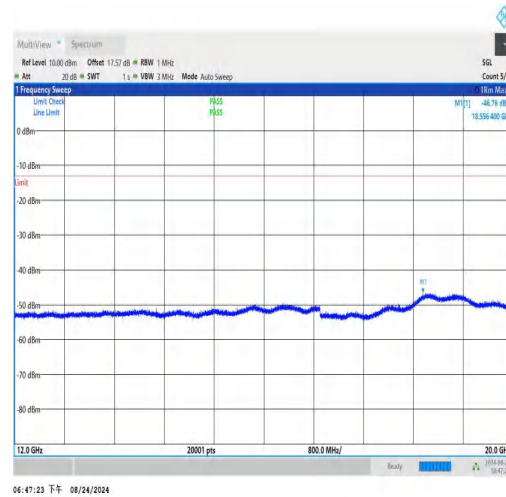
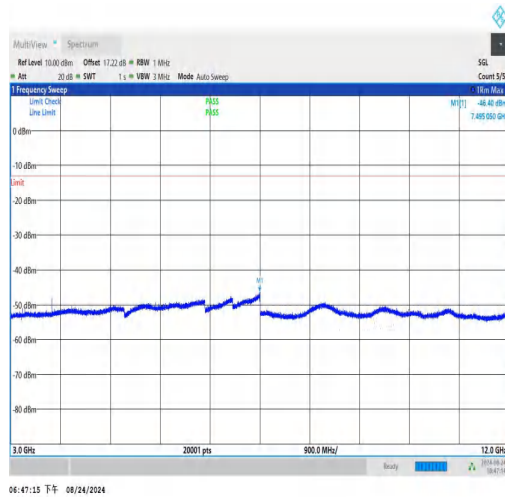


NTNV_N2_PC3_15_40_H_TID1_N/A_30_1000_#

1

NTNV_N2_PC3_15_40_H_TID1_N/A_1000_3000

_#1

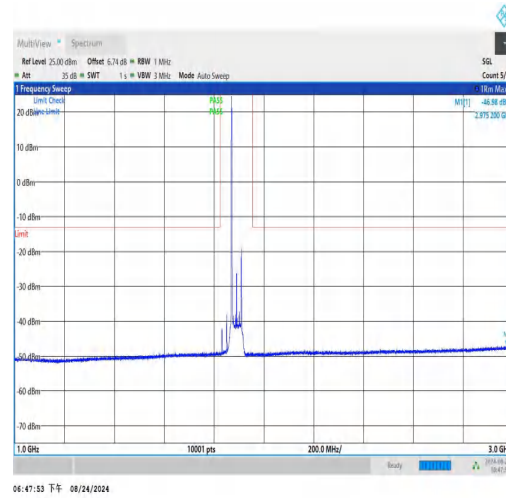
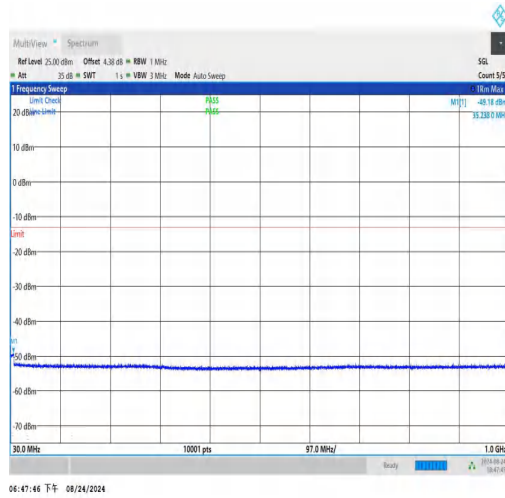


NTNV_N2_PC3_15_40_H_TID1_N/A_3000_1200

0_#1

NTNV_N2_PC3_15_40_H_TID1_N/A_12000_200

00_#1

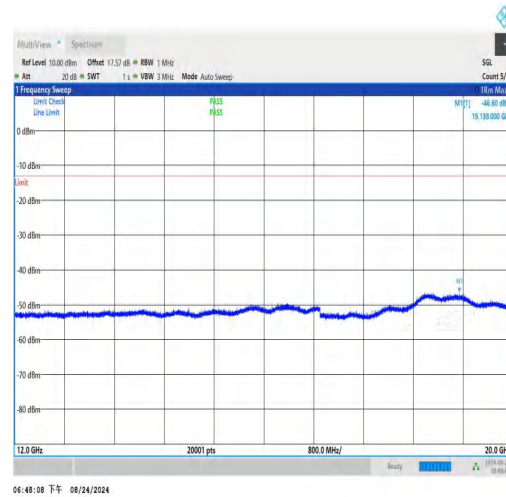
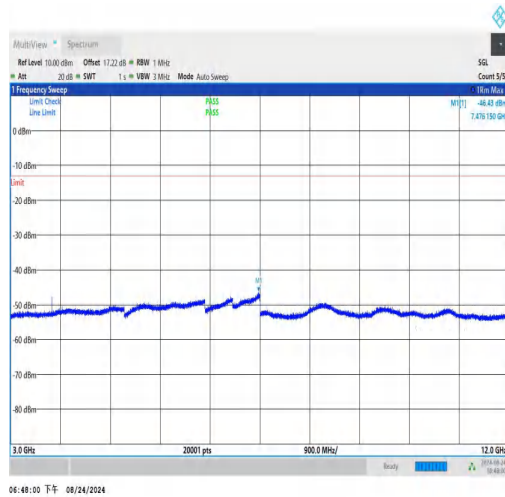


NTNV_N2_PC3_15_40_H_TID2_N/A_30_1000_#

1

NTNV_N2_PC3_15_40_H_TID2_N/A_1000_3000

_#1



NTNV_N2_PC3_15_40_H_TID2_N/A_3000_1200

0_#1

NTNV_N2_PC3_15_40_H_TID2_N/A_12000_200

00_#1



Appendix F: Frequency Stability

Test Result

Frequency Error VS. Voltage

Voltage										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation (Hz)	Deviation (ppm)	Verdict
N2	15	40	DFT-PI2BPSK	L	Outer_Full	VN	NT	2.00	0.00	PASS
N2	15	40	DFT-PI2BPSK	L	Outer_Full	VL	NT	15.00	0.01	PASS
N2	15	40	DFT-PI2BPSK	L	Outer_Full	VH	NT	2.00	0.00	PASS
N2	15	40	DFT-QPSK	L	Outer_Full	VN	NT	2.00	0.00	PASS
N2	15	40	DFT-QPSK	L	Outer_Full	VL	NT	14.00	0.01	PASS
N2	15	40	DFT-QPSK	L	Outer_Full	VH	NT	5.00	0.00	PASS
N2	15	40	DFT-PI2BPSK	M	Outer_Full	VN	NT	14.00	0.01	PASS
N2	15	40	DFT-PI2BPSK	M	Outer_Full	VL	NT	1.00	0.00	PASS
N2	15	40	DFT-PI2BPSK	M	Outer_Full	VH	NT	3.00	0.00	PASS
N2	15	40	DFT-QPSK	M	Outer_Full	VN	NT	14.00	0.01	PASS
N2	15	40	DFT-QPSK	M	Outer_Full	VL	NT	4.00	0.00	PASS
N2	15	40	DFT-QPSK	M	Outer_Full	VH	NT	17.00	0.01	PASS
N2	15	40	DFT-PI2BPSK	H	Outer_Full	VN	NT	4.00	0.00	PASS
N2	15	40	DFT-PI2BPSK	H	Outer_Full	VL	NT	17.00	0.01	PASS
N2	15	40	DFT-PI2BPSK	H	Outer_Full	VH	NT	3.00	0.00	PASS
N2	15	40	DFT-QPSK	H	Outer_Full	VN	NT	12.00	0.01	PASS
N2	15	40	DFT-QPSK	H	Outer_Full	VL	NT	18.00	0.01	PASS
N2	15	40	DFT-QPSK	H	Outer_Full	VH	NT	15.00	0.01	PASS

Frequency Error VS. Temperature

Temperature										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation (Hz)	Deviation (ppm)	Verdict
N2	15	40	DFT-PI2BPSK	L	Outer_Full	NV	-30	8.000000	0.004278	PASS
N2	15	40	DFT-PI2BPSK	L	Outer_Full	NV	-20	13.000000	0.006952	PASS
N2	15	40	DFT-PI2BPSK	L	Outer_Full	NV	-10	19.000000	0.010160	PASS
N2	15	40	DFT-PI2BPSK	L	Outer_Full	NV	0	10.000000	0.005348	PASS
N2	15	40	DFT-PI2BPSK	L	Outer_Full	NV	10	3.000000	0.001604	PASS



N2	15	40	DFT-PI2BPSK	L	Outer_Full	NV	20	6.000000	0.003209	PASS
N2	15	40	DFT-PI2BPSK	L	Outer_Full	NV	30	12.000000	0.006417	PASS
N2	15	40	DFT-PI2BPSK	L	Outer_Full	NV	40	5.000000	0.002674	PASS
N2	15	40	DFT-PI2BPSK	L	Outer_Full	NV	50	18.000000	0.009626	PASS
N2	15	40	DFT-QPSK	L	Outer_Full	NV	-30	10.000000	0.005348	PASS
N2	15	40	DFT-QPSK	L	Outer_Full	NV	-20	11.000000	0.005882	PASS
N2	15	40	DFT-QPSK	L	Outer_Full	NV	-10	13.000000	0.006952	PASS
N2	15	40	DFT-QPSK	L	Outer_Full	NV	0	7.000000	0.003743	PASS
N2	15	40	DFT-QPSK	L	Outer_Full	NV	10	10.000000	0.005348	PASS
N2	15	40	DFT-QPSK	L	Outer_Full	NV	20	14.000000	0.007487	PASS
N2	15	40	DFT-QPSK	L	Outer_Full	NV	30	7.000000	0.003743	PASS
N2	15	40	DFT-QPSK	L	Outer_Full	NV	40	10.000000	0.005348	PASS
N2	15	40	DFT-QPSK	L	Outer_Full	NV	50	4.000000	0.002139	PASS
N2	15	40	DFT-PI2BPSK	M	Outer_Full	NV	-30	9.000000	0.004787	PASS
N2	15	40	DFT-PI2BPSK	M	Outer_Full	NV	-20	3.000000	0.001596	PASS
N2	15	40	DFT-PI2BPSK	M	Outer_Full	NV	-10	12.000000	0.006383	PASS
N2	15	40	DFT-PI2BPSK	M	Outer_Full	NV	0	18.000000	0.009574	PASS
N2	15	40	DFT-PI2BPSK	M	Outer_Full	NV	10	12.000000	0.006383	PASS
N2	15	40	DFT-PI2BPSK	M	Outer_Full	NV	20	9.000000	0.004787	PASS
N2	15	40	DFT-PI2BPSK	M	Outer_Full	NV	30	12.000000	0.006383	PASS
N2	15	40	DFT-PI2BPSK	M	Outer_Full	NV	40	12.000000	0.006383	PASS
N2	15	40	DFT-PI2BPSK	M	Outer_Full	NV	50	15.000000	0.007979	PASS
N2	15	40	DFT-QPSK	M	Outer_Full	NV	-30	4.000000	0.002128	PASS
N2	15	40	DFT-QPSK	M	Outer_Full	NV	-20	16.000000	0.008511	PASS
N2	15	40	DFT-QPSK	M	Outer_Full	NV	-10	3.000000	0.001596	PASS
N2	15	40	DFT-QPSK	M	Outer_Full	NV	0	12.000000	0.006383	PASS
N2	15	40	DFT-QPSK	M	Outer_Full	NV	10	15.000000	0.007979	PASS
N2	15	40	DFT-QPSK	M	Outer_Full	NV	20	9.000000	0.004787	PASS
N2	15	40	DFT-QPSK	M	Outer_Full	NV	30	18.000000	0.009574	PASS
N2	15	40	DFT-QPSK	M	Outer_Full	NV	40	12.000000	0.006383	PASS
N2	15	40	DFT-QPSK	M	Outer_Full	NV	50	5.000000	0.002660	PASS
N2	15	40	DFT-PI2BPSK	H	Outer_Full	NV	-30	6.000000	0.003175	PASS
N2	15	40	DFT-PI2BPSK	H	Outer_Full	NV	-20	19.000000	0.010053	PASS
N2	15	40	DFT-PI2BPSK	H	Outer_Full	NV	-10	12.000000	0.006349	PASS
N2	15	40	DFT-PI2BPSK	H	Outer_Full	NV	0	18.000000	0.009524	PASS
N2	15	40	DFT-PI2BPSK	H	Outer_Full	NV	10	2.000000	0.001058	PASS
N2	15	40	DFT-PI2BPSK	H	Outer_Full	NV	20	18.000000	0.009524	PASS
N2	15	40	DFT-PI2BPSK	H	Outer_Full	NV	30	8.000000	0.004233	PASS
N2	15	40	DFT-PI2BPSK	H	Outer_Full	NV	40	2.000000	0.001058	PASS



N2	15	40	DFT-PI2BPSK	H	Outer_Full	NV	50	15.000000	0.007937	PASS
N2	15	40	DFT-QPSK	H	Outer_Full	NV	-30	13.000000	0.006878	PASS
N2	15	40	DFT-QPSK	H	Outer_Full	NV	-20	6.000000	0.003175	PASS
N2	15	40	DFT-QPSK	H	Outer_Full	NV	-10	9.000000	0.004762	PASS
N2	15	40	DFT-QPSK	H	Outer_Full	NV	0	2.000000	0.001058	PASS
N2	15	40	DFT-QPSK	H	Outer_Full	NV	10	12.000000	0.006349	PASS
N2	15	40	DFT-QPSK	H	Outer_Full	NV	20	9.000000	0.004762	PASS
N2	15	40	DFT-QPSK	H	Outer_Full	NV	30	3.000000	0.001587	PASS
N2	15	40	DFT-QPSK	H	Outer_Full	NV	40	12.000000	0.006349	PASS
N2	15	40	DFT-QPSK	H	Outer_Full	NV	50	3.000000	0.001587	PASS

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