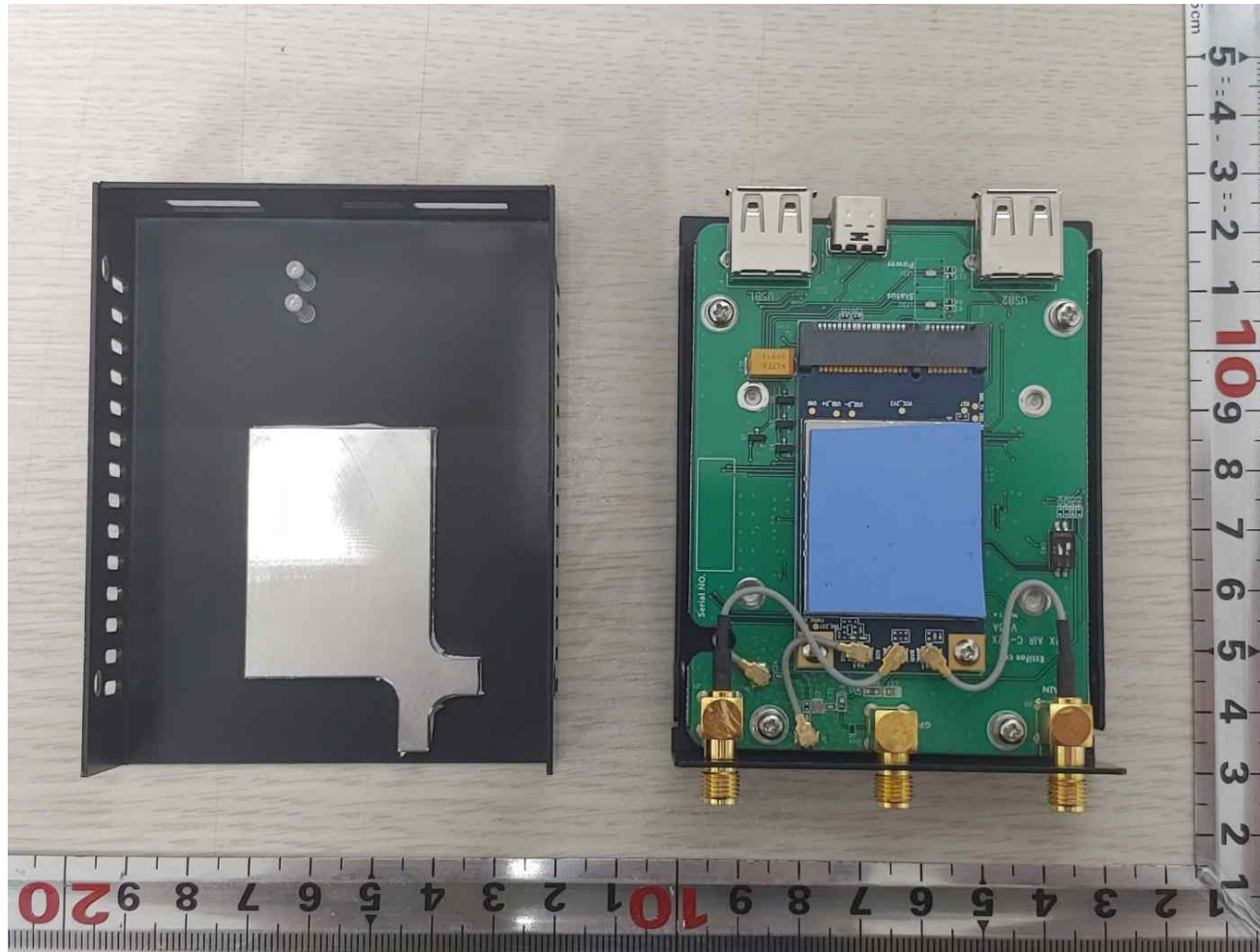
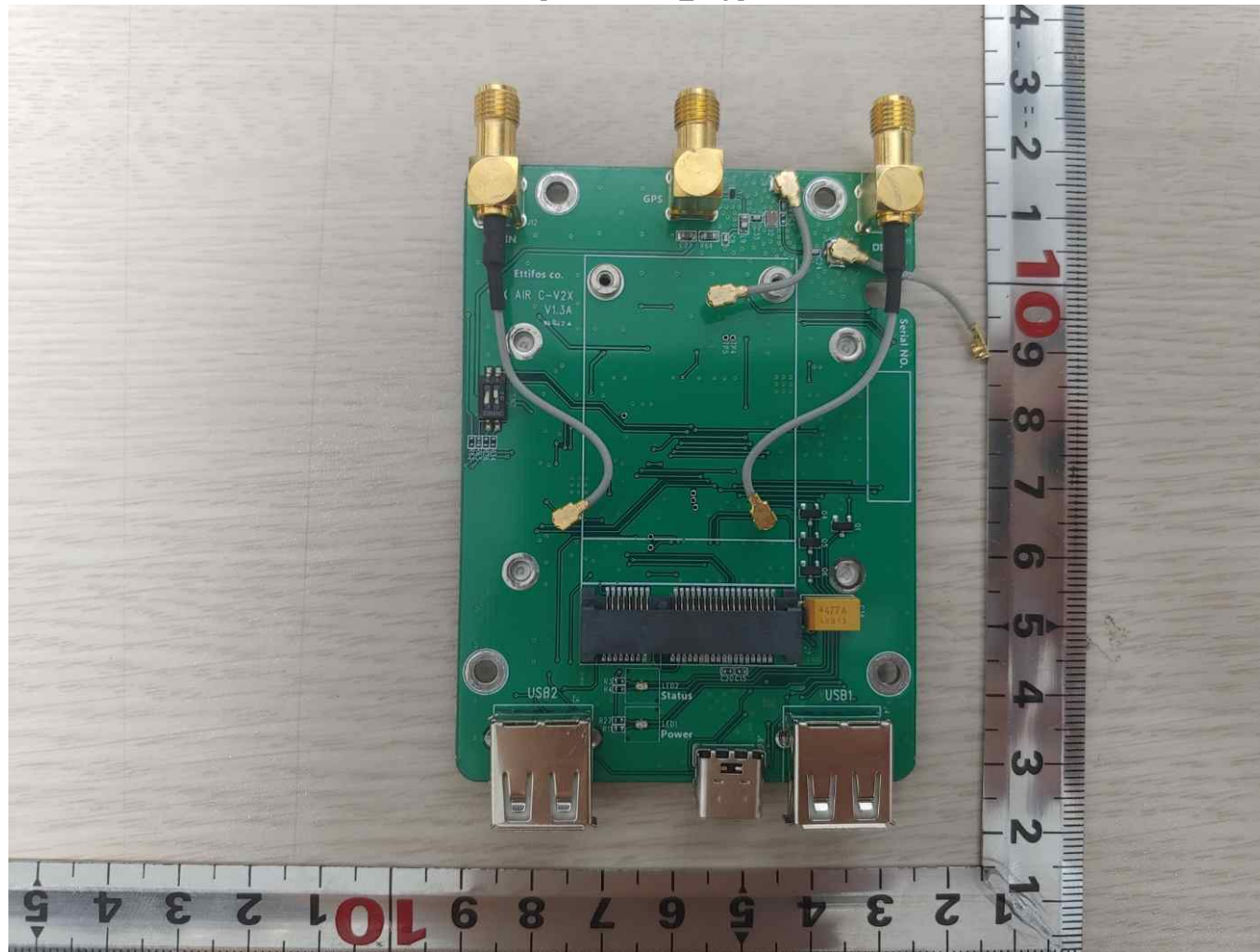


**Appendix 3. Photographs of EUT [Internal View]**

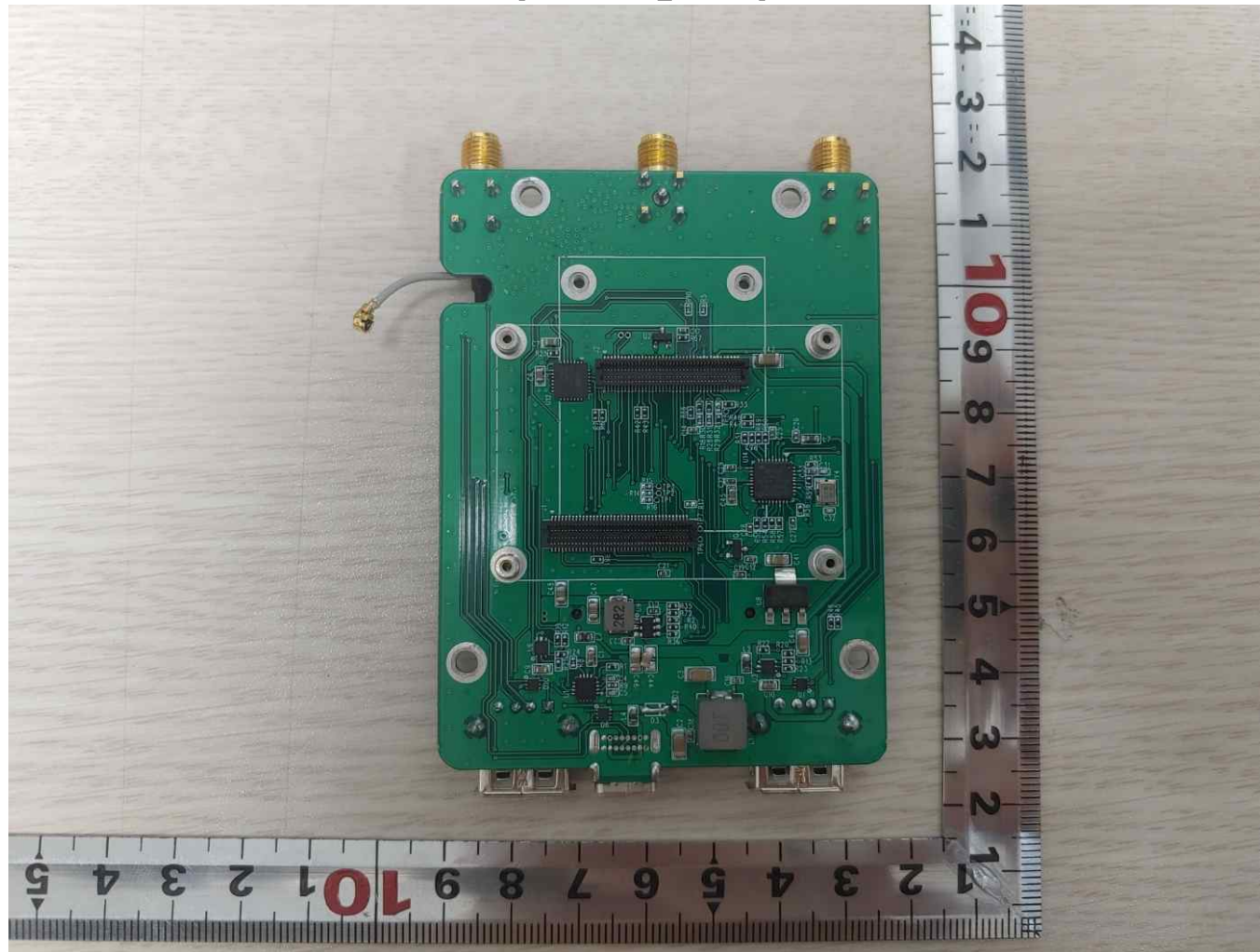
[Inside]



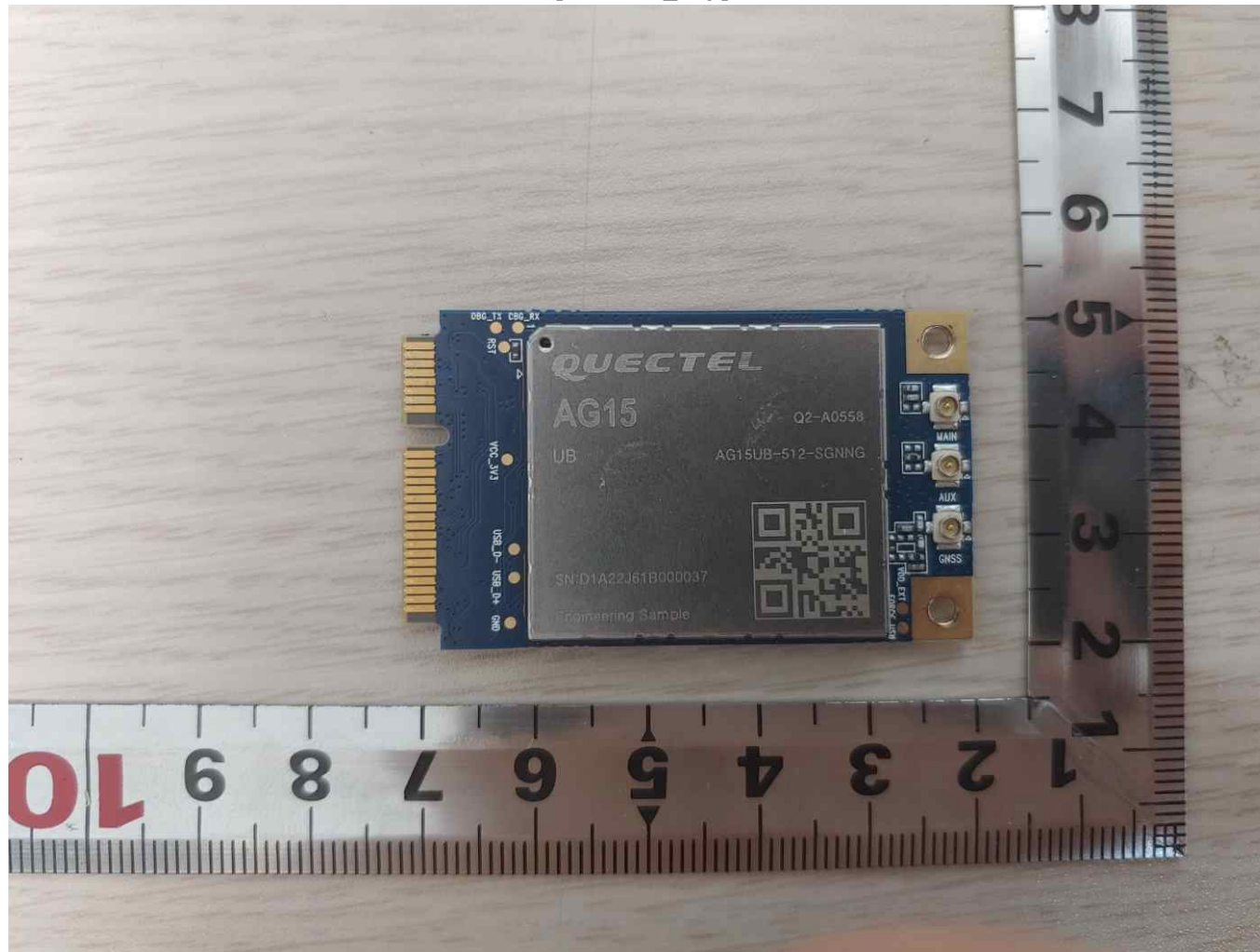
[Main board\_Top]



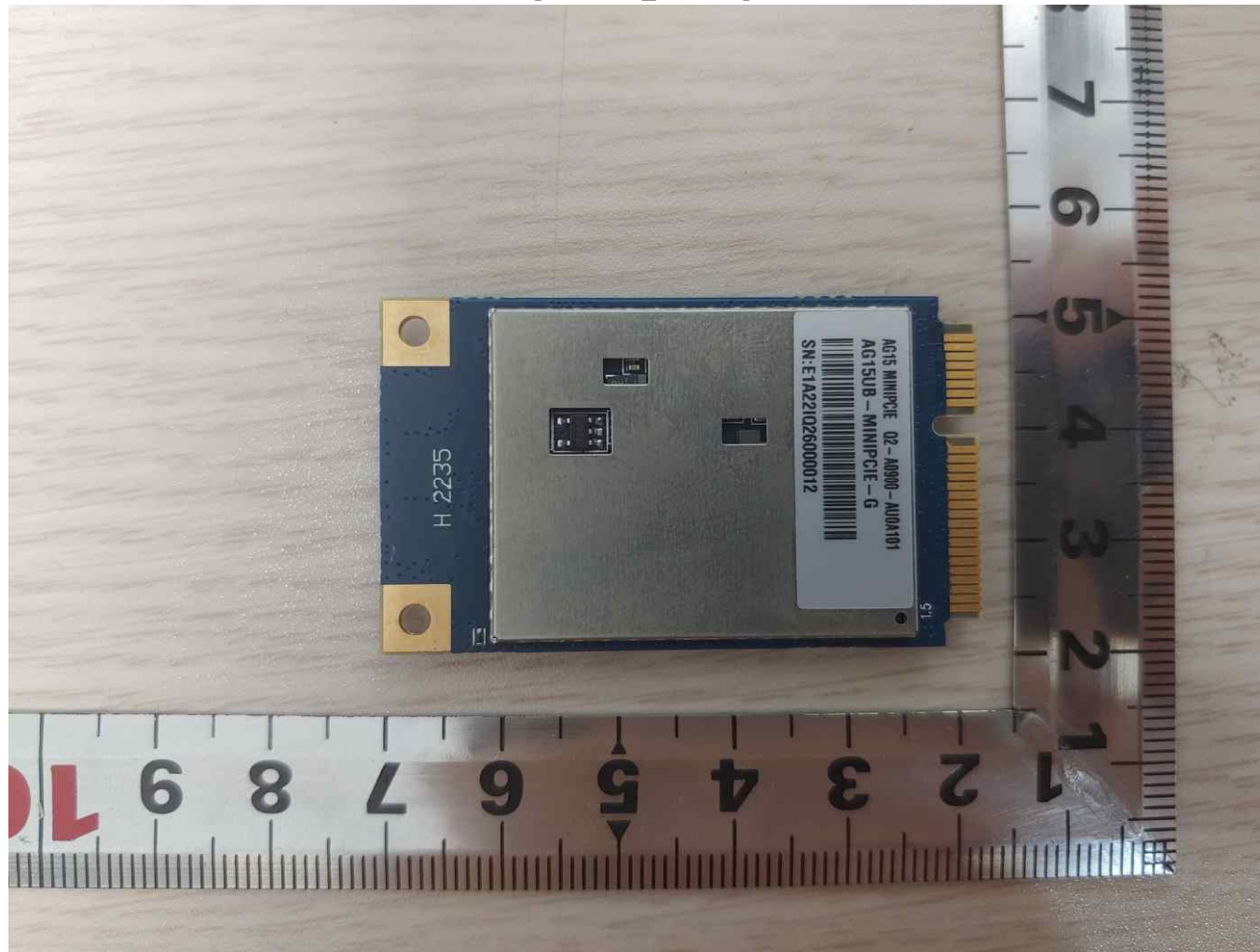
[Main board\_Bottom]



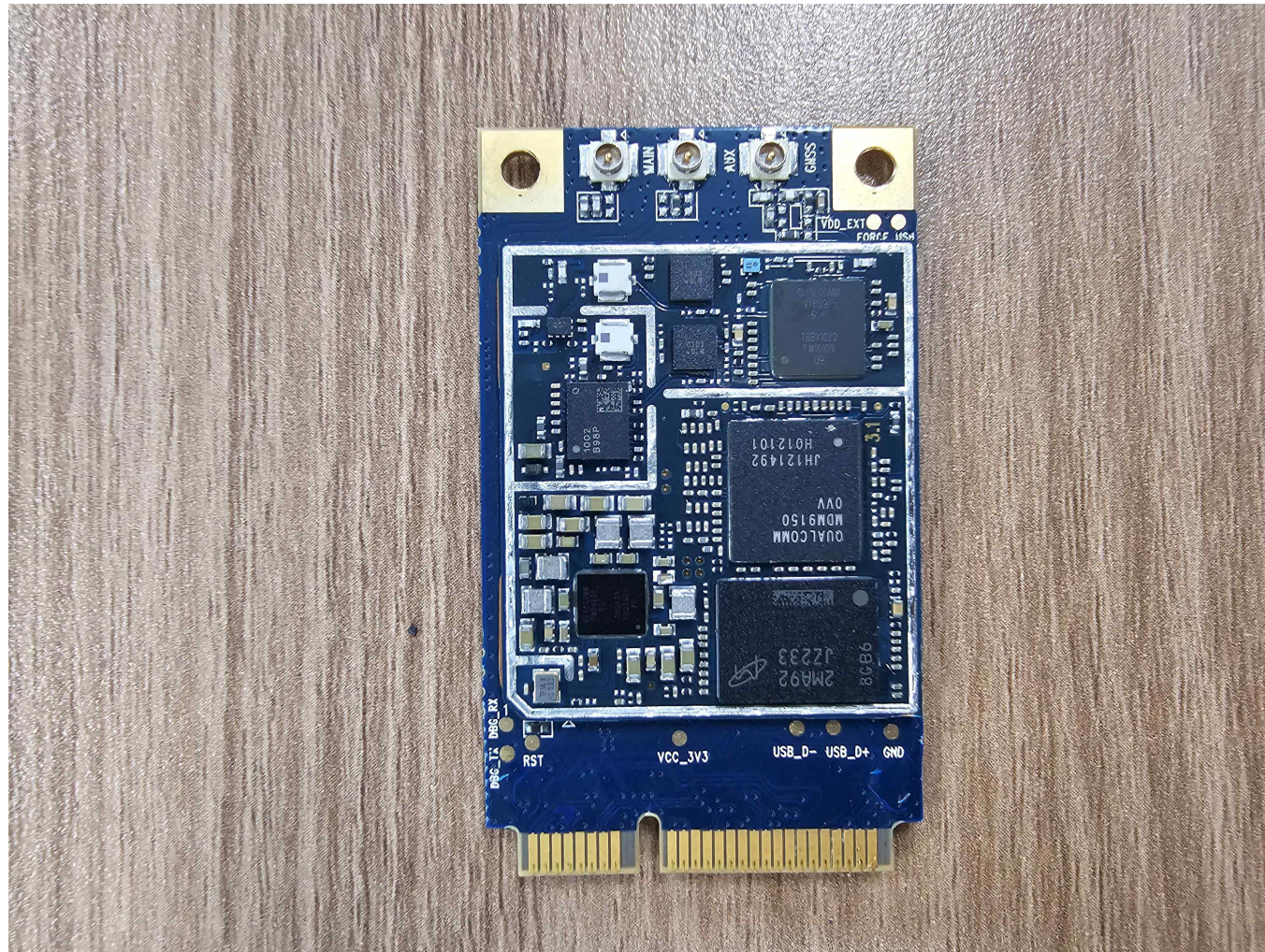
[MINI PCI\_Top]



[MINI PCI\_Bottom]



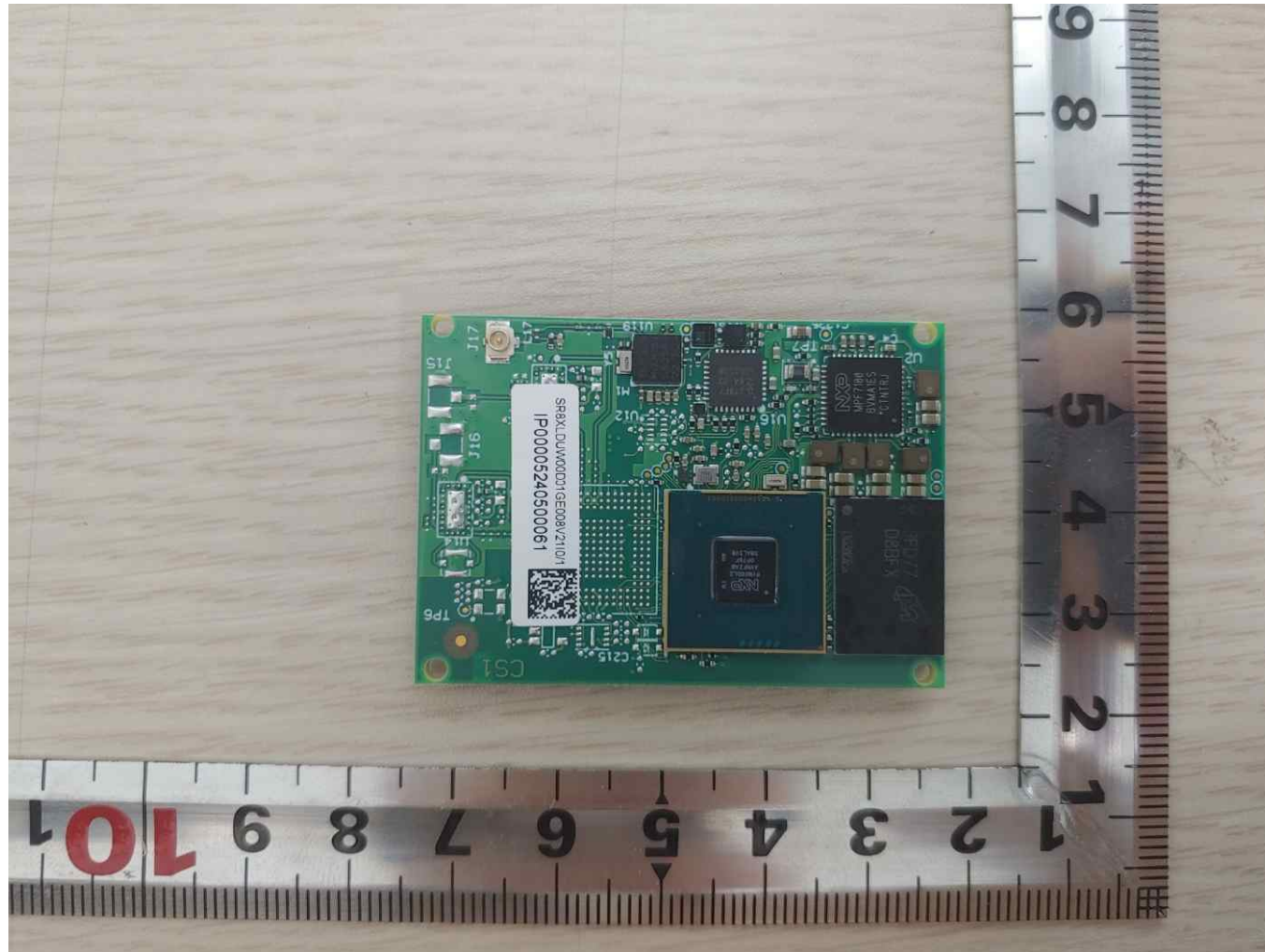
[MINI PCI\_Top\_Shield revmved]



[MINI PCI\_Bottom\_Shield revmoved]



[Sub board\_Top]



[Sub board\_Bottom]

