

Appendix Report

Project No.:	CISR250617181
Test Engineer:	James Wang
Supervised by:	Rory Huang

8.1. Duty Cycle

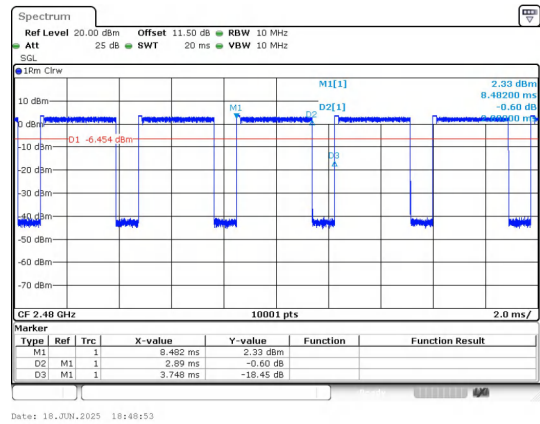
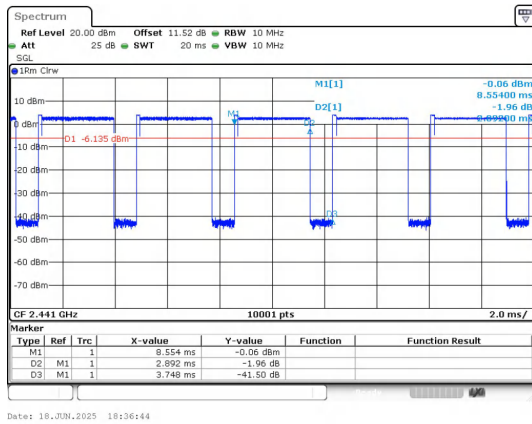
Test Result

Left:

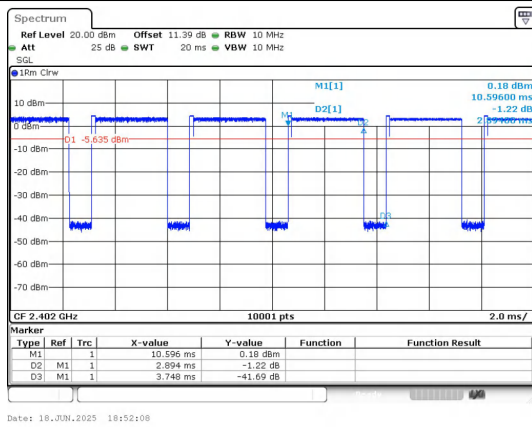
Modulation	Packets	Channel	On Time (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle (linear)	Duty Cycle Factor (dB)	1/T
GFSK	DH5	0	2.886	3.748	77.00	0.7700	1.1351	0.3465
		39	2.886	3.748	77.00	0.7700	1.1351	0.3465
		78	2.886	3.748	77.00	0.7700	1.1351	0.3465
$\pi/4$ DQPSK	2-DH5	0	2.892	3.748	77.16	0.7716	1.1261	0.3458
		39	2.892	3.748	77.16	0.7716	1.1261	0.3458
		78	2.890	3.748	77.11	0.7711	1.1289	0.3460
8DPSK	3-DH5	0	2.894	3.748	77.21	0.7721	1.1233	0.3455
		39	2.894	3.748	77.21	0.7721	1.1233	0.3455
		78	2.894	3.748	77.21	0.7721	1.1233	0.3455

Test Graphs

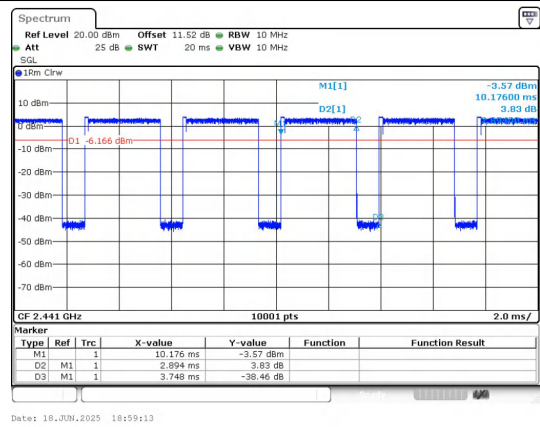




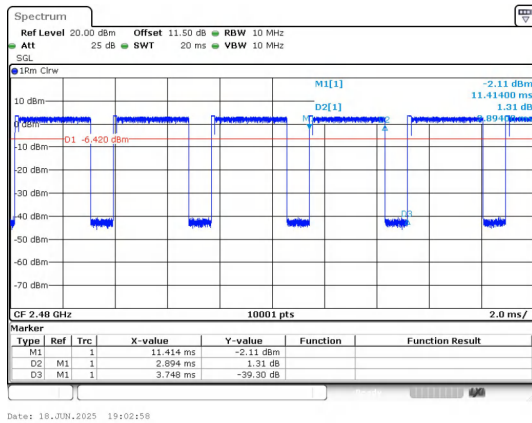
$\pi/4$ DQPSK(2-DH5)_Channel 39



$\pi/4$ DQPSK(2-DH5)_Channel 78



8DPSK(3-DH5)_Channel 0



8DPSK(3-DH5)_Channel 39

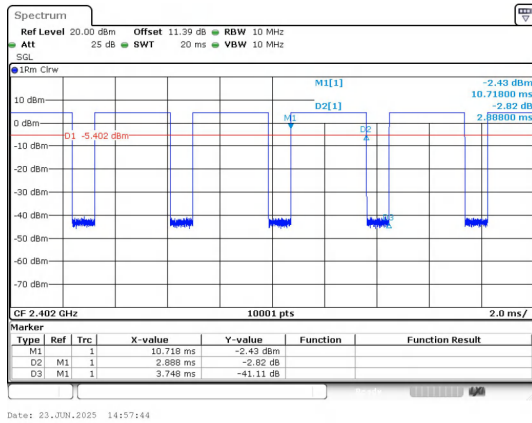


8DPSK(3-DH5)_Channel 78

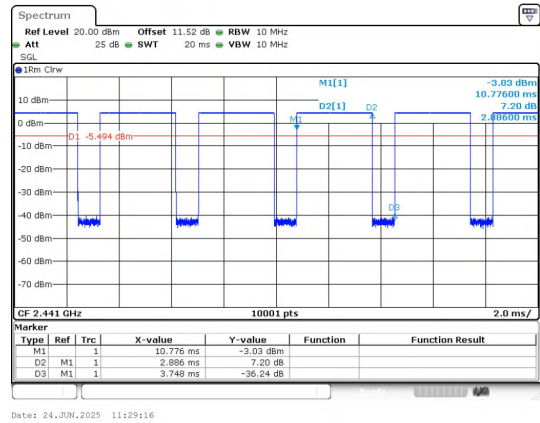
Right:

Modulation	Packets	Channel	On Time (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle (linear)	Duty Cycle Factor (dB)	1/T
GFSK	DH5	0	2.888	3.748	77.05	0.7705	1.1323	0.3463
		39	2.886	3.748	77.00	0.7700	1.1351	0.3465
		78	2.886	3.748	77.00	0.7700	1.1351	0.3465
$\pi/4$ DQPSK	2-DH5	0	2.890	3.748	77.11	0.7711	1.1289	0.3460
		39	2.892	3.748	77.16	0.7716	1.1261	0.3458
		78	2.890	3.748	77.11	0.7711	1.1289	0.3460
8DPSK	3-DH5	0	2.894	3.748	77.21	0.7721	1.1233	0.3455
		39	2.892	3.748	77.16	0.7716	1.1261	0.3458
		78	2.892	3.748	77.16	0.7716	1.1261	0.3458

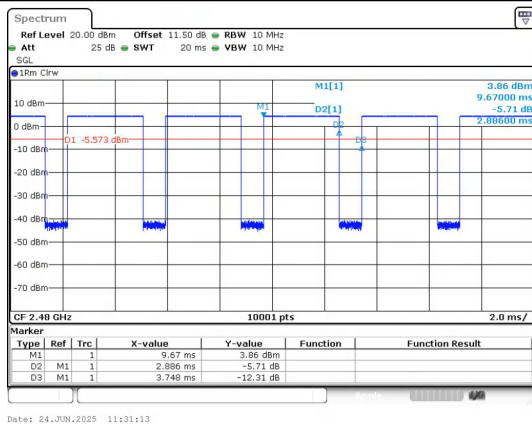
Test Graphs



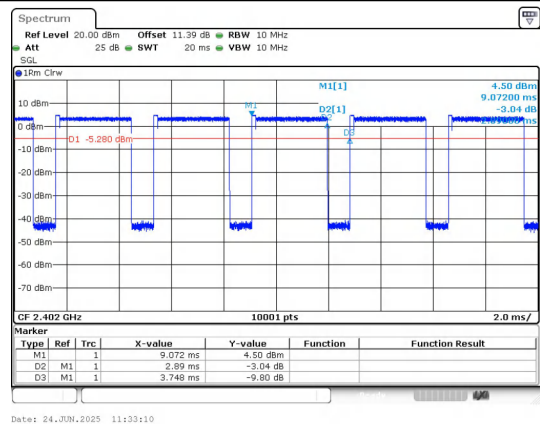
GFSK(DH5)_Channel 0



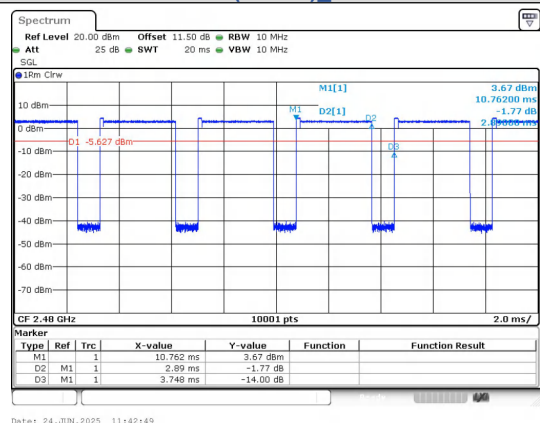
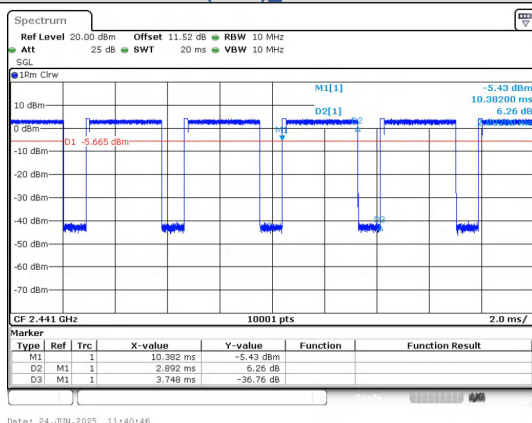
GFSK(DH5)_Channel 39



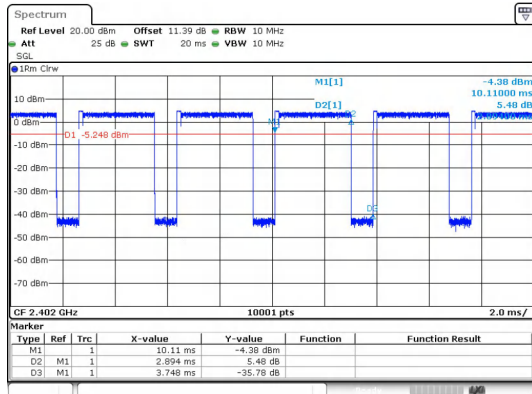
GFSK(DH5)_Channel 78



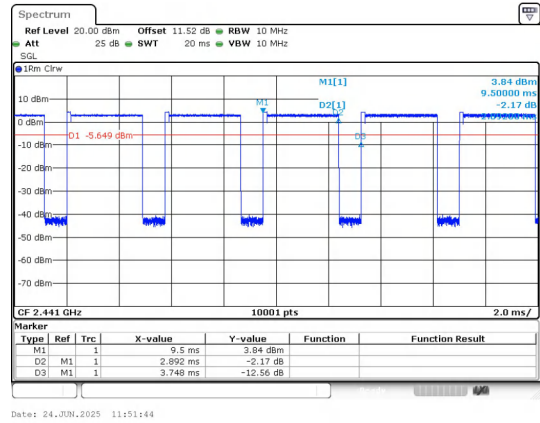
$\pi/4$ DQPSK(2-DH5)_Channel 0



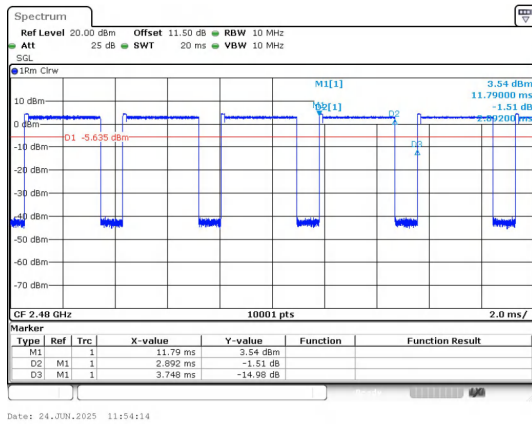
$\pi/4$ DQPSK(2-DH5)_Channel 39



$\pi/4$ DQPSK(2-DH5)_Channel 78



8DPSK(3-DH5)_Channel 0



8DPSK(3-DH5)_Channel 39

8DPSK(3-DH5)_Channel 78

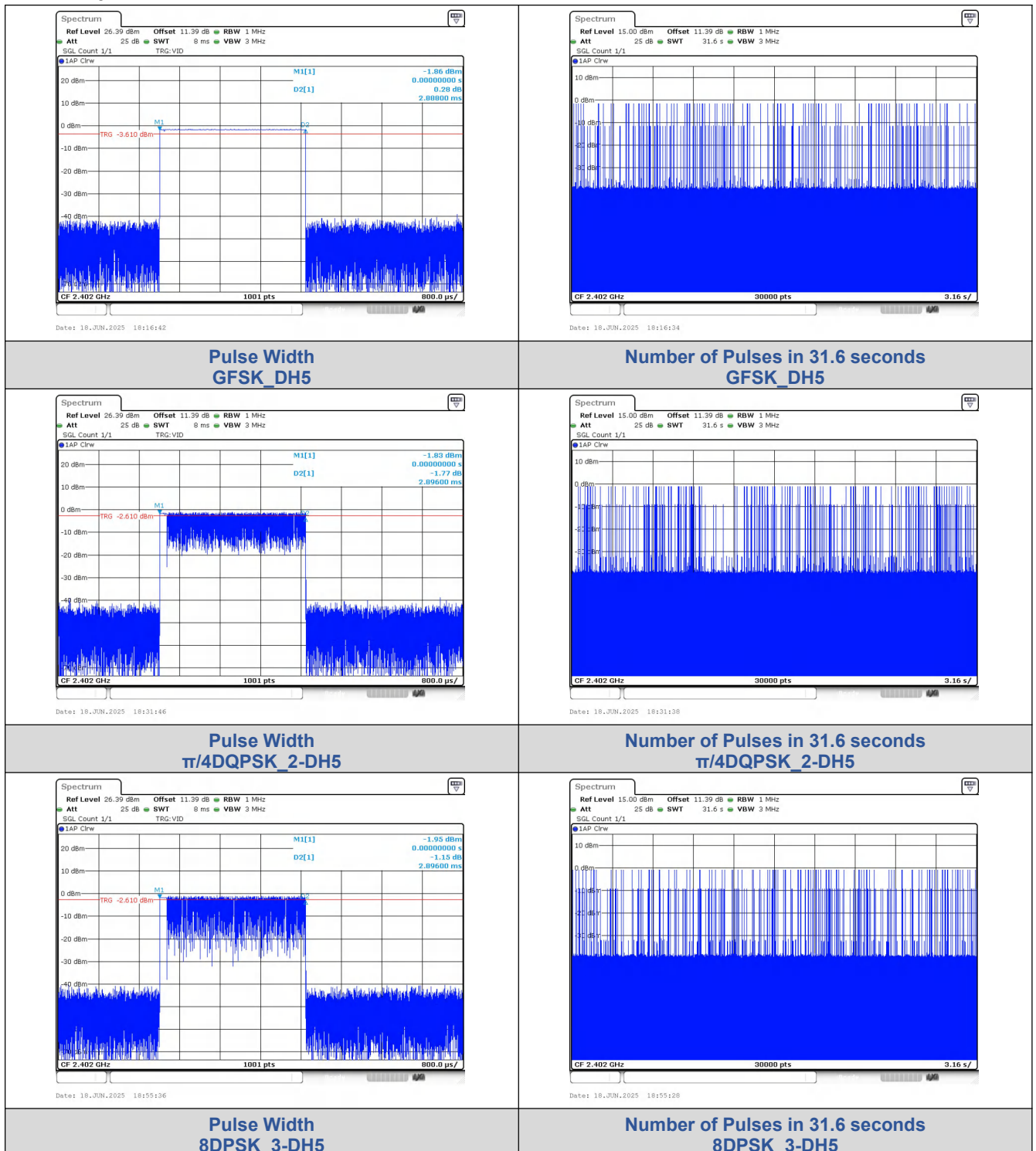
8.2. Dwell Time

Test Result

Left:

Modulation	Packet	Channel	Pulse Width (ms)	Number of Pulses in 31.6 seconds	Dwell Time (ms)	Limit (ms)	Result
GFSK	DH5	CH0 (2402MHz)	2.888	107	309.02	< 400	PASS
$\pi/4$ DQPSK	2-DH5		2.896	127	367.79		PASS
8DPSK	3-DH5		2.896	111	321.46		PASS

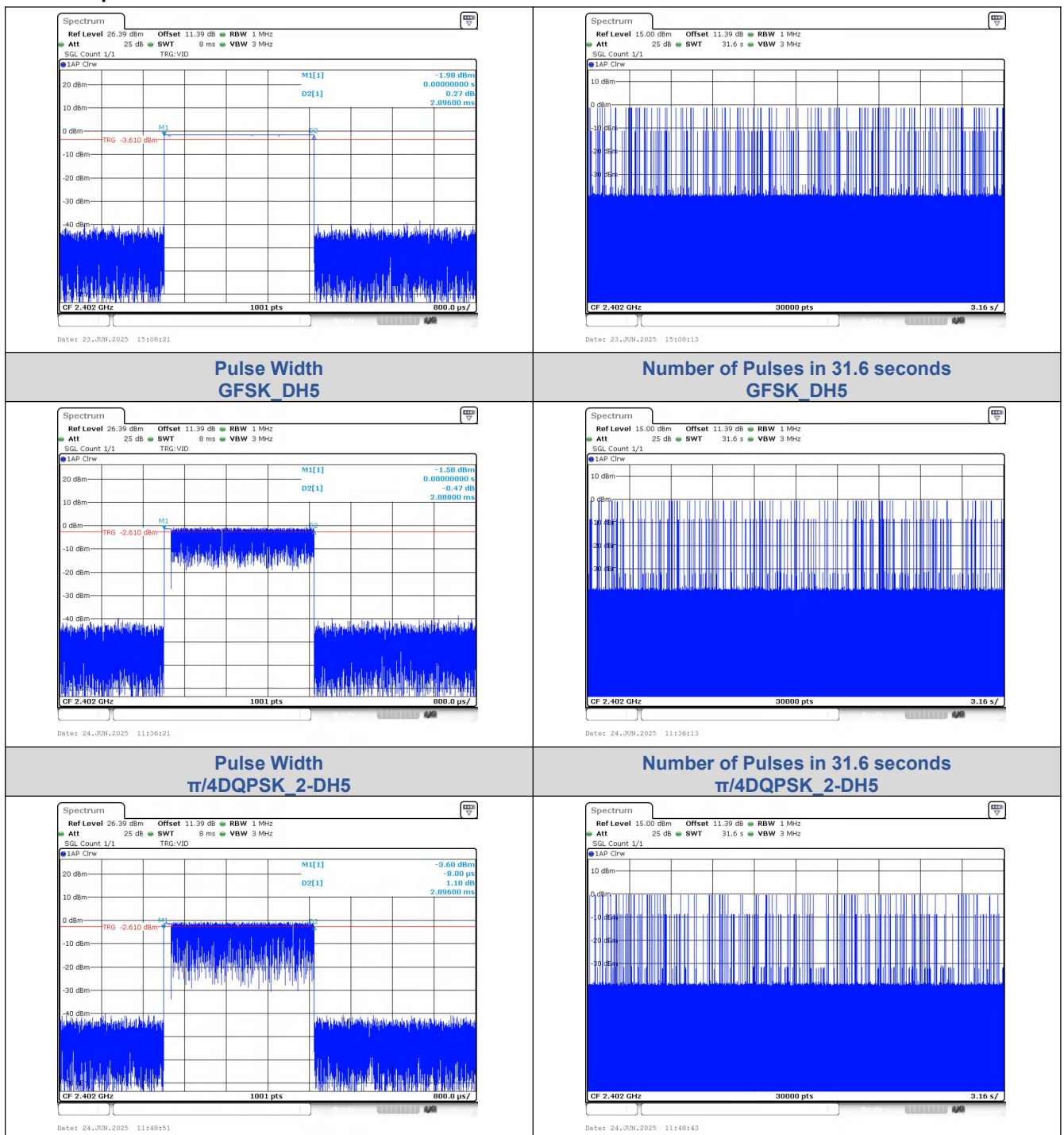
Test Graphs



Right:

Modulation	Packet	Channel	Pulse Width (ms)	Number of Pulses in 31.6 seconds	Dwell Time (ms)	Limit (ms)	Result
GFSK	DH5	CH0 (2402MHz)	2.896	123	356.21	< 400	PASS
$\pi/4$ DQPSK	2-DH5		2.888	103	297.46		PASS
8DPSK	3-DH5		2.896	112	324.35		PASS

Test Graphs



Pulse Width 8DPSK_3-DH5	Number of Pulses in 31.6 seconds 8DPSK_3-DH5
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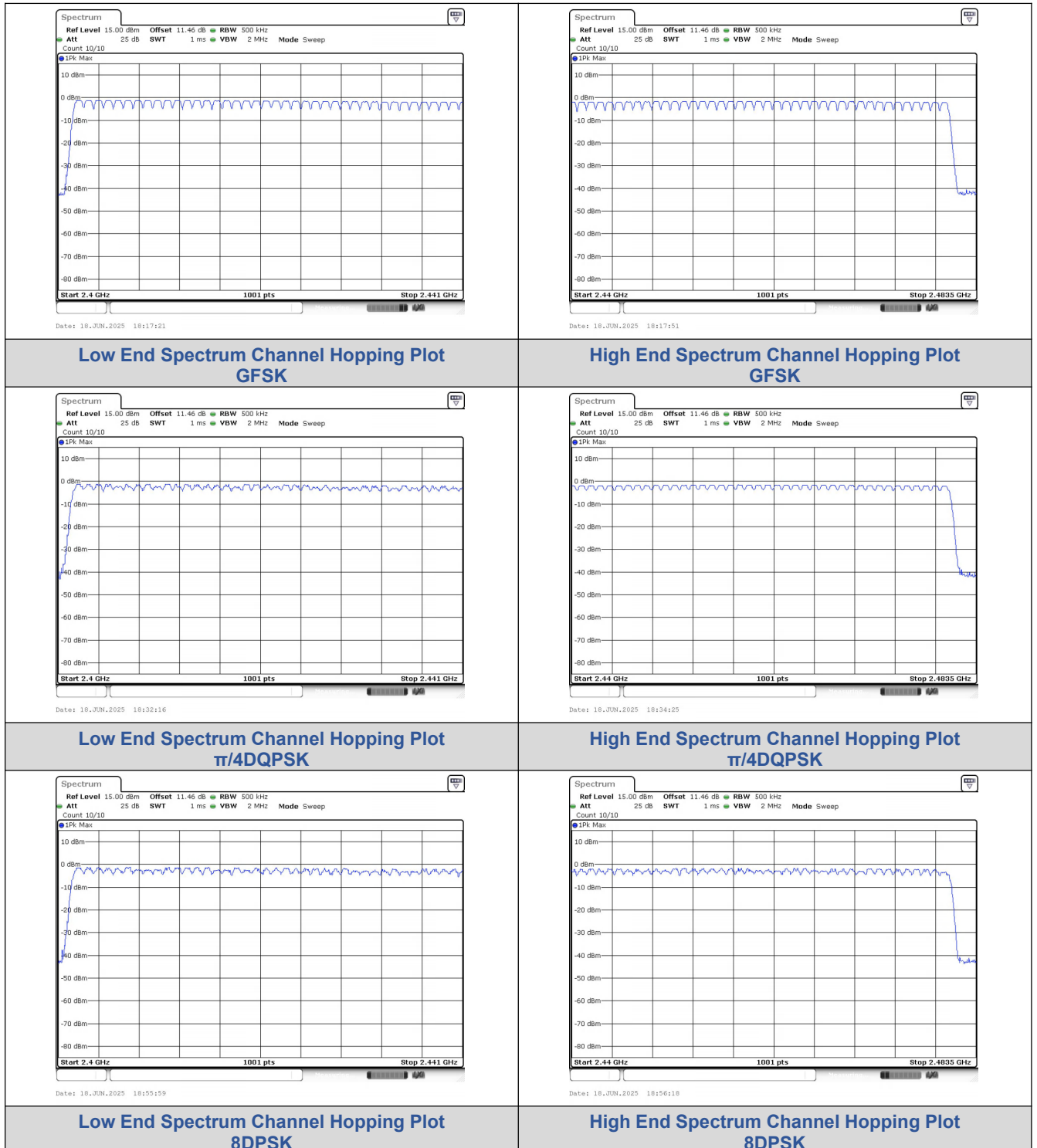
8.3. Number Of Hopping Channel

Test Result

Left:

Modulation	Packet	Number of Hopping Channel	Limit	Result
GFSK	DH5	79	15	PASS
$\pi/4$ DQPSK	2-DH5	79	15	PASS
8DPSK	3-DH5	79	15	PASS

Test Graphs

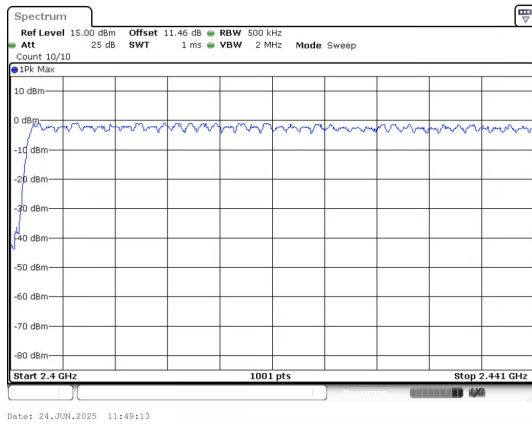


Right:

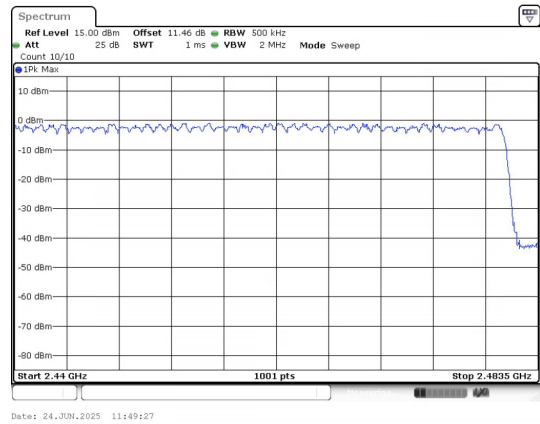
Modulation	Packet	Number of Hopping Channel	Limit	Result
GFSK	DH5	79	15	PASS
$\pi/4$ DQPSK	2-DH5	79	15	PASS
8DPSK	3-DH5	79	15	PASS

Test Graphs





**Low End Spectrum Channel Hopping Plot
8DPSK**



**High End Spectrum Channel Hopping Plot
8DPSK**

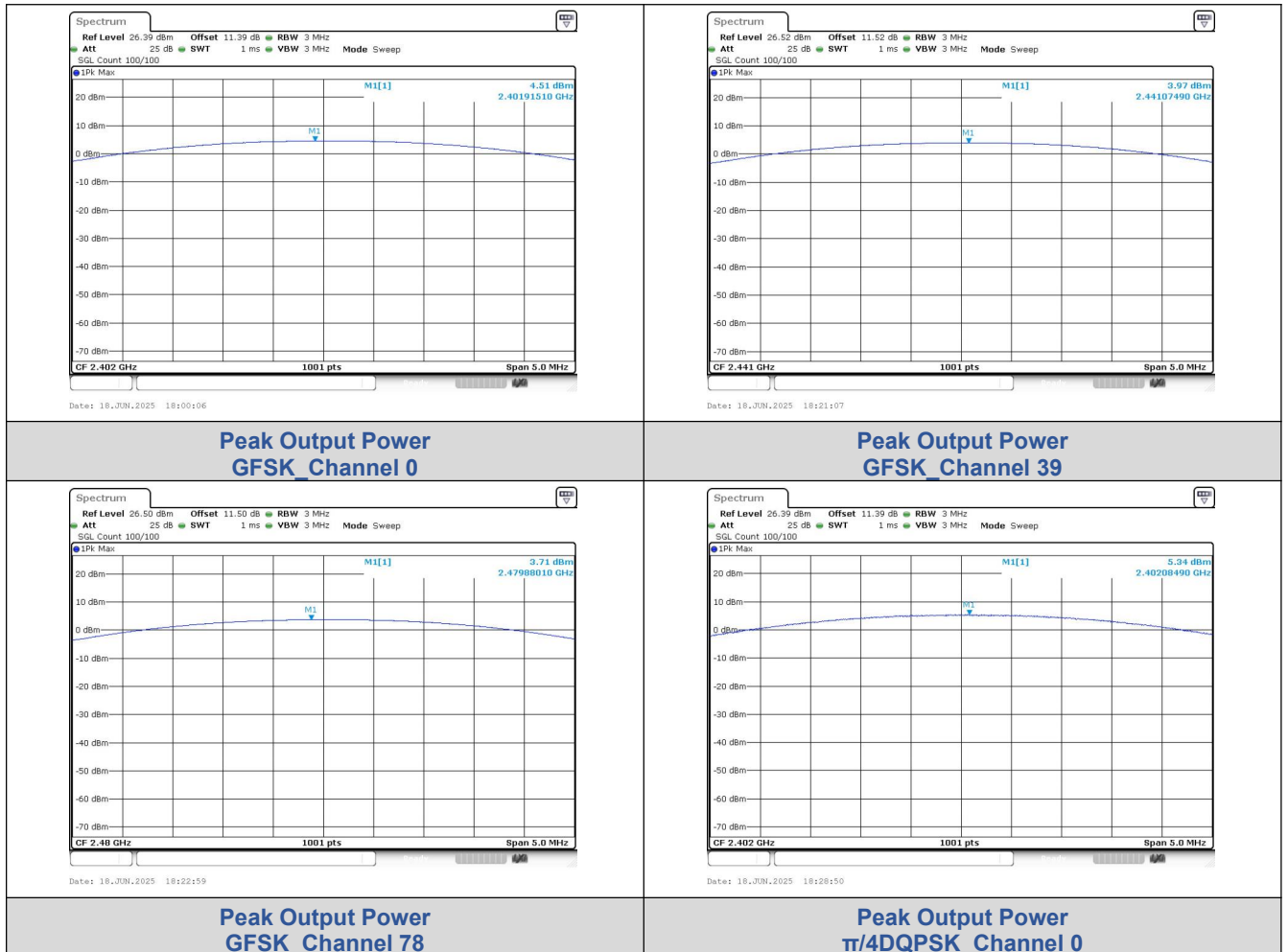
8.4. Conducted Peak Output Power

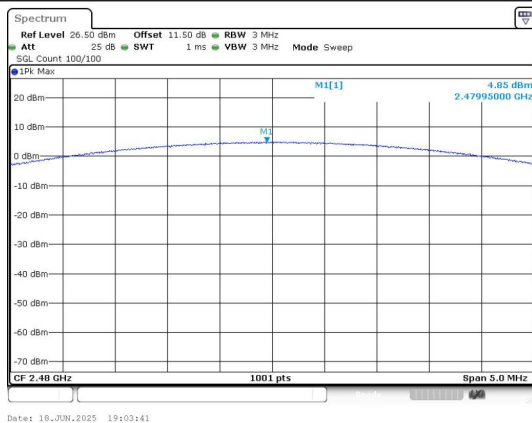
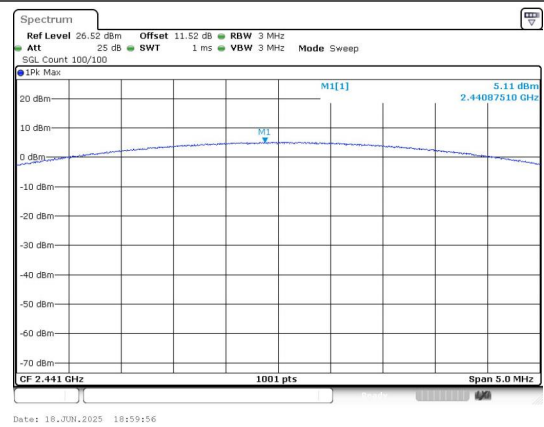
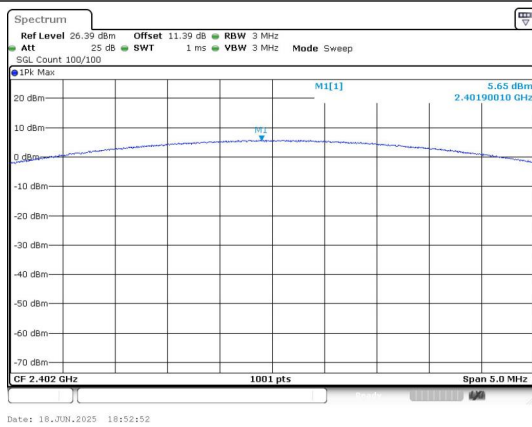
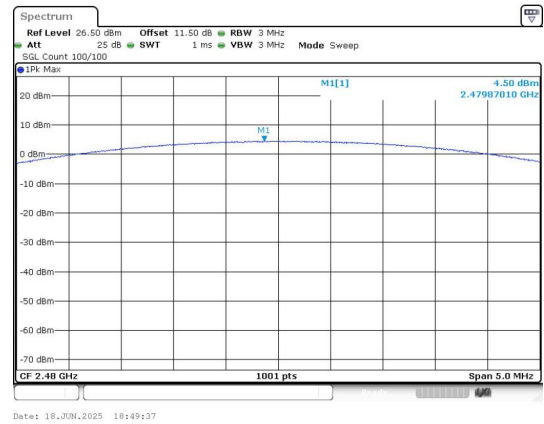
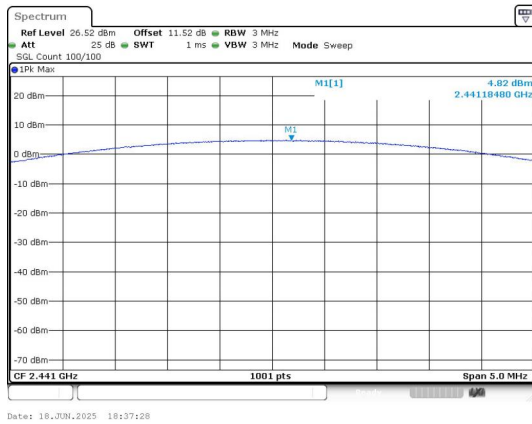
Test Result

Left:

Modulation	Packet Type	Channel	Peak Output Power (dBm)	Peak Output Power (mW)	Limit (dBm)	Result
GFSK	DH5	0	4.51	2.82	≤30	PASS
		39	3.97	2.49		PASS
		78	3.71	2.35		PASS
$\pi/4$ DQPSK	2-DH5	0	5.34	3.42	≤20.97	PASS
		39	4.82	3.03		PASS
		78	4.50	2.82		PASS
8DPSK	3-DH5	0	5.65	3.67		PASS
		39	5.11	3.24		PASS
		78	4.85	3.05		PASS

Test Graphs

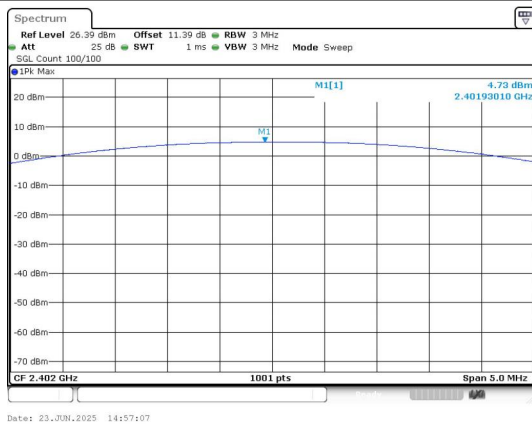




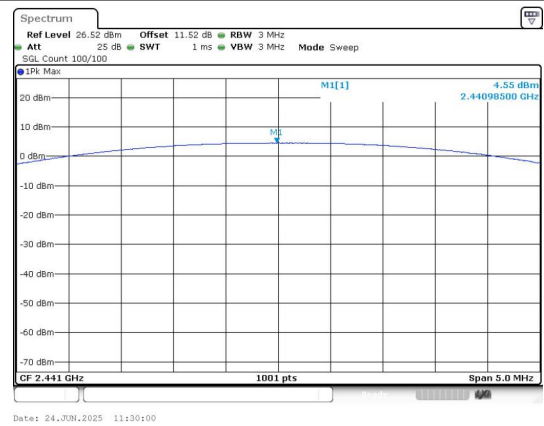
Right:

Modulation	Packet Type	Channel	Peak Output Power (dBm)	Peak Output Power (mW)	Limit (dBm)	Result
GFSK	DH5	0	4.73	2.97	≤30	PASS
		39	4.55	2.85		PASS
		78	4.38	2.74		PASS
$\pi/4$ DQPSK	2-DH5	0	5.67	3.69	≤20.97	PASS
		39	5.29	3.38		PASS
		78	5.28	3.37		PASS
8DPSK	3-DH5	0	6.07	4.05		PASS
		39	5.68	3.70		PASS
		78	5.63	3.66		PASS

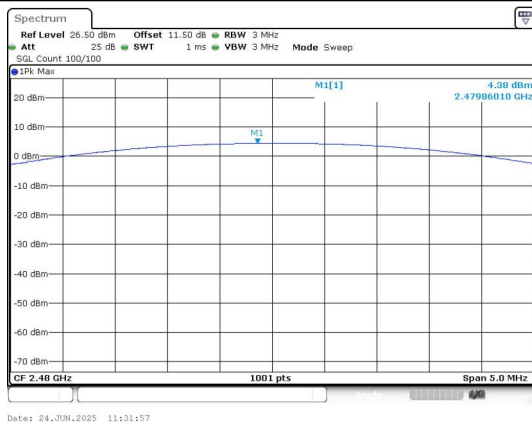
Test Graphs



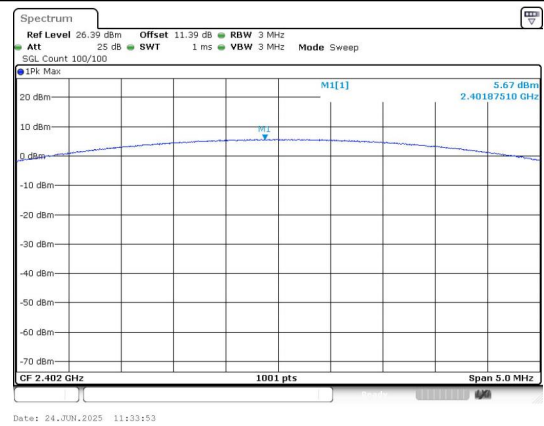
Peak Output Power
GFSK_Channel 0



Peak Output Power
GFSK_Channel 39



Peak Output Power
GFSK_Channel 78



Peak Output Power
 $\pi/4$ DQPSK_Channel 0