

# Appendix Report

Project No.: CISR250519101

Test Engineer: James Wang

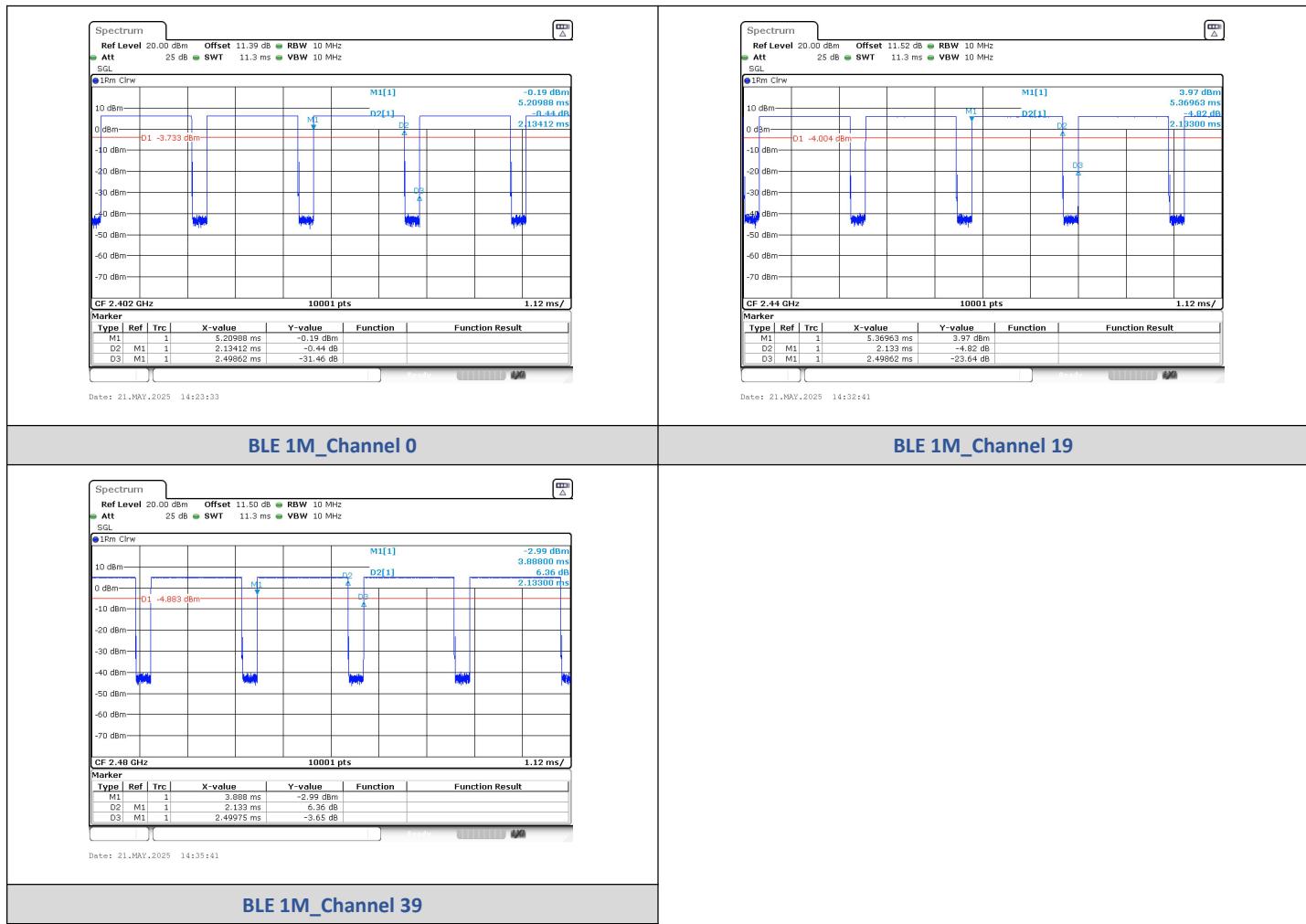
Supervised by: Rory Huang

# 1) Duty Cycle

## Test Result

Mode	Channel	On Time (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle (linear)	Duty Cycle Factor (dB)	1/T
BLE 1M	0	2.134	2.499	85.41	0.8541	0.6849	0.4686
	19	2.133	2.499	85.37	0.8537	0.6869	0.4688
	39	2.133	2.500	85.33	0.8533	0.689	0.4688

## Test Graphs

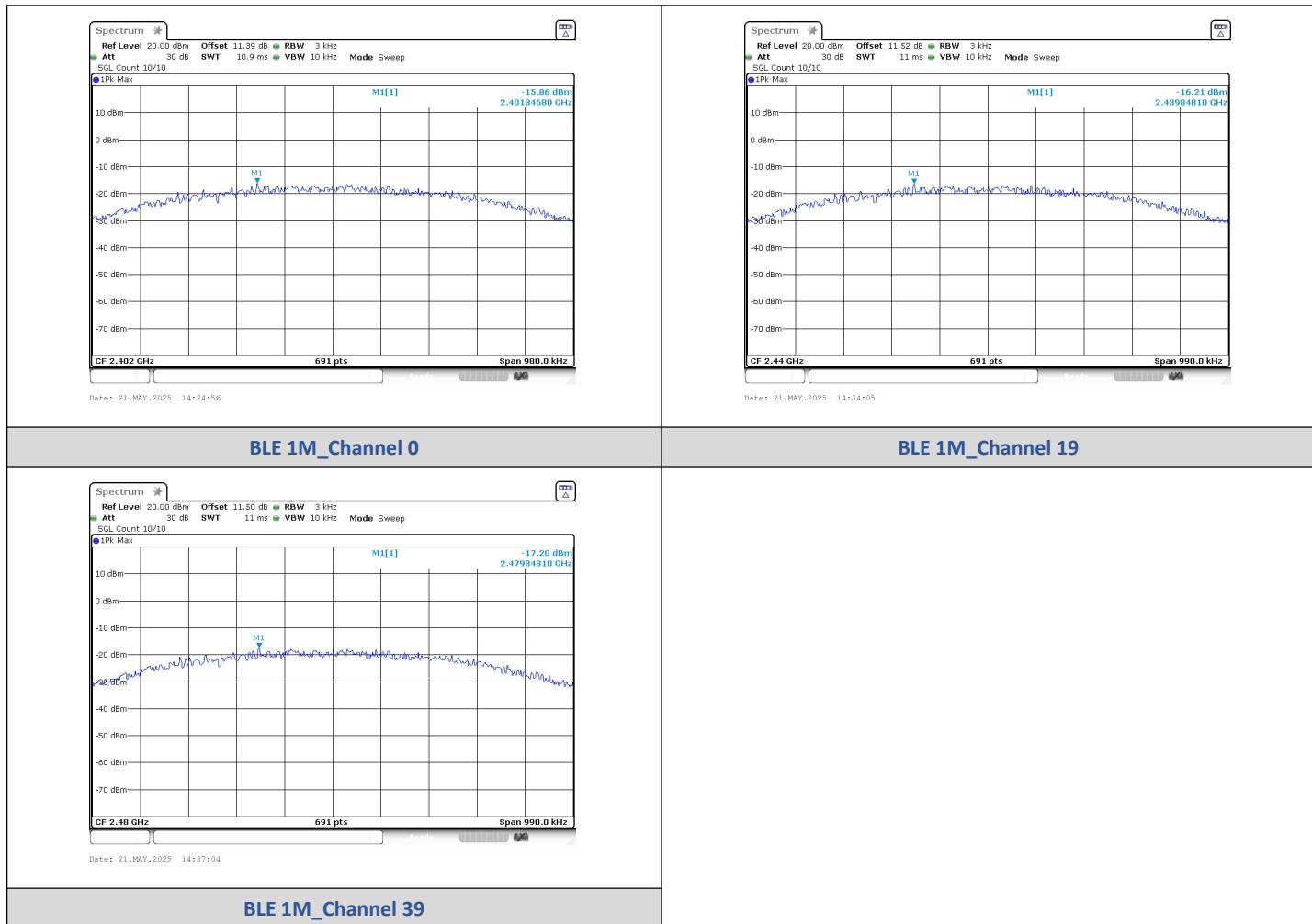


## 2) Power Spectral Density

### Test Result

Mode	Channel	PSD (dBm/3kHz)	Limit (dBm/3kHz)	Result
BLE 1M	0	-15.860	≤8	PASS
BLE 1M	19	-16.210	≤8	PASS
BLE 1M	39	-17.200	≤8	PASS

### Test Graphs

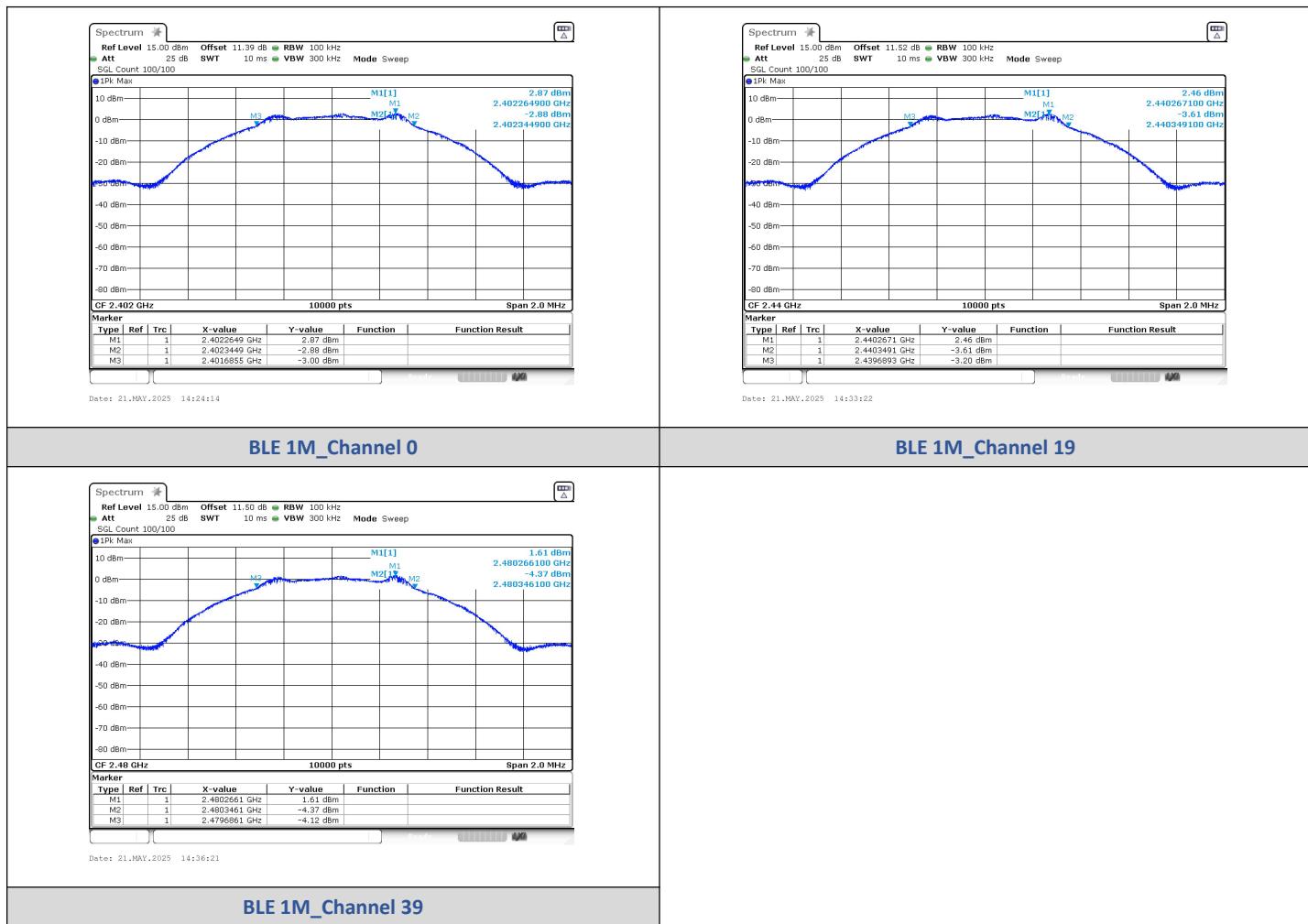


### 3) 6dB Bandwidth

#### Test Result

Mode	Channel	Center Frequency (MHz)	6 dB Bandwidth (MHz)	Limit (MHz)	Result
BLE 1M	0	2402	0.6500	≥0.5	PASS
	19	2440	0.6600		PASS
	39	2480	0.6600		PASS

#### Test Graphs

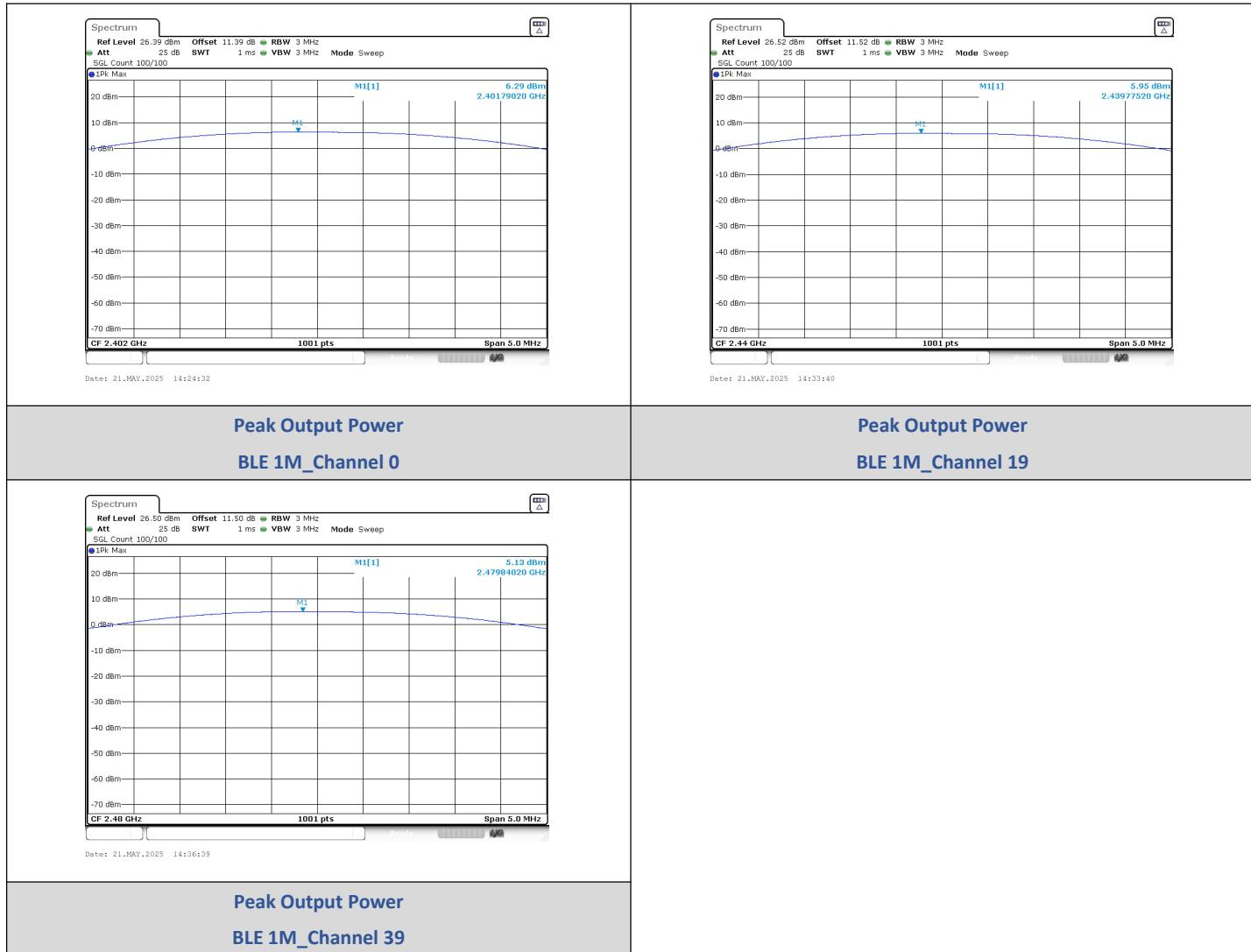


## 4) Conducted Output Power

### Test Result

Mode	Channel	Peak Output Power (dBm)	Peak Output Power (mW)	Limit (dBm)	Result
BLE 1M	0	6.29	4.26	≤30	PASS
	19	5.95	3.94	≤30	PASS
	39	5.13	3.26	≤30	PASS

### Test Graphs

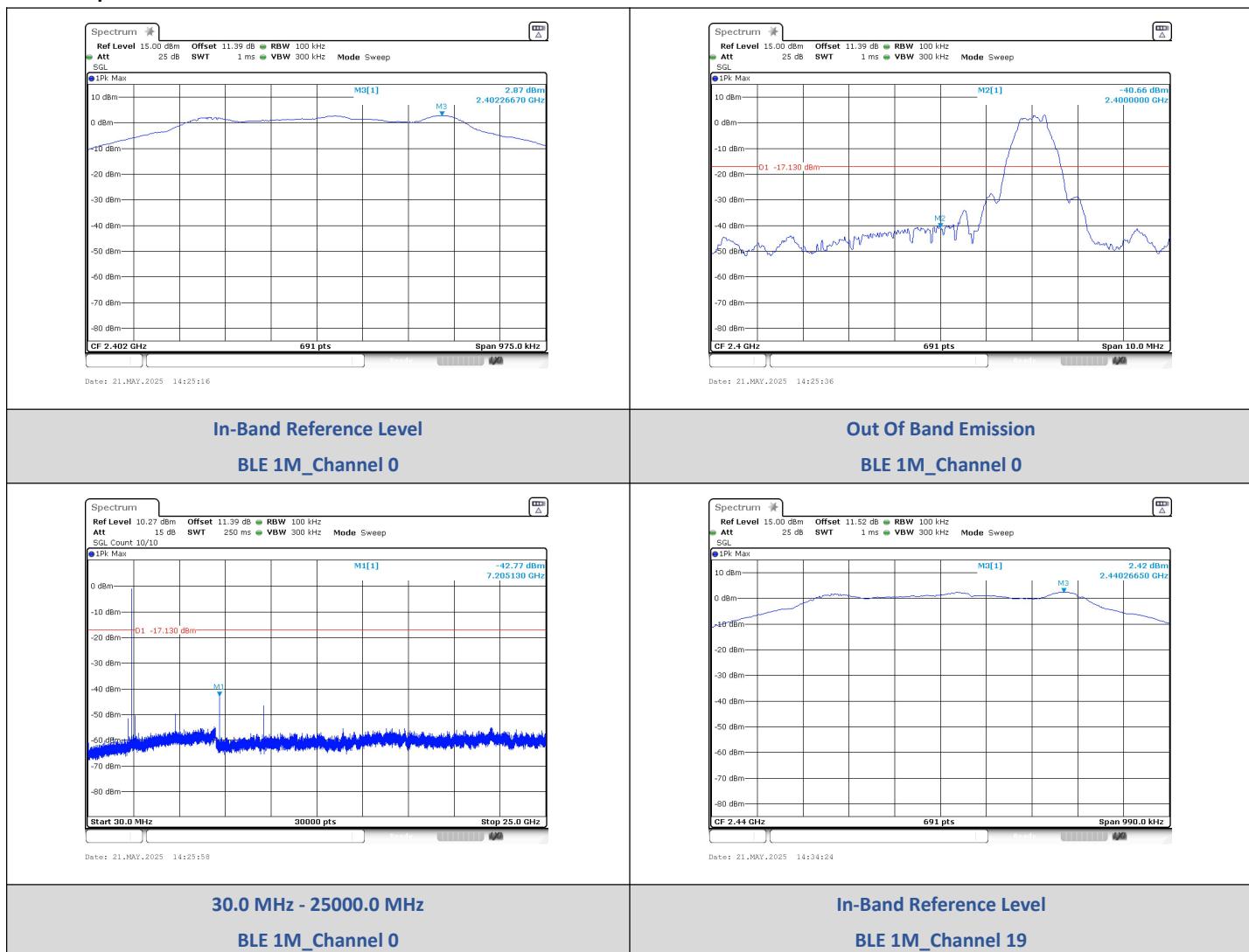


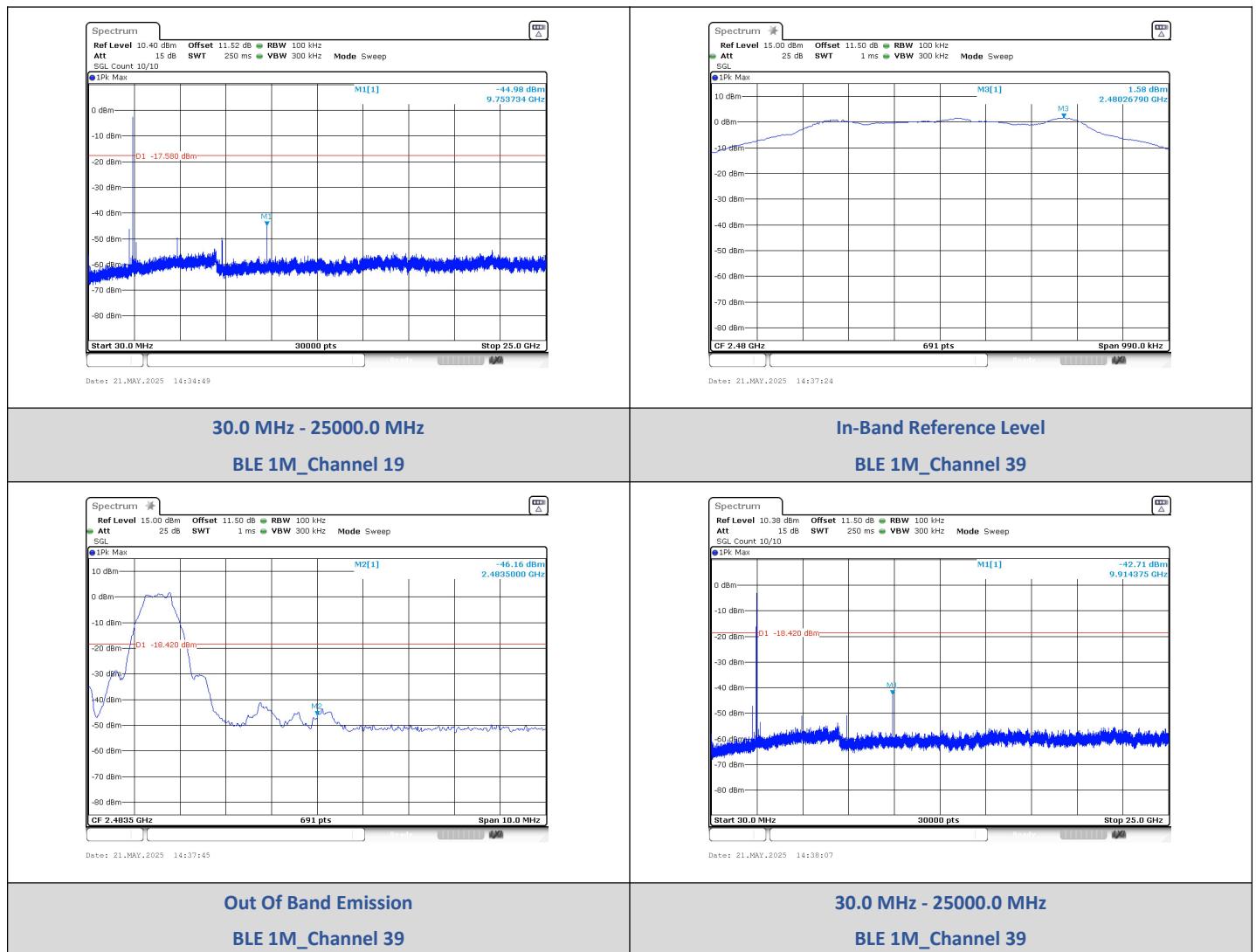
## 5) Conducted Out Of Band Emission

### Test Result

Mode	Channel	OOB Emission Frequency (MHz)	OOB Emission Level (dBm)	Limit (dBm)	Over Limit (dB)	Result
BLE 1M	0	2400.00	-40.660	-17.13	-23.530	PASS
		7205.13	-42.767	-17.13	-25.637	PASS
	19	9753.73	-44.976	-17.58	-27.396	PASS
	39	2483.50	-46.160	-18.42	-27.740	PASS
		9914.37	-42.706	-18.42	-24.286	PASS

### Test Graphs





-----End of the report-----