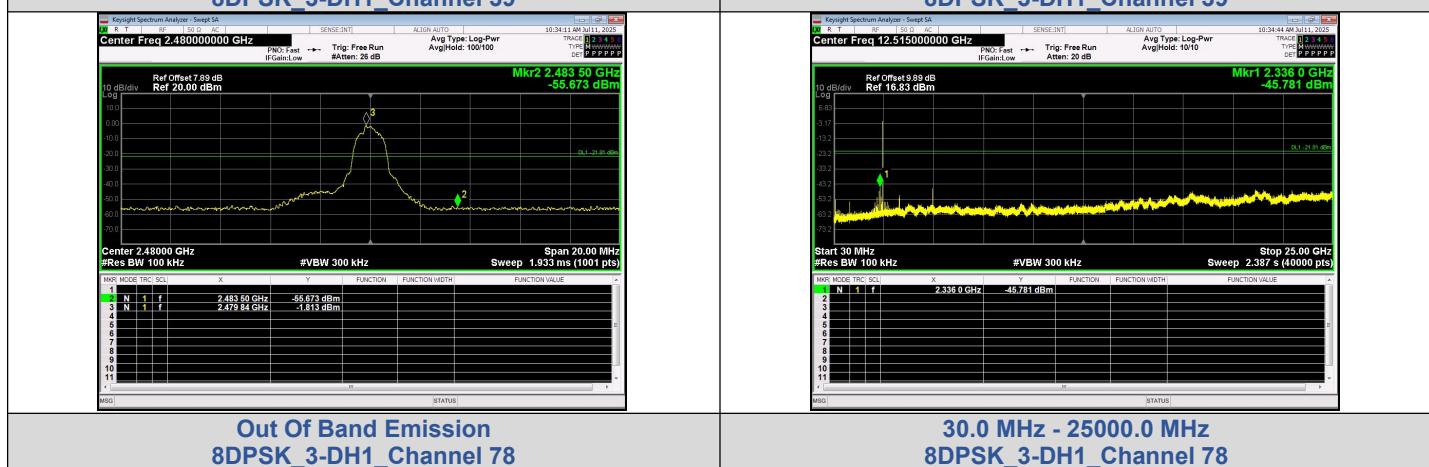
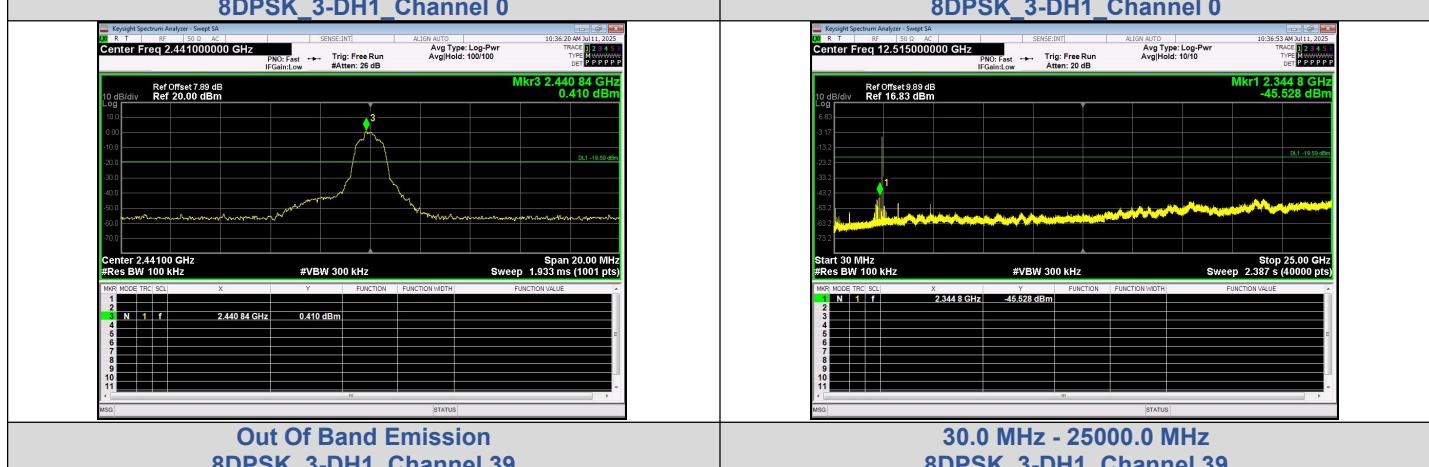
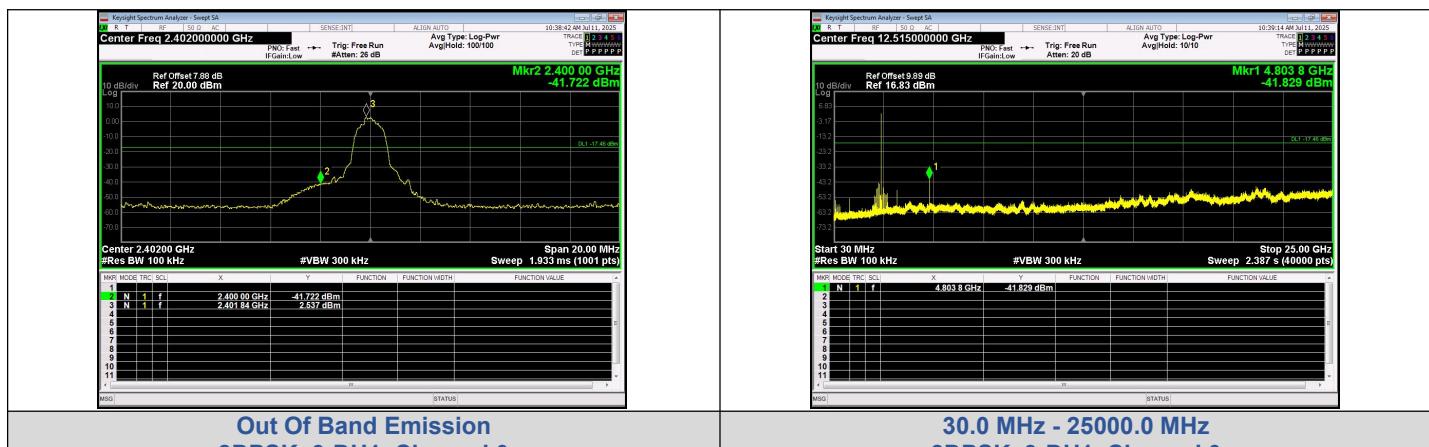
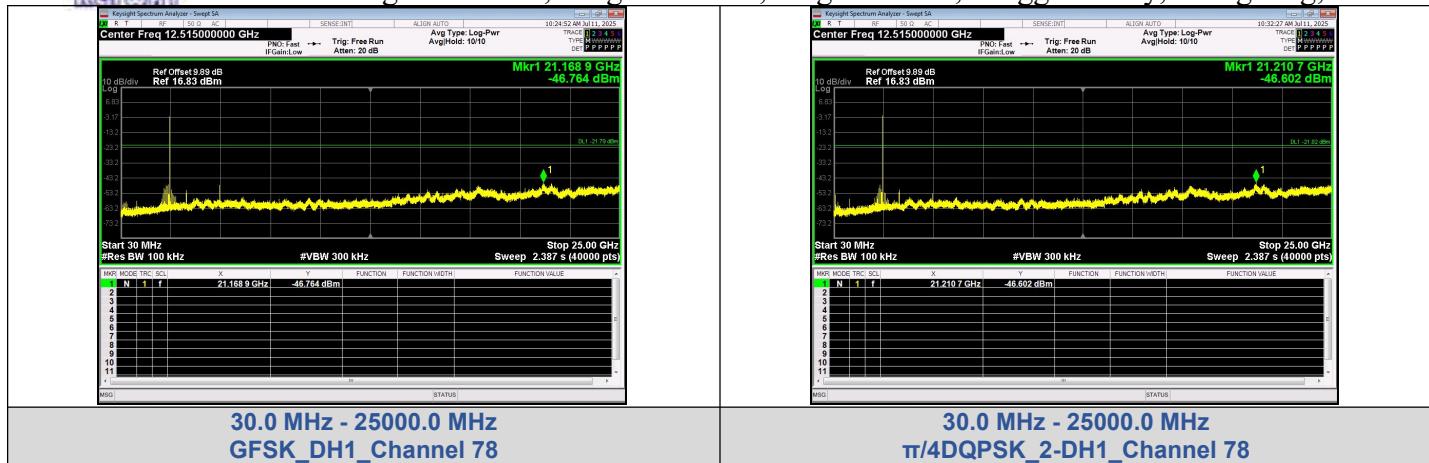


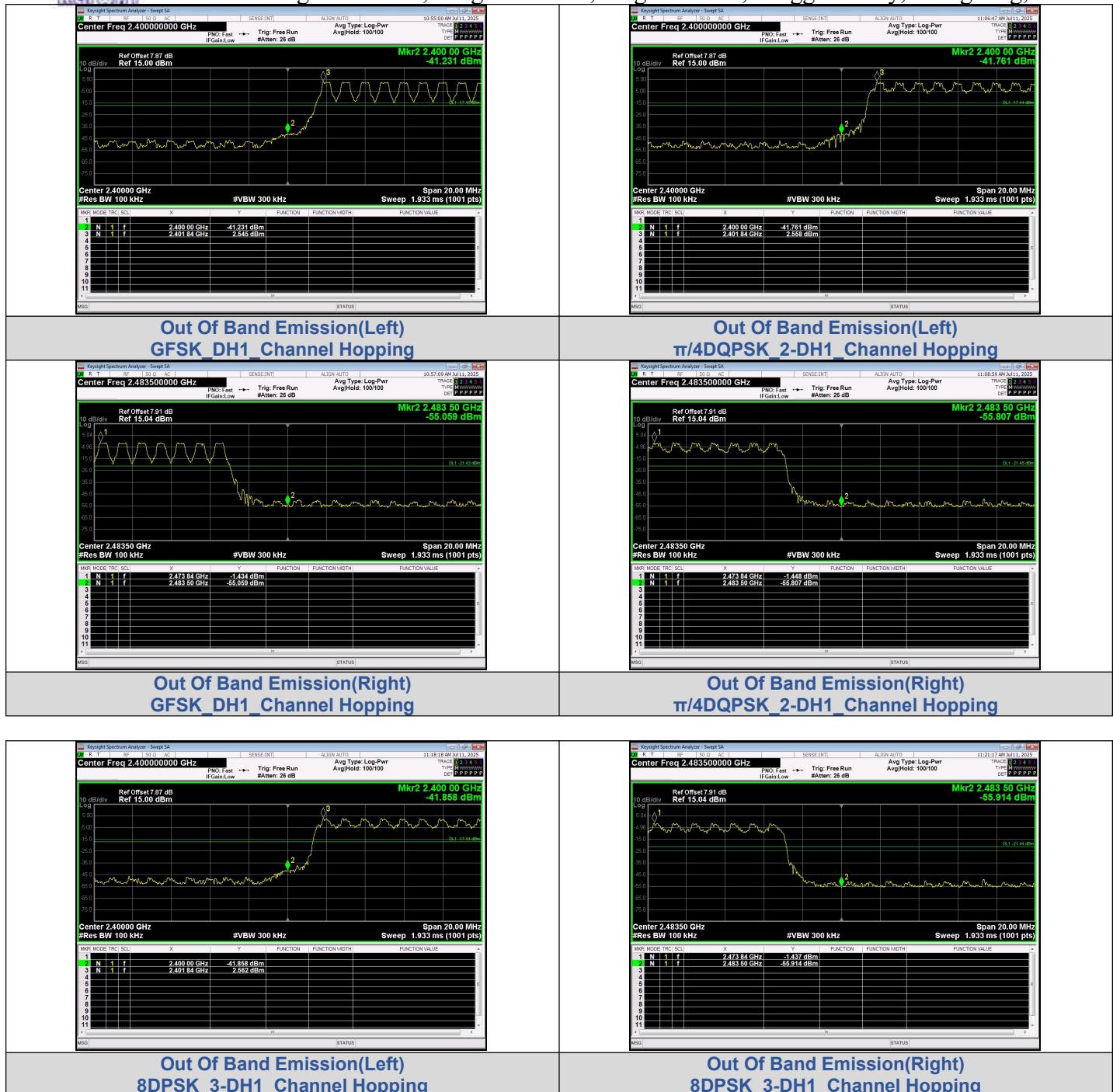


Keyway Testing Technology (Guangdong) Co., Ltd
21 Floor, Building 6, Dongyi Intelligent Equipment New Energy Vehicle Park, N.30
of Tangxia District, Dongshen Road, Tangxia Town, Dongguan City, Guangdong, China





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21 Floor, Building 6, Dongyi Intelligent Equipment New Energy Vehicle Park, N.30
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A.6 DUTY CYCLE

Test Result

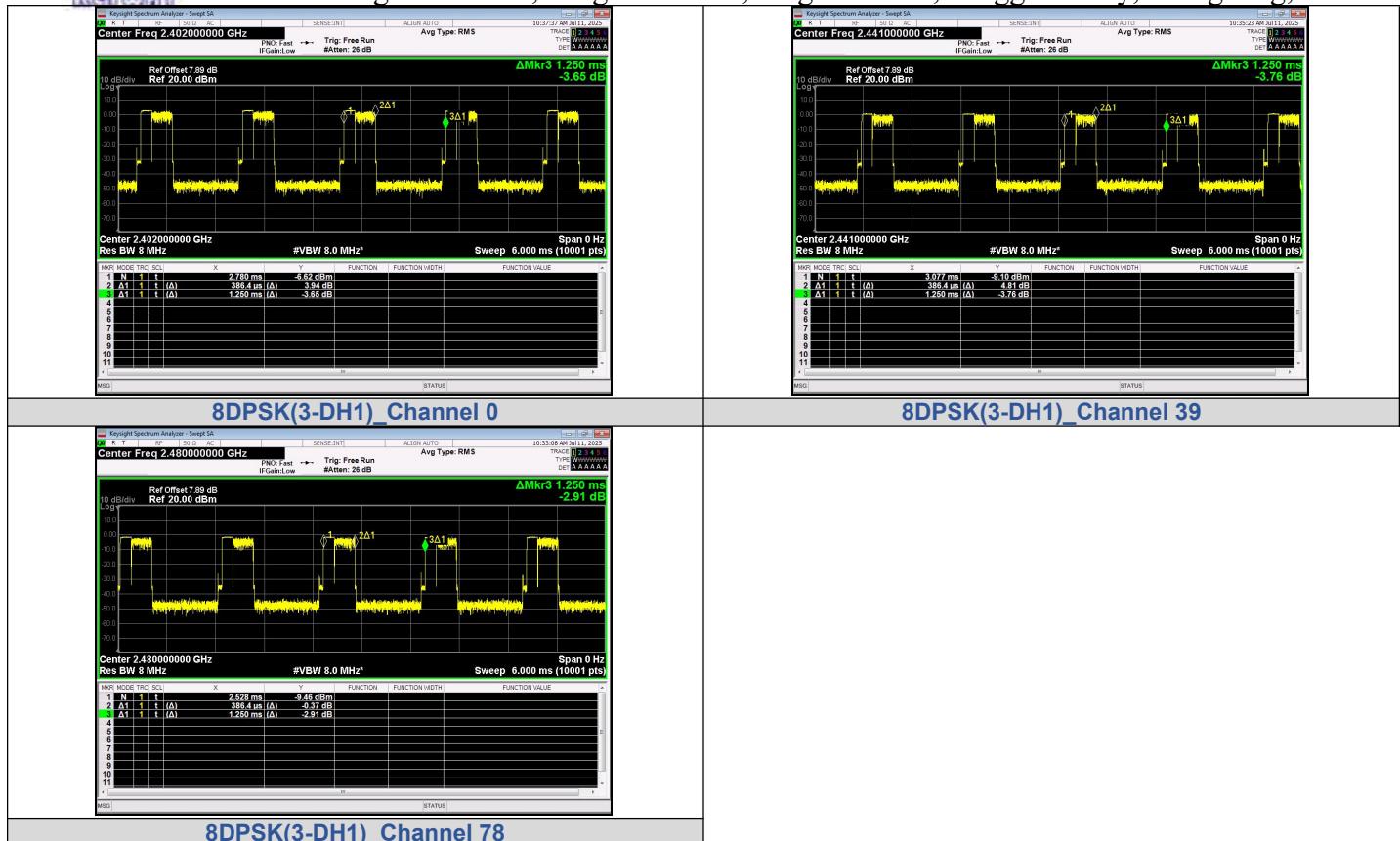
Modulation	packets	Channel	On Time (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle (linear)	Duty Cycle Factor (dB)	1/T
GFSK	DH1	0	0.379	1.249	30.36	0.3036	5.177	2.6385
		39	0.379	1.250	30.34	0.3034	5.1798	2.6385
		78	0.379	1.250	30.34	0.3034	5.1798	2.6385
$\pi/4$ DQPSK	2-DH1	0	0.386	1.249	30.88	0.3088	5.1032	2.5907
		39	0.386	1.250	30.87	0.3087	5.1046	2.5907
		78	0.386	1.249	30.88	0.3088	5.1032	2.5907
8DPSK	3-DH1	0	0.386	1.250	30.92	0.3092	5.0976	2.5907
		39	0.386	1.250	30.92	0.3092	5.0976	2.5907
		78	0.386	1.250	30.92	0.3092	5.0976	2.5907

Test Graphs





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A.7 DWELL TIME

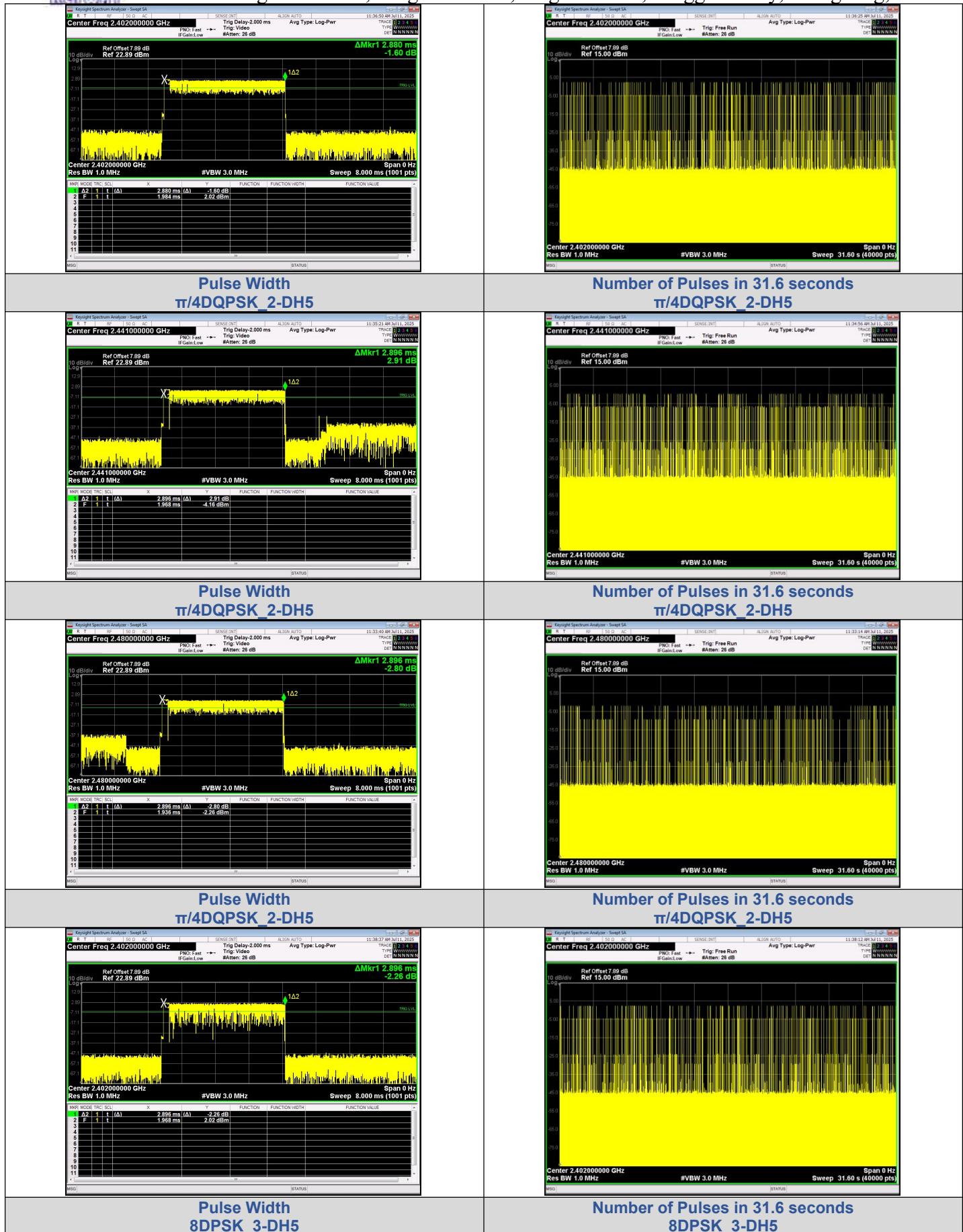
Test Result

Modulation	Packet	Channel	Pulse Width (ms)	Number of Pulses in 31.6 seconds	Dwell Time (ms)	Limit (ms)	Result
GFSK	DH5	Hopping	2.880	110	316.8	< 400	PASS
	DH5		2.896	99	286.7		PASS
	DH5		2.896	105	304.08		PASS
π/4DQPSK	2-DH5	Hopping	2.880	109	313.92	< 400	PASS
	2-DH5		2.896	99	286.7		PASS
	2-DH5		2.896	108	312.77		PASS
8DPSK	3-DH5	Hopping	2.896	108	312.77	< 400	PASS
	3-DH5		2.896	109	315.66		PASS
	3-DH5		2.896	105	304.08		PASS

Test Graphs



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