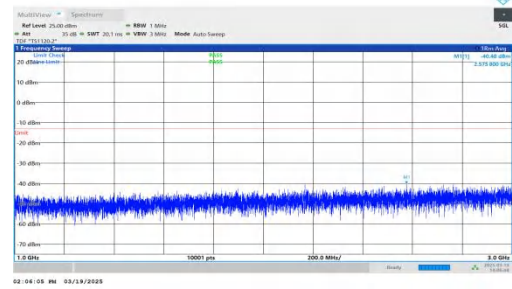
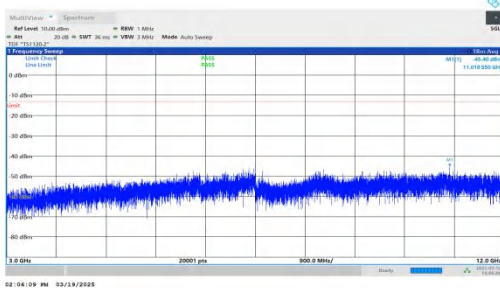


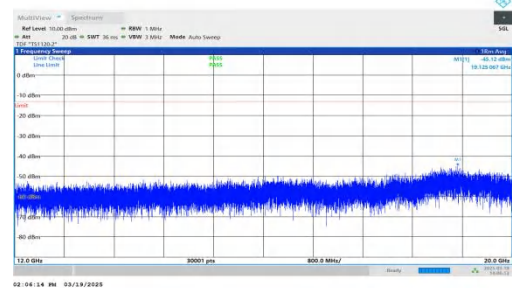
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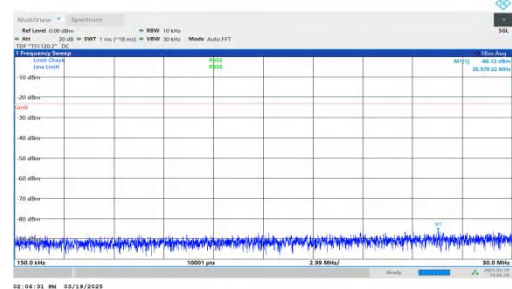
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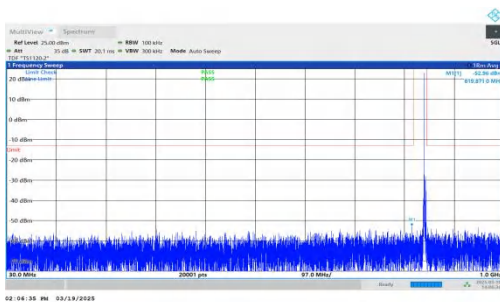
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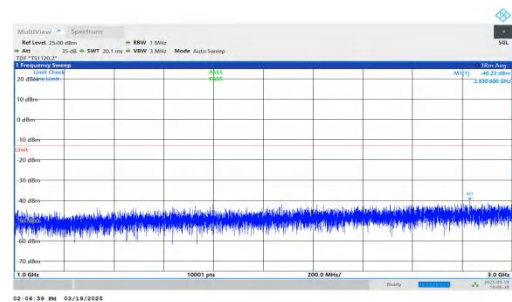
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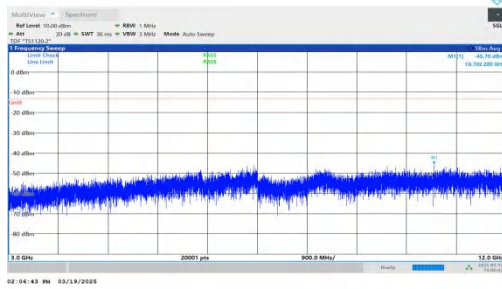
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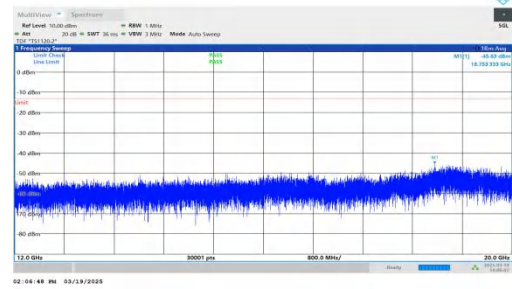
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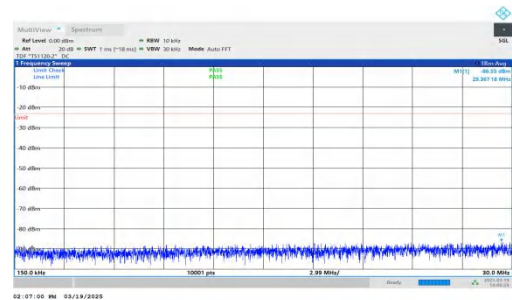
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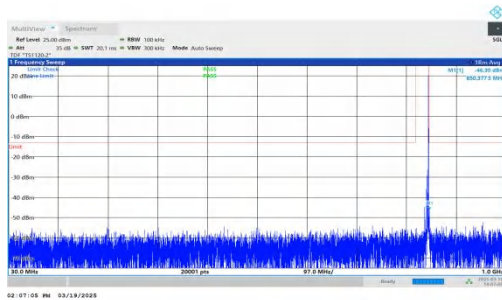
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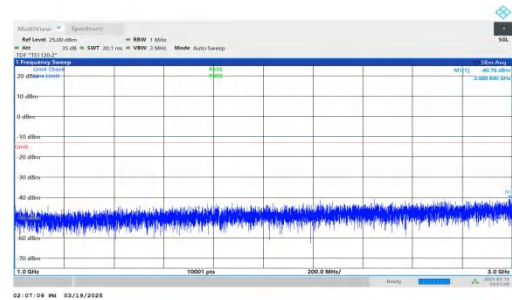
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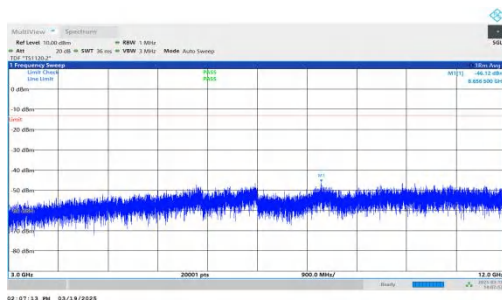
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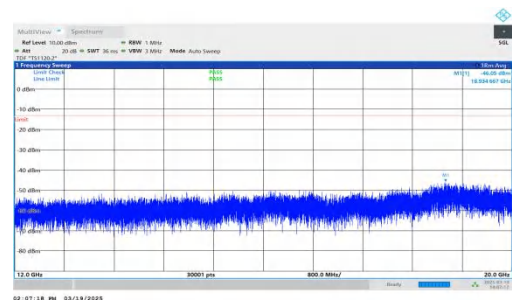
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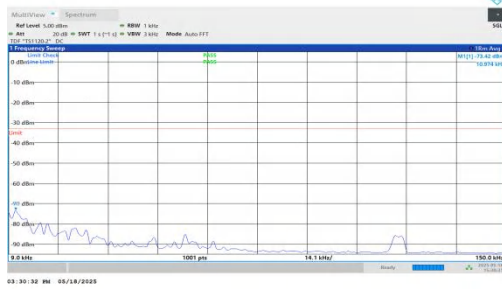
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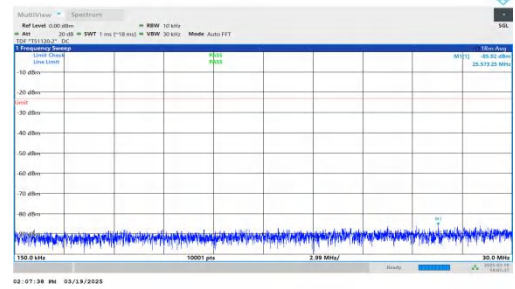
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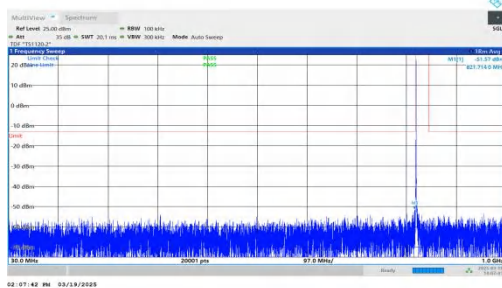
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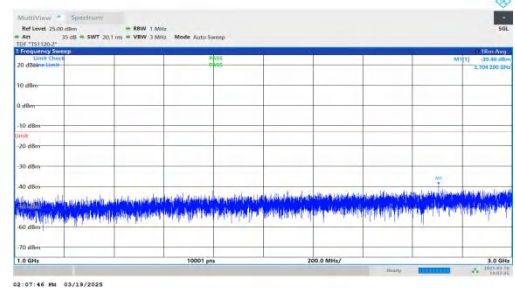
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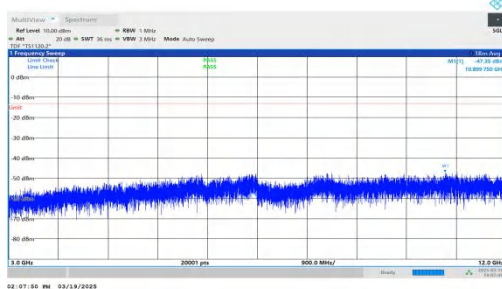
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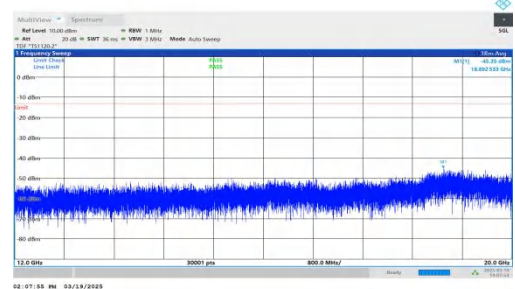
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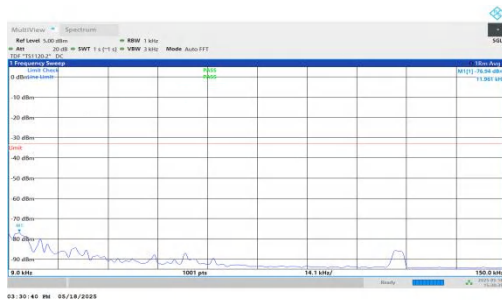
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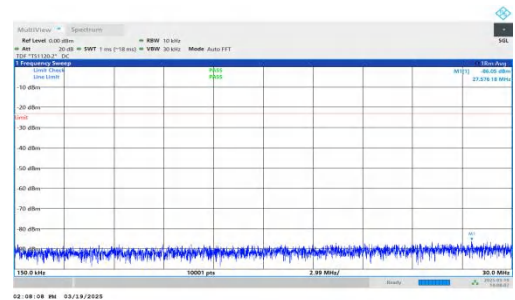
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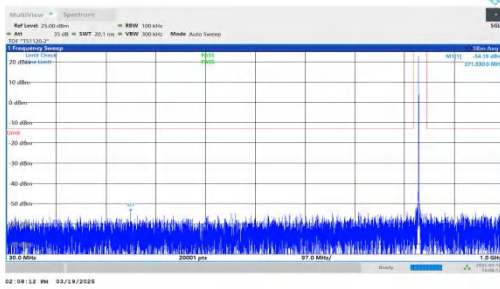
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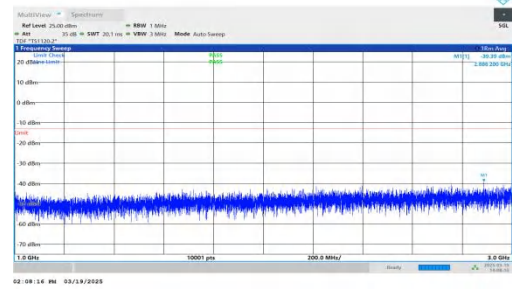
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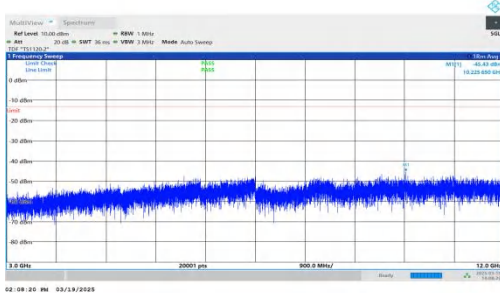
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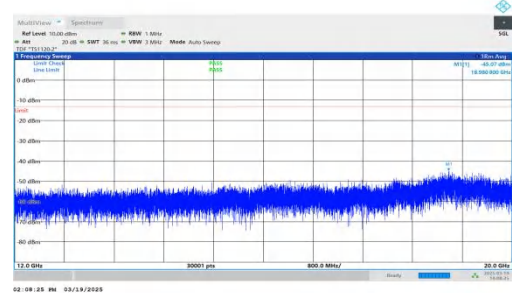
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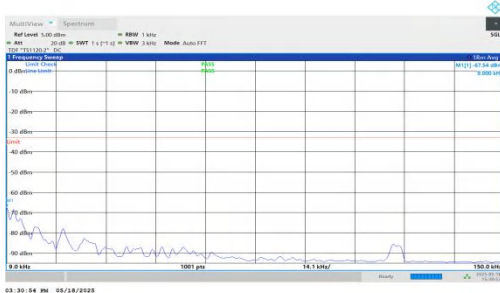
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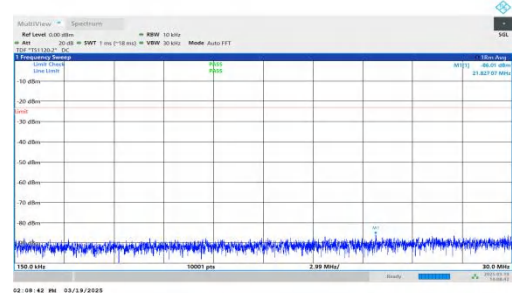
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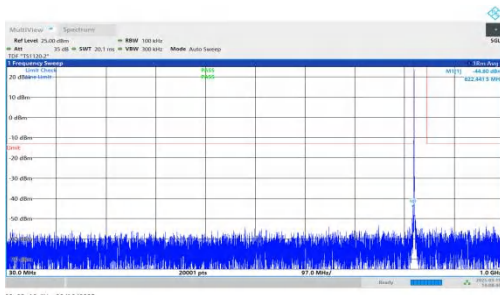
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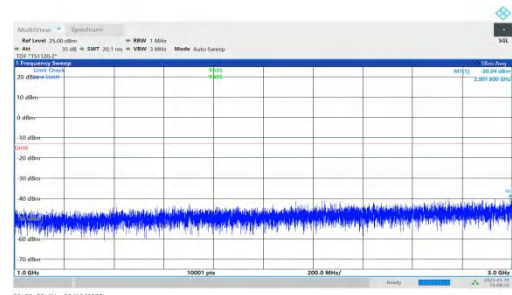
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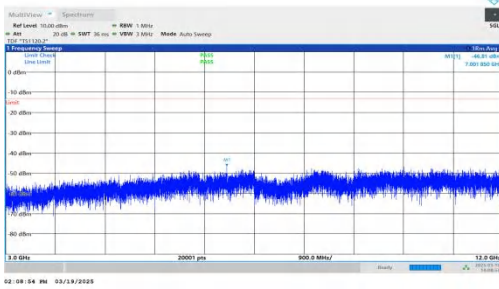
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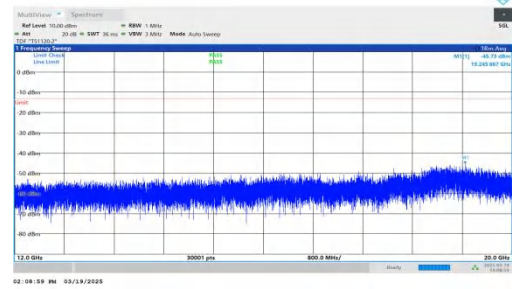
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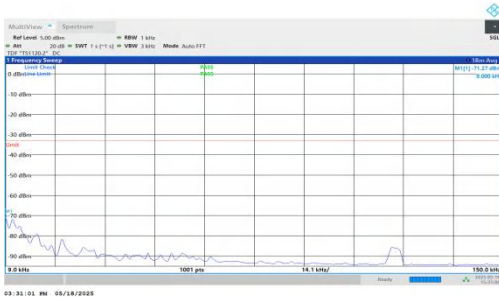
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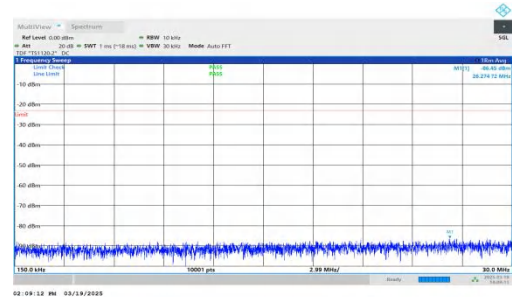
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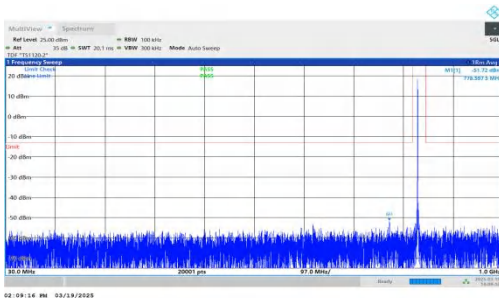
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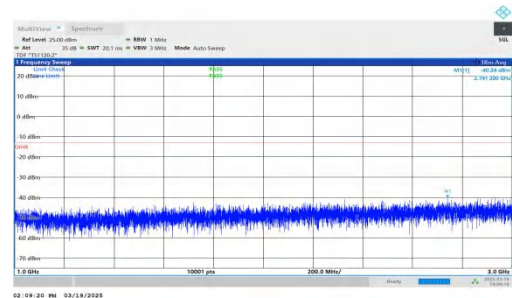
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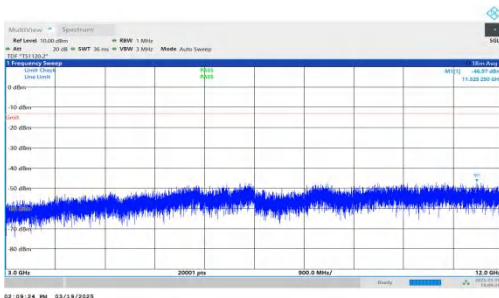
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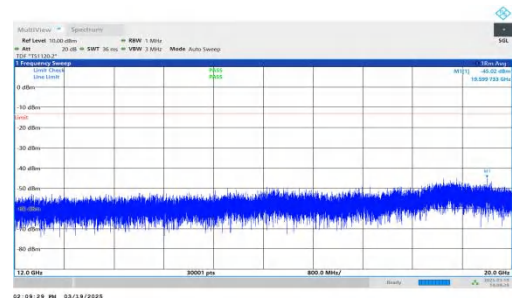
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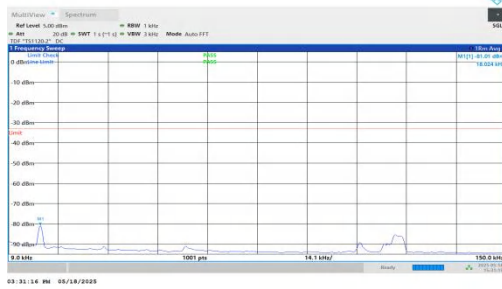
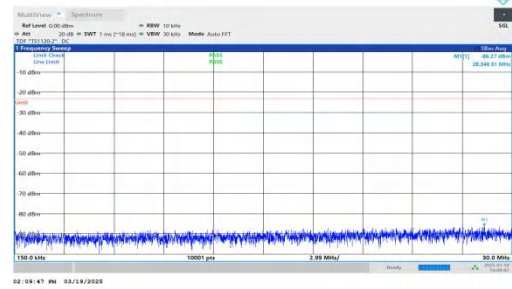
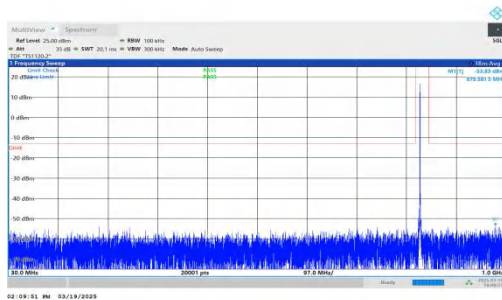
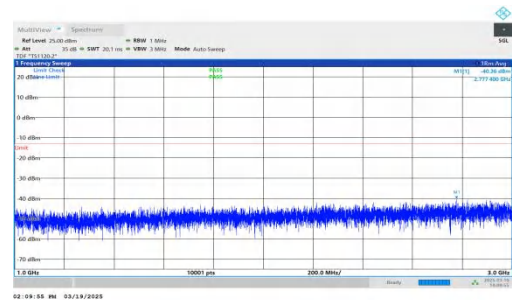
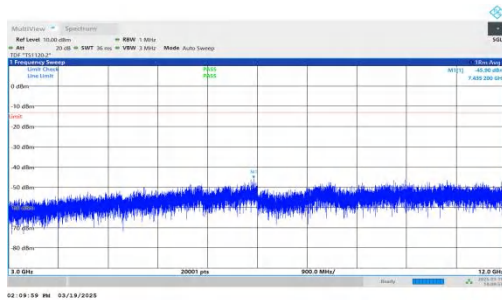
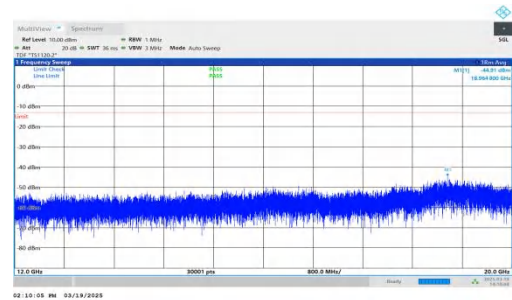
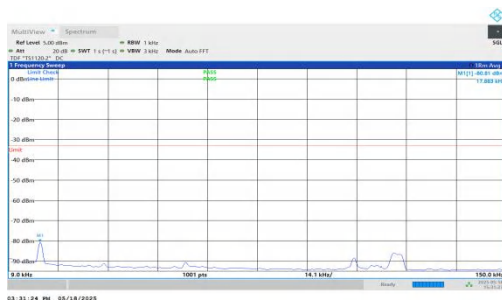
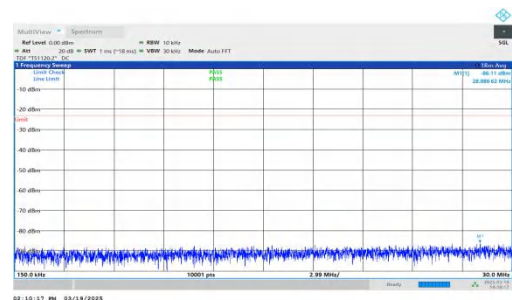
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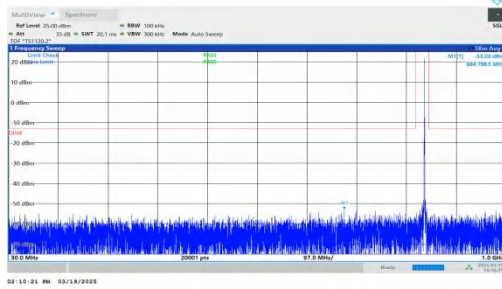


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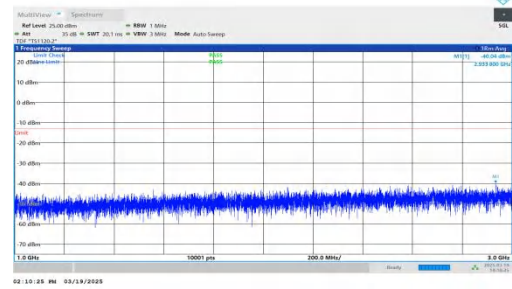


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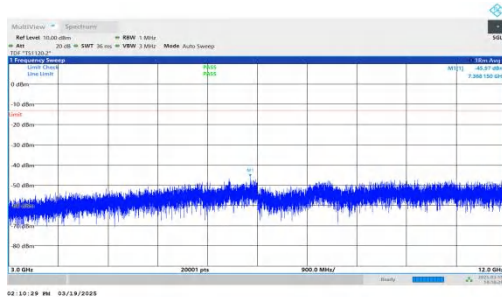

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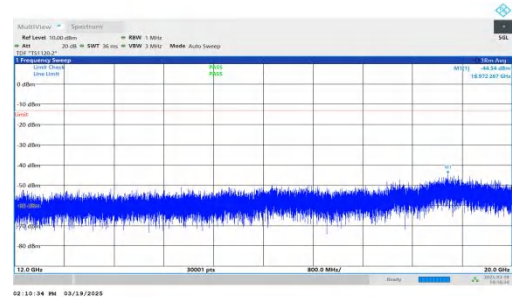
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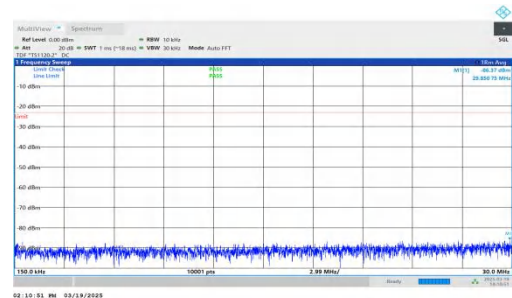
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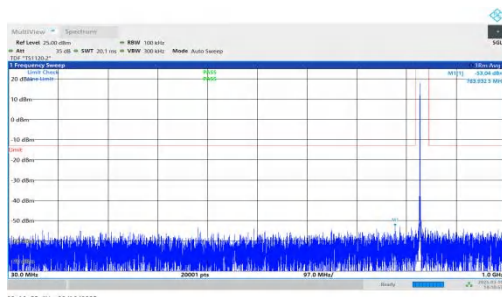
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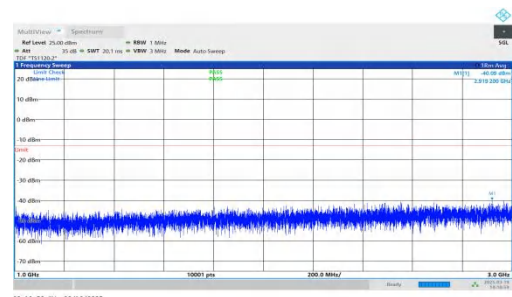
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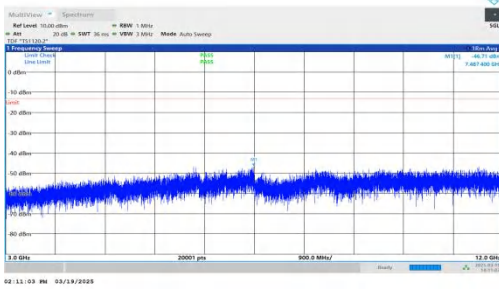
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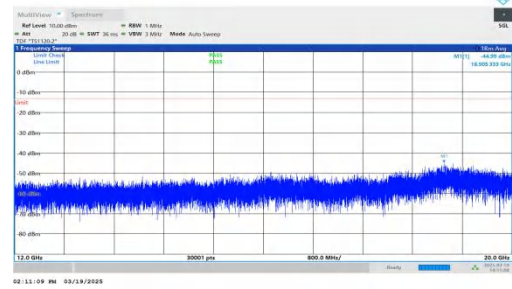
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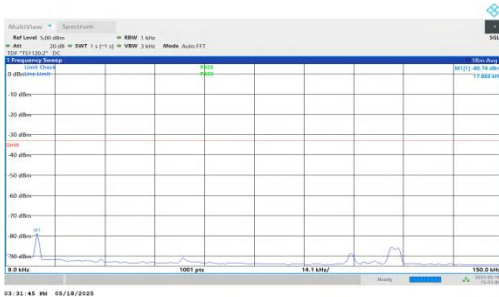
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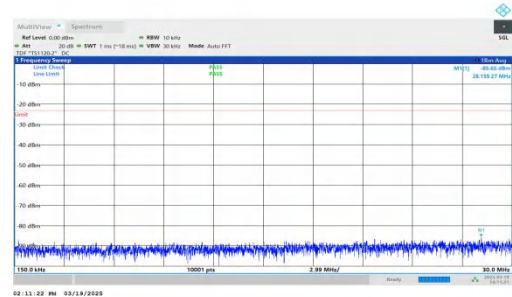
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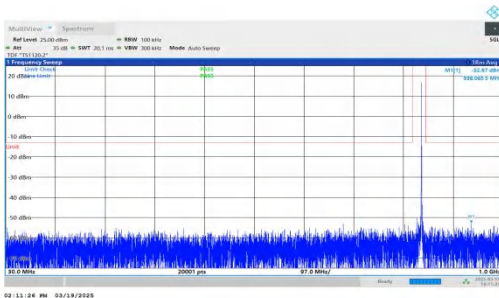
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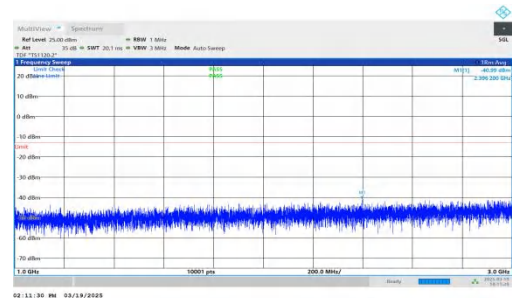
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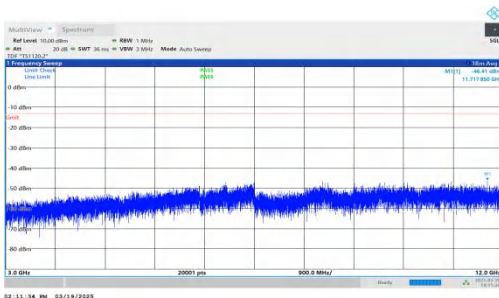
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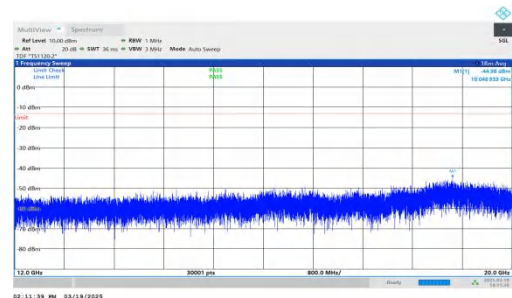
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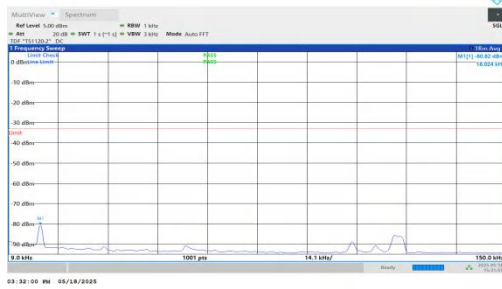
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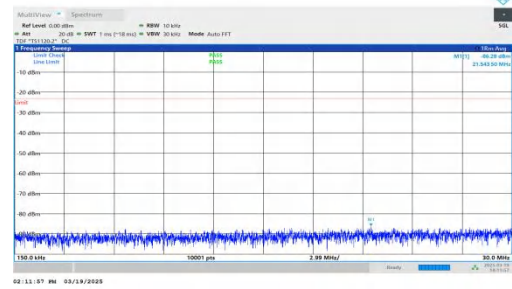
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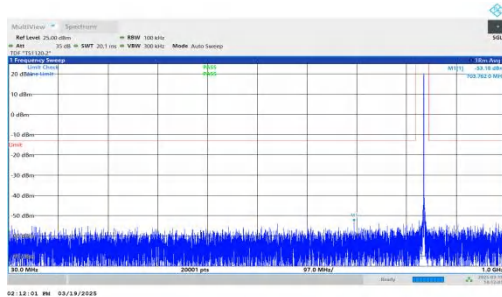
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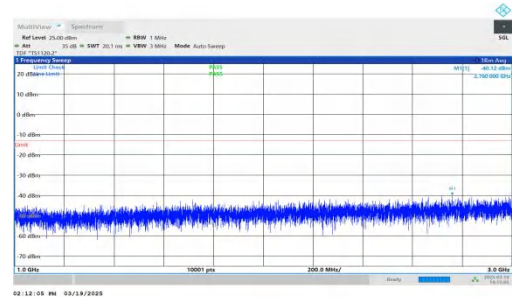
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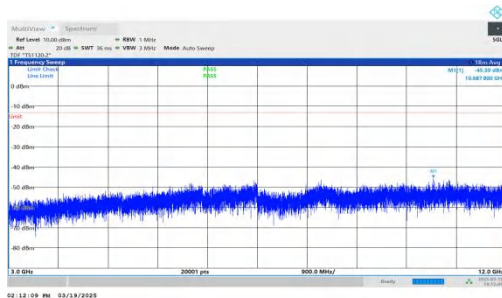
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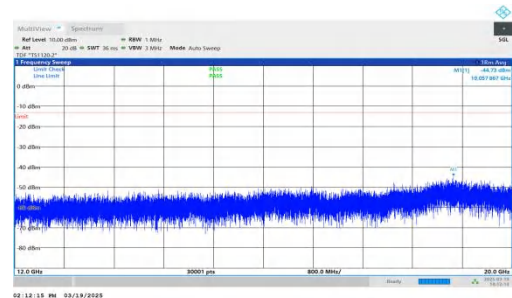
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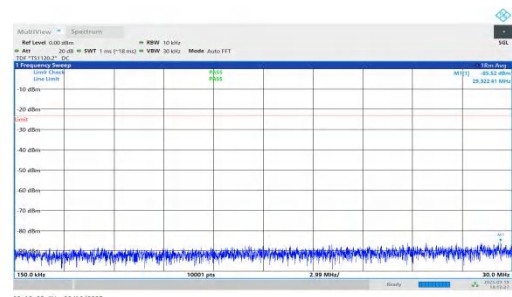
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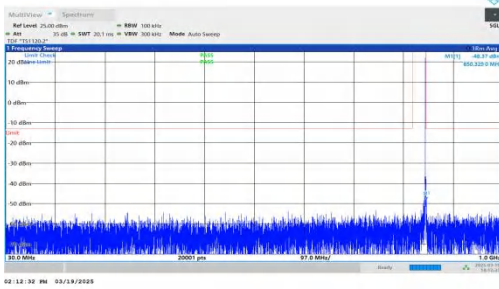
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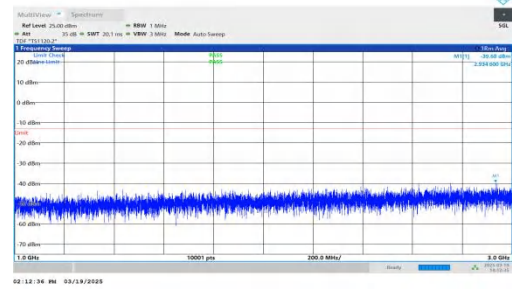
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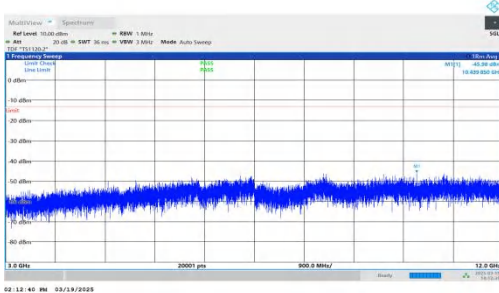
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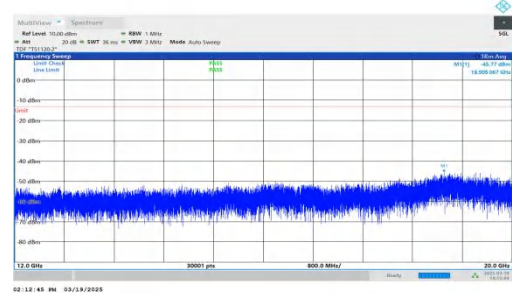
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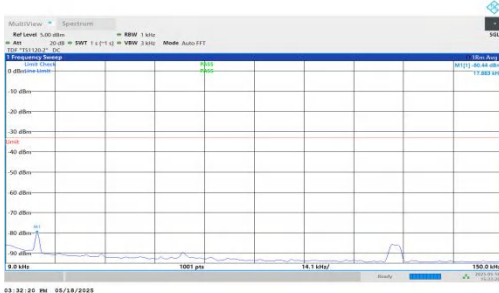
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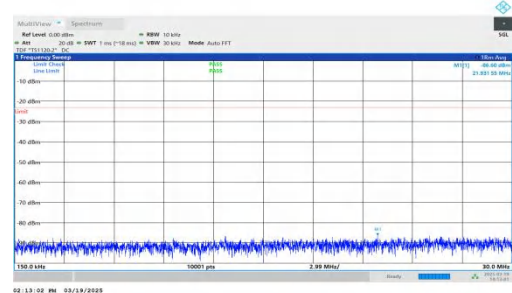
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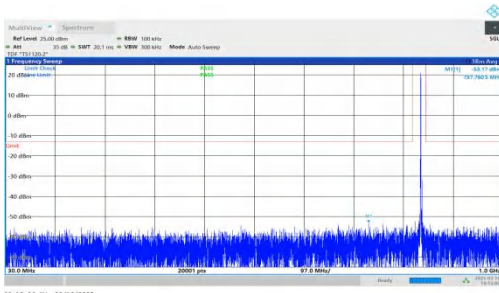
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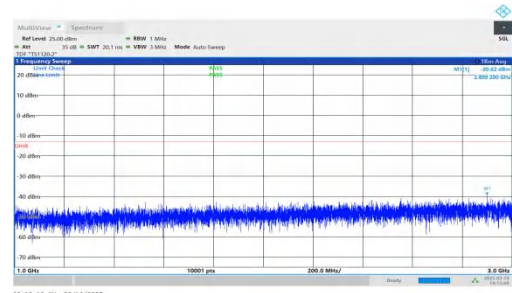
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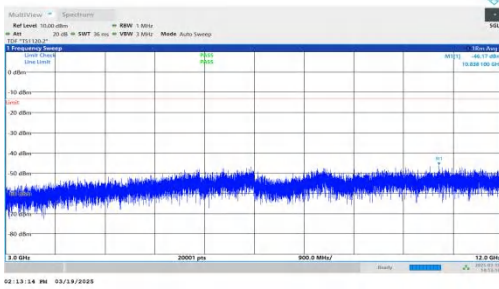
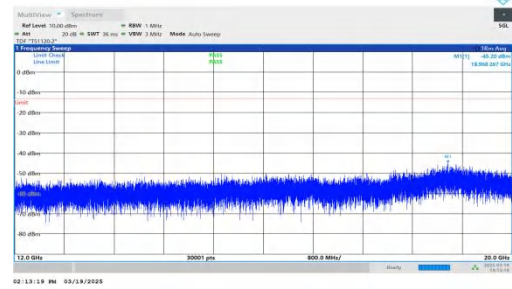
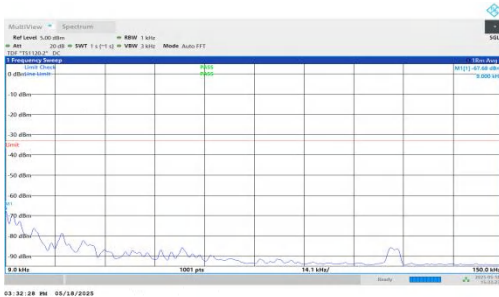
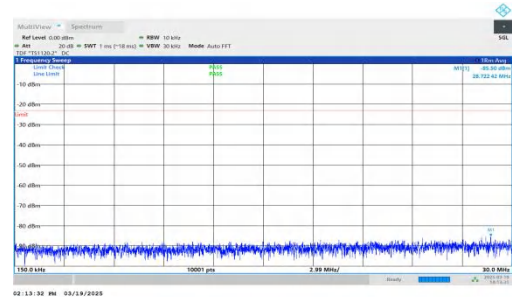
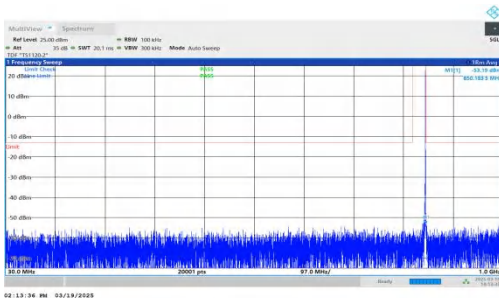
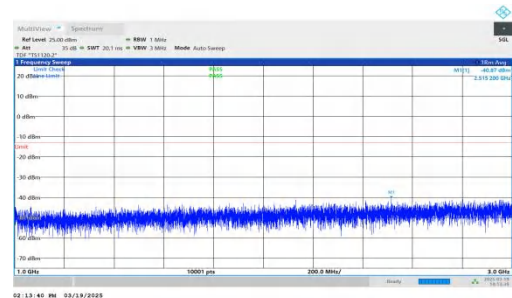
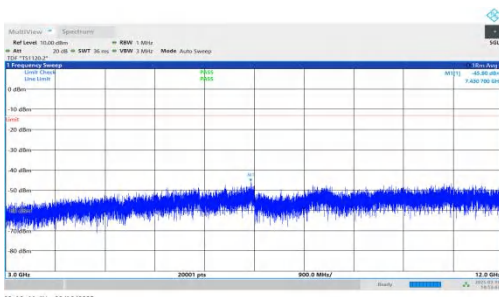
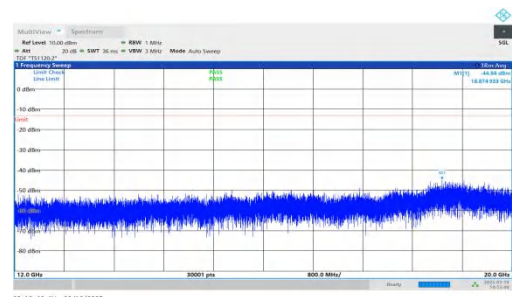
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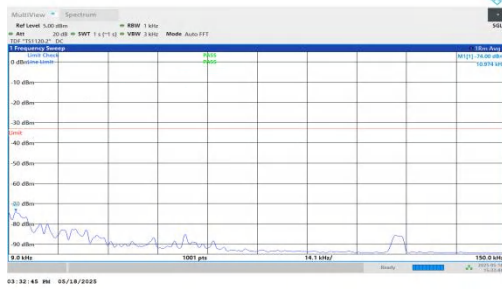


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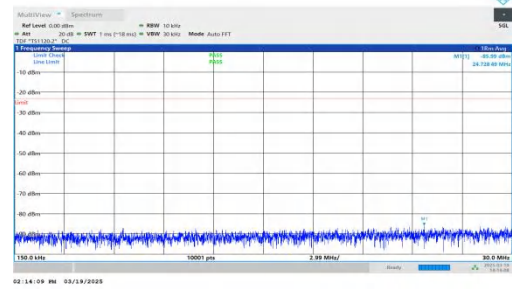


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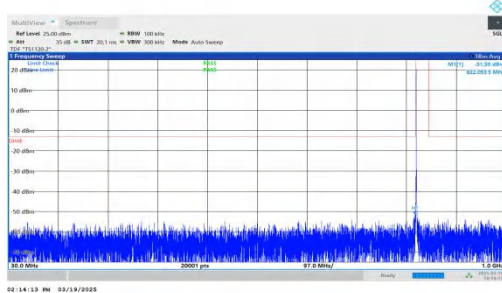

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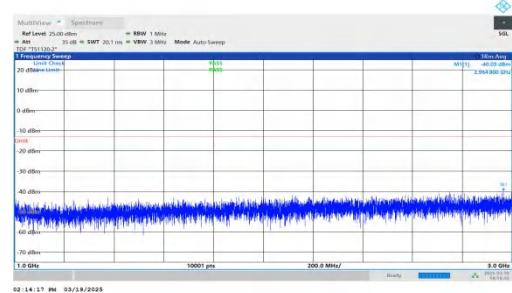
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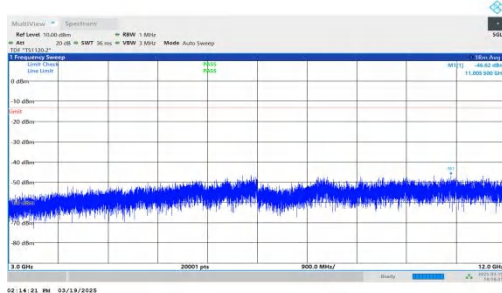
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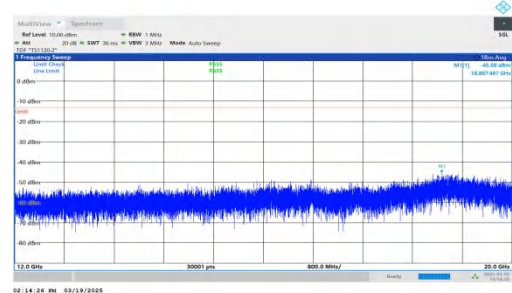
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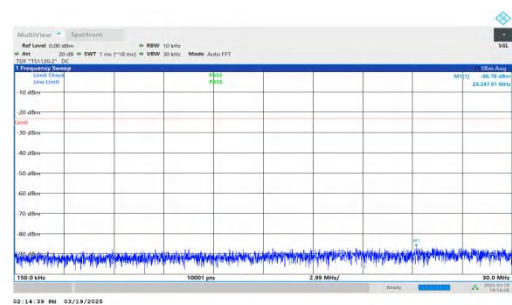
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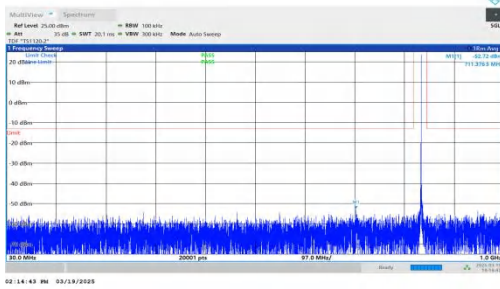
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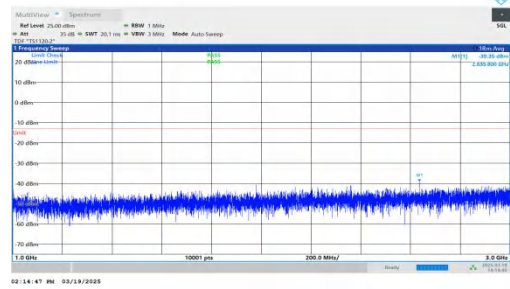
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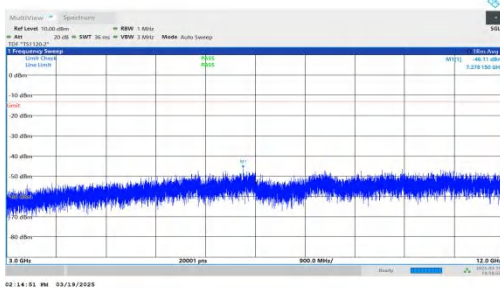
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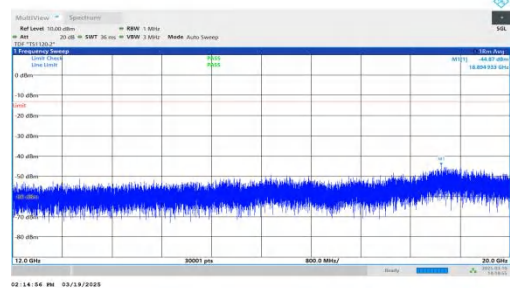
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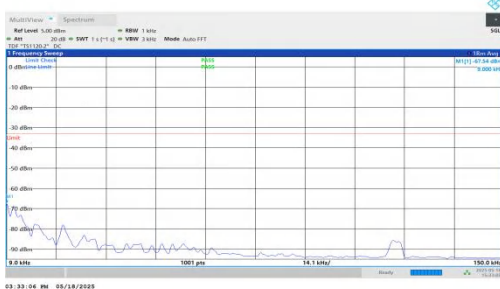
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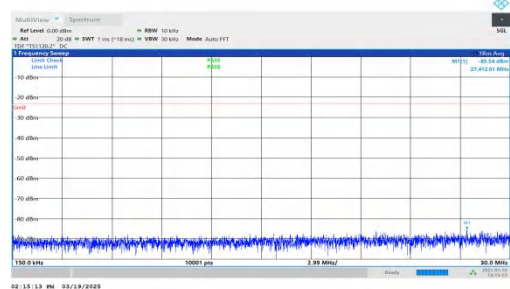
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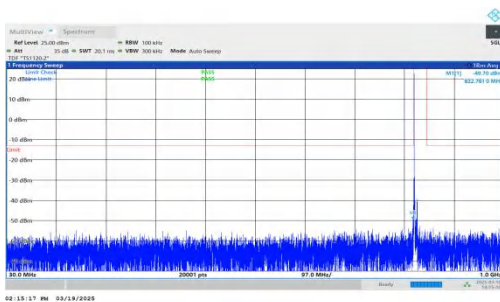
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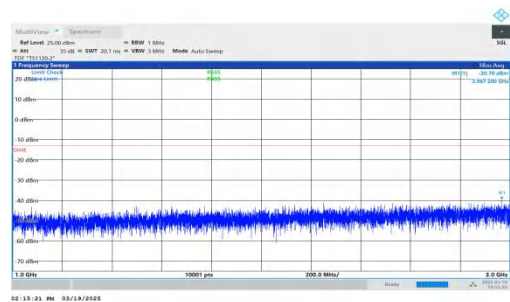
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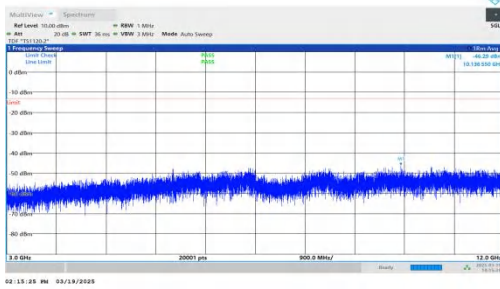
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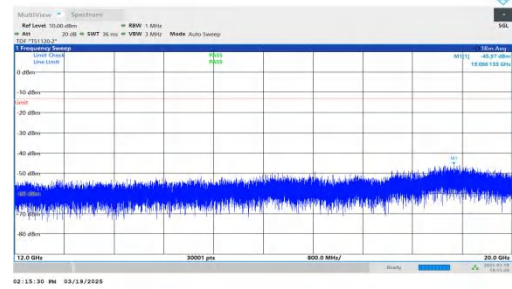
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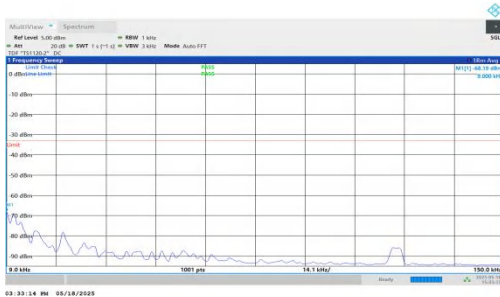
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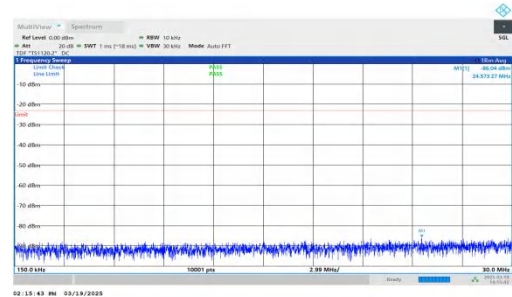
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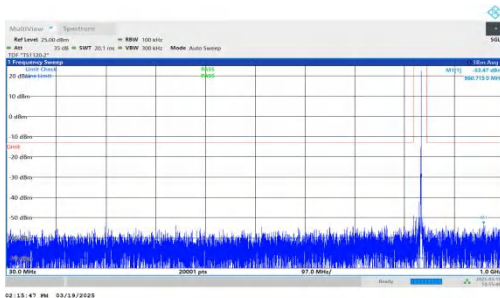
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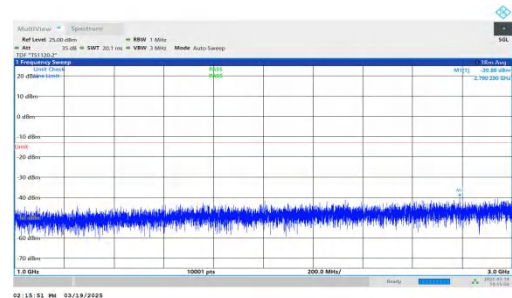
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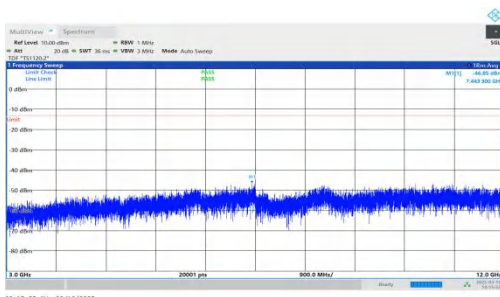
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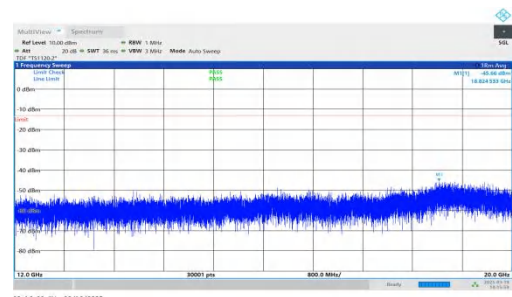
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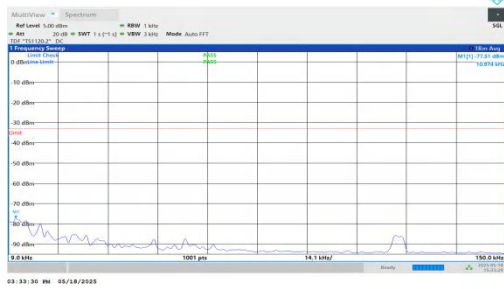
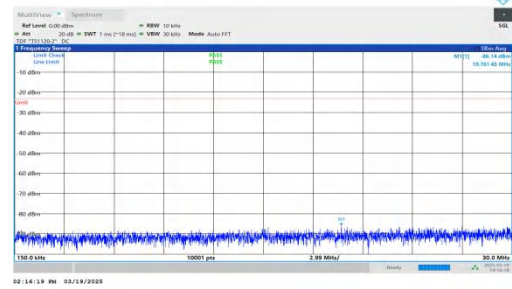
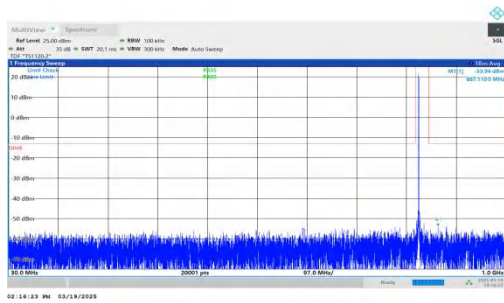
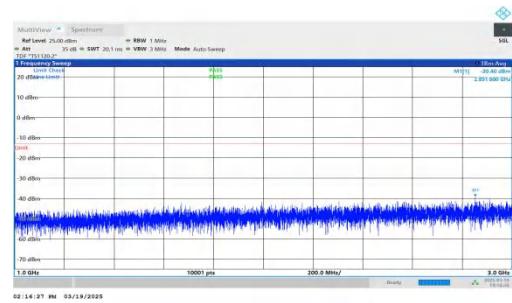
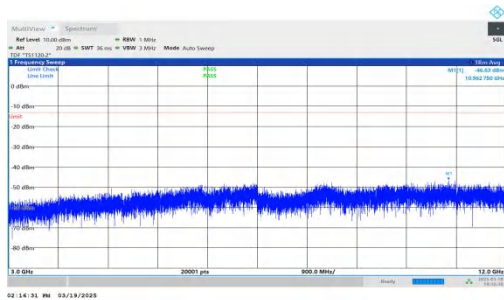
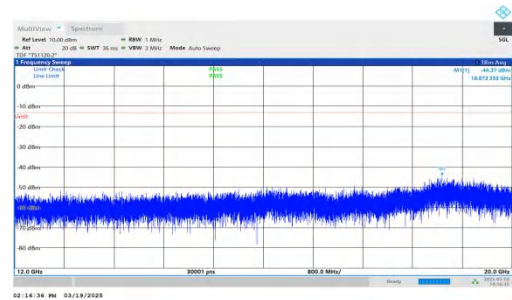
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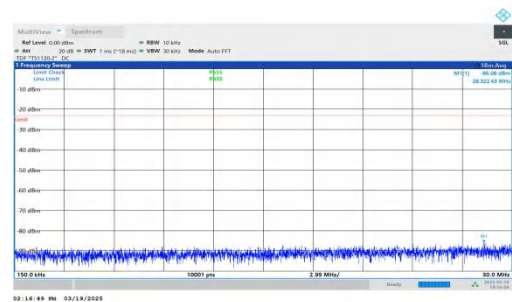


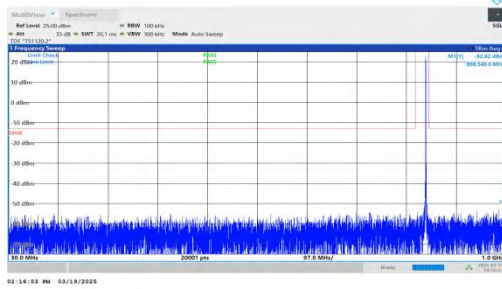
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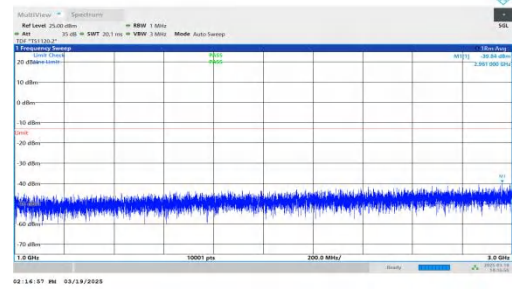
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ner_1RB_Left-3000-12000-PASS

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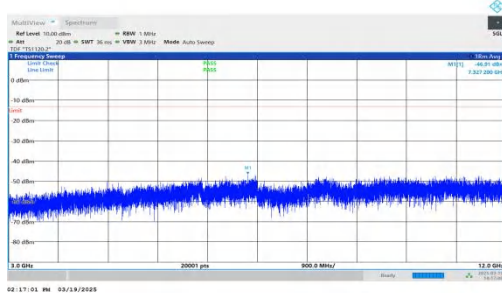
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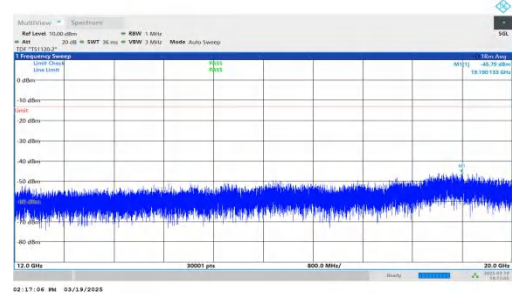
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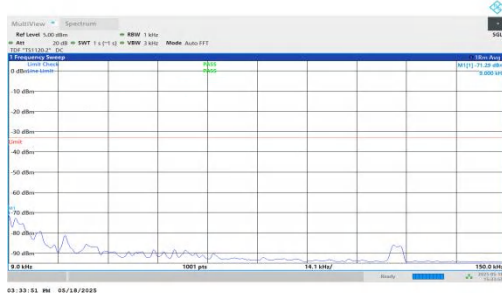
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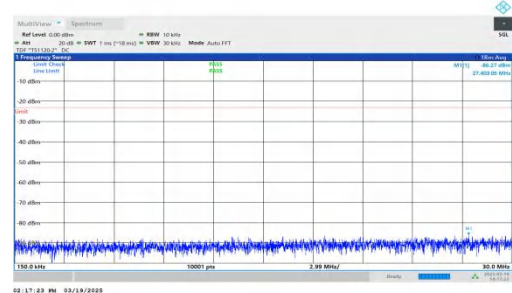
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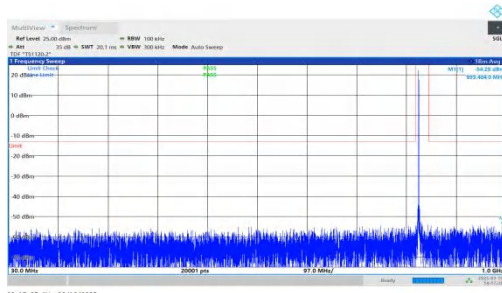
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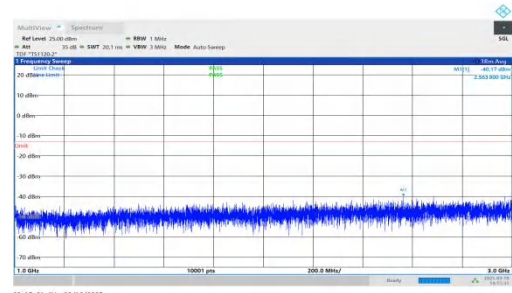
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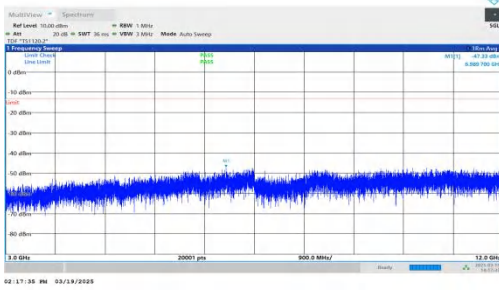
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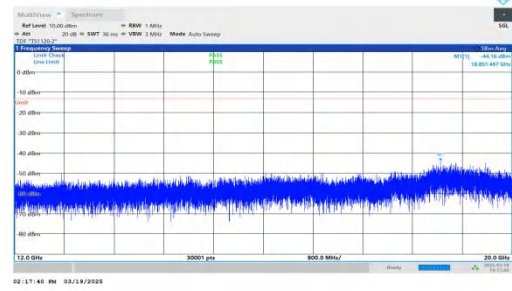
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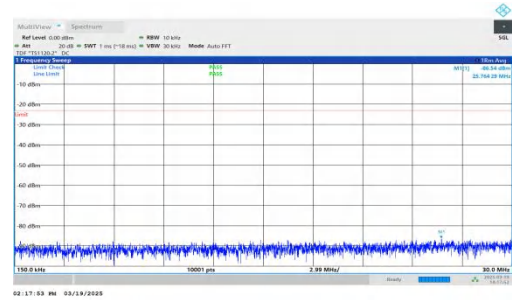
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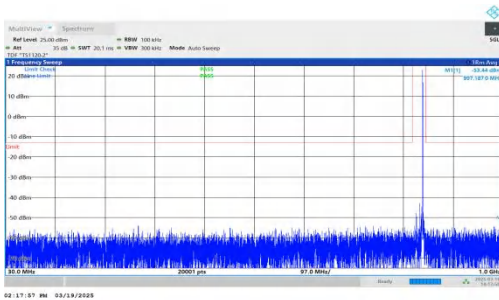
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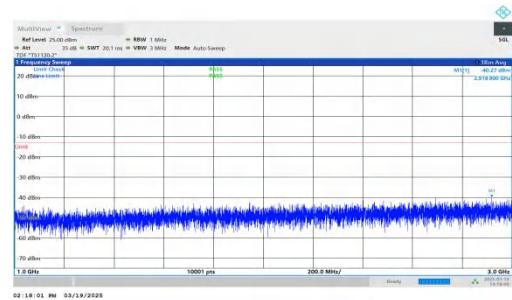
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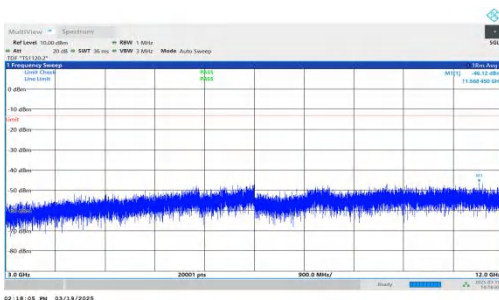
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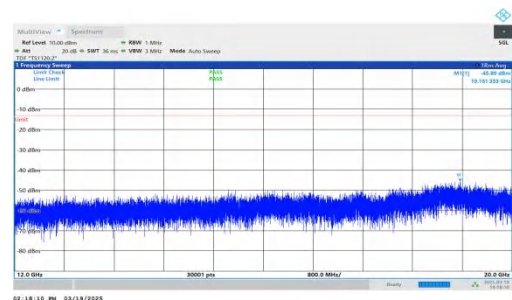
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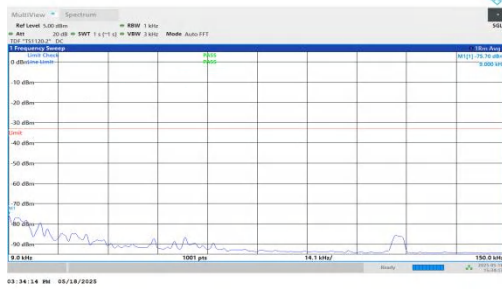
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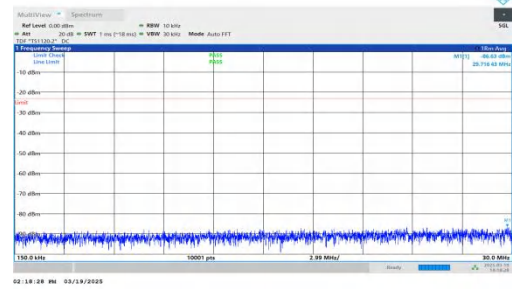
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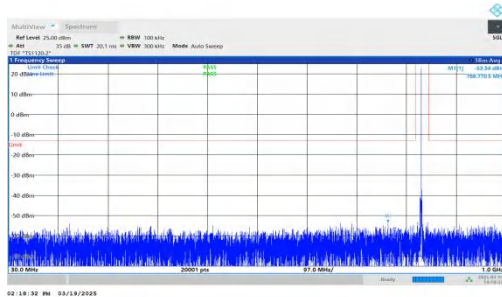
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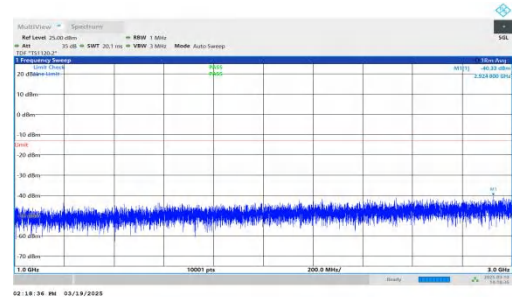
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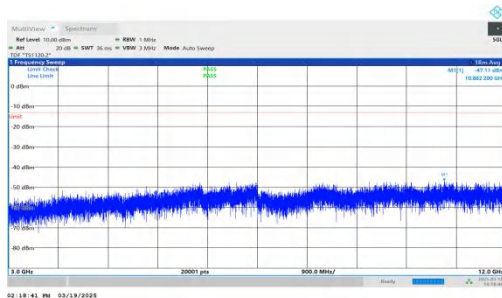
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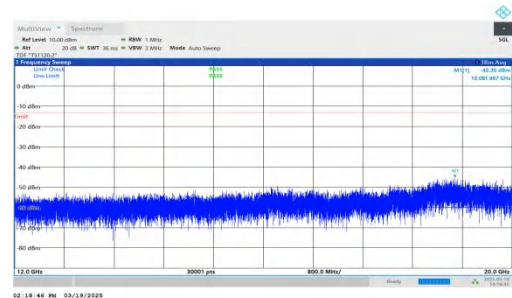
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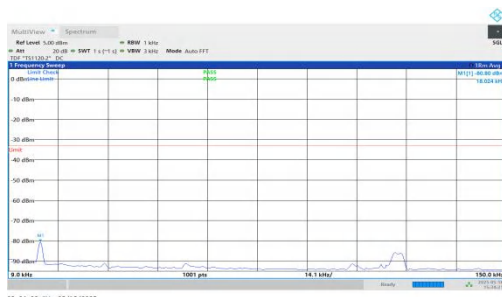
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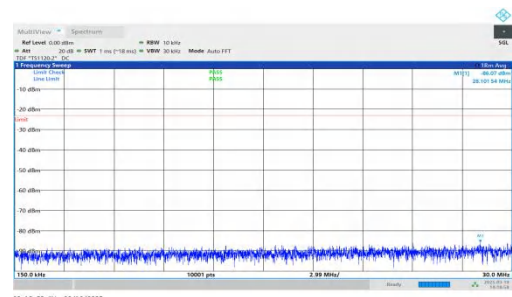
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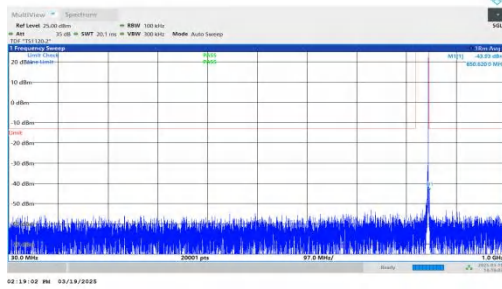
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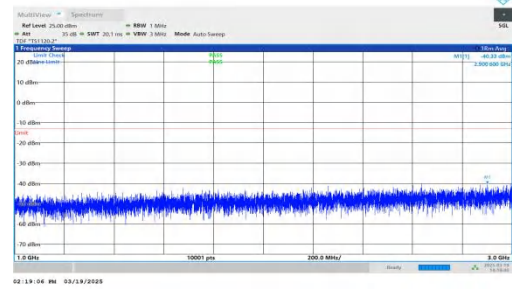
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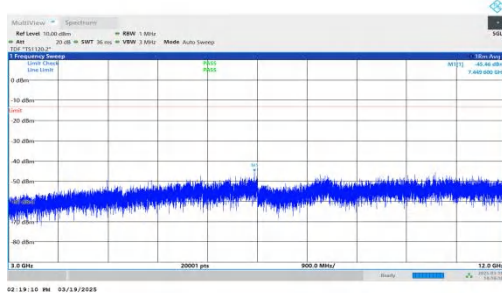
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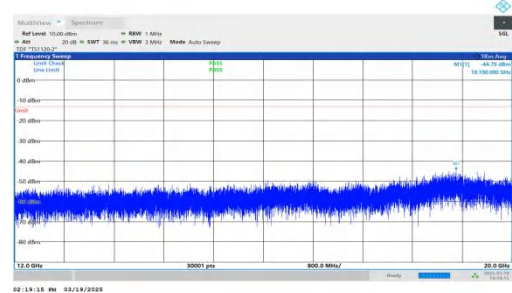
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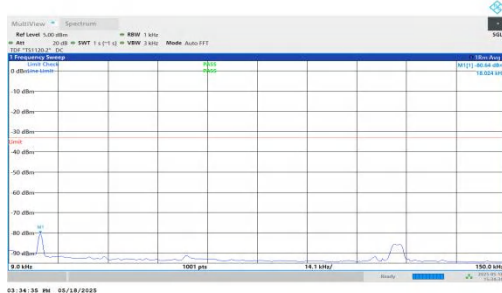
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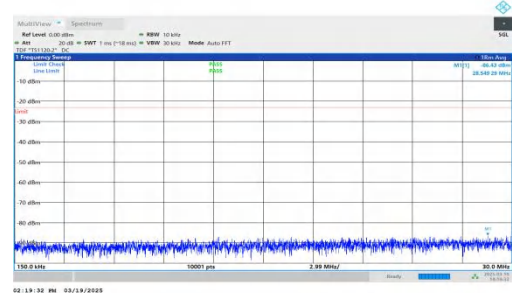
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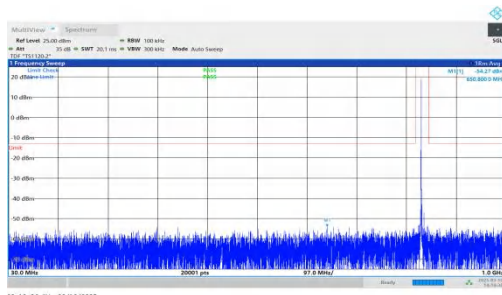
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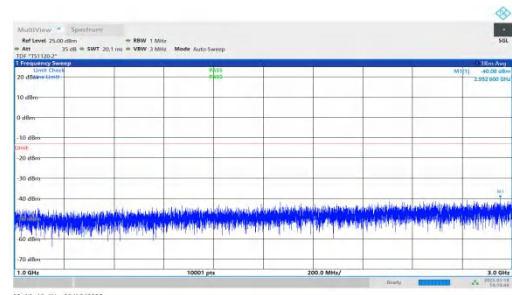
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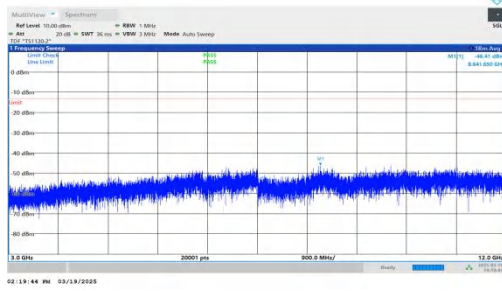
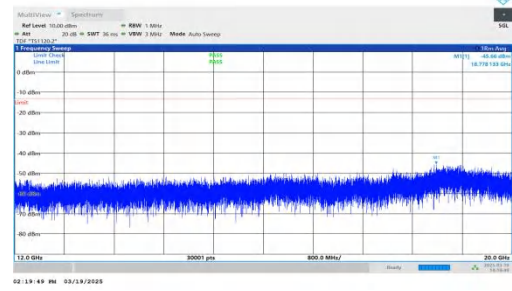
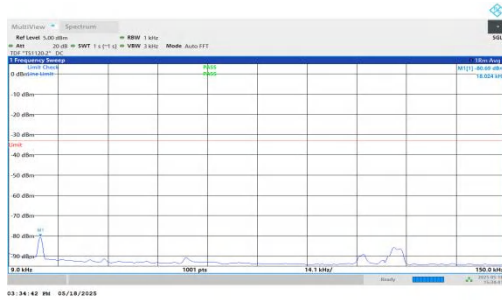
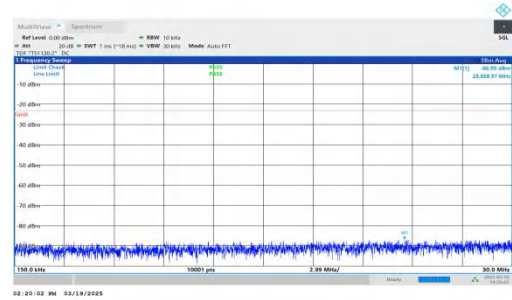
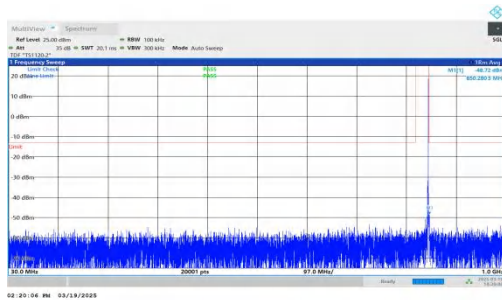
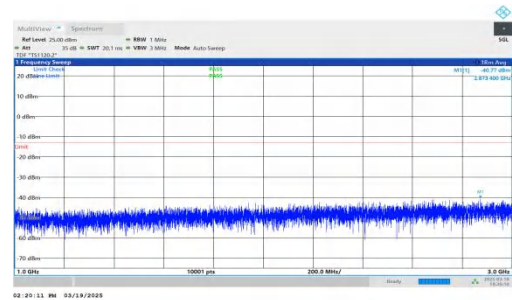
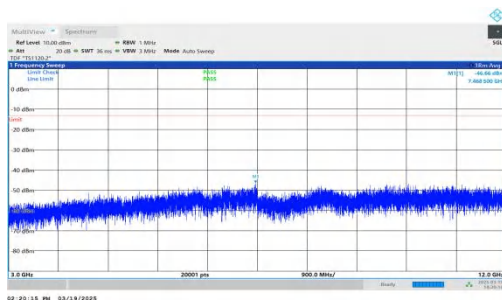
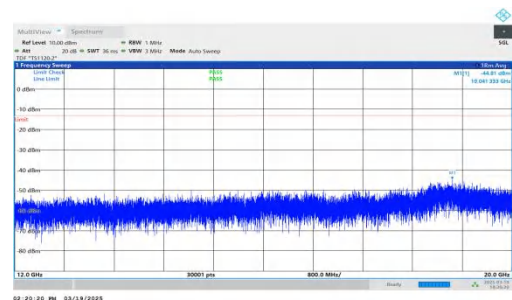
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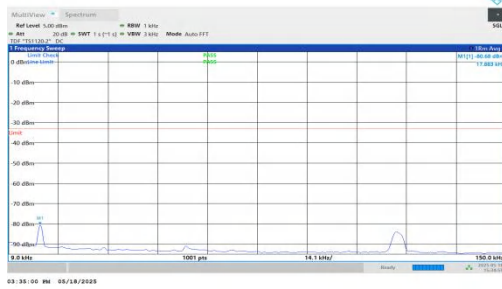


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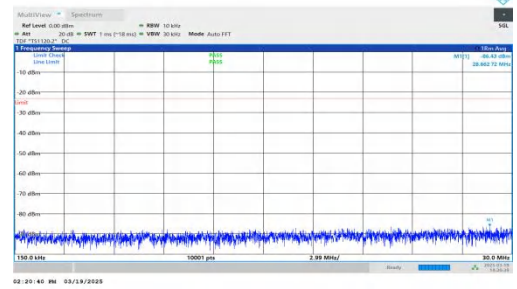


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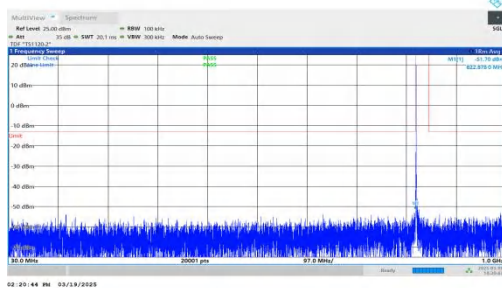

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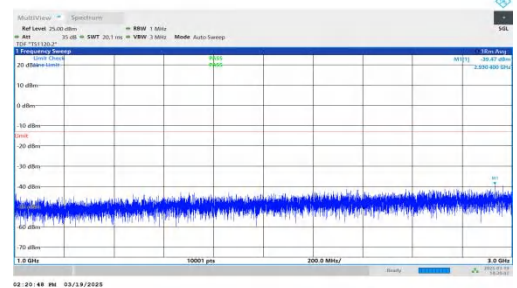
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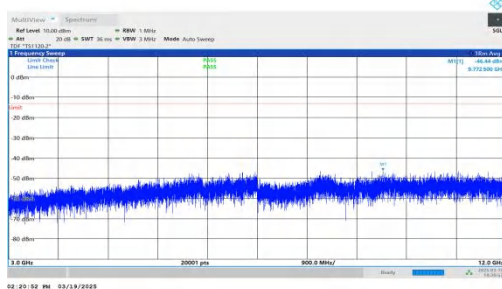
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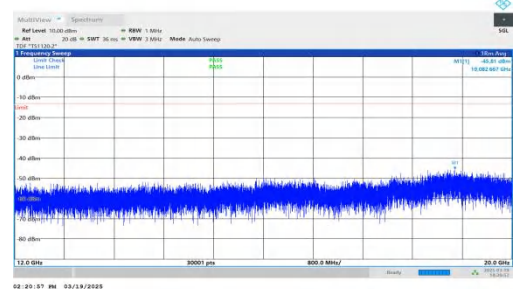
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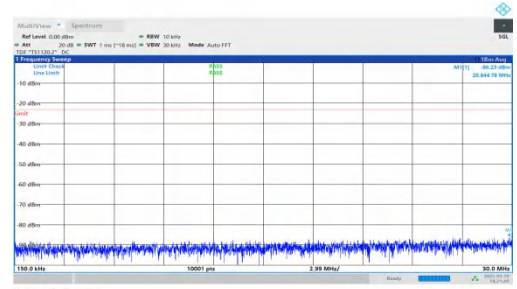
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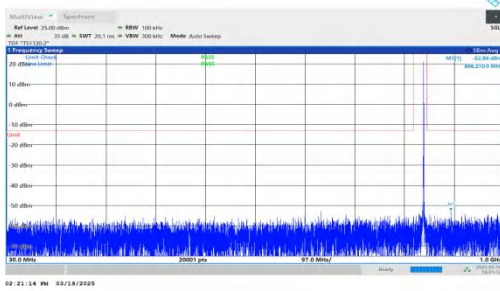
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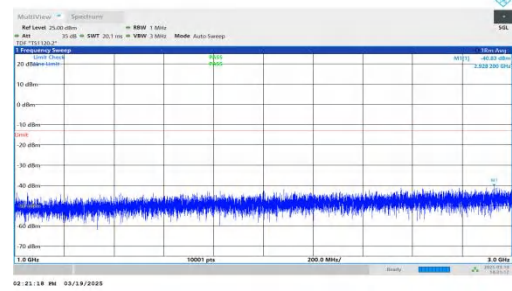
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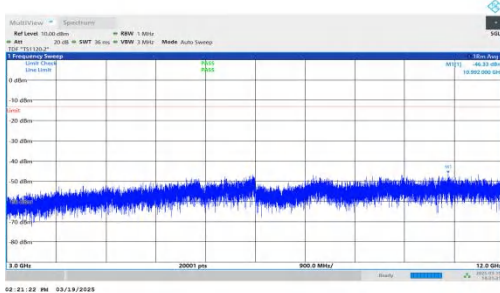
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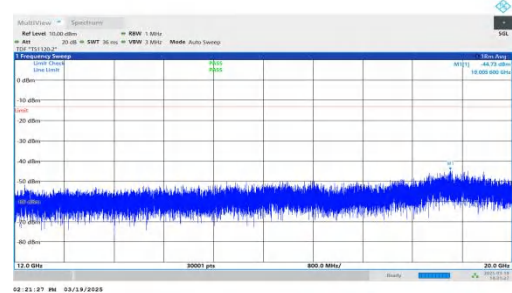
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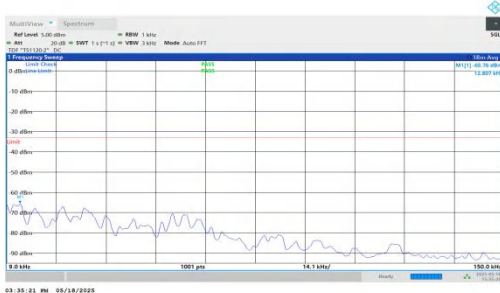
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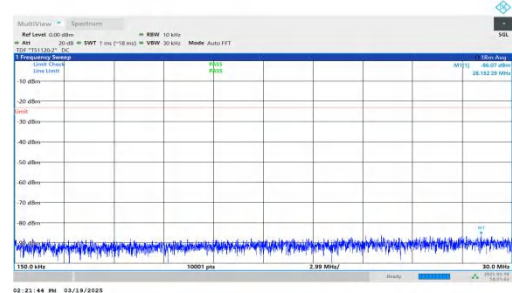
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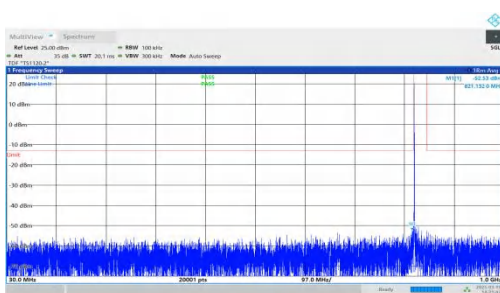
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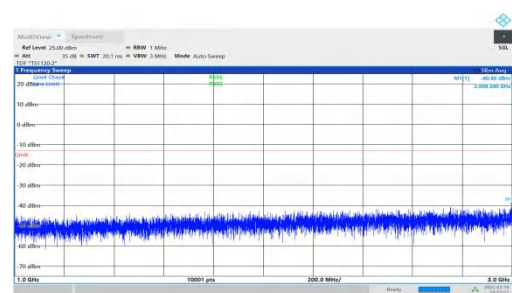
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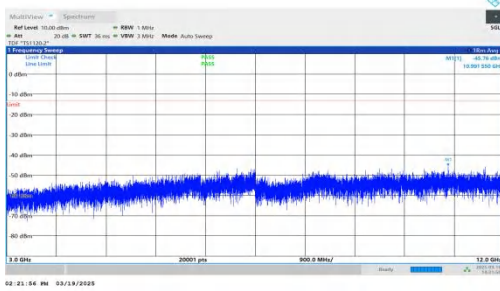
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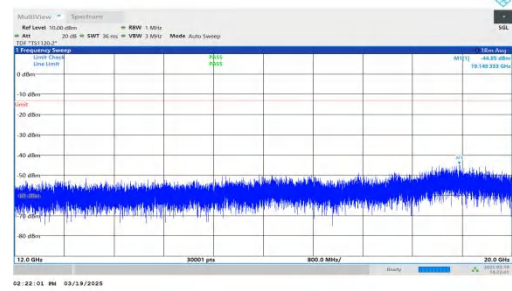
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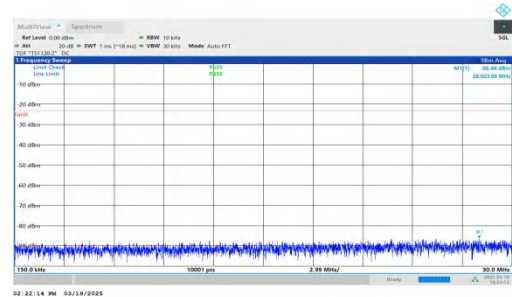
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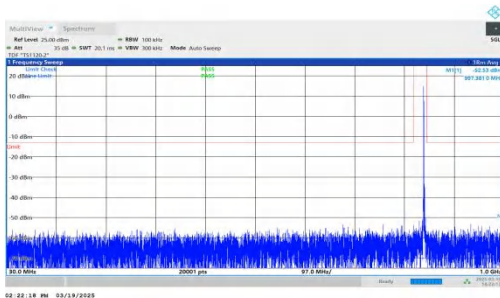
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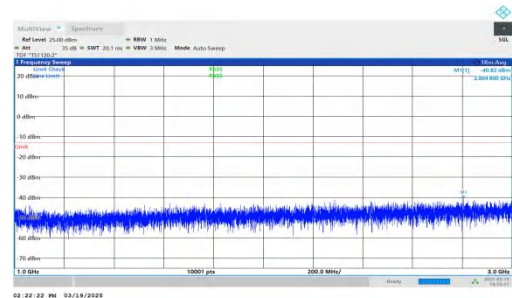
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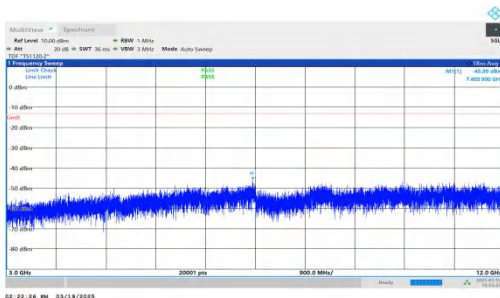
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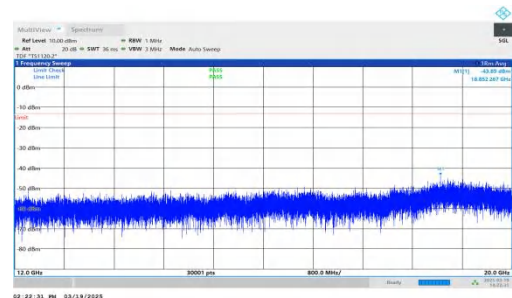
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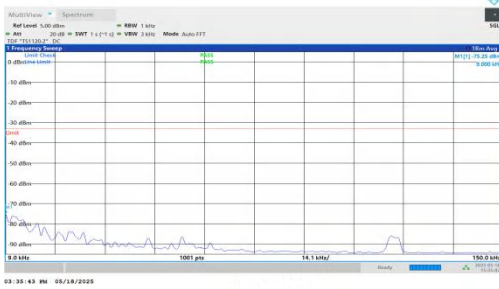
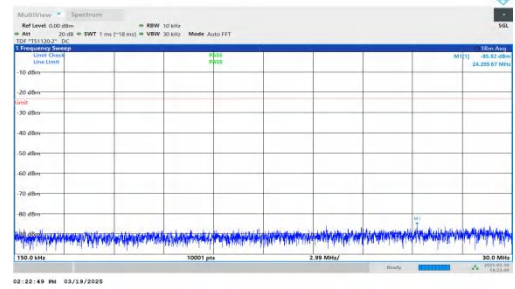
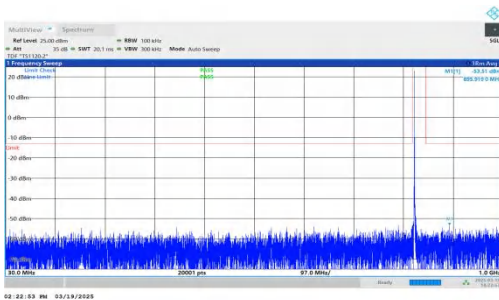
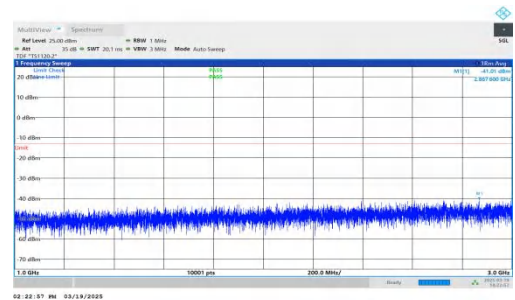
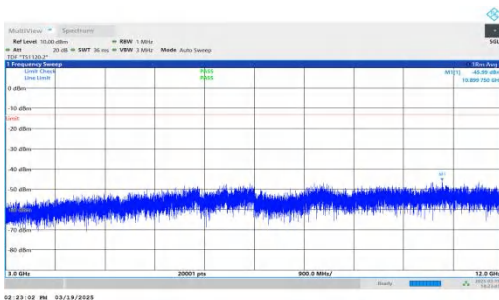
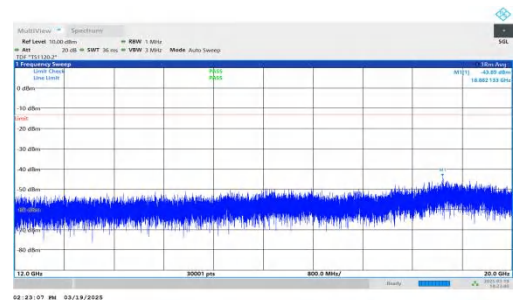
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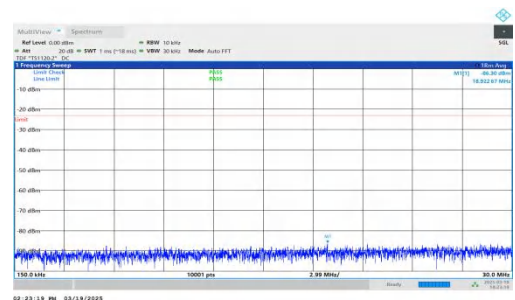


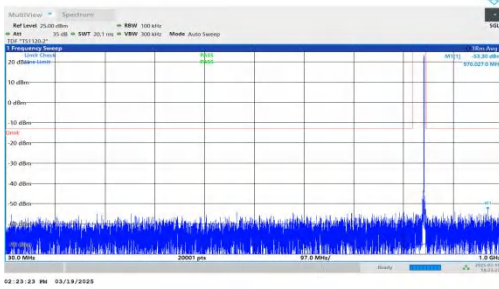
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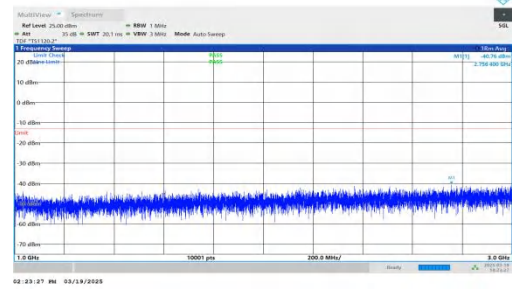
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ner_1RB_Left-0.009-0.15-PASS

NTNV-DC_12A-n5A-PC3-15-5+20-M+M-QPSK-IDLE-DFT-BPSK-In
ner_1RB_Left-0.15-30-PASS

NTNV-DC_12A-n5A-PC3-15-5+20-M+M-QPSK-IDLE-DFT-BPSK-In
ner_1RB_Left-30-1000-PASS

NTNV-DC_12A-n5A-PC3-15-5+20-M+M-QPSK-IDLE-DFT-BPSK-In
ner_1RB_Left-1000-3000-PASS

NTNV-DC_12A-n5A-PC3-15-5+20-M+M-QPSK-IDLE-DFT-BPSK-In
ner_1RB_Left-3000-12000-PASS

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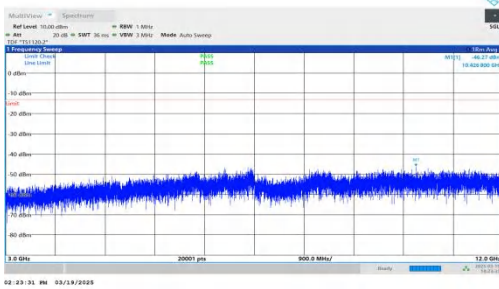
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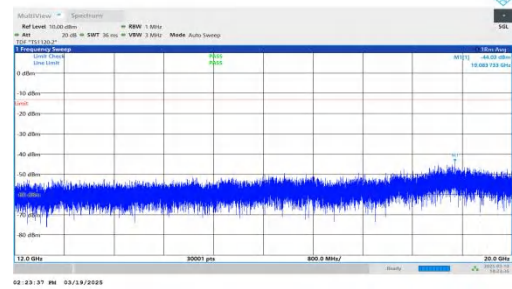
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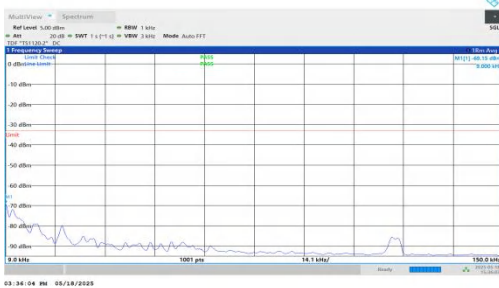
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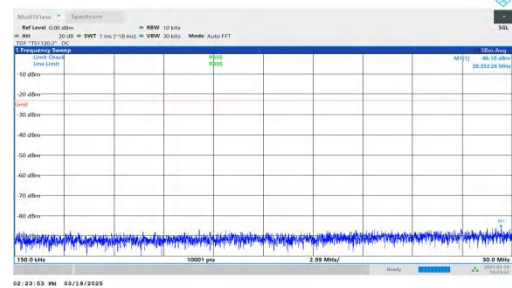
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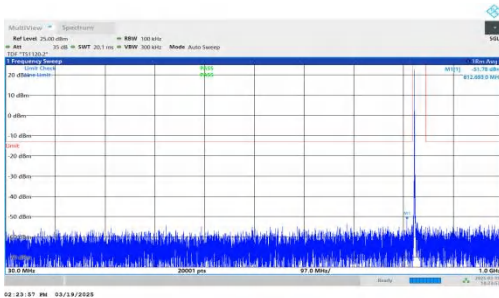
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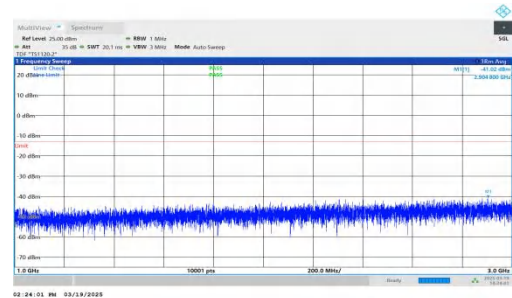
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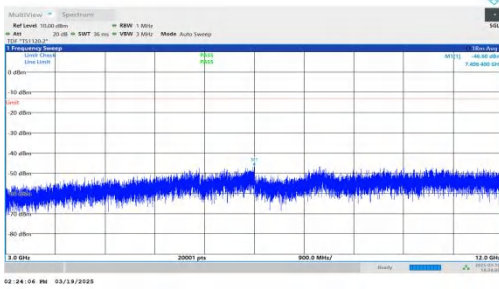
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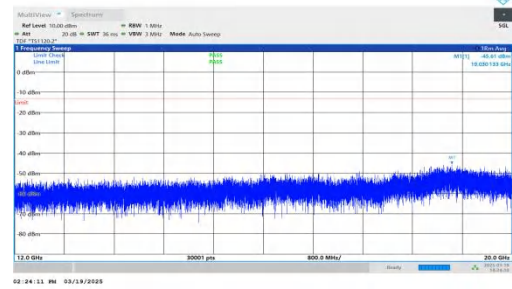
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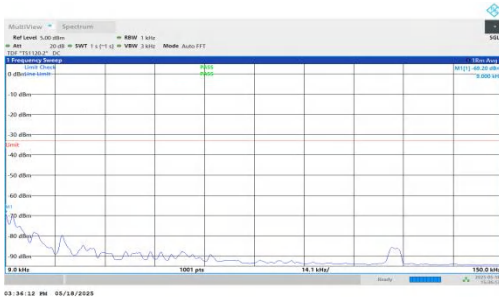
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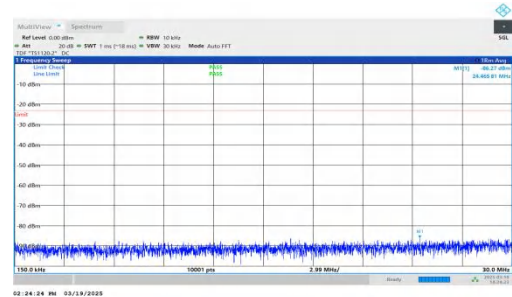
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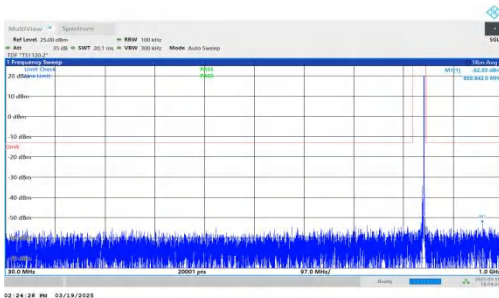
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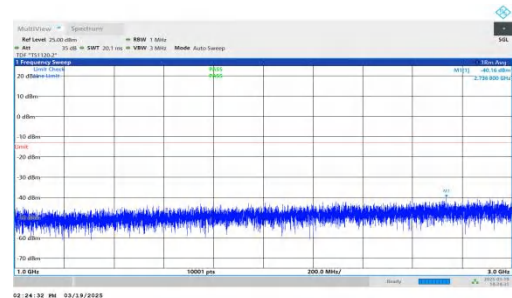
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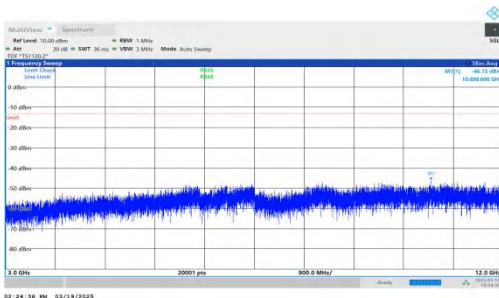
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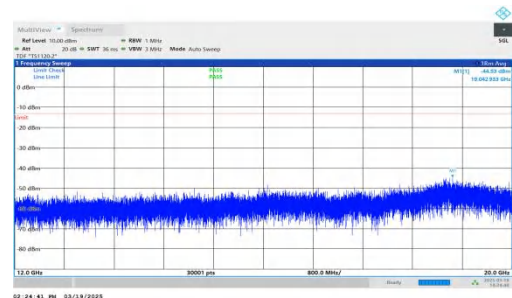
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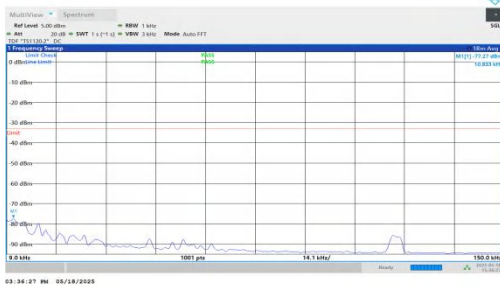
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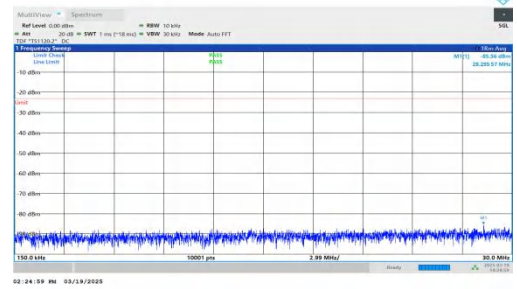
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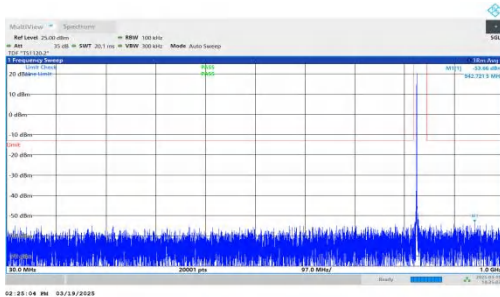
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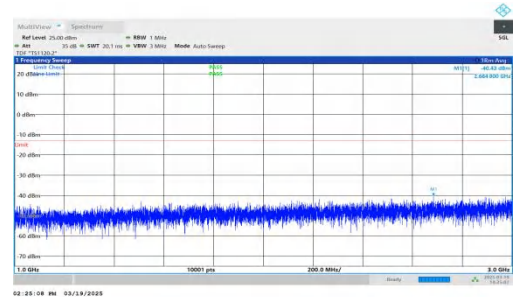
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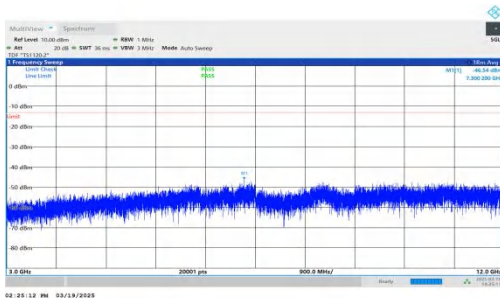
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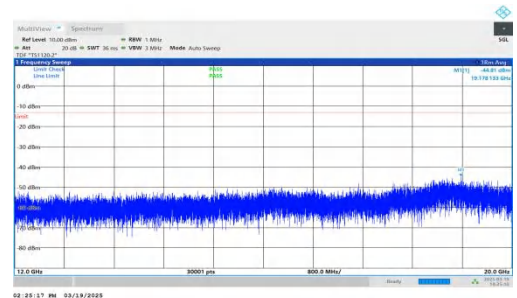
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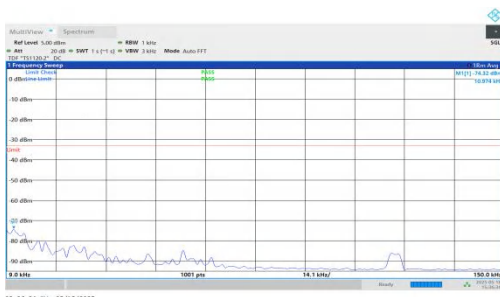
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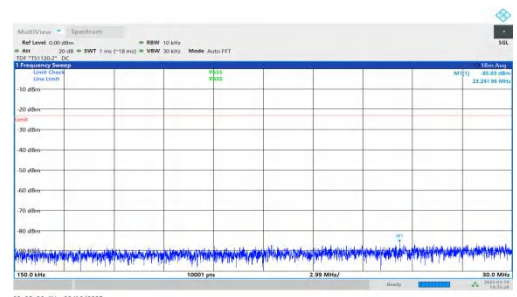
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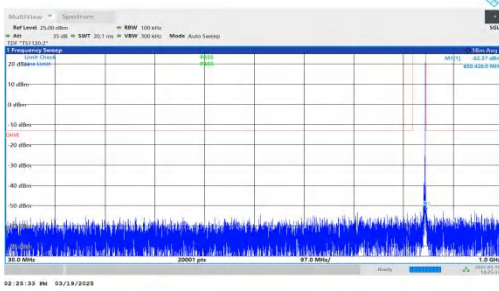
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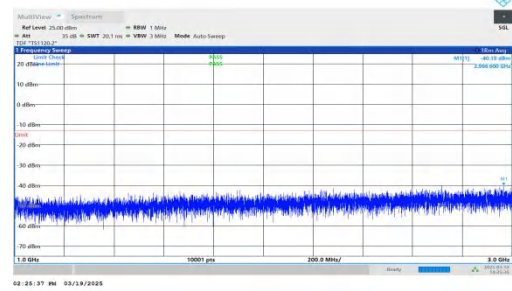
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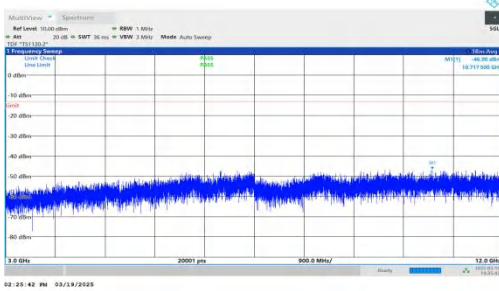
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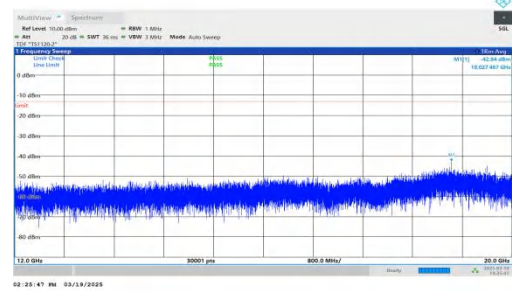
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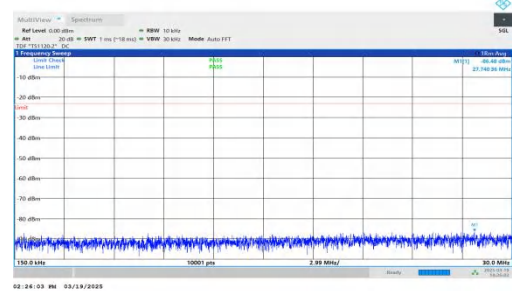
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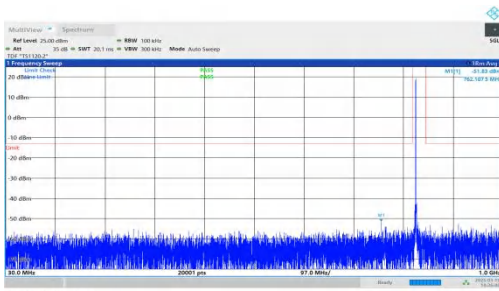
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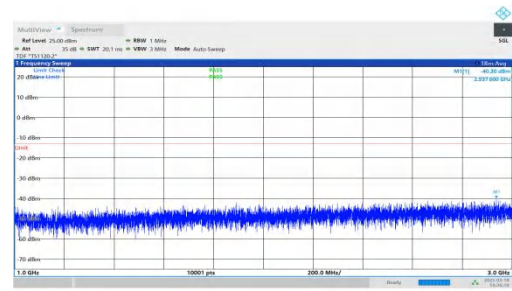
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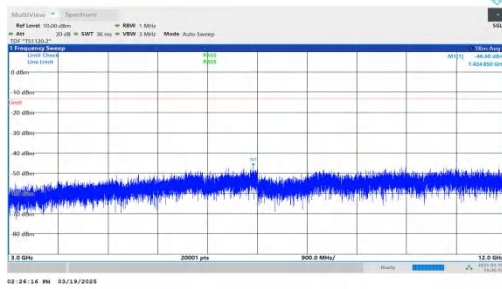
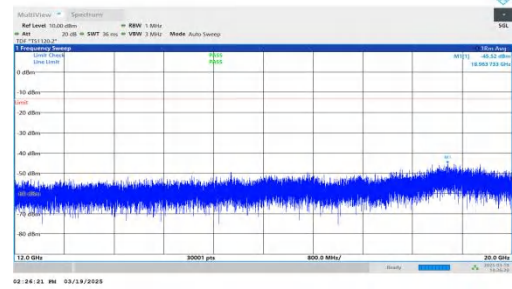
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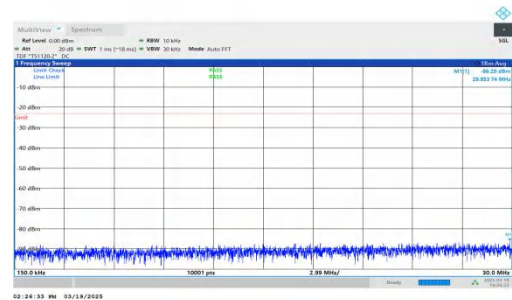
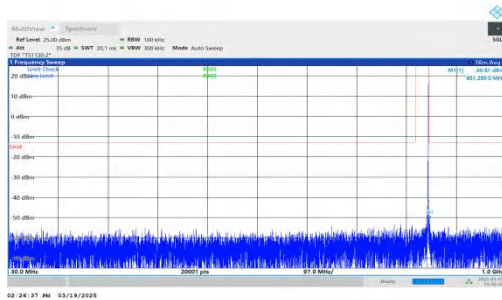
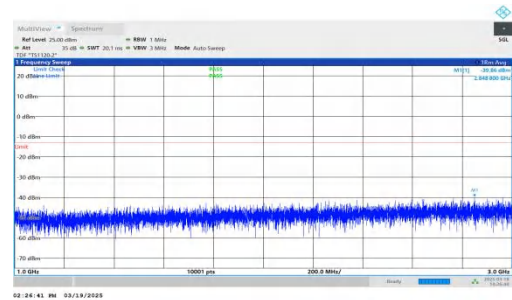
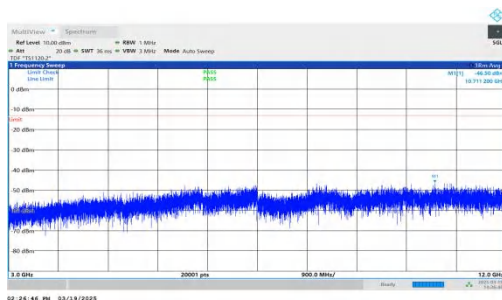
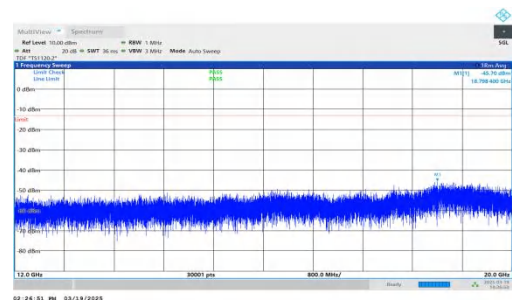


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NTNV-DC_12A-n5A-PC3-15-5+20-M+H-QPSK-IDLE-DFT-QPSK-In
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NTNV-DC_12A-n5A-PC3-15-5+20-M+H-QPSK-IDLE-DFT-QPSK-In
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ner_1RB_Right-3000-12000-PASS

NTNV-DC_12A-n5A-PC3-15-5+20-M+H-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Right-12000-20000-PASS

Frequency Stability for ENDC

Test Result

Voltage											
Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Voltage (Vdc)	Temperature(°C)	Deviation(Hz)	Deviation (ppm)	Limit (ppm)	Verdict
DC_12A-n5A	15	5+20	DFT-QPSK	M+M	Outer_Full	VL	NT	1.96	0.002343	±2.5	PASS
DC_12A-n5A	15	5+20	DFT-QPSK	M+M	Outer_Full	VN	NT	1.18	0.001411	±2.5	PASS
DC_12A-n5A	15	5+20	DFT-QPSK	M+M	Outer_Full	VH	NT	3.49	0.004172	±2.5	PASS

Temperature											
Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Voltage (Vdc)	Temperature(°C)	Deviation(Hz)	Deviation (ppm)	Limit (ppm)	Verdict
DC_12A-n5A	15	5+20	DFT-QPSK	M+M	Outer_Full	NV	-30	2.73	0.003264	±2.5	PASS
DC_12A-n5A	15	5+20	DFT-QPSK	M+M	Outer_Full	NV	-20	2.95	0.003527	±2.5	PASS
DC_12A-n5A	15	5+20	DFT-QPSK	M+M	Outer_Full	NV	-10	2.58	0.003084	±2.5	PASS
DC_12A-n5A	15	5+20	DFT-QPSK	M+M	Outer_Full	NV	0	2.95	0.003527	±2.5	PASS
DC_12A-n5A	15	5+20	DFT-QPSK	M+M	Outer_Full	NV	10	2.21	0.002642	±2.5	PASS
DC_12A-n5A	15	5+20	DFT-QPSK	M+M	Outer_Full	NV	20	2.38	0.002845	±2.5	PASS
DC_12A-n5A	15	5+20	DFT-QPSK	M+M	Outer_Full	NV	30	1.9	0.002271	±2.5	PASS
DC_12A-n5A	15	5+20	DFT-QPSK	M+M	Outer_Full	NV	40	1.9	0.002271	±2.5	PASS
DC_12A-n5A	15	5+20	DFT-QPSK	M+M	Outer_Full	NV	50	2.81	0.003359	±2.5	PASS